EE2027Electronic Circuits

Review of Basic Concepts

World of Signals

Audio:20Hz~20kHz

Mic: 5~50mV

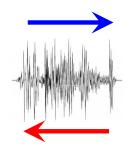
Earphone: 0.316V_{rms} (nominal)

Radio: 850, 900, 1800, 1900, 2100MHz

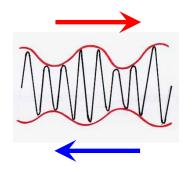
Transmit: +33dBm (10V)

Receive: $-104dBm (1.4\mu V_{rms})$





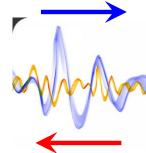


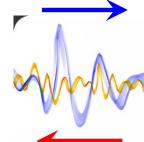




Motion:

Vibration: 50Hz@5V Movement: ±8g, 12-bit





Camera:

Picture: 8 Megapixel

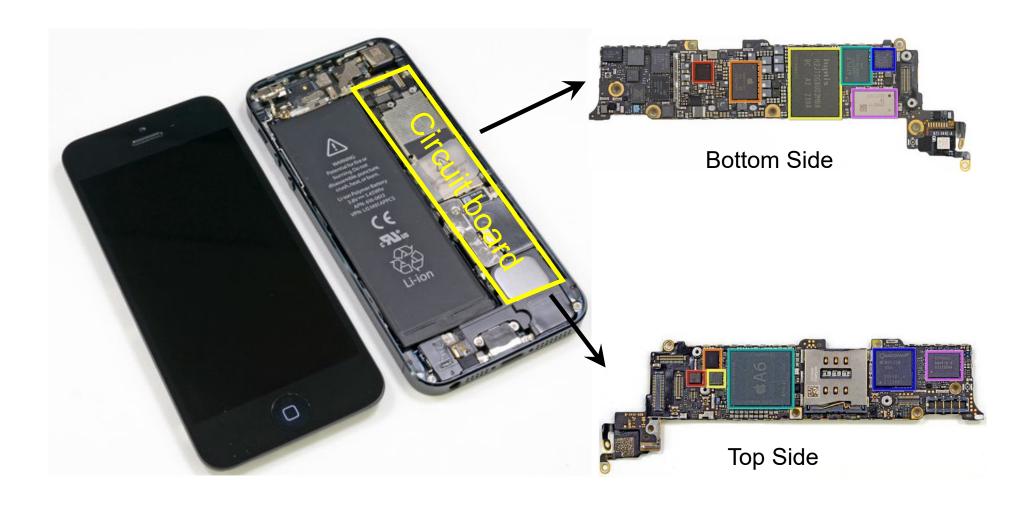
Video: HD, 30fps



Important things:

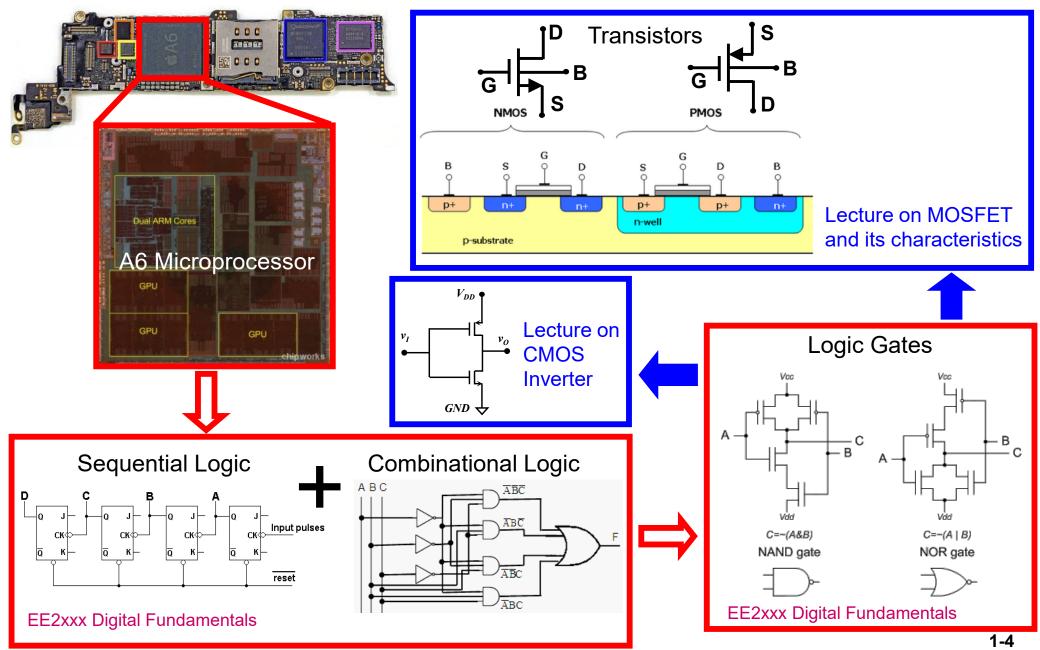
- 1. Signal level
- 2. Signal frequency

Teardown

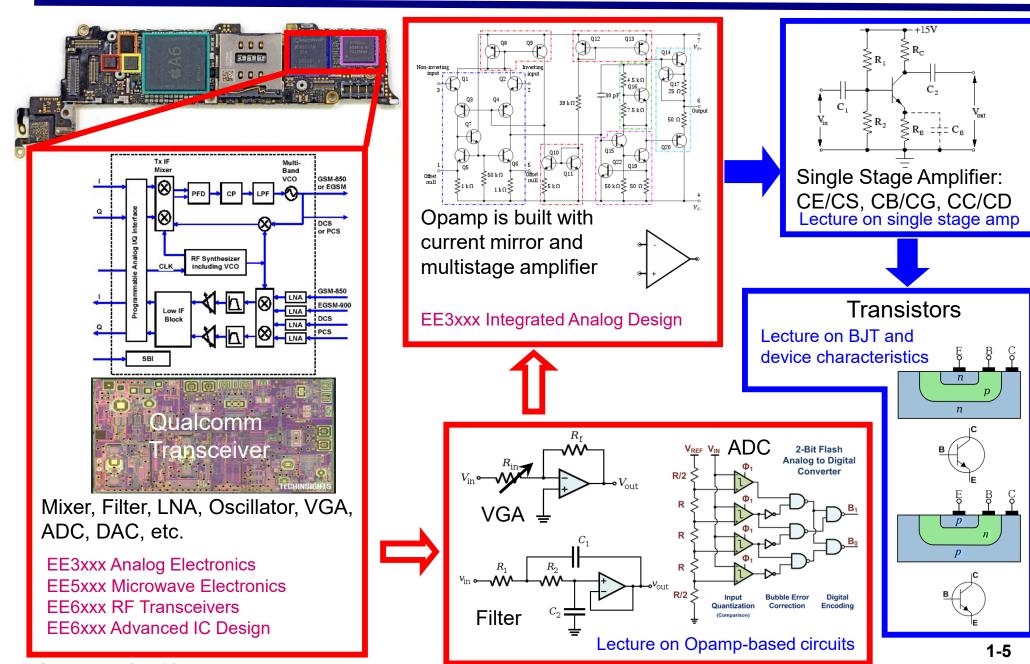


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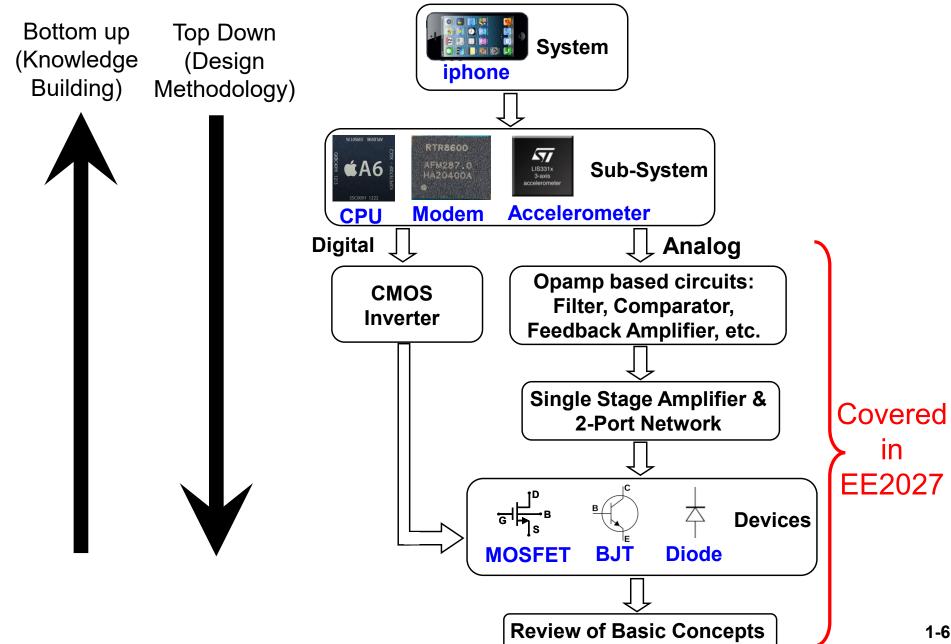
Microprocessor



Transceiver



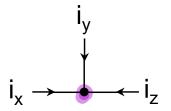
Overview



Topics

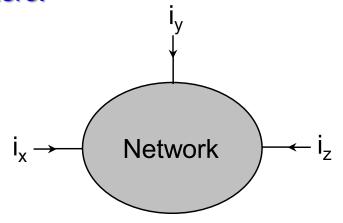
- Node, Mesh Analysis (KCL and KVL) and Linear Superposition
- Thevenin & Norton Equivalent;
- AC Signal Quantities Peak, RMS and Average values
- Phasors Phase and Amplitude
- Impedance of Capacitor and Inductor
- RC Circuit AC Analysis Passive Filter
- Maximum Power Transfer
- RC Circuit Transients Charge and Discharge (Self Reading)
- Power and Power Factor (Self Reading)

Node Analysis (KCL)



Conventional Kirchhoff's Current Law (KCL):

Currents flowing into a **node sum to zero**.

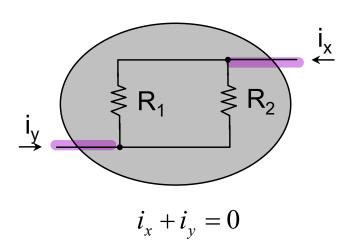


Generalized KCL:

Currents flowing into a **network** sum to **zero**

$$\sum_{k} i_k = i_x + i_y + i_z + \dots = 0$$

Example:



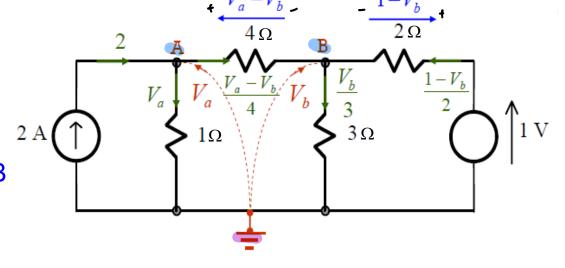
Since current i is equal to the rate of flow of charge q (i.e., i = dq/dt), KCL corresponds to the conservation of charge.

Node Analysis (KCL)

Step 1: Identify nodes & assign nodal voltages, V_a and V_b , w.r.t. reference ground node.

Step 2: Determine branch voltages, e.g., $(V_a - V_b)$ and $(1 - V_b)$.

Note: Potential of node A w.r.t. node B $V_{ab} = V_a - V_b$



Step 3: Find branch currents.

Step 4: Apply KCL to nodes A and B.

Node A:
$$\frac{V_a}{1\Omega} + \frac{V_a - V_b}{4\Omega} = 2A \implies 5V_a - V_b = 8$$
 Multiply both sides with 4

Node B:
$$\frac{V_a - V_b}{4\Omega} + \frac{1 - V_b}{2\Omega} + \frac{0 - V_b}{3\Omega} = 0 \implies 3V_a - 13V_b = -6$$
 Multiply both sides with 12

Step 5: Solve resulting equations to obtain the nodal voltages.

$$V_a = \frac{55}{31} \text{ V}, \quad V_b = \frac{27}{31} \text{ V}$$

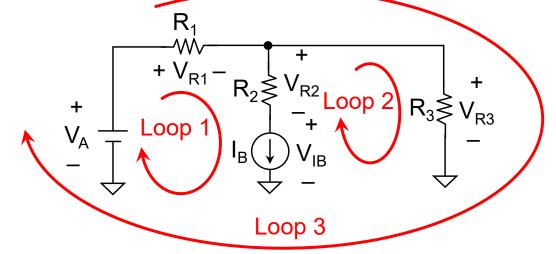
Mesh Analysis (KVL)

Kirchhoff's Voltage Law (KVL):

The sum of potential differences around any closed-loop is zero. KVL corresponds to the conservation of energy (qV) around any closed loop.

$$\sum_{k} V_k = V_x + V_y + V_z + \dots = 0.$$

Example:



Loop 1:

$$V_A + (-V_{R1}) + (-V_{R2}) + (-V_{IB}) = 0$$

Loop 3:

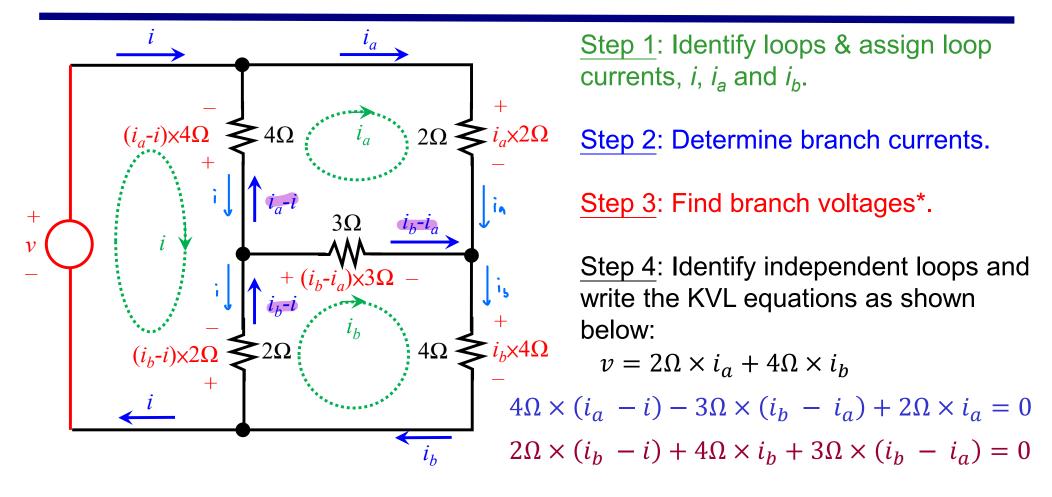
Loop 2:

$$V_A + (-V_{R1}) + (-V_{R3}) = 0$$

$$\mathbf{V}_{\mathbf{IB}} + \mathbf{V}_{\mathbf{R}2} + (-\mathbf{V}_{\mathbf{R}3}) = 0$$

Note: voltage across the current source, I_B , is assumed to be $V_{IB} \neq 0$.

Mesh Analysis (KVL)



* + and – of voltage across a resistor follow the defined current flow direction, i.e., high to low Step 5: Solve resulting equations.

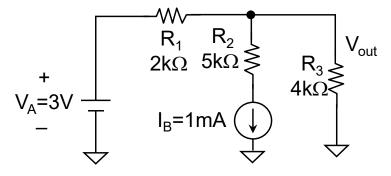
$$v = \frac{17i}{6} \Rightarrow R_{equivalent} = \frac{v}{i} = \frac{17}{6} \Omega$$

Linear Superposition

- The combined effect of various independent sources can be determined by summing the individual impact from various sources.
- When determining the impact of an individual source, you need to kill all other voltage sources by short-circuiting them, and all other current sources by open-circuiting them.

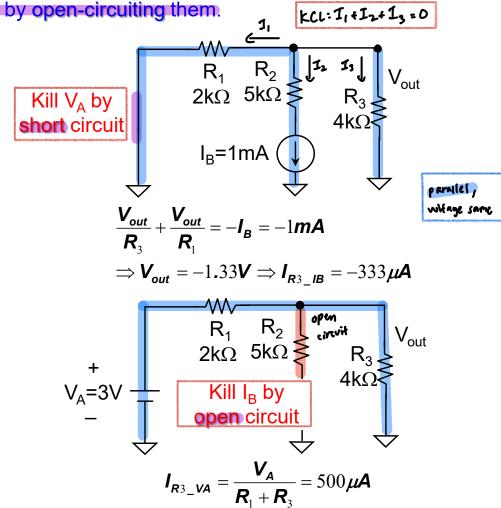
Example:

Determine I_{R3}



Superposition:

$$I_{R3} = I_{R3} I_{B} + I_{R3} V_{A} = 167 \mu A$$



Thevenin & Norton Equivalent

Thevenin:

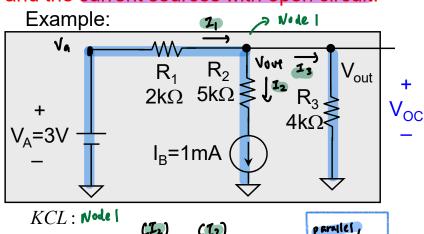
Any linear network with one port output can be replaced with an equivalent Thevenin voltage source (V_{THV}) in series with a Thevenin resistance (R_{THV}) .

Norton:

Any linear network with one port output can be replaced with an equivalent Norton current source (I_{NOR}) in parallel with a Norton resistance (R_{NOR}) .

Notes:

- 1) The **Thevenin voltage** source (V_{THV}) is found by evaluating the **open-circuit** voltage at the port.
- 2) The Norton current source (I_{NOR}) is found by evaluating the short-circuit current at the port.
- 3) In finding the **equivalent resistance** looking into the port, **kill** the voltage sources with short circuit, and the current sources with open circuit.



$$KCL: \text{Node I}$$

$$CI_2) \quad CI_3)$$

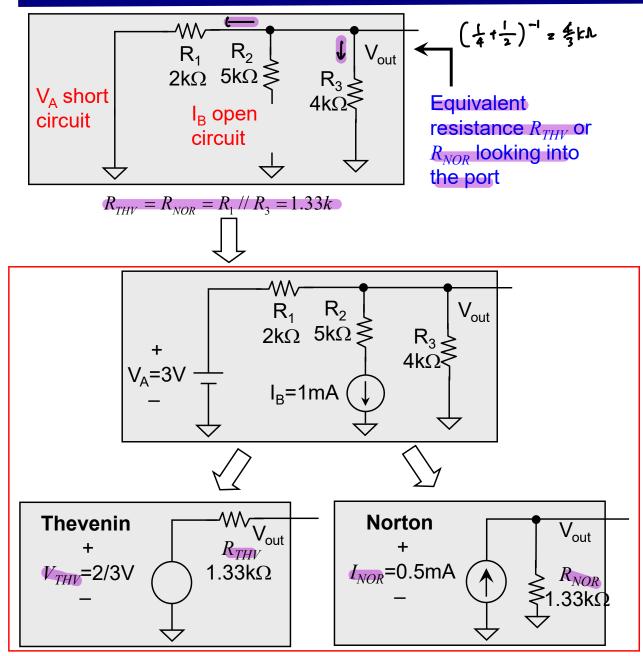
$$CI_4) \quad \frac{V_A - V_{out}}{R_1} + \frac{0 - V_{out}}{R_3} + (-I_B) = 0$$

$$\Rightarrow 3V_{out} = 2V_A - I_B \times R_3 = 2V$$

$$\Rightarrow V_{out} = \frac{2}{3}V = V_{THV} \quad \text{(Open-circuit voltage)}$$

$$\begin{split} &KCL: \text{ Node I} \\ &I_{R1} + \left(-I_B\right) + \left(-I_{R3}\right) + \left(-I_{SC}\right) = 0 \\ &I_{R1} = \frac{V_A}{R_1} = 1.5 mA \quad I_{R3} = 0 \\ &\Rightarrow I_{SC} = I_{R1} - I_B = 0.5 mA = I_{NOR} \quad \text{(Short-circuit current)} \end{split}$$

Thevenin & Norton Equivalent



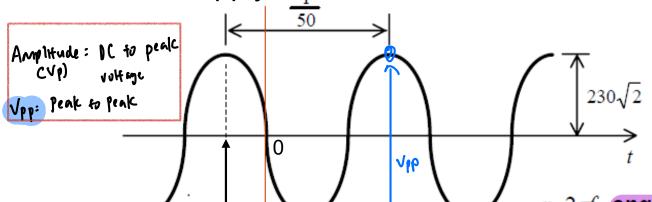
Significance of Thevenin and Norton Equivalents:

In reality, there is **no need** for you to open up the black box, determine the components and circuits, and work out the Thevenin or Norton equivalent. You just need a **multimeter**, and measure open-circuit voltage (When multimeter is used to measure voltage, it actually behaves like open circuit) to get V_{THV} , and measure short-circuit current (When multimeter is used to measure current, it actually behaves like short circuit) to get I_{NOR} .

 R_{THV} and R_{NOR} can be obtained from the expression V_{THV}/I_{NOR} .

AC Signal Quantities

In alternating current (AC) circuits, voltages and currents change with time in a sinusoidal manner. The most common AC voltage source is the mains supply:



$$\theta$$
 = **phase** = 1.57 rad

$$f = frequency = 50Hz$$

$$\omega = 2\pi f =$$
angular frequency $= 100\pi = 314 \text{rad/s}$

$$v(t) = \sqrt{2}r\cos(2\pi f t + \theta) = \sqrt{2}r\cos(\omega t + \theta) = \sqrt{2}r\cos(\frac{2\pi t}{T} + \theta)$$

$$= 230\sqrt{2}\cos(100\pi t + 1.57)$$

$$T = \frac{1}{f} = period = \frac{1}{50} = 0.02s$$

$$\sqrt{2}r = peak \ value = 230\sqrt{2} = 324V$$

$$T = \frac{1}{f} =$$
period $= \frac{1}{50} = 0.02$

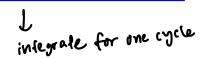
$$\sqrt{2}r$$
=peak value=230 $\sqrt{2}$ =324V

Average value,
$$v_{av} = \frac{1}{T} \int_0^T v(t) dt$$

 $t = \frac{-1.57}{2\pi(50)}$

r=rms (root mean square) value=230V

Only applicable to sinusoidal signals



AC Signal Quantities

Instantaneous power: IXV

Avy power: Ims x Vm

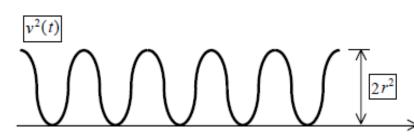
Root Mean Square (rms) value (can be defined for any periodic signal

with period T) as follows:

$$v(t) = \sqrt{2}r\cos(2\pi f t + \theta) = \sqrt{2}r\cos\left(\frac{2\pi t}{T} + \theta\right)$$

$$v^{2}(t) = 2r^{2} \cos^{2}\left(\frac{2\pi t}{T} + \theta\right) = r^{2}\left[1 + \cos\left(\frac{4\pi t}{T} + 2\theta\right)\right]$$

 $2 \cos^2(x) = 1 + \cos(2x)$ since



Mean (Average) of the square value is:

$$\frac{1}{T} \int_0^T v^2(t) dt = \frac{1}{T} \int_0^T r^2 \left[1 + \cos\left(\frac{4\pi t}{T} + 2\theta\right) \right] dt = \frac{1}{T} \int_0^T r^2 dt = r^2$$

I if not pendic need to find based on one cycle

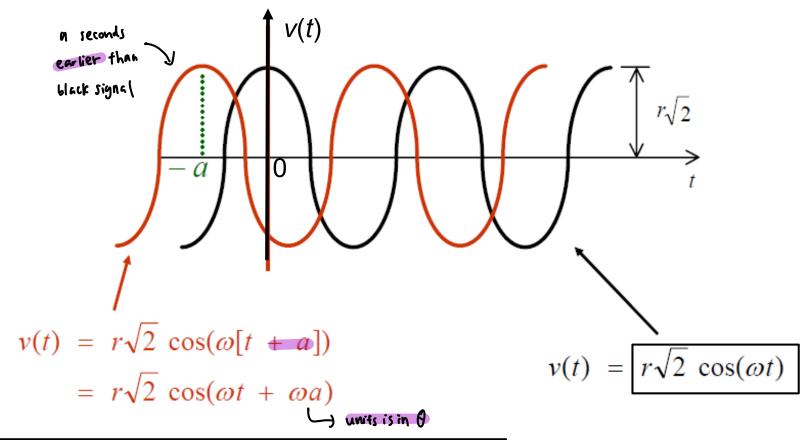
Sin cos signel (sinusoid)

Square root of the mean of the square value is the rms value = $\left| \frac{1}{T} \int_{0}^{T} v^{2}(t) dt \right| = r$

The expression within the blue box is valid for any periodic signal

Phasors

Determination of Phase for a sinusoidal function:



Phase $\theta = \omega a$, where $-\pi \le \theta \le \pi$

Phasors

A sinusoidal signal (voltage or current) is typically represented using complex number format:

$$v(t) = \sqrt{2} r \cos(\omega t + \theta) = \sqrt{2} r Re \left[e^{j(\omega t + \theta)} \right] = Re \left[(re^{j\theta})(\sqrt{2}e^{j\omega t}) \right]$$
Euler's Formula: $e^{j\omega} = \cos(\omega) + j \sin(\omega)$

"redundanf"

Using Phasors, the above time-varying AC voltage v(t) becomes a complex time-invariant number/voltage:

$$V = r e^{j\theta} = r \underline{\theta}$$

where r = |V| = Magnitude/Modulus of V = rms value of v(t) $\theta = \text{Arg } [V] = \text{Phase of } v(t)$ $\theta = \text{Arg } [V] = \text{Phase of } v(t)$

Phasors

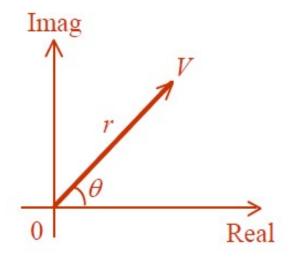
Time-varying sinusoidal voltage:

$$v(t) = \sqrt{2} \, r \cos(wt + \theta) = \sqrt{2} \, r \, Re \left[e^{j(\omega t + \theta)} \right] = Re \left[(re^{j\theta})(\sqrt{2}e^{j\omega t}) \right]$$
inter changeable (need to know)

Phasor notation:

$$V = r e^{j\theta} = r \underline{\theta}$$

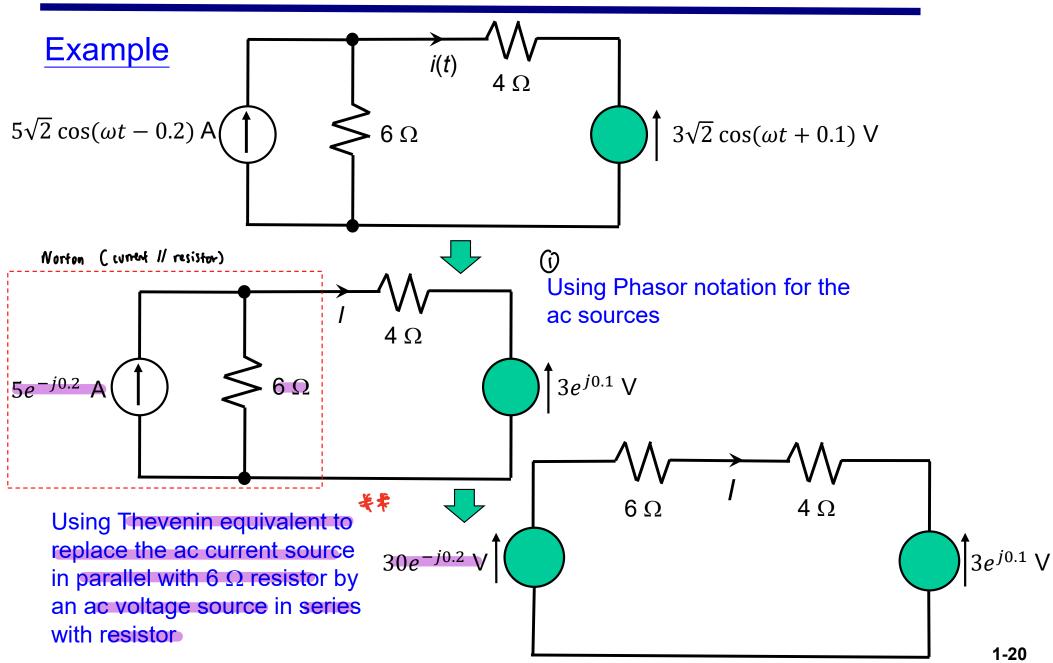
Graphically, on a Phasor diagram:



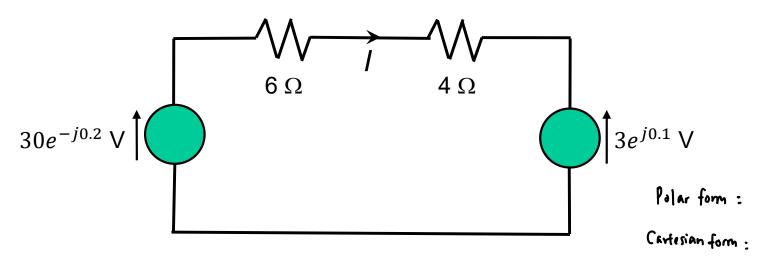
Complex Plane

Note: Using Phasors, all time-varying ac quantities become complex DC quantities, and all DC circuit analysis techniques (e.g., Node and Mesh analysis) can be employed for AC circuits (see following example).

Phasors (Circuit Analysis)



Phasors (Circuit Analysis)



Applying KVL:
$$I = \frac{30e^{-j0.2} - 3e^{j0.1}}{10}$$

$$= 3[\cos(-0.2) + j\sin(-0.2)] - 0.3[\cos(0.1) + j\sin(0.1)]$$

$$= (2.940 - j0.596) - (0.299 + j0.030) = 2.641 - j0.626$$

$$= \sqrt{(2.641)^2 + (0.626)^2} e^{j\tan^{-1}(\frac{-0.626}{2.641})} = 2.714 e^{-j0.233} \text{ A}^{\frac{1}{2}}$$

$$i(t) = 2.714\sqrt{2}\cos(\omega t - 0.233) \, \text{A}$$

$$\downarrow$$

$$\land \text{ red for multiply back } 52$$

$$+ \text{ Heng} \qquad + \text{ oper VP}$$

Capacitor

 $\begin{array}{c}
i(t) \downarrow \\
+ \\
v(t) \\
\end{array}$ C

continues, hence

Circuit symbol for a capacitor with capacitance C is:

Current-voltage (i-v) relationship of a capacitor (with voltage polarity and current direction as indicated) is:

$$i(t) = C \left. \frac{dv(t)}{dt} \right)$$

Note: For a finite current i(t), voltage v(t) across a capacitor cannot change abruptly.

For **DC** circuits, v(t) = constant.

$$\Rightarrow \frac{dv(t)}{dt} = 0 \Rightarrow i(t) = 0$$

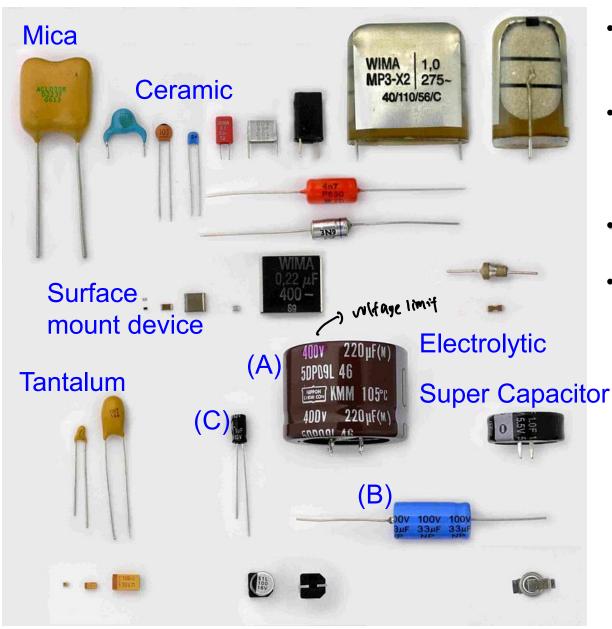
That is why we treat the capacitor as an open circuit in DC circuit analysis. (DC means angular frequency $\omega = 0$)

Unit for capacitance: Farad (F). Practical capacitors in electronic circuits (excluding power electronics) typically have values of micro-Farad (μ F or 10⁻⁶ F) or pico-Farad (pF or 10⁻¹² F).

Types of Capacitors

Туре	Capacitance range	Maximum voltage	Accuracy	Temperature stability	Leakage	Comments	
Mica	1pF-0.01μF	100-600	Good		Good	Excellent; good at RF	
Tubular ceramic	0.5pF-100pF	100-600		Selectable		Several tempcos (including zero)	
Ceramic	10pF-1μF	50-30,000	Poor	Poor	Moderate	Small, inexpen- sive, very popular	
Polyester (Mylar)	0.001 μ F-50 μ F	50-600	Good	Poor	Good	Inexpensive, good, popular	
Polystyrene	10pF-2.7μF	100-600	Excellent	Good	Excellent	High quality, large; signal filters	
Polycarbonate	100pF-30μF	50-800	Excellent	Excellent	Good	High quality, small	
Polypropylene	100pF-50μF	100-800	Excellent	Good	Excellent	High quality, low dielectric absorption	
Teflon	1000pF-2μF	50-200	Excellent	Best	Best	High quality, lowest dielectric absorption	
Glass	10pF-1000pF	100-600	Good		Excellent	Long-term stability	
Porcelain	100pF-0.1μF	50-400	Good	Good	Good	Good long-term stability	
Tantalum	0.1μF-500μF	6-100	Poor	Poor		High capaci- tance; polarized, —3 small; low inductance	have polarify (-veltue)
Electrolytic	0.1μF-1.6F	3-600	Terrible	Ghastly	Awful	Power-supply filters; polarized; short life	
Double layer	0.1F-10F	1.5-6	Poor	Poor	Goodr	Memory backup; high series resistance	Source: P. Horowitz, The A
Oil	0.1 <i>μ</i> F-20 <i>μ</i> F	200-10,000			Good	High-voltage filters; large, long life	of Electronics, Cambridge University Press.
Vacuum	1pF-5000pF	2000-36,000)		Excellent	Transmitters	1-2

Types of Capacitors



- Size (Area) of a capacitor is directly proportional to its capacitance value.
- For example, note the relative size difference of the (A) 220 μF, (B) 3.3 μF and (C) 1 μF electrolytic capacitors.
- Super capacitor: Large capacitance (1F) with small dimension
- Some capacitors have polarity. For example, note the polarity (negative or minus terminal) of electrolytic capacitors.



Capacitor

Consider the operation of a capacitor in an AC circuit:

$$v(t) = r_v \sqrt{2} \cos(\omega t + \theta_v)$$

$$\downarrow \rhoolow convert$$

$$r_v \angle \theta_v$$

$$\downarrow c = \frac{i(t) = C \frac{dv(t)}{dt} = -\omega C r_v \sqrt{2} \sin(\omega t + \theta_v)$$

$$= \omega C r_v \sqrt{2} \cos(\omega t + \theta_v + \frac{\pi}{2})$$

Using Phasor representation (Refer to slides 1-17 ~ 1-21):

$$V = r_{v}e^{j\theta_{v}} \wedge \frac{\int_{-\infty}^{\infty} I = \omega C r_{v}e^{j\theta_{v}}e^{j\frac{\pi}{2}}}{\int_{-\infty}^{\infty} I} = j\omega C r_{v}e^{j\theta_{v}} = j\omega C V$$

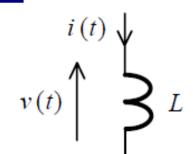
$$Z \implies \frac{V}{I} = \frac{1}{j\omega C}$$

With Phasor representation, the capacitor behaves as if it is a resistor with a "complex resistance" or reactance of

$$X_C = \frac{1}{j\omega C}$$

Inductor

An inductor consists of a coil of wire that establishes a magnetic field when current flows through it.



Circuit symbol for an inductor with inductance L:

Current-voltage (i-v) relationship of an inductor (with voltage polarity and current direction as indicated) is:

$$v(t) = L \frac{di(t)}{dt}$$

 $v(t) = L \frac{di(t)}{dt}$ Note: For a finite voltage v(t), current i(t) through an inductor cannot change abruptly.

continus, herce

For dc circuits, i(t) = constant.

$$\Rightarrow \frac{di(t)}{dt} = 0 \Rightarrow v(t) = 0$$

That is why we treat the inductor as a short circuit in DC circuit analysis.

Unit for inductance: Henry (H). Practical inductors typically have values of milli-Henry (mH or 10⁻³ H) or micro-Henry (μH or 10⁻⁶ H).

Inductor

Consider the operation of an inductor in an AC circuit:

$$v(t) = r_i \sqrt{2} \cos(\omega t + \theta_i)$$

$$L \quad v(t) = L \frac{di(t)}{dt} = -\omega L r_i \sqrt{2} \sin(\omega t + \theta_i)$$

$$= \omega L r_i \sqrt{2} \cos(\omega t + \theta_i + \frac{\pi}{2})$$

Using Phasor representation (Refer to slides 1-17 ~ 1-21):

$$V(t) = \omega L r_i e^{j\theta_i}$$

$$V(t) = \omega L r_i e^{j\theta_i} e^{j\pi/2} = j\omega L r_i e^{j\theta_i} = (j\omega L)I$$

$$I = r_i e^{j\theta_i}$$

$$J\omega L = J\omega L$$

$$V = \omega L r_i e^{j\theta_i} e^{j\pi/2} = j\omega L r_i e^{j\theta_i} = (j\omega L)I$$

With Phasor representation, the inductor behaves as if it is a resistor with a "complex resistance" or reactance of

$$X_L = j\omega L$$

Impedance and Admittance

Impedance (Z): Resistance (R) + Reactance (X)

$$\int Z = R + jX$$

Admittance (Y): Conductance (G) + Susceptance (B)

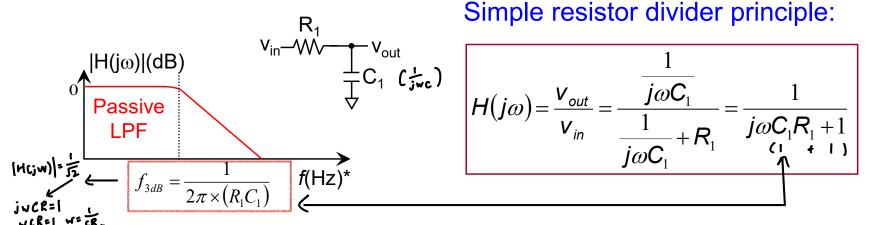
$$\left(Y = \frac{1}{Z} = G + jB\right)$$

$$Y = \frac{1}{Z} = \frac{1}{R+jX} = \frac{R-jX}{(R+jX)(R-jX)} = \frac{R}{R^2+X^2} - \frac{jX}{R^2+X^2}$$

$$\Rightarrow \left(G = \frac{R}{R^2 + X^2}\right) \text{ and } \left(B = \frac{-X}{R^2 + X^2}\right)$$

The RC circuit shown below acts as a 1st order passive low pass filter.

Passive 1st Order Low Pass Filter



- At low frequency (ω → 0), ignore 1st term of denominator (jωC₁R₁) ⇒ H(jω) ≈ 1
 ⇒ Unity gain
- At high frequency (ω → ∞), ignore 2nd term of denominator ⇒ H(jω) ≈ 1/(jωC₁R₁)
 ⇒ Gain reduces with increasing frequency
- To appreciate the frequency response of the passive low pass filter (i.e., plot of gain $|H(j\omega)|$ in decibels (dB) versus frequency f^* , where $\omega = 2\pi f$), we first need to understand the definition of decibel first.

^{*} frequency, f, is plotted on a log-scale. Frequency response is known as **Bode plot**.

Definition of Decibel:

Decibel (dB)[Power Ratio]

$$Y(dB) = 10 \times log \left(\frac{|P_{out}|}{|P_{in}|} \right) = 10 \times log \left(|Power Gain| \right)$$



Power (dBm)

$$P(dBm) = 10 \times log \left(\frac{P_{out}}{1mW} \right)$$

Example:

Typical WLAN/Bluetooth output power is 100mW

$$\Rightarrow 10 \log \left(\frac{100 mW}{1 mW} \right) = 20 dBm$$

Typical mobile phone output power is 2W

$$\Rightarrow 10 \log \left(\frac{2W}{1mW} \right) = 33dBm$$

Decibel (dB)[Voltage Ratio]

$$Y(dB) = 20 \times log \left(\frac{|V_{out}|}{|V_{in}|} \right) = 20 \times log \left(|Gain| \right)$$

Example:

If
$$Gain = 1 \Rightarrow 20 \times log(|Gain|) = 0dB$$

If
$$Gain = 100 \Rightarrow 20 \times log(|Gain|) = 40dB$$

If
$$Gain = 0.001 \Rightarrow 20 \times log(Gain) = -60dB$$

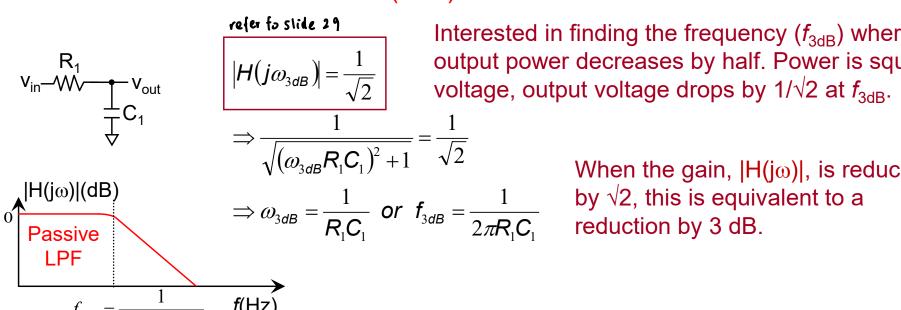
A negative gain in linear scale does not imply loss. But a negative gain in dB scale means loss.

Half-power point of a filter or amplifier is the frequency at which the output power has decreased to half of its peak value. In decibels, this corresponds to a decrease of 3 dB from the peak gain (in dB).

$$10 \log_{10}(0.5) = -3 \text{ dB}$$

The half-power point is a commonly used definition of the 3-dB frequency (f_{3dB}).

Passive 1st Order Low Pass Filter (LPF)



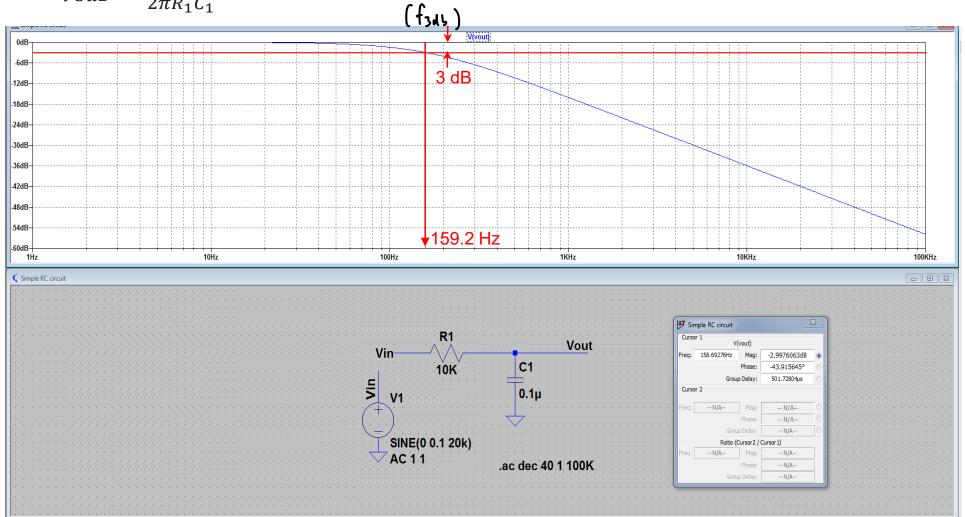
Interested in finding the frequency (f_{3dB}) where the output power decreases by half. Power is square of

When the gain, $|H(j\omega)|$, is reduced

Simulation of Passive 1st Order LPF

• $R_1 = 10 \text{ k}\Omega$, $C_1 = 0.1 \mu\text{F}$

• $f_{3dB} = \frac{1}{2\pi R_1 C_1} = 159.2 \text{ Hz}$



Note: Above frequency response curve is known as Bode plot, where frequency, f, is plotted on a log-scale.

Passive 1st Order High Pass Filter can be obtained by interchanging the positions of R_1 and C_1 in the passive Low Pass Filter circuit.

Passive 1st Order High Pass Filter (HPF)

$$\begin{array}{c|c}
V_{in} & C_1 & V_{out} \\
\hline
R_1 & \\
\end{array}$$

$$H(j\omega) = \frac{V_{out}}{V_{in}} = \frac{R_1}{\frac{1}{j\omega C_1} + R_1} = \frac{j\omega C_1 R_1}{j\omega C_1 R_1 + 1} = \frac{j\omega}{j\omega + \frac{1}{C_1 R_1}}$$

Interested in finding frequency (f_{3dB}) where the power decreases by half (Power is square of voltage).

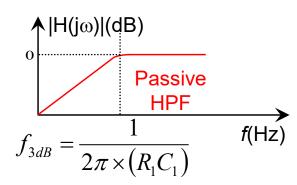
$$|H(j\omega_{3dB})| = \frac{1}{\sqrt{2}}$$

$$\Rightarrow \frac{1}{\sqrt{\left(\frac{1}{\omega_{3dB}R_{1}C_{1}}\right)^{2}+1}} = \frac{1}{\sqrt{2}}$$

$$\Rightarrow \omega_{3dB} = \frac{1}{R_1 C_1} \text{ or } f_{3dB} = \frac{1}{2\pi R_1 C_1}$$

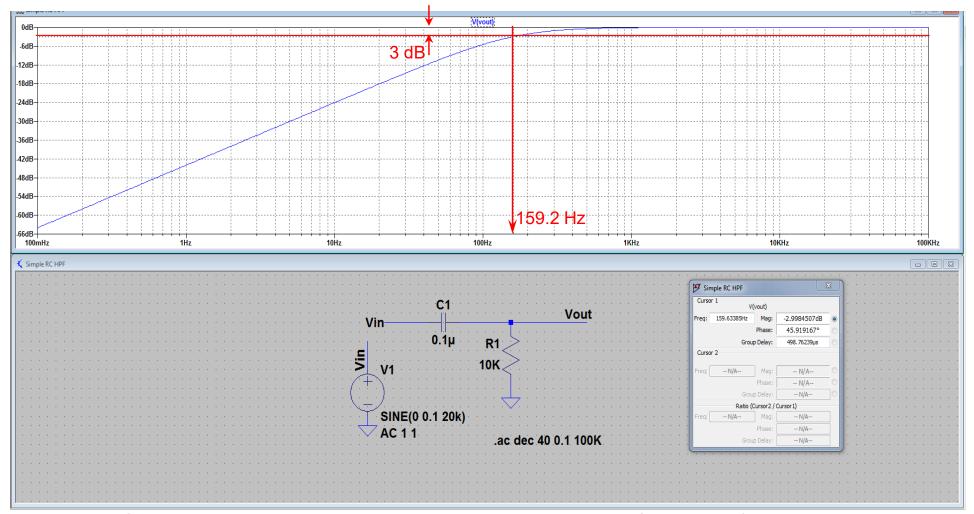
 $\Rightarrow \frac{1}{\sqrt{\left(\frac{1}{\omega_{3dB}R_{1}C_{1}}\right)^{2}+1}} = \frac{1}{\sqrt{2}}$ When the gain, $|H(j\omega)|$, is reduced by $\sqrt{2}$, this is equivalent to a reduction k 3 dB. equivalent to a reduction by

- At low frequency $(\omega \to 0)$, ignore 1st term of denominator (jω) $\Rightarrow H(j\omega) \approx j\omega C_1 R_1 \Rightarrow Gain$ increases with increasing frequency
- At high frequency $(\omega \to \infty)$, ignore 2^{nd} term of denominator $(1/(C_1R_1))$ \Rightarrow H(j ω) \approx 1 \Rightarrow Unity gain



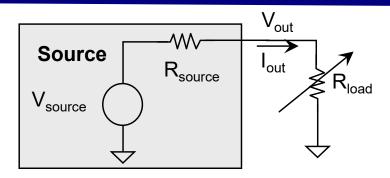
Simulation of Passive 1st Order HPF

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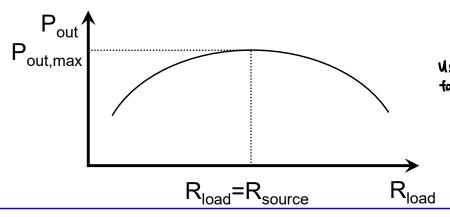


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Maximum Power Transfer



What is R_{load} that will result in maximum power transfer from source to load?



- 1) When R_{load} is large, V_{out} is large but I_{out} is small $\Rightarrow P_{out} = V_{out} \times I_{out}$ is small
- 2) When R_{load} is small, V_{out} is small but I_{out} is large $\Rightarrow P_{out} = V_{out} \times I_{out}$ is small
- 3) There exists an optimum R_{load} with moderate V_{out} and I_{out} which gives rise to maximum $P_{out} = V_{out} \times I_{out}$

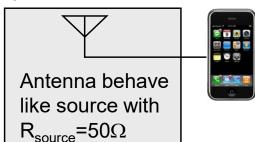
$$P_{out} = \frac{V_{source}}{R_{source} + R_{load}} \times \frac{R_{load}V_{source}}{R_{source} + R_{load}}$$

$$\frac{R_{load}}{R_{source} + R_{load}} \times \frac{R_{load}V_{source}}{R_{source} + R_{load}}$$

$$\frac{R_{load}}{R_{load}} = \frac{R_{load}}{(R_{source} + R_{load})^2} - \frac{2R_{load}}{(R_{source} + R_{load})^3} = 0$$

$$\Rightarrow R_{load} = R_{source}$$





Cell phone should be designed to present R_{load} =50 Ω when interface with antenna



Audio source should be designed to present R_{source} =8 Ω when interface with speaker



Speaker behaves as

 $R_{load} = 8\Omega$

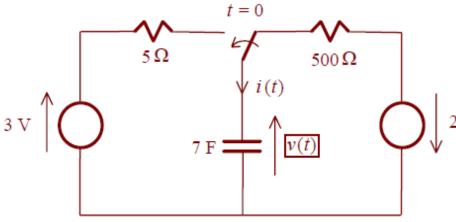
Basic Concepts Covered -

- KCL: Sum of currents into a node, $\sum_{k} i_{k} = i_{x} + i_{y} + i_{z} + \cdots = 0$
- KVL: Sum of voltages in a closed loop, $\sum_{k} V_{k} = V_{x} + V_{y} + V_{z} + \cdots = 0$
- Linear superposition: while considering effect of one independent source, kill all other independent sources
- Thevenin/Norton Equivalent: V_{THV}/I_{THV} & R_{THV}/I_{THV}
- AC analysis/phasor: $v(t) = \sqrt{2}r\cos(\omega t + \theta)$ vs $V = re^{j\theta}$
- AC analysis: capacitor: $X_C = \frac{1}{j\omega C}$; Inductor, $X_L = j\omega L$
- Passive 1st order filters: RC low & high pass filters
- Definition of dB (decibel) & f_{3dB} ($\omega_{3dB} = 2\pi f_{3dB}$)
- Maximum power transfer: $R_{load} = R_{source}$

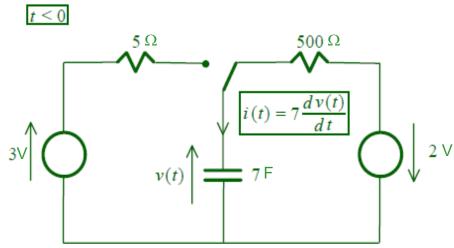
Transients (Self Reading)

- What is transient analysis?
- DC and AC circuit analysis are typically known as steady state analysis, where transients caused by switching of signal sources are assumed to have died down and all voltages and currents have stabilized.
- On the other hand, when the circuit is first switched on or off, the circuit will not be in the steady state.
- In the non-steady state situation, the determination of the voltages and currents as a function of time is known as transient analysis.

Consider finding v(t) in the following RC circuit:

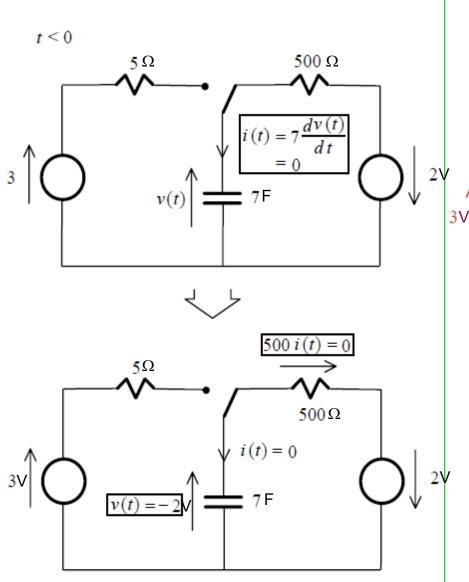


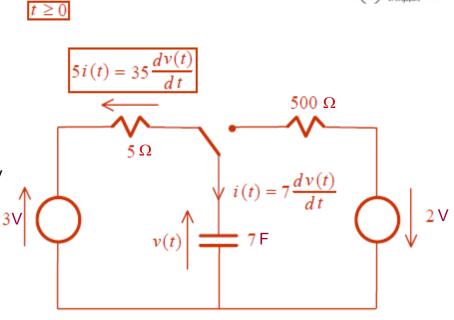
where the switch is in the position shown for t < 0 and is in the other position for $t \ge 0$.



Taking the switch to be in this position starting from $t = -\infty$, the voltages and currents will have settled down to constant values for practically all t < 0.

$$i(t) = 7 \frac{dv(t)}{dt} = 7 \frac{d(\text{constant})}{dt} = 0, \ t < 0$$





Applying KVL:

$$35\frac{dv(t)}{dt} + v(t) = u(t) = 3, \ t \ge 0$$

which has a solution

$$v(t) = v_{ss}(t) + v_{tr}(t), \quad t \ge 0$$

$$35\frac{dv(t)}{dt} + v(t) = 3, \qquad t \ge 0$$

Consider first the steady state response:
$$\frac{dv(t)}{dt} = 0$$
 and $v(t) = v_{ss}(t)$
 $v_{ss}(t) = 0$ and $v(t) = v_{ss}(t)$

Next, consider the transient response:

$$35\frac{dv_{tr}(t)}{dt} + v_{tr}(t) = 0, \qquad t \ge 0$$

$$\frac{dv_{tr}(t)/dt}{v_{tr}(t)} = -\frac{1}{35} , \qquad t \ge 0$$

$$v_{tr}(t) = ke^{-\frac{t}{35}}, \qquad t \ge 0$$

Therefore, the complete response is: $v(t) = 3 + ke^{-\frac{t}{35}}$, $t \ge 0$

- To determine the constant k in the transient response of the RC circuit, we use the concept that the voltage v(t) across a capacitor must be continuous and cannot change abruptly.
- For the RC circuit, the complete solution for v(t) is:

$$v(t) = \begin{cases} -2 \text{ V,} \\ \left(3 + ke^{-\frac{t}{35}}\right) \text{ V,} \end{cases}$$

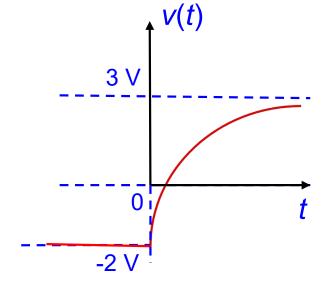
t < 0

 $t \geq 0$



$$v(0) = -2 = 3 + k$$

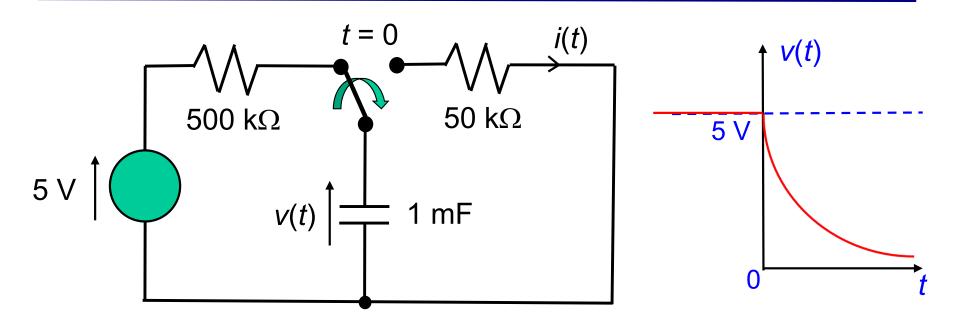


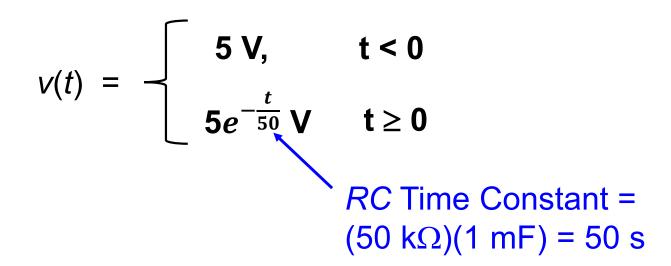


• Hence, the complete solution for v(t) is:

$$v(t) = \begin{cases} -2 \text{ V}, & t < 0 \\ (3 - 5e^{-\frac{t}{35}}) \text{ V}, & t \ge 0 \end{cases}$$

1-41



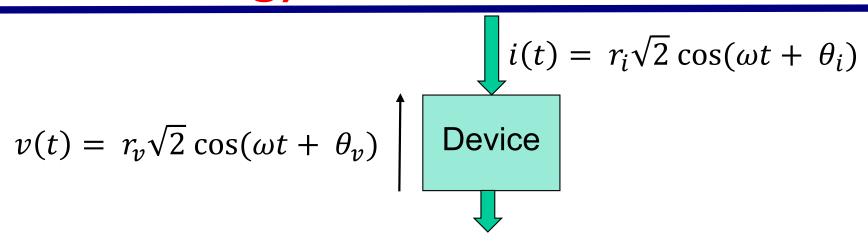


Transients (RL & RLC Circuits)

 Similar approach applies to the transient analysis of RL and RLC circuits: Apply KVL to determine the resulting differential equation and then solve it (using appropriate initial condition to determine the constant).

 For RLC circuits, the resulting differential equation is of 2nd order.

Power (Instantaneous & Average) (Self Reading)



Instantaneous power consumed by the device is:

$$p(t) = i(t)v(t) = 2r_i r_v \cos(\omega t + \theta_i) \cos(\omega t + \theta_v)$$
$$= r_i r_v [\cos(\theta_i - \theta_v) + \cos(2\omega t + \theta_i + \theta_v)]$$

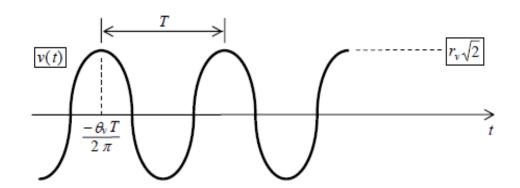
Since $2\cos(x_1)\cos(x_2) = \cos(x_1 - x_2) + \cos(x_1 + x_2)$

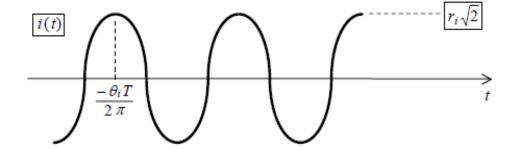
(Take note of the current direction and voltage polarity in the calculation of power consumed)

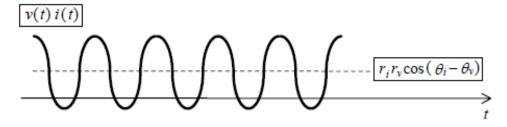
Average power consumed by the device is:

$$P_{av} = \frac{1}{T} \int_0^T p(t)dt = \frac{r_i r_v}{T} \int_0^T \left[\cos(\theta_i - \theta_v) + \cos(\frac{4\pi t}{T} + \theta_i + \theta_v)\right] dt$$
$$= r_i r_v \cos(\theta_i - \theta_v), \text{ where } T = \text{Period and } \omega = 2\pi / T$$

Average Power (P_{av})







Using Phasor notation:

$$V = r_v e^{j\theta_v}$$

$$\Rightarrow V^* = r_v e^{-j\theta_v}$$

$$I = r_i e^{j\theta_i}$$

$$\Rightarrow I^* = r_i e^{-j\theta_i}$$

$$V^*I = r_v r_i e^{j(\theta_i - \theta_v)}$$

$$VI^* = r_v r_i e^{j(\theta_v - \theta_i)}$$

$$P_{av} = r_v r_i \cos(\theta_v - \theta_i) = r_v r_i \cos(\theta_i - \theta_v)$$

$$= Re[r_v r_i e^{j(\theta_v - \theta_i)}] = Re[r_v r_i e^{j(\theta_i - \theta_v)}]$$

$$= Re[V^*I] = Re[VI^*]$$

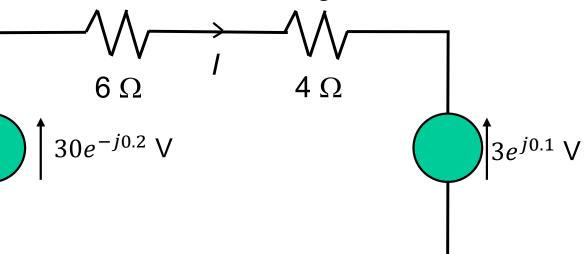
Average Power

- The average power equation $P_{av} = \text{Re}[I^*V] = \text{Re}[IV^*]$ is based on rms current and voltage in ac circuits.
- The average power equation is also valid for dc circuits, which is a special case of ac circuits with *f* = 0 and *V* and *I* having real values.

To show that the Net Average Power (Supplied + Consumed) = 0

Example:

$$I = \frac{30e^{-j0.2} - 3e^{j0.1}}{6+4} = 2.71e^{-j0.23} \text{ A}$$



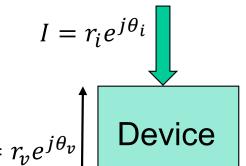
$$30e^{-j0.2}$$
 V source: $Re\left[-\left(2.7e^{-j0.23}\right)^*\left(30e^{-j0.2}\right)\right] = -81\cos(0.03) = -80.96$ W (Negative power indicates power supplied)

$$3e^{j0.1} \text{ V source: } Re\left[\left(2.7e^{-j0.23}\right)^*\left(3e^{j0.1}\right)\right] = Re\left[8.1e^{j0.33}\right] = 8.1\cos(0.33) = 7.66 \text{ W}$$

6 Ω resistor: $Re\left[\left(2.7e^{-j0.23}\right)^*\left(6\times2.7e^{-j0.23}\right)\right] = 6(2.7)^2 = 43.74 \text{ W}$
4 Ω resistor: $Re\left[\left(2.7e^{-j0.23}\right)^*\left(4\times2.7e^{-j0.23}\right)\right] = 4(2.7)^2 = 29.16 \text{ W}$

Hence, Net Average Power = -80.96 + 7.66 + 43.74 + 29.16 = 0 W

Power Factor (Self Reading)



Consider the previous example of the ac device with current and voltage in Phasor notation:

Real (Average) Power = Re[
$$V^*I$$
] = $r_v r_i \cos(\theta_i - \theta_v)$ (Unit: W)

Apparent Power (or Voltage-Current Rating) = $|V||/| = r_v r_i$ (Unit: VA)

Power Factor of the device is the ratio of Real Power to Apparent

Power:

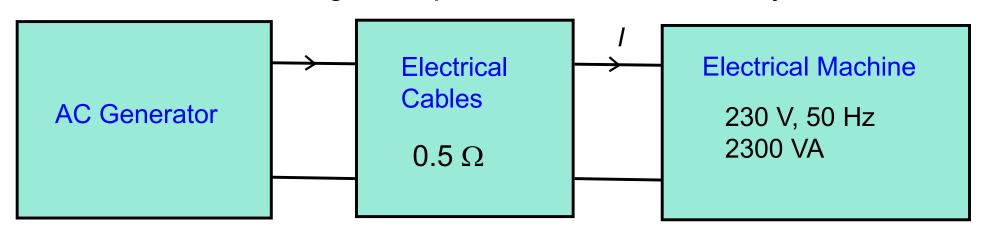
$$Power Factor = \frac{Real Power}{Apparent Power} = \cos(\theta_i - \theta_v)$$

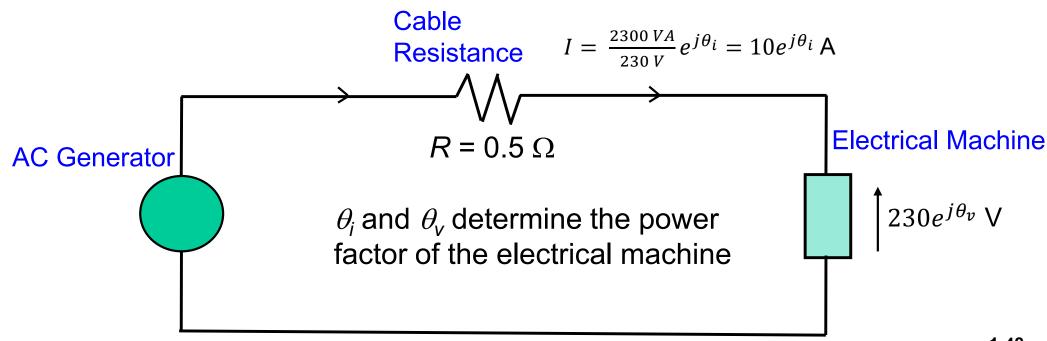
When V and I are in phase $\Rightarrow \theta_i = \theta_v$, and power factor is unity.

Leading Power Factor =>
$$I$$
 leads V => θ_i > θ_v
Lagging Power Factor => I lags V => θ_i < θ_v

Power Factor (Example)

Consider the following example of an AC electrical system:





Power Factor (Example)

Power consumed by the electrical machine and power loss at different power factors:

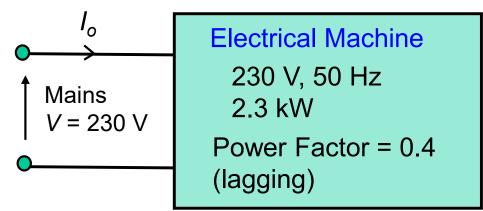
Voltage V (V)	230	230	230
Current I (A)	10	10	10
Voltage-Current Rating (VA) (Apparent Power)	2300	2300	2300
Power Factor = $cos(\theta_i - \theta_v)$	0.4 leading (<i>I</i> leads <i>V</i>)	1 (<i>I</i> in phase with <i>V</i>)	0.4 lagging (<i>I</i> lags <i>V</i>)
θ_i - θ_v (rad)	$\cos^{-1}(0.4) = 1.16$	0	$-\cos^{-1}(0.4) = -1.16$
Power consumed (W) (Real Power)	(2300)(0.4) = 920	2300	(2300)(0.4) = 920
Power loss in cable resistance = I^2R (W)	$(10)^2(0.5) = 50$	$(10)^2(0.5) = 50$	$(10)^2(0.5) = 50$

Power Factor

- At low values of power factor, more apparent power needs to be transferred to obtain the same real power.
- That is, a load with a low power factor draws more current than a load with a high power factor for the same amount of useful (real) power transferred.
- Higher current drawn increases the energy lost in the distribution system, and requires larger wires and other equipment.
- As a result of the additional cost of larger equipment and wasted energy, electrical utilities will typically charge a higher cost to industrial or commercial customers if the power factor is low.
- A low power factor load will require power factor correction or improvement.

Power Factor Improvement (Example)

 Due to the small power factor, the electrical machine cannot be connected directly to a standard 230 V, 13 A mains outlet even though it consumes only 2.3 kW of power.



Current I_o can be obtained as follows:

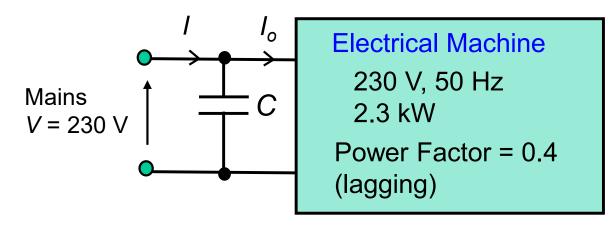
$$|I_o| = \frac{2300 \text{ W}}{(230 \text{ V})(0.4)} = 25 \text{ A}$$
 $\theta_i - \theta_v = -\cos^{-1}(0.4) = -1.16 \text{ rad}$
 $I_o = 25e^{-j1.16}$

Question: How can the power factor be improved so that the machine can be connected to a standard 230 V, 13 A mains outlet?

Power Factor Improvement (Example)

Answer: A capacitor can be connected in parallel across the machine to improve the power factor. We can find the value of the capacitor C such

that the power factor becomes unity.



Power film capacitor (typically using polypropylene film as dielectric) packaged in a cylindrical metal can for power factor correction

$$I = \frac{V}{Z_C} + 25e^{-j1.16} = (230)(j2\pi)(50)C + 10 - j23 = 10 + j(23000\pi C - 23)$$

- If we choose $23000\pi C = 23$, then the imaginary term in *I* will be zero and the overall power factor will be unity.
- Hence C = 0.32 mF and I = 10 A.

References

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- R.J. Smith and R.C. Dorf, Circuits, Devices and Systems: A First Course in Electrical Engineering, John Wiley, 5th Edition, 1992.
- Paul Horowitz, The Art of Electronics, Cambridge University Press, 3rd Edition, 2015.