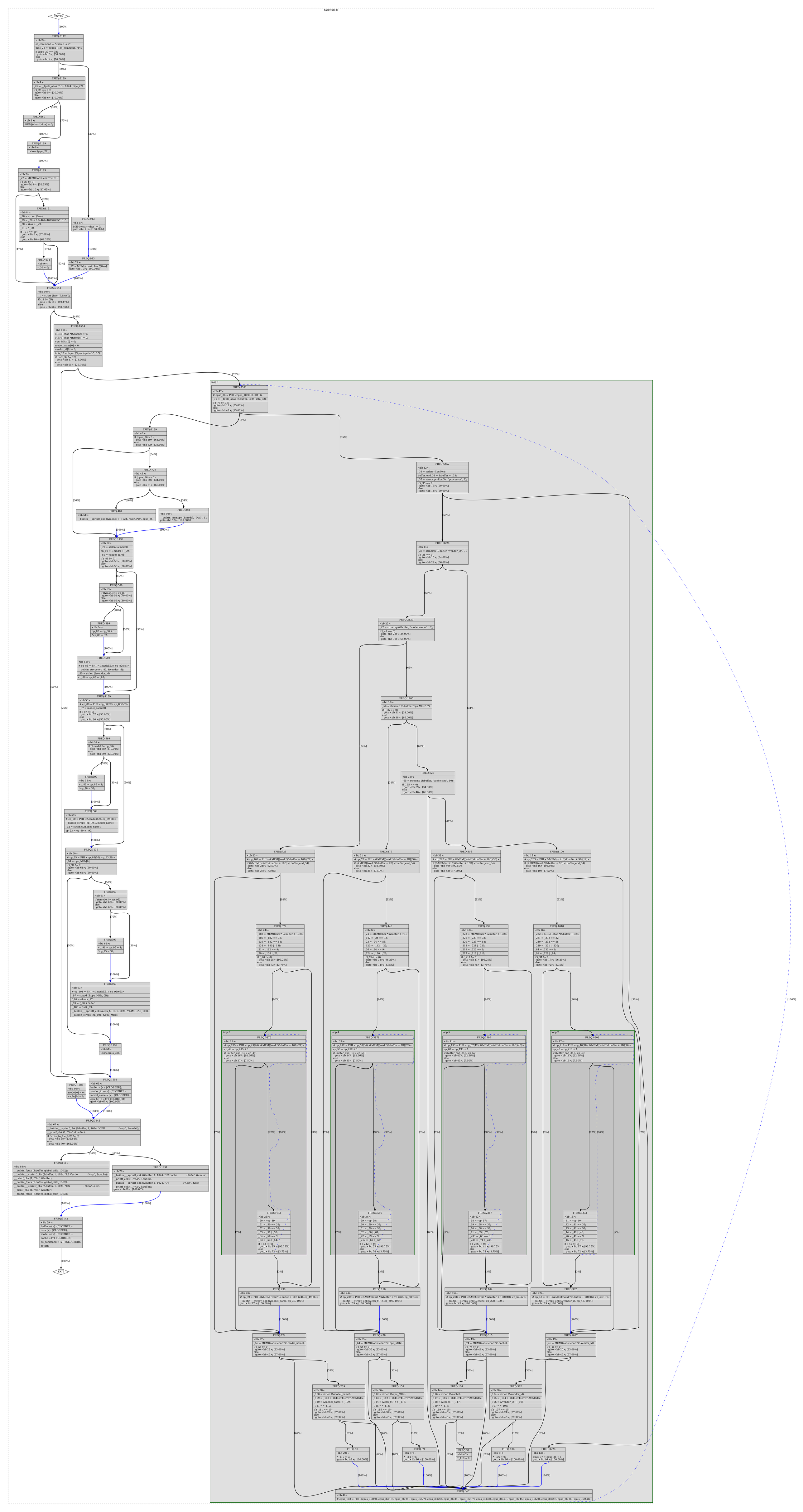
hardware ()
ENTRY
100%
FREQ:2142
%bb 2+:
uu_command = "uname -s -r";
pipe_22 = popen (&uu_command, "r");
if !pipe_22 == 0[]
goto %bb 3+; [30.00%]
else
goto %bb 4+; [70.00%]
70%
30%
FREQ:2139
%bb 4+:
_25 = __fgetx_alloc (&un, 1024, pipe_22);
if ( 25 == 0B)
goto %bb 5+; [30.00%]
else
goto %bb 6+; [70.00%]
70%
30%
FREQ:669
%bb 5+:
MEM[(char *)&un] = 0;
100%
FREQ:2199
%bb 6+:
pclose (pipe_22);
100%
FREQ:2150
%bb 7+:
_27 = MEM[(const char *)&un];
if ( 27 != 0)
goto %bb 8+; [52.35%]
else
goto %bb 10+; [47.65%]
52%
48%
FREQ:1151
%bb 8+:
_28 = strlen (&un);
_29 = _28 + 18446744073709551615;
_30 = &un + _29;
_31 = *_30;
if ( 31 == 10)
goto %bb 9+; [47.68%]
else
goto %bb 10+; [81.32%]
47%
53%
82%
FREQ:438
%bb 9+:
* 30 = 0;
100%
FREQ:943
%bb 10+:
_33 = MEM[(const char *)&un];
goto %bb 11+; [100.00%]
100%
FREQ:3142
%bb 11+:
# _34 = PHI <_33(7), 1(9)>
_1 = strcmp (&un, "Linux");
if ( 1 != 0)
goto %bb 13+; [49.47%]
else
goto %bb 66+; [50.53%]
49%
51%
FREQ:1354
%bb 13+:
MEMcache[] = 0;
MEMcache[] = 0;
MEMcache[] = 0;
cpu_MHz01 = 0;
model_name02 = 0;
cache02 = 0;
new_32 = popen ("/proc/cpuinfo", "r");
if (info_32 != 0)
goto %bb 67+; [71.26%]
else
goto %bb 65+; [28.74%]
71%
29%
loop 1
FREQ:7591
%bb 67+:
# cpus_36 = PHI <cpus_103(40), 0(11)>
_75 = __fgetx_alloc (&buffer, 1024, info_32);
if ( 75 == 0B)
goto %bb 12+; [85.00%]
else
goto %bb 68+; [15.00%]
15%
85%
FREQ:1139
%bb 68+:
if (cpus_36 == 1)
goto %bb 85+; [64.00%]
else
goto %bb 52+; [36.00%]
64%
36%
FREQ:729
%bb 69+:
if (cpus_36 == 2)
goto %bb 70+; [34.00%]
else
goto %bb 53+; [66.00%]
34%
66%
FREQ:481
%bb 51+:
builtin___sprintf_chk (&model, 1, 1024, "%d CPU", cpus_36);
100%
FREQ:248
%bb 50+:
builtin___sprintf_chk (&model, "%sall", 1);
goto %bb 52+; [100.00%]
100%
FREQ:1158
%bb 52+:
_78 = strlen (&model);
cp_80 = &model + _78;
_81 = *cp_80 != 0B;
if ( 81 != 0)
goto %bb 53+; [50.00%]
else
goto %bb 54+; [50.00%]
50%
50%
FREQ:569
%bb 53+:
if (&model != cp_80)
goto %bb 54+; [70.00%]
else
goto %bb 55+; [30.00%]
70%
30%
FREQ:399
%bb 54+:
cp_82 = cp_80 + 1;
*cp_80 = 32;
100%
FREQ:569
%bb 55+:
# cp_83 = PHI <&model(53), cp_82(54)>
_84 = builtin___strcpn (cp_83, &vendor_id);
_85 = strlen (&model);
cp_86 = cp_83 + _85;
100%
FREQ:1158
%bb 56+:
# cp_88 = PHI <cp_86(52), cp_89(55)>
_87 = model_name02;
if ( 87 != 0)
goto %bb 57+; [50.00%]
else
goto %bb 60+; [50.00%]
50%
50%
FREQ:569
%bb 57+:
if (&model != cp_88)
goto %bb 58+; [70.00%]
else
goto %bb 59+; [30.00%]
70%
30%
FREQ:399
%bb 58+:
cp_89 = cp_88 + 1;
*cp_88 = 32;
100%
FREQ:569
%bb 59+:
# cp_90 = PHI <&model(57), cp_89(58)>
builtin_strcpy (cp_90, &model_name);
_92 = strlen (&model_name);
cp_93 = cp_90 + _92;
100%
FREQ:1139
%bb 60+:
# cp_95 = PHI <cp_88(56), cp_93(59)>
_94 = cpu_MHz02;
if ( 94 != 0)
goto %bb 61+; [50.00%]
else
goto %bb 64+; [50.00%]
50%
50%
FREQ:569
%bb 61+:
if (&model != cp_95)
goto %bb 62+; [70.00%]
else
goto %bb 63+; [30.00%]
70%
30%
FREQ:399
%bb 62+:
cp_96 = cp_95 + 1;
*cp_95 = 32;
100%
FREQ:569
%bb 63+:
# cp_101 = PHI <&model(61), cp_96(62)>
_97 = strtod (&cpu_MHz, 0B);
if (_98 > 0.0e0)
goto %bb 64+; [50.00%]
else
goto %bb 65+; [50.00%]
99 = (int) _98;
_100 = (int) _99;
builtin___sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", _100);
builtin_strcpy (cp_101, &cpu_MHz);
100%
FREQ:1139
%bb 64+:
fclose (info_32);
100%
FREQ:1354
%bb 65+:
buffer = {v} {CLOBBER};
vendor_id = {v} {CLOBBER};
model_name = {v} {CLOBBER};
cpu_MHz = {v} {CLOBBER};
goto %bb 67+; [100.00%]
100%
FREQ:2188
%bb 66+:
model02 = 0;
cache02 = 0;
100%
FREQ:3142
%bb 67+:
builtin___sprintf_chk (&buffer, 1, 1024, "CPU:\t\t\t%s\n", &model);
printf_chk (1, "%s", &buffer);
if (write_to_file_8(D) != 0)
goto %bb 68+; [36.46%]
else
goto %bb 70+; [63.54%]
36%
64%
FREQ:1331
%bb 68+:
__builtin_fputs (&buffer, global_stfile_19203);
builtin___sprintf_chk (&buffer, 1, 1024, "L2 Cache\t\t\t%s\n", &cache);
printf_chk (1, "%s", &buffer);
__builtin_fputs (&buffer, global_stfile_19203);
builtin___sprintf_chk (&buffer, 1, 1024, "OS\t\t\t%s\n", &os);
printf_chk (1, "%s", &buffer);
__builtin_fputs (&buffer, global_stfile_19203);
100%
FREQ:1091
%bb 70+:
builtin___sprintf_chk (&buffer, 1, 1024, "L2 Cache\t\t\t%s\n", &cache);
printf_chk (1, "%s", &buffer);
builtin___sprintf_chk (&buffer, 1, 1024, "OS\t\t\t%s\n", &os);
printf_chk (1, "%s", &buffer);
100%
FREQ:3142
%bb 69+:
buffer = {v} {CLOBBER};
vendor_id = {v} {CLOBBER};
model = {v} {CLOBBER};
cache = {v} {CLOBBER};
os_command = {v} {CLOBBER};
return;
100%
EXIT
FREQ:6452
%bb 12+:
_33 = strlen (&buffer);
buffer_end_34 = &buffer + _33;
_35 = strncmp (&buffer, "processor", 9);
if (_35 == 0)
goto %bb 13+; [50.00%]
else
goto %bb 14+; [50.00%]
50%
50%
FREQ:3226
%bb 14+:
_38 = strncmp (&buffer, "vendor_id", 9);
if (_38 == 0)
goto %bb 15+; [34.00%]
else
goto %bb 22+; [66.00%]
34%
66%
FREQ:2329
%bb 22+:
_47 = strncmp (&buffer, "model name", 10);
if (_47 == 0)
goto %bb 23+; [34.00%]
else
goto %bb 30+; [66.00%]
34%
66%
FREQ:2405
%bb 30+:
_56 = strncmp (&buffer, "cpu MHz", 7);
if (_56 == 0)
goto %bb 31+; [34.00%]
else
goto %bb 38+; [66.00%]
34%
66%
FREQ:927
%bb 38+:
_65 = strncmp (&buffer, "cache size", 10);
if (_65 == 0)
goto %bb 39+; [34.00%]
else
goto %bb 46+; [66.00%]
34%
66%
FREQ:336
%bb 23+:
# cp_102 = PHI <&MEM[void *&buffer + 1032](21>
if (&MEM[void *&buffer + 1088] < buffer_end_34)
goto %bb 24+; [7.50%]
else
goto %bb 27+; [7.50%]
7%
93%
FREQ:479
%bb 31+:
# cp_78 = PHI <&MEM[void *&buffer + 7](30>
if (&MEM[void *&buffer + 78] < buffer_end_34)
goto %bb 32+; [7.50%]
else
goto %bb 35+; [7.50%]
7%
93%
FREQ:316
%bb 39+:
# cp_232 = PHI <&MEM[void *&buffer + 1088](38>
if (&MEM[void *&buffer + 1081 < buffer_end_34)
goto %bb 40+; [7.50%]
else
goto %bb 43+; [7.50%]
7%
93%
FREQ:1100
%bb 15+:
# cp_212 = PHI <&MEM[void *&buffer + 983](14>
if (&MEM[void *&buffer + 981 < buffer_end_34)
goto %bb 16+; [7.50%]
else
goto %bb 19+; [7.50%]
7%
93%
FREQ:872
%bb 24+:
_102 = MEM[(char *)&buffer + 1088];
100 = _102 == 32;
138 = _102 == 58;
138 = 100 | 138;
21 = _102 == 9;
21 = 138 | 21;
if (21 != 0)
goto %bb 25+; [86.25%]
else
goto %bb 73+; [13.75%]
86%
14%
FREQ:443
%bb 32+:
_58 = MEM[(char *)&buffer + 78];
142 = _58 == 32;
23 = _58 == 58;
120 = 142 | 23;
26 = _58 == 9;
23 = 120 | 26;
if (23 != 0)
goto %bb 33+; [86.25%]
else
goto %bb 74+; [13.75%]
86%
14%
FREQ:292
%bb 40+:
_233 = MEM[(char *)&buffer + 1088];
231 = _233 == 32;
229 = _233 == 58;
218 = 231 | 229;
219 = _233 == 9;
217 = 218 | 219;
if (217 != 0)
goto %bb 41+; [86.25%]
else
goto %bb 75+; [13.75%]
86%
14%
FREQ:1018
%bb 16+:
_232 = MEM[(char *)&buffer + 98];
231 = _232 == 32;
229 = _232 == 58;
229 = 231 | 229;
84 = _232 == 9;
90 = 229 | 84;
if (90 != 0)
goto %bb 17+; [86.25%]
else
goto %bb 72+; [13.75%]
86%
14%
FREQ:2560
%bb 41+:
# cp_193 = PHI <cp_87(43), &MEM[void *&buffer + 1088](40)>
cp_87 = cp_193 + 1;
if (buffer_end_34 > cp_87)
goto %bb 42+; [92.50%]
else
goto %bb 43+; [7.50%]
93%
7%
FREQ:2267
%bb 42+:
_68 = *cp_87;
69 = _68 == 32;
70 = _68 == 58;
71 = 69 | 70;
239 = _68 == 9;
236 = 71 | 239;
if (_236 != 0)
goto %bb 43+; [86.25%]
else
goto %bb 75+; [13.75%]
86%
14%
FREQ:8903
%bb 17+:
# cp_216 = PHI <cp_85(18), &MEM[void *&buffer + 98](16)>
cp_85 = cp_216 + 1;
if (buffer_end_34 > cp_85)
goto %bb 18+; [92.50%]
else
goto %bb 19+; [7.50%]
93%
7%
FREQ:4233
%bb 18+:
_71 = *cp_85;
72 = _71 == 32;
73 = _71 == 58;
74 = 72 | 73;
75 = _71 == 9;
76 = 74 | 75;
if (_83 != 0)
goto %bb 17+; [86.25%]
else
goto %bb 72+; [13.75%]
86%
14%
loop 3
FREQ:5876
%bb 25+:
# cp_215 = PHI <cp_83(24), &MEM[void *&buffer + 1088](24>
cp_89 = cp_215 + 1;
if (buffer_end_34 > cp_89)
goto %bb 26+; [92.50%]
else
goto %bb 27+; [7.50%]
93%
7%
FREQ:2433
%bb 26+:
_76 = *cp_89;
77 = _76 == 32;
78 = _76 == 58;
79 = 77 | 78;
54 = _76 == 9;
83 = 79 | 54;
if ( 83 != 0)
goto %bb 25+; [86.25%]
else
goto %bb 73+; [13.75%]
86%
14%
loop 4
FREQ:3878
%bb 33+:
# cp_212 = PHI <cp_58(34), &MEM[void *&buffer + 78](32)>
cp_58 = cp_212 + 1;
if (buffer_end_34 > cp_58)
goto %bb 34+; [92.50%]
else
goto %bb 35+; [7.50%]
93%
7%
FREQ:2566
%bb 34+:
_59 = *cp_58;
60 = _59 == 32;
61 = _59 == 58;
62 = 60 | 61;
72 = _59 == 9;
242 = 62 | 72;
if ( 242 != 0)
goto %bb 33+; [86.25%]
else
goto %bb 74+; [13.75%]
86%
14%
loop 5
FREQ:2569
%bb 43+:
# cp_218 = PHI <&MEM[void *&buffer + 1088](40), cp_87(42)>
cp_93 = cp_218 + 1;
if (buffer_end_34 > cp_93)
goto %bb 44+; [92.50%]
else
goto %bb 45+; [7.50%]
93%
7%
FREQ:2267
%bb 44+:
_94 = *cp_93;
95 = _94 == 32;
96 = _94 == 58;
97 = 95 | 96;
239 = _94 == 9;
236 = 97 | 239;
if (_236 != 0)
goto %bb 45+; [86.25%]
else
goto %bb 75+; [13.75%]
86%
14%
loop 2
FREQ:8903
%bb 19+:
# cp_217 = PHI <cp_85(18), &MEM[void *&buffer + 98](16)>
cp_89 = cp_217 + 1;
if (buffer_end_34 > cp_89)
goto %bb 20+; [92.50%]
else
goto %bb 21+; [7.50%]
93%
7%
FREQ:4233
%bb 20+:
_76 = *cp_89;
77 = _76 == 32;
78 = _76 == 58;
79 = 77 | 78;
54 = _76 == 9;
83 = 79 | 54;
if ( 83 != 0)
goto %bb 17+; [86.25%]
else
goto %bb 72+; [13.75%]
86%
14%
FREQ:239
%bb 73+:
# cp_29 = PHI <&MEM[void *&buffer + 1088](24), cp_89(26)>
builtin___strcpn_sse42 (&model, cpu_MHz, cp_29, 1024);
goto %bb 27+; [100.00%]
100%
FREQ:238
%bb 74+:
# cp_208 = PHI <&MEM[void *&buffer + 78](32), cp_58(34)>
builtin___strcpn_sse42 (&cpu_MHz, cp_208, 1024);
goto %bb 35+; [100.00%]
100%
FREQ:238
%bb 75+:
# cp_208 = PHI <&MEM[void *&buffer + 1088](40), cp_87(42)>
builtin___strcpn_sse42 (&cpu_MHz, cp_208, 1024);
goto %bb 45+; [100.00%]
100%
FREQ:262
%bb 72+:
# cp_48 = PHI <&MEM[void *&buffer + 983](16), cp_89(18)>
builtin___strcpn_sse42 (&model_id, cp_48, 1024);
goto %bb 73+; [100.00%]
100%
FREQ:728
%bb 27+:
_55 = MEM[(const char *)&model_name];
if ( 55 != 0)
goto %bb 28+; [53.00%]
else
goto %bb 46+; [47.00%]
53%
47%
FREQ:478
%bb 35+:
_64 = MEM[(const char *)&cpu_MHz];
if ( 64 != 0)
goto %bb 36+; [53.00%]
else
goto %bb 46+; [47.00%]
53%
47%
FREQ:315
%bb 45+:
_74 = MEM[(const char *)&cache];
if ( 74 != 0)
goto %bb 44+; [53.00%]
else
goto %bb 46+; [47.00%]
53%
47%
FREQ:897
%bb 19+:
_88 = MEM[(const char *)&vendor_id];
if ( 88 != 0)
goto %bb 20+; [53.00%]
else
goto %bb 46+; [47.00%]
53%
47%
FREQ:238
%bb 28+:
_108 = strlen (&model_name);
109 = _108 + 18446744073709551615;
110 = &model_name + _109;
_111 = *_110;
111 = (int) _111;
if (111 != 10)
goto %bb 29+; [37.68%]
else
goto %bb 46+; [62.32%]
37%
63%
FREQ:158
%bb 36+:
_112 = strlen (&cpu_MHz);
113 = _112 + 18446744073709551615;
114 = &cpu_MHz + _113;
_115 = *_114;
115 = (int) _115;
if (115 != 77)
goto %bb 37+; [37.68%]
else
goto %bb 46+; [62.32%]
37%
63%
FREQ:194
%bb 44+:
_116 = strlen (&cache);
117 = _116 + 18446744073709551615;
118 = &cache + _117;
_119 = *_118;
119 = (int) _119;
if (119 != 75)
goto %bb 45+; [37.68%]
else
goto %bb 46+; [62.32%]
37%
63%
FREQ:362
%bb 20+:
_106 = strlen (&vendor_id);
105 = _104 + 18446744073709551615;
106 = &vendor_id + _105;
_107 = *_106;
107 = (int) _107;
if (107 != 116)
goto %bb 21+; [37.68%]
else
goto %bb 46+; [62.32%]
37%
63%
FREQ:90
%bb 29+:
* 110 = 0;
goto %bb 46+; [100.00%]
100%
FREQ:59
%bb 37+:
* 114 = 0;
goto %bb 46+; [100.00%]
100%
FREQ:20
%bb 45+:
* 118 = 0;
100%
FREQ:136
%bb 21+:
* 106 = 0;
goto %bb 46+; [100.00%]
100%
FREQ:3226
%bb 13+:
cpus_37 = cpus_36 + 1;
goto %bb 46+; [100.00%]
100%
FREQ:6411
%bb 46+:
# cpus_103 = PHI <cpus_36(39), cpus_37(13), cpus_36(21), cpus_36(27), cpus_36(29), cpus_36(35), cpus_36(37), cpus_36(38), cpus_36(43), cpus_36(45), cpus_36(20), cpus_36(28), cpus_36(36), cpus_36(44)>
100%