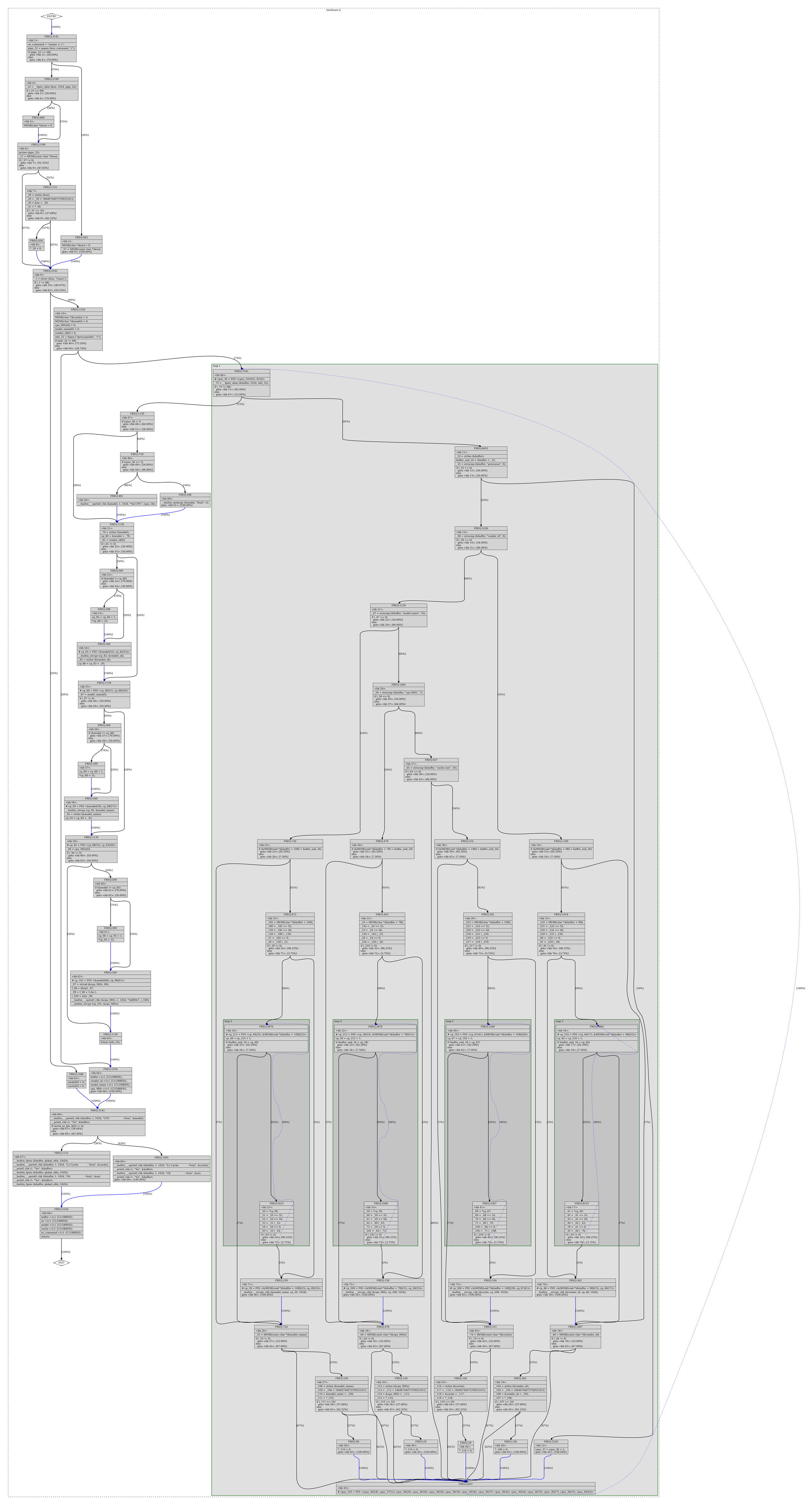
hardware ()
ENTRY
110%
FREQ:3142
%bb 2=:
m_command = "uname -s -r";
pipe_22 = popen (&m_command, "r");
if (pipe_22 == 0)
goto %bb 3=; [10.00%]
goto %bb 4=; [70.00%]
70%
FREQ:2109
%bb 4=:
_25 = __fpets_alias (&m, 1024, pipe_22);
if (_25 == 0)
goto %bb 5=; [30.00%]
else
goto %bb 6=; [70.00%]
30%
70%
FREQ:3660
%bb 5=:
MEMD[char *]&m[] = 0;
110%
FREQ:2199
%bb 6=:
picture.type_23;
_27 = MEM[(const char *)&m];
if (_27 == 0)
goto %bb 7=; [52.33%]
else
goto %bb 9=; [47.65%]
52%
FREQ:3153
%bb 7=:
_28 = strlen (&m);
_29 = __28 + 18446744073709551615;
_30 = &m + _29;
_31 = *_30;
if (_31 == 10)
goto %bb 8=; [37.68%]
else
goto %bb 9=; [62.32%]
47%
37%
FREQ:438
%bb 8=:
* _30 = 0;
62%
FREQ:943
%bb 9=:
MEMD[char *]&m[] = 0;
37 = MEM[(const char *)&m];
goto %bb 9=; [100.00%]
100%
100%
FREQ:3142
%bb 9=:
_1 = strstr (&m, "Linux");
if (_1 == 0)
goto %bb 10=; [49.47%]
else
goto %bb 65=; [50.53%]
49%
FREQ:3154
%bb 10=:
MEMD[char *]&cache[] = 0;
MEMD[char *]&model[] = 0;
cpu_MHz[0] = 0;
model_name[0] = 0;
vendor_id[0] = 0;
info_32 = fopen ("/proc/cpuinfo", "r");
if (info_32 != 0)
goto %bb 64=; [73.26%]
else
goto %bb 64=; [26.74%]
73%
26%
loop 1
FREQ:3591
%bb 66=:
# cpus_38 = PHI <cpus_103(45), 0(10)>
_75 = __fpets_alias (&buffer, 1024, info_32);
if (_75 != 0)
goto %bb 11=; [85.00%]
goto %bb 67=; [15.00%]
85%
15%
FREQ:1139
%bb 67=:
if (cpus_38 != 1)
goto %bb 68=; [64.00%]
else
goto %bb 11=; [36.00%]
64%
36%
FREQ:729
%bb 68=:
if (cpus_38 == 2)
goto %bb 69=; [34.00%]
else
goto %bb 50=; [66.00%]
66%
34%
FREQ:481
%bb 50=:
__builtin___sprintf_chk (&model, 1, 1024, "%d CPU", cpus_38);
110%
FREQ:248
%bb 69=:
__builtin_memcpy (&model, "Dual", 5);
goto %bb 51=; [100.00%]
100%
110%
FREQ:1139
%bb 51=:
_79 = strlen (&model);
cp_80 = &model + _79;
_81 = vendor_id[0];
if (_81 == 0)
goto %bb 52=; [50.00%]
else
goto %bb 55=; [50.00%]
50%
50%
FREQ:569
%bb 52=:
if (&model != cp_80)
goto %bb 53=; [70.00%]
else
goto %bb 54=; [30.00%]
70%
30%
FREQ:399
%bb 53=:
cp_82 = cp_80 + 1;
*cp_80 = 32;
100%
FREQ:569
%bb 54=:
cp_83 = PHI <&model(52), cp_82(53)>
__builtin_strcpy (cp_83, &vendor_id);
_85 = strlen (&vendor_id);
cp_86 = cp_83 + _85;
100%
FREQ:1139
%bb 55=:
# cp_88 = PHI <cp_80(51), cp_86(54)>
_87 = model_name[0];
if (_87 != 0)
goto %bb 56=; [50.00%]
else
goto %bb 59=; [50.00%]
50%
50%
FREQ:569
%bb 56=:
if (&model != cp_88)
goto %bb 57=; [70.00%]
else
goto %bb 58=; [30.00%]
70%
30%
FREQ:399
%bb 57=:
cp_89 = cp_88 + 1;
*cp_88 = 32;
100%
FREQ:569
%bb 58=:
cp_90 = PHI <&model(56), cp_89(57)>
__builtin_strcpy (cp_90, &model_name);
_92 = strlen (&model_name);
cp_93 = cp_90 + _92;
100%
FREQ:1139
%bb 59=:
# cp_95 = PHI <cp_88(55), cp_93(58)>
_94 = cpu_MHz[0];
if (_94 != 0)
goto %bb 60=; [50.00%]
else
goto %bb 63=; [50.00%]
50%
50%
FREQ:569
%bb 60=:
if (&model != cp_95)
goto %bb 61=; [70.00%]
else
goto %bb 62=; [30.00%]
70%
30%
FREQ:399
%bb 61=:
cp_96 = cp_95 + 1;
*cp_95 = 32;
100%
FREQ:668
%bb 62=:
# cp_121 = PHI <&model(60), cp_96(61)>
_97 = strtod (&cpu_MHz, 0B);
f_98 = (float) _97;
_99 = f_98 * 5.0e-1;
i_100 = (int) _99;
__builtin___sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", i_100);
__builtin_strcpy (cp_101, &cpu_MHz);
100%
FREQ:1139
%bb 63=:
fclose (info_32);
100%
FREQ:1554
%bb 64=:
buffer = (v1) {CLOBBER};
vendor_id = (v1) {CLOBBER};
model_name = (v1) {CLOBBER};
cpu_MHz = (v1) {CLOBBER};
goto %bb 66=; [100.00%]
100%
FREQ:1568
%bb 65=:
model[0] = 0;
cache[0] = 0;
100%
100%
FREQ:3142
%bb 66=:
__builtin___sprintf_chk (&buffer, 1, 1024, "CPU\t\t: %s\n", &model);
__printf_chk (1, "%s", &buffer);
if (vector_ba_fun_5[0] != 0)
goto %bb 67=; [61.38%]
else
goto %bb 69=; [61.38%]
61%
39%
FREQ:1151
%bb 67=:
__builtin_fputs (&buffer, global_alias_1025);
__builtin___sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
__printf_chk (1, "%s", &buffer);
__builtin_fputs (&buffer, global_alias_1025);
__builtin___sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
__printf_chk (1, "%s", &buffer);
__builtin_fputs (&buffer, global_alias_1025);
100%
FREQ:1991
%bb 69=:
__builtin___sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
__printf_chk (1, "%s", &buffer);
__builtin___sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
__printf_chk (1, "%s", &buffer);
goto %bb 68=; [100.00%]
100%
100%
FREQ:1142
%bb 68=:
buffer = (v1) {CLOBBER};
os = (v1) {CLOBBER};
model = (v1) {CLOBBER};
cache = (v1) {CLOBBER};
os_command = (v1) {CLOBBER};
return;
100%
EXIT
FREQ:6452
%bb 11=:
_33 = strlen (&buffer);
buffer_end_34 = &buffer + _33;
_35 = strncmp (&buffer, "processor", 9);
if (_35 == 0)
goto %bb 12=; [50.00%]
else
goto %bb 13=; [50.00%]
50%
50%
FREQ:2226
%bb 13=:
_38 = strncmp (&buffer, "vendor_id", 9);
if (_38 == 0)
goto %bb 14=; [34.00%]
else
goto %bb 21=; [66.00%]
34%
66%
FREQ:2129
%bb 21=:
_47 = strncmp (&buffer, "model name", 10);
if (_47 == 0)
goto %bb 22=; [34.00%]
else
goto %bb 29=; [66.00%]
34%
66%
FREQ:1405
%bb 29=:
_56 = strncmp (&buffer, "cpu MHz", 7);
if (_56 == 0)
goto %bb 30=; [34.00%]
else
goto %bb 37=; [66.00%]
34%
66%
FREQ:927
%bb 37=:
_65 = strncmp (&buffer, "cache size", 10);
if (_65 == 0)
goto %bb 38=; [34.00%]
else
goto %bb 45=; [66.00%]
34%
66%
FREQ:736
%bb 22=:
# cp_23 = PHI <cp_49(21), &MEM[(void *)&buffer + 1088]2>
cp_48 = cp_23 + 1;
if (buffer_end_34 < cp_48)
goto %bb 23=; [92.50%]
else
goto %bb 26=; [7.50%]
92%
8%
FREQ:679
%bb 30=:
if (&MEM[(void *)&buffer + 784] < buffer_end_34)
goto %bb 31=; [92.50%]
else
goto %bb 34=; [7.50%]
92%
8%
FREQ:1156
%bb 38=:
if (&MEM[(void *)&buffer + 1168] < buffer_end_34)
goto %bb 39=; [92.50%]
else
goto %bb 42=; [7.50%]
92%
8%
FREQ:1100
%bb 14=:
if (&MEM[(void *)&buffer + 980] < buffer_end_34)
goto %bb 15=; [92.50%]
else
goto %bb 18=; [7.50%]
92%
8%
FREQ:872
%bb 23=:
_102 = MEM[(char *)&buffer + 1035];
_160 = _102 == 32;
_159 = _102 == 58;
_158 = _160 | _159;
_21 = _102 == 9;
_20 = _158 | _21;
if (_20 != 0)
goto %bb 24=; [96.25%]
else
goto %bb 73=; [3.75%]
96%
4%
FREQ:443
%bb 31=:
_24 = MEM[(char *)&buffer + 781];
_142 = _24 == 32;
_23 = _24 == 58;
_120 = _142 | _23;
_26 = _24 == 9;
_224 = _120 | _26;
if (_224 != 0)
goto %bb 32=; [96.25%]
else
goto %bb 72=; [3.75%]
96%
4%
FREQ:292
%bb 39=:
_223 = MEM[(char *)&buffer + 1081];
_221 = _223 == 32;
_220 = _223 == 58;
_218 = _221 | _220;
_219 = _223 == 9;
_217 = _218 | _219;
if (_217 != 0)
goto %bb 40=; [96.25%]
else
goto %bb 73=; [3.75%]
96%
4%
FREQ:1018
%bb 15=:
_232 = MEM[(char *)&buffer + 981];
_231 = _232 == 32;
_230 = _232 == 58;
_229 = _231 | _230;
_84 = _232 == 9;
_91 = _229 | _84;
if (_91 != 0)
goto %bb 16=; [96.25%]
else
goto %bb 70=; [3.75%]
96%
4%
FREQ:5433
%bb 27=:
_50 = *cp_49;
_51 = _50 == 32;
_52 = _50 == 58;
_53 = _51 | _52;
_54 = _50 == 9;
_63 = _53 | _54;
if (_63 != 0)
goto %bb 28=; [96.25%]
else
goto %bb 73=; [3.75%]
96%
4%
FREQ:3586
%bb 33=:
_59 = *cp_58;
_60 = _59 == 32;
_61 = _59 == 58;
_62 = _60 | _61;
_57 = _59 == 9;
_242 = _62 | _57;
if (_242 != 0)
goto %bb 32=; [96.25%]
else
goto %bb 72=; [3.75%]
96%
4%
FREQ:2367
%bb 43=:
_68 = *cp_67;
_69 = _68 == 32;
_70 = _68 == 58;
_71 = _69 | _70;
_239 = _68 == 9;
_238 = _71 | _239;
if (_238 != 0)
goto %bb 40=; [96.25%]
else
goto %bb 73=; [3.75%]
96%
4%
FREQ:8233
%bb 17=:
_41 = *cp_40;
_42 = _41 == 32;
_43 = _41 == 58;
_44 = _42 | _43;
_94 = _41 == 9;
_45 = _44 | _94;
if (_45 != 0)
goto %bb 16=; [96.25%]
else
goto %bb 70=; [3.75%]
96%
4%
loop 3
FREQ:5676
%bb 24=:
# cp_233 = PHI <cp_49(25), &MEM[void *&buffer + 1088]2>
cp_48 = cp_233 + 1;
if (buffer_end_34 < cp_48)
goto %bb 25=; [92.50%]
else
goto %bb 26=; [7.50%]
92%
8%
FREQ:239
%bb 71=:
# cp_39 = PHI <&MEM[void *&buffer + 1088]2(3), cp_49(25)>
buffer = strcpy (&b, &model_name, cp_39, 1024);
goto %bb 26=; [100.00%]
100%
FREQ:724
%bb 26=:
_55 = MEM[(const char *)&model_name];
if (_55 != 0)
goto %bb 27=; [33.00%]
else
goto %bb 45=; [67.00%]
33%
67%
FREQ:239
%bb 27=:
_108 = strlen (&model_name);
_109 = _108 + 18446744073709551615;
_110 = &model_name + _109;
_111 = *_110;
if (_111 == 10)
goto %bb 28=; [37.68%]
else
goto %bb 45=; [62.32%]
37%
63%
FREQ:90
%bb 28=:
* _110 = 0;
goto %bb 45=; [100.00%]
100%
loop 4
FREQ:5678
%bb 32=:
# cp_232 = PHI <cp_58(33), &MEM[void *&buffer + 783]2>
cp_58 = cp_232 + 1;
if (buffer_end_34 < cp_58)
goto %bb 33=; [92.50%]
else
goto %bb 34=; [7.50%]
92%
8%
FREQ:478
%bb 34=:
_64 = MEM[(const char *)&cpu_MHz];
if (_64 != 0)
goto %bb 35=; [33.00%]
else
goto %bb 45=; [67.00%]
33%
67%
FREQ:158
%bb 72=:
# cp_209 = PHI <&MEM[void *&buffer + 783]2(1), cp_58(33)>
buffer = strcpy (&cpu_MHz, cp_209, 1024);
goto %bb 34=; [100.00%]
100%
FREQ:1158
%bb 35=:
_112 = strlen (&cpu_MHz);
_113 = _112 + 18446744073709551615;
_114 = &cpu_MHz + _113;
_115 = *_114;
if (_115 == 10)
goto %bb 36=; [37.68%]
else
goto %bb 45=; [62.32%]
37%
63%
FREQ:59
%bb 36=:
* _114 = 0;
goto %bb 45=; [100.00%]
100%
loop 5
FREQ:5560
%bb 40=:
# cp_193 = PHI <cp_67(41), &MEM[void *&buffer + 1088]2>
cp_67 = cp_193 + 1;
if (buffer_end_34 < cp_67)
goto %bb 41=; [92.50%]
else
goto %bb 42=; [7.50%]
92%
8%
FREQ:315
%bb 42=:
_74 = MEM[(const char *)&cache];
if (_74 != 0)
goto %bb 43=; [33.00%]
else
goto %bb 45=; [67.00%]
33%
67%
FREQ:194
%bb 43=:
_116 = strlen (&cache);
_117 = _116 + 18446744073709551615;
_118 = &cache + _117;
_119 = *_118;
if (_119 == 10)
goto %bb 44=; [37.68%]
else
goto %bb 45=; [62.32%]
37%
63%
FREQ:136
%bb 44=:
* _118 = 0;
100%
loop 2
FREQ:5603
%bb 16=:
# cp_216 = PHI <cp_40(17), &MEM[void *&buffer + 981]1>
cp_40 = cp_216 + 1;
if (buffer_end_34 < cp_40)
goto %bb 17=; [92.50%]
else
goto %bb 18=; [7.50%]
92%
8%
FREQ:1097
%bb 18=:
_46 = MEM[(const char *)&vendor_id];
if (_46 != 0)
goto %bb 19=; [33.00%]
else
goto %bb 45=; [67.00%]
33%
67%
FREQ:362
%bb 19=:
_104 = strlen (&vendor_id);
_105 = _104 + 18446744073709551615;
_106 = &vendor_id + _105;
_107 = *_106;
if (_107 == 10)
goto %bb 20=; [37.68%]
else
goto %bb 45=; [62.32%]
37%
63%
FREQ:5226
%bb 20=:
* _106 = 0;
goto %bb 45=; [100.00%]
100%
FREQ:6483
%bb 45=:
cpus_103 = PHI <cpus_38(18), cpus_37(33), cpus_36(20), cpus_36(28), cpus_36(28), cpus_36(28), cpus_36(36), cpus_36(37), cpus_36(42), cpus_36(44), cpus_36(59), cpus_36(27), cpus_36(35), cpus_36(43)>
100%