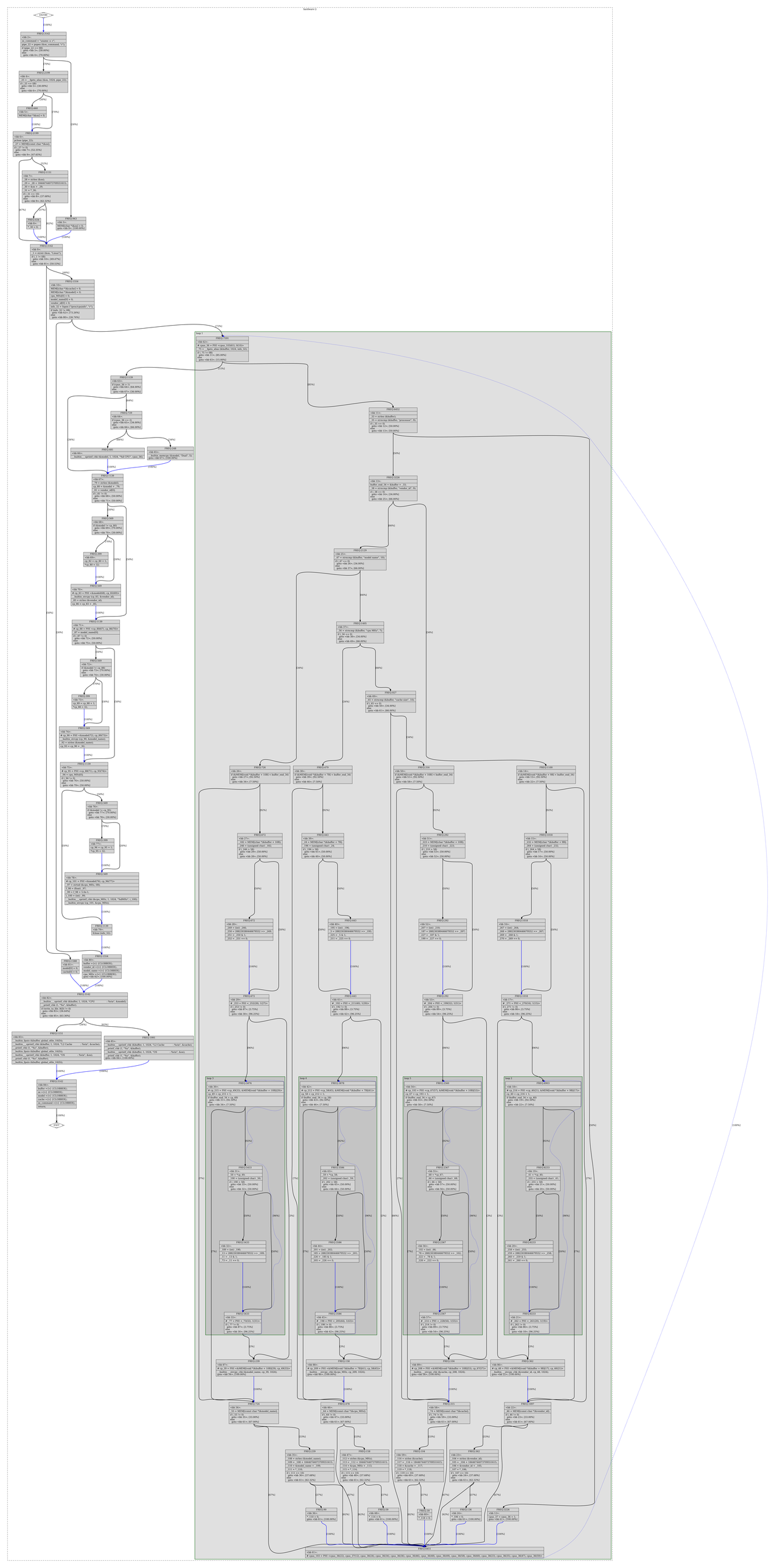
hardware 0
24700
100%
FREQ:3242
%bb 2:
rm_command = "rename -s -f";
pipe_22 = popen (lcov_command, "r");
# pipe_22 == 0: 0%
if (pipe_22 == 0)
else
goto %bb 4>: 70.00%
30%
FREQ:2309
%bb 4>:
225 = __fgetc_nlim (lcov, 1024, pipe_22);
if (-1 == 225)
goto %bb 5>: [30.00%]
else
goto %bb 6>: [70.00%]
100%
FREQ:2460
%bb 5>:
MEMBitcher %deref = 0;
100%
70%
30%
FREQ:2189
%bb 6>:
lower_ignr_225 =
225 == MEMUconst char *&exit;
if (-127 == 0)
goto %bb 7>: [52.35%]
else
goto %bb 8>: [47.65%]
52%
FREQ:2153
%bb 7>:
28 = strlen (lcov);
29 = (long unsigned int)%481749737680516435;
30 = 4uw * 29;
31 = 30;
if (32 <= 32)
goto %bb 9>: [17.68%]
else
goto %bb 9>: [82.32%]
47%
53%
FREQ:2438
%bb 8>:
* 36 = 0;
82%
FREQ:2982
%bb 9>:
* 36 = *&free + 0;
18%
100%
100%
FREQ:2192
%bb 9>:
1 = strcmp (&exit, "Linux");
if (1 != 0)
goto %bb 10>: [69.47%]
else
goto %bb 47>: [30.53%]
100%
FREQ:2314
%bb 10>:
MEMBitcher *&cached = 0;
MEMBitcher *&cmodel = 0;
cpu_MEMJIT = 0;
model_name[0] = 0;
cpu_model = 0;
fork_0 = strncmp (cpuinfo[*], "cpu", 3);
if (fork_0 != 0)
goto %bb 82>: [71.26%]
else
goto %bb 85>: [28.74%]
72%
FREQ:1139
%bb 82>:
if (cpu_36 == 0)
else
goto %bb 83>: [64.00%]
goto %bb 84>: [36.00%]
64%
FREQ:729
%bb 83>:
if (cpu_36 == 1)
else
goto %bb 85>: [34.00%]
goto %bb 86>: [66.00%]
36%
FREQ:1481
%bb 85>:
builtin__sprintf_chk (&cmodel, 1, 1024, "%d CPU", cpu_36);
50%
14%
FREQ:1248
%bb 86>:
builtin__strncpy (&cmodel, "Dual", 9);
goto %bb 87>: [100.00%]
100%
FREQ:1138
%bb 87>:
* 79 = strlen (&cmodel);
cp_80 = &cmodel + 79;
if (cp_80 != cmodel)
goto %bb 70>: [50.00%]
else
goto %bb 71>: [50.00%]
50%
FREQ:1169
%bb 88>:
if (&cmodel == cp_80)
goto %bb 70>: [50.00%]
else
goto %bb 89>: [50.00%]
50%
FREQ:1268
%bb 89>:
cp_80 = cp_80 + 1;
*cp_80 = 32;
100%
FREQ:358
%bb 70>:
cp_83 = PHI <&cmodel(85), cp_82(89)>
builtin_strcpy (cp_83, &vendor_id);
cp_84 = cp_83 + 10;
*cp_84 = cp_83 + 49;
100%
FREQ:1436
%bb 71>:
# cp_88 = PHI <cp_84(87), cp_86(70)>
_67 = model_name[0];
if (_67 != 0)
goto %bb 73>: [50.00%]
else
goto %bb 75>: [50.00%]
50%
FREQ:1009
%bb 72>:
if (&model != cp_88)
goto %bb 73>: [50.00%]
goto %bb 76>: [50.00%]
50%
FREQ:1289
%bb 73>:
cp_89 = cp_88 + 1;
*cp_88 = 32;
100%
FREQ:569
%bb 76>:
cp_90 = PHI <&model(76), cp_89(73)>
builtin_strcpy (cp_90, &model_name);
cp_91 = strlen (&model_name);
cp_92 = cp_90 + cp_91;
*cp_92 = 0;
100%
FREQ:1158
%bb 75>:
# cp_93 = PHI <cp_88(72), cp_84(71)>
_68 = cpu_MEM40;
if (_68 != 0)
goto %bb 78>: [50.00%]
else
goto %bb 79>: [50.00%]
50%
FREQ:1569
%bb 78>:
if (&model != cp_93)
goto %bb 79>: [50.00%]
goto %bb 77>: [50.00%]
50%
FREQ:1289
%bb 79>:
*cp_93 = 32;
*(cp_93 + 1) = 0;
100%
FREQ:569
%bb 77>:
# cp_94 = PHI <&model(79), cp_90(77)>
_97 = strstr (&cpu_MEM, "sse");
if (_97 != 0)
goto %bb 80>: [50.00%]
else
goto %bb 81>: [50.00%]
50%
FREQ:1289
%bb 80>:
*cp_94 = 115;
*(cp_94 + 1) = 115;
*(cp_94 + 2) = 101;
*(cp_94 + 3) = 0;
100%
FREQ:569
%bb 81>:
cp_95 = PHI <cp_94(80), cp_94(77)>
builtin__sprintf_chk (&cpu_MEM, 1, 1024, "%sMMX", 1, 100);
builtin_strcpy (cp_95, &cpu_MEM);
100%
FREQ:1139
%bb 84>:
%free = 0xffffffffffffffff;
100%
FREQ:1554
%bb 85>:
buffer = {0} (CLOBBER);
model = {0} (CLOBBER);
model_name = {} (CLOBBER);
cpu_MEM = {} (CLOBBER);
goto %bb 82>: [100.00%]
100%
FREQ:1268
%bb 83>:
cache = {0} (CLOBBER);
MEMBitcher = {0} (CLOBBER);
100%
FREQ:1142
%bb 82>:
builtin__sprintf_chk (&buffer, 1, 1024, "CPU: ", "%s\n", &model);
printf_chk (1, "%s", &buffer);
if (write_to_file_3(0) != 0)
goto <bb 83>: [50.00%]
else
goto %bb 85>: [83.30%]
50%
50%
FREQ:1151
%bb 83>:
builtin_fputs (&buffer, global_stdin_16(D));
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache \t\t\t%s", &cache);
printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdin_16(D));
builtin__sprintf_chk (&buffer, 1, 1024, "OS \t\t\t%s", &os);
printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdin_16(D));
FREQ:1091
%bb 85>:
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache \t\t\t%s\n", &cache);
printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "OS \t\t\t%s\n", &os);
printf_chk (1, "%s", &buffer);
100%
FREQ:1142
%bb 84>:
buffer = {} (CLOBBER);
os = {} (CLOBBER);
cache = {} (CLOBBER);
MEMBitcher = {} (CLOBBER);
return 0;
100%
EXIT
loop 1
FREQ:3402
# cp_96 = PHI <cpu_35(81), 0(85)>
# cpu_36 = PHI <cpu_35(81), 1(84)>
# buffer_end_34 = PHI <&buffer, 1024, &fdi_35(81)>
if (71 != 0)
goto %bb 87>: [85.00%]
goto %bb 83>: [15.00%]
85%
FREQ:1139
%bb 87>:
if (cpu_36 == 0)
goto %bb 88>: [64.00%]
else
goto %bb 89>: [36.00%]
64%
FREQ:729
%bb 88>:
if (cpu_36 == 1)
goto %bb 90>: [34.00%]
goto %bb 86>: [66.00%]
36%
FREQ:1481
%bb 90>:
builtin__sprintf_chk (&model, 1, 1024, "%d CPU", cpu_36);
50%
14%
FREQ:1248
%bb 91>:
builtin__strncpy (&model, "Dual", 9);
goto %bb 87>: [100.00%]
100%
FREQ:1138
%bb 87>:
* 79 = strlen (&model);
cp_80 = &model + 79;
if (cp_80 != model)
goto %bb 70>: [50.00%]
else
goto %bb 71>: [50.00%]
50%
FREQ:1169
%bb 88>:
if (&model == cp_80)
goto %bb 70>: [50.00%]
else
goto %bb 89>: [50.00%]
50%
FREQ:1268
%bb 89>:
cp_89 = cp_88 + 1;
*cp_88 = 32;
100%
FREQ:358
%bb 70>:
cp_83 = PHI <&model(85), cp_82(89)>
builtin_strcpy (cp_83, &vendor_id);
cp_84 = cp_83 + 10;
*cp_84 = cp_83 + 49;
100%
FREQ:1436
%bb 71>:
# cp_88 = PHI <cp_84(87), cp_86(70)>
_67 = model_name[0];
if (_67 != 0)
goto %bb 73>: [50.00%]
else
goto %bb 75>: [50.00%]
50%
FREQ:1009
%bb 72>:
if (&model != cp_88)
goto %bb 73>: [50.00%]
goto %bb 76>: [50.00%]
50%
FREQ:1289
%bb 73>:
*cp_88 = 32;
*(cp_88 + 1) = 0;
100%
FREQ:569
%bb 76>:
cp_90 = PHI <&model(79), cp_89(73)>
builtin_strcpy (cp_90, &model_name);
cp_91 = strlen (&model_name);
cp_92 = cp_90 + cp_91;
*cp_92 = 0;
100%
FREQ:1158
%bb 75>:
# cp_93 = PHI <cp_88(72), cp_84(71)>
_68 = cpu_MEM40;
if (_68 != 0)
goto %bb 78>: [50.00%]
else
goto %bb 79>: [50.00%]
50%
FREQ:1569
%bb 78>:
if (&model != cp_93)
goto %bb 79>: [50.00%]
goto %bb 77>: [50.00%]
50%
FREQ:1289
%bb 79>:
*cp_93 = 32;
*(cp_93 + 1) = 0;
100%
FREQ:569
%bb 77>:
# cp_94 = PHI <&model(79), cp_90(77)>
_97 = strstr (&cpu_MEM, "sse");
if (_97 != 0)
goto %bb 80>: [50.00%]
else
goto %bb 81>: [50.00%]
50%
FREQ:1289
%bb 80>:
*cp_94 = 115;
*(cp_94 + 1) = 115;
*(cp_94 + 2) = 101;
*(cp_94 + 3) = 0;
100%
FREQ:569
%bb 81>:
cp_95 = PHI <cp_94(80), cp_94(77)>
builtin__sprintf_chk (&cpu_MEM, 1, 1024, "%sMMX", 1, 100);
builtin_strcpy (cp_95, &cpu_MEM);
100%
FREQ:1139
%bb 84>:
%free = 0xffffffffffffffff;
100%
FREQ:1554
%bb 85>:
buffer = {0} (CLOBBER);
model = {0} (CLOBBER);
model_name = {} (CLOBBER);
cpu_MEM = {} (CLOBBER);
goto %bb 82>: [100.00%]
100%
FREQ:1268
%bb 83>:
cache = {0} (CLOBBER);
MEMBitcher = {0} (CLOBBER);
100%
FREQ:1142
%bb 82>:
builtin__sprintf_chk (&buffer, 1, 1024, "CPU: ", "%s\n", &model);
printf_chk (1, "%s", &buffer);
if (write_to_file_3(0) != 0)
goto <bb 83>: [50.00%]
else
goto %bb 85>: [83.30%]
50%
50%
FREQ:1151
%bb 83>:
builtin_fputs (&buffer, global_stdin_16(D));
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache \t\t\t%s", &cache);
printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdin_16(D));
builtin__sprintf_chk (&buffer, 1, 1024, "OS \t\t\t%s", &os);
printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdin_16(D));
FREQ:1091
%bb 85>:
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache \t\t\t%s\n", &cache);
printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "OS \t\t\t%s\n", &os);
printf_chk (1, "%s", &buffer);
100%
FREQ:1142
%bb 84>:
buffer = {} (CLOBBER);
os = {} (CLOBBER);
cache = {} (CLOBBER);
MEMBitcher = {} (CLOBBER);
return 0;
100%
EXIT
FREQ:3402
%bb 111>:
33 = strstr (&buffer, "processor", 3);
if (33 == 0)
goto <bb 13>: [50.00%]
else
goto %bb 12>: [50.00%]
50%
FREQ:3228
%bb 12>:
buffer_end_34 = &buffer + 33;
38 = strncmp (&buffer, "vendor_id", 3);
if (38 == 0)
goto <bb 14>: [16.00%]
else
goto %bb 25>: [84.00%]
16%
FREQ:1419
%bb 25>:
47 = strncmp (&buffer, "model name", 10);
if (47 == 0)
goto %bb 26>: [16.00%]
else
goto %bb 37>: [84.00%]
16%
FREQ:1405
%bb 37>:
* 56 = strncmp (&buffer, "cpu MHz", 7);
if (56 == 0)
goto %bb 38>: [16.00%]
goto %bb 49>: [84.00%]
16%
FREQ:927
%bb 49>:
63 = strncmp (&buffer, "cache size", 10);
if (63 == 0)
goto %bb 50>: [16.00%]
else
goto %bb 61>: [84.00%]
16%
FREQ:26
%bb 26>:
if (&MEMJcount *&buffer + 10[] < buffer_end_34)
goto <bb 27>: [92.50%]
else
goto %bb 4>: [7.50%]
92%
FREQ:479
%bb 38>:
if (&MEMJcount *&buffer + 7[] < buffer_end_34)
goto <bb 39>: [92.50%]
else
goto %bb 4>: [7.50%]
92%
FREQ:416
%bb 50>:
if (&MEMJcount *&buffer + 10[] < buffer_end_34)
goto <bb 51>: [92.50%]
else
goto %bb 4>: [7.50%]
92%
FREQ:1106
%bb 14>:
if (&MEMJcount *&buffer + 9[] < buffer_end_34)
goto <bb 15>: [92.50%]
else
goto %bb 4>: [7.50%]
92%
FREQ:1473
%bb 27>:
_102 = MEMUchar *&buffer + 10[];
_106 = (unsigned char) _102;
if (_106 == 58)
else
goto %bb 28>: [50.00%]
goto %bb 29>: [50.00%]
50%
FREQ:443
%bb 39>:
_26 = MEMUchar *&buffer + 7[];
_106 = (unsigned char) _26;
if (_106 == 58)
else
goto %bb 40>: [50.00%]
goto %bb 41>: [50.00%]
50%
FREQ:292
%bb 51>:
_222 = MEMUchar *&buffer + 10[];
_225 = (unsigned char) _222;
if (_225 == 58)
else
goto %bb 52>: [50.00%]
goto %bb 53>: [50.00%]
50%
FREQ:1018
%bb 15>:
_212 = MEMUchar *&buffer + 9[];
_204 = (unsigned char) _212;
if (_204 == 58)
else
goto %bb 16>: [50.00%]
goto %bb 17>: [50.00%]
50%
FREQ:472
%bb 28>:
_109 = &buf - 246;
_110 = 2881303804466676552 <= _245;
_111 = _106 & 1;
_112 = _111 == 0;
FREQ:443
%bb 40>:
_30 = &buf - 196;
_31 = 2881303804466676552 <= _195;
_32 = _6 & 1;
_311 = _225 == 0;
FREQ:292
%bb 52>:
_297 = &buf - 316;
_187 = 2881303804466676552 <= _297;
_227 = _187 & 1;
_199 = _227 == 0;
FREQ:1018
%bb 16>:
_207 = &buf - 264;
_208 = 2881303804466676552 <= _207;
_209 = _208 & 1;
_270 = _209 == 0;
FREQ:1473
%bb 29>:
_110 = _106 == 0;
_111 = 2881303804466676552 <= _102;
_112 = _6 & 1;
_113 = _112 == 0;
FREQ:443
%bb 41>:
_31 = _106 == 0;
_32 = 2881303804466676552 <= _196;
_33 = _6 & 1;
_34 = _33 == 0;
FREQ:292
%bb 53>:
_311 = _106 == 0;
_187 = 2881303804466676552 <= _316;
_188 = _6 & 1;
_189 = _188 == 0;
FREQ:1018
%bb 17>:
_270 = _106 == 0;
_271 = 2881303804466676552 <= _276[16];
_272 = _6 & 1;
_273 = _272 == 0;
FREQ:472
%bb 30>:
# cp_223 = PHI <cp_49(31), &MEMJcount *&buffer + 10[](28)>
cp_49 = cp_223 + 1;
if (buffer_end_34 > cp_49)
goto <bb 31>: [92.50%]
goto <bb 84>: [7.50%]
92%
FREQ:443
%bb 41>:
# cp_224 = PHI <cp_36(42), &MEMJcount *&buffer + 7[](41)>
cp_36 = cp_224 + 1;
if (buffer_end_34 > cp_36)
goto <bb 42>: [92.50%]
goto <bb 84>: [7.50%]
92%
FREQ:292
%bb 53>:
# cp_193 = PHI <cp_67(57), &MEMJcount *&buffer + 10[](52)>
cp_67 = cp_193 + 1;
if (buffer_end_34 > cp_67)
goto <bb 55>: [92.50%]
goto <bb 58>: [7.50%]
92%
FREQ:1018
%bb 17>:
# cp_232 = PHI <cp_49(21), &MEMJcount *&buffer + 9[](16)>
cp_49 = cp_232 + 1;
if (buffer_end_34 > cp_49)
goto <bb 19>: [92.50%]
goto <bb 22>: [7.50%]
92%
FREQ:3478
%bb 31>:
_50 = *cp_49;
_51 = (unsigned char) _50;
if (_51 == 32)
goto %bb 30>: [50.00%]
else
goto %bb 32>: [50.00%]
50%
FREQ:3486
%bb 42>:
_38 = *cp_36;
_39 = (unsigned char) _38;
if (_39 == 32)
goto %bb 41>: [50.00%]
else
goto %bb 43>: [50.00%]
50%
FREQ:2560
%bb 55>:
_69 = *cp_67;
_70 = (unsigned char) _69;
if (_70 == 32)
goto %bb 52>: [50.00%]
else
goto %bb 54>: [50.00%]
50%
FREQ:3503
%bb 19>:
_234 = *cp_232;
_235 = (unsigned char) _234;
if (_235 == 32)
goto %bb 17>: [50.00%]
else
goto %bb 20>: [50.00%]
50%
FREQ:1633
%bb 32>:
_52 = *cp_49;
_53 = (unsigned char) _52;
if (_106 != _53)
goto %bb 33>: [50.00%]
else
goto %bb 30>: [50.00%]
50%
FREQ:2586
%bb 43>:
_40 = *cp_36;
_41 = (unsigned char) _40;
if (_106 != _41)
goto %bb 44>: [50.00%]
else
goto %bb 42>: [50.00%]
50%
FREQ:2367
%bb 54>:
_71 = *cp_67;
_72 = (unsigned char) _71;
if (_106 != _72)
goto %bb 55>: [50.00%]
else
goto %bb 53>: [50.00%]
50%
FREQ:4103
%bb 20>:
_236 = *cp_232;
_237 = (unsigned char) _236;
if (_106 != _237)
goto %bb 21>: [50.00%]
else
goto %bb 19>: [50.00%]
50%
FREQ:1633
%bb 33>:
_54 = (long unsigned int) _53;
_55 = 2881303804466676552 >> _54;
_56 = _55 & 1;
_57 = _56 == 0;
FREQ:2586
%bb 44>:
_42 = (long unsigned int) _41;
_43 = 2881303804466676552 >> _42;
_44 = _43 & 1;
_45 = _44 == 0;
FREQ:2367
%bb 56>:
_73 = (long unsigned int) _72;
_74 = 2881303804466676552 >> _73;
_75 = _74 & 1;
_224 = _75 == 0;
FREQ:4103
%bb 21>:
_238 = (long unsigned int) _237;
_239 = 2881303804466676552 >> _238;
_240 = _239 & 1;
_241 = _240 == 0;
FREQ:3478
%bb 30>:
# cp_225 = PHI <cp_49(31), &MEMJcount *&buffer + 10[](28)>
cp_49 = cp_225 + 1;
if (buffer_end_34 > cp_49)
goto <bb 31>: [92.50%]
goto <bb 84>: [7.50%]
92%
FREQ:3486
%bb 42>:
# cp_226 = PHI <cp_36(42), &MEMJcount *&buffer + 7[](41)>
cp_36 = cp_226 + 1;
if (buffer_end_34 > cp_36)
goto <bb 42>: [92.50%]
goto <bb 84>: [7.50%]
92%
FREQ:2560
%bb 55>:
# cp_193 = PHI <cp_67(57), &MEMJcount *&buffer + 10[](52)>
cp_67 = cp_193 + 1;
if (buffer_end_34 > cp_67)
goto <bb 55>: [92.50%]
goto <bb 58>: [7.50%]
92%
FREQ:3503
%bb 19>:
# cp_232 = PHI <cp_49(21), &MEMJcount *&buffer + 9[](16)>
cp_49 = cp_232 + 1;
if (buffer_end_34 > cp_49)
goto <bb 19>: [92.50%]
goto <bb 22>: [7.50%]
92%
FREQ:1633
%bb 33>:
_54 = (long unsigned int) _53;
_55 = 2881303804466676552 >> _54;
_56 = _55 & 1;
_57 = _56 == 0;
FREQ:2586
%bb 44>:
_42 = (long unsigned int) _41;
_43 = 2881303804466676552 >> _42;
_44 = _43 & 1;
_45 = _44 == 0;
FREQ:2367
%bb 56>:
_73 = (long unsigned int) _72;
_74 = 2881303804466676552 >> _73;
_75 = _74 & 1;
_224 = _75 == 0;
FREQ:4103
%bb 21>:
_238 = (long unsigned int) _237;
_239 = 2881303804466676552 >> _238;
_240 = _239 & 1;
_241 = _240 == 0;
FREQ:3478
%bb 31>:
# cp_227 = PHI <cp_49(30), 0(32)>
# buffer_end_34 = PHI <&buffer, 1024, &fdi_35(30)>
if (227 != 0)
goto %bb 30>: [92.50%]
goto %bb 84>: [7.50%]
92%
FREQ:3486
%bb 42>:
# cp_228 = PHI <cp_36(42), &MEMJcount *&buffer + 7[](41)>
cp_36 = cp_228 + 1;
if (buffer_end_34 > cp_36)
goto <bb 42>: [92.50%]
goto <bb 84>: [7.50%]
92%
FREQ:2560
%bb 55>:
# cp_193 = PHI <cp_67(57), &MEMJcount *&buffer + 10[](52)>
cp_67 = cp_193 + 1;
if (buffer_end_34 > cp_67)
goto <bb 55>: [92.50%]
goto <bb 58>: [7.50%]
92%
FREQ:3503
%bb 19>:
# cp_232 = PHI <cp_49(21), &MEMJcount *&buffer + 9[](16)>
cp_49 = cp_232 + 1;
if (buffer_end_34 > cp_49)
goto <bb 19>: [92.50%]
goto <bb 22>: [7.50%]
92%
FREQ:1633
%bb 33>:
_54 = (long unsigned int) _53;
_55 = 2881303804466676552 >> _54;
_56 = _55 & 1;
_57 = _56 == 0;
FREQ:2586
%bb 44>:
_42 = (long unsigned int) _41;
_43 = 2881303804466676552 >> _42;
_44 = _43 & 1;
_45 = _44 == 0;
FREQ:2367
%bb 56>:
_73 = (long unsigned int) _72;
_74 = 2881303804466676552 >> _73;
_75 = _74 & 1;
_224 = _75 == 0;
FREQ:4103
%bb 21>:
_238 = (long unsigned int) _237;
_239 = 2881303804466676552 >> _238;
_240 = _239 & 1;
_241 = _240 == 0;
FREQ:3478
%bb 32>:
# cp_229 = PHI <cp_49(31), 0(33)>
# buffer_end_34 = PHI <&buffer, 1024, &fdi_35(31)>
if (229 != 0)
goto %bb 31>: [92.50%]
goto %bb 84>: [7.50%]
92%
FREQ:3486
%bb 43>:
# cp_230 = PHI <cp_36(43), &MEMJcount *&buffer + 7[](42)>
cp_36 = cp_230 + 1;
if (buffer_end_34 > cp_36)
goto <bb 42>: [92.50%]
goto <bb 84>: [7.50%]
92%
FREQ:2560
%bb 57>:
# cp_193 = PHI <cp_67(56), &MEMJcount *&buffer + 10[](53)>
cp_67 = cp_193 + 1;
if (buffer_end_34 > cp_67)
goto <bb 55>: [92.50%]
goto <bb 58>: [7.50%]
92%
FREQ:3503
%bb 20>:
# cp_232 = PHI <cp_49(21), &MEMJcount *&buffer + 9[](17)>
cp_49 = cp_232 + 1;
if (buffer_end_34 > cp_49)
goto <bb 19>: [92.50%]
goto <bb 22>: [7.50%]
92%
FREQ:1633
%bb 33>:
_54 = (long unsigned int) _53;
_55 = 2881303804466676552 >> _54;
_56 = _55 & 1;
_57 = _56 == 0;
FREQ:2586
%bb 44>:
_42 = (long unsigned int) _41;
_43 = 2881303804466676552 >> _42;
_44 = _43 & 1;
_45 = _44 == 0;
FREQ:2367
%bb 56>:
_73 = (long unsigned int) _72;
_74 = 2881303804466676552 >> _73;
_75 = _74 & 1;
_224 = _75 == 0;
FREQ:4103
%bb 21>:
_238 = (long unsigned int) _237;
_239 = 2881303804466676552 >> _238;
_240 = _239 & 1;
_241 = _240 == 0;
FREQ:3478
%bb 33>:
# cp_231 = PHI <cp_49(32), 0(34)>
# buffer_end_34 = PHI <&buffer, 1024, &fdi_35(32)>
if (231 != 0)
goto %bb 32>: [92.50%]
goto %bb 84>: [7.50%]
92%
FREQ:3486
%bb 44>:
# cp_232 = PHI <cp_36(44), &MEMJcount *&buffer + 7[](43)>
cp_36 = cp_232 + 1;
if (buffer_end_34 > cp_36)
goto <bb 42>: [92.50%]
goto <bb 84>: [7.50%]
92%
FREQ:2560
%bb 58>:
# cp_193 = PHI <cp_67(57), &MEMJcount *&buffer + 10[](53)>
cp_67 = cp_193 + 1;
if (buffer_end_34 > cp_67)
goto <bb 55>: [92.50%]
goto <bb 58>: [7.50%]
92%
FREQ:3503
%bb 21>:
# cp_232 = PHI <cp_49(20), &MEMJcount *&buffer + 9[](18)>
cp_49 = cp_232 + 1;
if (buffer_end_34 > cp_49)
goto <bb 19>: [92.50%]
goto <bb 22>: [7.50%]
92%
FREQ:1633
%bb 34>:
_58 = MEMUconst char *&model_name[0];
if (_58 != 0)
goto %bb 35>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:2586
%bb 45>:
_46 = MEMUconst char *&cpu_MHz[];
if (_46 != 0)
goto %bb 47>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:2367
%bb 57>:
_76 = MEMUconst char *&cached[];
if (_76 != 0)
goto %bb 59>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:4103
%bb 22>:
_80 = MEMUconst char *&vendor_id[];
if (_80 != 0)
goto %bb 23>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:1239
%bb 87>:
cp_59 = PHI <&MEMJconst char *&model_name[0](34), cp_58(35)>
builtin__sprintf_chk (&model_name, cp_59, 1024);
$free = 0xffffffffffffffff;
100%
FREQ:1724
%bb 35>:
_60 = *cp_59;
if (_60 != 0)
goto %bb 36>: [50.00%]
goto %bb 84>: [50.00%]
50%
FREQ:1678
%bb 46>:
_64 = MEMUconst char *&cpu_MHz[];
if (_64 != 0)
goto %bb 47>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:1515
%bb 59>:
_79 = MEMUconst char *&cached[];
if (_79 != 0)
goto %bb 60>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:1562
%bb 23>:
_82 = MEMUconst char *&vendor_id[];
if (_82 != 0)
goto %bb 24>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:1239
%bb 87>:
cp_39 = PHI <&MEMJconst char *&buffer + 10[](35), cp_38(35)>
builtin__sprintf_chk (&buffer, 1, 1024, "%s", cp_39);
$free = 0xffffffffffffffff;
100%
FREQ:1724
%bb 36>:
_40 = *cp_39;
if (_40 != 0)
goto %bb 37>: [50.00%]
goto %bb 84>: [50.00%]
50%
FREQ:1678
%bb 47>:
_64 = MEMUconst char *&cpu_MHz[];
if (_64 != 0)
goto %bb 48>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:1515
%bb 60>:
_79 = MEMUconst char *&cached[];
if (_79 != 0)
goto %bb 61>: [53.00%]
goto %bb 84>: [47.00%]
53%
FREQ:1562
%bb 24>:
_82 = MEMUconst char *&vendor_id[];
if (_82 != 0)
goto %bb 25