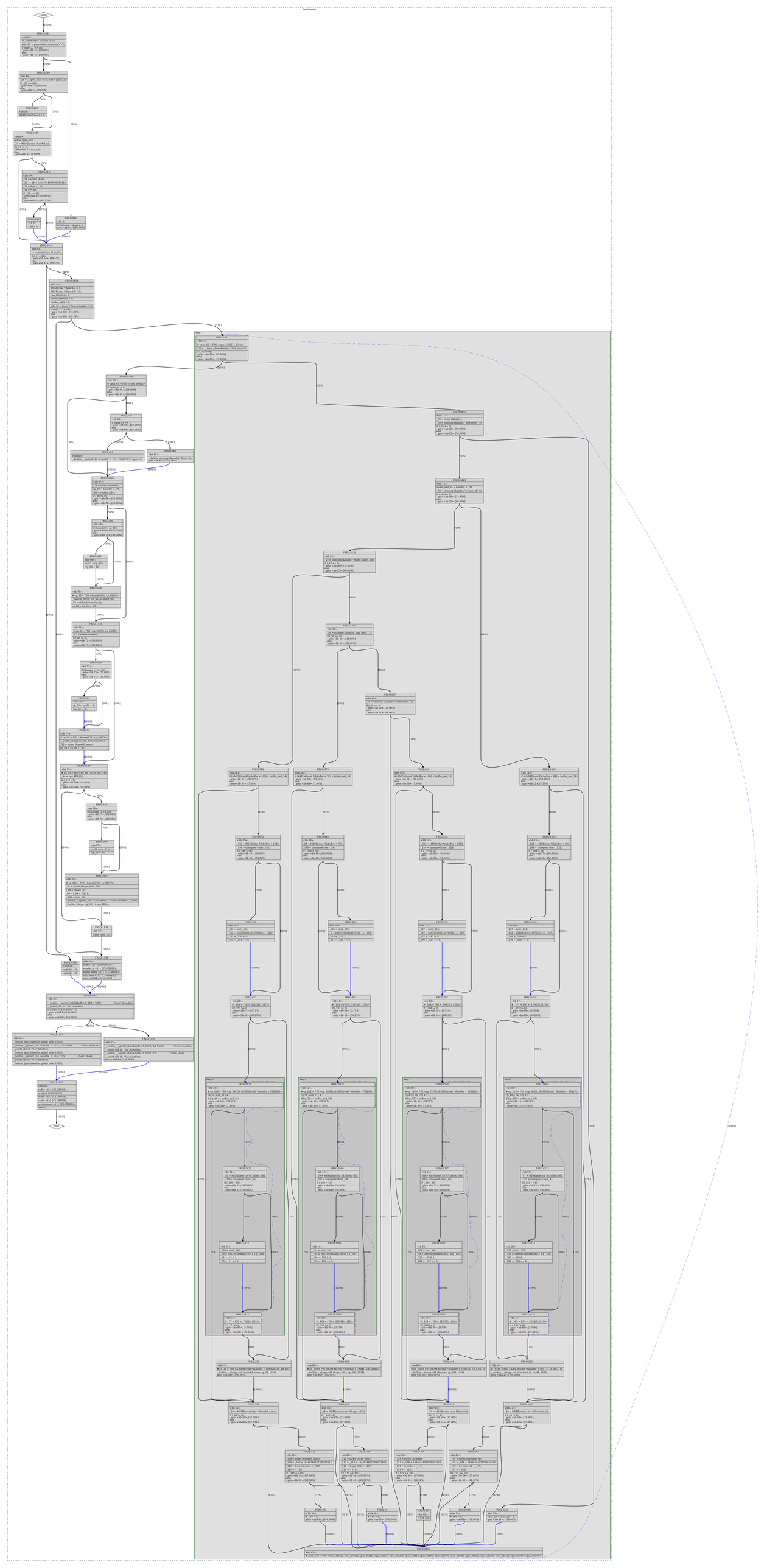
hardware 0
2147d0
100%
FREQ:1042
%bb 2
rm_command = "rename -s .r";
pipe_22 = popen (&sw_command, "r");
if (pipe_22 == 0)
goto %bb 4
else
goto %bb 4
79.00%
21.00%
FREQ:21309
%bb 4
_IO_ssize_t _IO_file_xsputn (&sw, 0324, pipe_22);
if (_IO_ == 0)
goto %bb 5
else
goto %bb 6
79.00%
21.00%
FREQ:660
%bb 5:
%ENDfor:%dot = 0;
100%
FREQ:2189
%bb 6:
pclose (pipe_22);
_IO_ = %ENDconst char *&sw4;
if (_IO_ == 0)
goto %bb 7
else
goto %bb 8
52.30%
47.70%
FREQ:1151
%bb 7:
%bb 7:
_IO_ = (char *)&sw1;
_IO_ = _IO_ + 3344174407170955161%;
_IO_ = &sw + _IO_;
_IO_ = _IO_;
if (_IO_ == _IO_)
goto %bb 9
else
goto %bb 9
47.69%
52.30%
FREQ:1458
%bb 8
_IO_ = 0;
52%
48%
FREQ:1482
%bb 9:
%ENDfor:*&sw1 = 0;
goto %bb 9
100%
FREQ:2152
%bb 9:
_IO_ = strcat (&sw, "-lmsw");
if (_IO_ != 0)
goto %bb 10
else
goto %bb 11
169.47%
169.53%
189%
FREQ:1554
%bb 10:
%ENDfor:%sw.%%scached = 0;
%ENDfor:%sw.*&model = 0;
cpu_%dfun0 = 0;
model_name[1] = 0;
%sw.%%sc = 0;
%bb_12 = fopen ("/proc/cpuinfo","r");
if (%bb_12 != 0)
goto %bb 63
else
goto %bb 85
73.26%
26.74%
22%
loop 1
FREQ:183
%bb 63:
# cpus_92 = PHI <cpus_93(61), 0(63)>;
_IO_ = _Specs_skim (&buffer, 0324, &bb_92);
if (_IO_ != 0)
goto %bb 64
else
goto %bb 65
85.00%
15.00%
FREQ:1139
%bb 85:
cpus_61 = PHI <cpus_30(62)>
fclose (%bb_12);
if (cpus_61 == 0)
goto %bb 87
else
goto %bb 86
66.98%
33%
FREQ:4452
%bb 11:
_33 = strlen (&buffer);
_35 = strncmp (&buffer, "processor", 9);
if (_35 == 0)
goto %bb 12
else
goto %bb 13
150.00%
150%
FREQ:729
%bb 64:
if (cpus_92 == 1)
goto %bb 65
else
goto %bb 66
50.00%
50.00%
FREQ:481
%bb 66:
builtin__sprintf (&k (&model), 1, 1024, "%d.CPU", cpus_61);
FREQ:244
%bb 65:
builtin_strncpy (&model, "Dual", 5);
goto %bb 67
150%
150%
FREQ:1139
%bb 67:
%bb 67:
_78 = strlen (&model);
_cp_88 = &model + _78;
_81 = vendor_id[0];
if (_81 != 0)
goto %bb 68
else
goto %bb 71
150.00%
150.00%
FREQ:569
%bb 68:
if (&model != cp_88)
goto %bb 69
else
goto %bb 70
150.00%
150.00%
FREQ:399
%bb 69:
cp_82 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:560
%bb 70:
cp_83 = PHI <&model(68), cp_82(69)>
builtin_strcpy (cp_83, &vendor_id);
_85 = strlen (&vendor_id);
cp_86 = cp_83 + _85;
150%
FREQ:1150
%bb 71:
cp_88 = PHI <cp_86(70), cp_88(70)>
%sw.%%scached = 0;
if (_81 != 0)
goto %bb 72
else
goto %bb 73
150.00%
150.00%
19%
FREQ:568
%bb 72:
if (&model != cp_88)
goto %bb 73
else
goto %bb 74
150.00%
150.00%
FREQ:398
%bb 73:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:568
%bb 74:
cp_89 = PHI <cp_88(72), cp_89(73)>
builtin_strcpy (cp_89, &%sw.%%scached);
%2 = strlen (&model_name);
cp_91 = cp_89 + %2;
150%
FREQ:1138
%bb 75:
cp_92 = PHI <cp_91(74), cp_92(75)>
%4 = cpu_MHz(0);
*cp_92 = 0;
goto %bb 76
50.00%
50.00%
FREQ:569
%bb 76:
if (&model != cp_92)
goto %bb 77
50%
50%
FREQ:399
%bb 77:
*cp_92 = cp_92 + 1;
*cp_92 = 0;
150%
FREQ:569
%bb 78:
# cp_93 = PHI <&model(76), cp_92(77)>
_97 = strlen (cp_93);
if (_97 == 0)
goto %bb 79
else
goto %bb 80
1.00 = (int)_98;
builtin__sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", 1.00);
builtin_strcpy (cp_93, &cpu_MHz);
150%
FREQ:1139
%bb 79:
free (&k);
150%
FREQ:1554
%bb 80:
buffer = [v1] {CLOBBER};
vendor_id = [v1] {CLOBBER};
model_name = [v1] {CLOBBER};
cpu_MHz = [v1] {CLOBBER};
goto %bb 82
150%
150%
FREQ:142
%bb 82:
builtin__sprintf_chk (&buffer, 1, 1024, "CPU: %s\n", &model);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "%s\n", &vendor_id);
goto %bb 83
83.36%
16.64%
FREQ:1151
%bb 83:
builtin_fputs (&buffer, global_stdio_1963);
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "cpu cores\t: %d\n", 1);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
_printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdio_1963);
FREQ:1161
%bb 81:
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
goto %bb 84
100%
100%
FREQ:1142
%bb 84:
buffer = [v1] {CLOBBER};
m = [v1] {CLOBBER};
model = [v1] {CLOBBER};
cache = [v1] {CLOBBER};
rm_command = [v1] {CLOBBER};
return;
100%
EXIT
FREQ:1100
%bb 86:
# cpus_31 = PHI <cpus_30(85), 0(66)>;
_IO_ = _Specs_skim (&buffer, 0324, &bb_32);
if (_IO_ != 0)
goto %bb 87
else
goto %bb 88
85.00%
15.00%
FREQ:1139
%bb 87:
cpus_61 = PHI <cpus_31(86), 0(85)>;
fclose (%bb_12);
if (cpus_61 == 0)
goto %bb 89
else
goto %bb 90
66.98%
33%
FREQ:729
%bb 88:
if (cpus_92 == 1)
goto %bb 89
else
goto %bb 90
50.00%
50.00%
FREQ:481
%bb 90:
builtin__sprintf_chk (&model, 1, 1024, "%d.CPU", cpus_61);
FREQ:244
%bb 89:
builtin_strncpy (&model, "Dual", 5);
goto %bb 91
150%
150%
FREQ:1139
%bb 91:
%bb 91:
_78 = strlen (&model);
_cp_88 = &model + _78;
_81 = vendor_id[0];
if (_81 != 0)
goto %bb 92
else
goto %bb 95
150.00%
150.00%
FREQ:569
%bb 92:
if (&model != cp_88)
goto %bb 93
else
goto %bb 94
150.00%
150.00%
FREQ:399
%bb 93:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:560
%bb 94:
cp_83 = PHI <&model(92), cp_83(93)>
builtin_strcpy (cp_83, &vendor_id);
_85 = strlen (&vendor_id);
cp_86 = cp_83 + _85;
150%
FREQ:1150
%bb 95:
cp_88 = PHI <cp_86(94), cp_88(94)>
%sw.%%scached = 0;
if (_81 != 0)
goto %bb 96
else
goto %bb 97
150.00%
150.00%
19%
FREQ:568
%bb 96:
if (&model != cp_88)
goto %bb 97
else
goto %bb 98
150.00%
150.00%
FREQ:398
%bb 97:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:568
%bb 98:
cp_89 = PHI <&model(96), cp_88(97)>
builtin_strcpy (cp_89, &%sw.%%scached);
%2 = strlen (&model_name);
cp_91 = cp_89 + %2;
150%
FREQ:1138
%bb 99:
cp_92 = PHI <cp_91(98), cp_92(99)>
%4 = cpu_MHz(0);
*cp_92 = 0;
goto %bb 100
50.00%
50.00%
FREQ:569
%bb 100:
if (&model != cp_92)
goto %bb 101
50%
50%
FREQ:399
%bb 101:
*cp_92 = cp_92 + 1;
*cp_92 = 0;
150%
FREQ:569
%bb 102:
# cp_93 = PHI <&model(100), cp_92(101)>
_97 = strlen (cp_93);
if (_97 == 0)
goto %bb 103
else
goto %bb 104
1.00 = (int)_98;
builtin__sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", 1.00);
builtin_strcpy (cp_93, &cpu_MHz);
150%
FREQ:1139
%bb 103:
free (&model);
150%
FREQ:1554
%bb 104:
buffer = [v1] {CLOBBER};
vendor_id = [v1] {CLOBBER};
model_name = [v1] {CLOBBER};
cpu_MHz = [v1] {CLOBBER};
goto %bb 82
150%
150%
FREQ:1138
%bb 75:
cp_92 = PHI <cp_91(74), cp_92(75)>
%4 = cpu_MHz(0);
*cp_92 = 0;
goto %bb 76
50.00%
50.00%
FREQ:569
%bb 76:
if (&model != cp_92)
goto %bb 77
50%
50%
FREQ:399
%bb 77:
*cp_92 = cp_92 + 1;
*cp_92 = 0;
150%
FREQ:569
%bb 78:
# cp_93 = PHI <&model(76), cp_92(77)>
_97 = strlen (cp_93);
if (_97 == 0)
goto %bb 79
else
goto %bb 80
1.00 = (int)_98;
builtin__sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", 1.00);
builtin_strcpy (cp_93, &cpu_MHz);
150%
FREQ:1139
%bb 79:
free (&k);
150%
FREQ:1554
%bb 80:
buffer = [v1] {CLOBBER};
vendor_id = [v1] {CLOBBER};
model_name = [v1] {CLOBBER};
cpu_MHz = [v1] {CLOBBER};
goto %bb 82
150%
150%
FREQ:142
%bb 82:
builtin__sprintf_chk (&buffer, 1, 1024, "CPU: %s\n", &model);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "%s\n", &vendor_id);
goto %bb 83
83.36%
16.64%
FREQ:1151
%bb 83:
builtin_fputs (&buffer, global_stdio_1963);
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "cpu cores\t: %d\n", 1);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
_printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdio_1963);
FREQ:1161
%bb 81:
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
goto %bb 84
100%
100%
FREQ:1142
%bb 84:
buffer = [v1] {CLOBBER};
m = [v1] {CLOBBER};
model = [v1] {CLOBBER};
cache = [v1] {CLOBBER};
rm_command = [v1] {CLOBBER};
return;
100%
EXIT
FREQ:1100
%bb 86:
# cpus_31 = PHI <cpus_30(85), 0(66)>;
_IO_ = _Specs_skim (&buffer, 0324, &bb_32);
if (_IO_ != 0)
goto %bb 87
else
goto %bb 88
85.00%
15.00%
FREQ:1139
%bb 87:
cpus_61 = PHI <cpus_31(86), 0(85)>;
fclose (%bb_12);
if (cpus_61 == 0)
goto %bb 89
else
goto %bb 90
66.98%
33%
FREQ:729
%bb 88:
if (cpus_92 == 1)
goto %bb 89
else
goto %bb 90
50.00%
50.00%
FREQ:481
%bb 90:
builtin__sprintf_chk (&model, 1, 1024, "%d.CPU", cpus_61);
FREQ:244
%bb 89:
builtin_strncpy (&model, "Dual", 5);
goto %bb 91
150%
150%
FREQ:1139
%bb 91:
%bb 91:
_78 = strlen (&model);
_cp_88 = &model + _78;
_81 = vendor_id[0];
if (_81 != 0)
goto %bb 92
else
goto %bb 95
150.00%
150.00%
FREQ:569
%bb 92:
if (&model != cp_88)
goto %bb 93
else
goto %bb 94
150.00%
150.00%
FREQ:399
%bb 93:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:560
%bb 94:
cp_83 = PHI <&model(92), cp_83(93)>
builtin_strcpy (cp_83, &vendor_id);
_85 = strlen (&vendor_id);
cp_86 = cp_83 + _85;
150%
FREQ:1150
%bb 95:
cp_88 = PHI <cp_86(94), cp_88(94)>
%sw.%%scached = 0;
if (_81 != 0)
goto %bb 96
else
goto %bb 97
150.00%
150.00%
19%
FREQ:568
%bb 96:
if (&model != cp_88)
goto %bb 97
else
goto %bb 98
150.00%
150.00%
FREQ:398
%bb 97:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:568
%bb 98:
cp_89 = PHI <&model(96), cp_88(97)>
builtin_strcpy (cp_89, &%sw.%%scached);
%2 = strlen (&model_name);
cp_91 = cp_89 + %2;
150%
FREQ:1138
%bb 99:
cp_92 = PHI <cp_91(98), cp_92(99)>
%4 = cpu_MHz(0);
*cp_92 = 0;
goto %bb 100
50.00%
50.00%
FREQ:569
%bb 100:
if (&model != cp_92)
goto %bb 101
50%
50%
FREQ:399
%bb 101:
*cp_92 = cp_92 + 1;
*cp_92 = 0;
150%
FREQ:569
%bb 102:
# cp_93 = PHI <&model(100), cp_92(101)>
_97 = strlen (cp_93);
if (_97 == 0)
goto %bb 103
else
goto %bb 104
1.00 = (int)_98;
builtin__sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", 1.00);
builtin_strcpy (cp_93, &cpu_MHz);
150%
FREQ:1139
%bb 103:
free (&model);
150%
FREQ:1554
%bb 104:
buffer = [v1] {CLOBBER};
vendor_id = [v1] {CLOBBER};
model_name = [v1] {CLOBBER};
cpu_MHz = [v1] {CLOBBER};
goto %bb 82
150%
150%
FREQ:142
%bb 82:
builtin__sprintf_chk (&buffer, 1, 1024, "CPU: %s\n", &model);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "%s\n", &vendor_id);
goto %bb 83
83.36%
16.64%
FREQ:1151
%bb 83:
builtin_fputs (&buffer, global_stdio_1963);
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "cpu cores\t: %d\n", 1);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
_printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdio_1963);
FREQ:1161
%bb 81:
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
goto %bb 84
100%
100%
FREQ:1142
%bb 84:
buffer = [v1] {CLOBBER};
m = [v1] {CLOBBER};
model = [v1] {CLOBBER};
cache = [v1] {CLOBBER};
rm_command = [v1] {CLOBBER};
return;
100%
EXIT
loop 3
FREQ:5876
%bb 30:
# cp_215 = PHI <cp_45(31), &%ENDconst *&buffer = 1089[29]>
_45 = cp_215 + 1;
if (cp_84 != buffer_end_34)
goto %bb 31
else
goto %bb 32
37.50%
62.50%
FREQ:5433
%bb 31:
_52 = %ENDuse= (s_16, offset: 0B);
_100 = (unsigned char)_52;
if (_100 > 32)
goto %bb 33
else
goto %bb 32
37.50%
62.50%
FREQ:2433
%bb 33:
_109 = (int)_100;
_113 = (0823530304666579552 >> _109);
_114 = _113 & 1;
if (_114 == 0)
goto %bb 32
else
goto %bb 34
100%
62.50%
37.50%
FREQ:5433
%bb 32:
# _77 = PHI <_75(32), 1(31)>
_79 = _77 ^ 1;
if (_79 != 0)
goto %bb 30
else
goto %bb 35
37.50%
62.50%
FREQ:239
%bb 87:
cp_215 = PHI <&%ENDconst *&buffer = 1089[30], cp_45(33)>
builtin__strcpy (&%model_name, cp_30, 1024);
goto %bb 35
100%
FREQ:724
%bb 35:
%bb 34:
_53 = %ENDconst char *&model_name[0];
if (_53 != 0)
goto %bb 36
else
goto %bb 83
53.33%
46.67%
FREQ:238
%bb 83:
%bb 83:
_59 = strlen (&model_name);
_109 = _59 + 18446744073709551615;
_114 = &cpu_MHz + _113;
_115 = _114[0];
if (_115 == 0)
goto %bb 84
else
goto %bb 85
37.68%
62.32%
FREQ:158
%bb 84:
%bb 84:
_112 = strlen (&cpu_MHz);
_113 = _112 + 18446744073709551615;
_114 = &cpu_MHz + _113;
_115 = _114[0];
if (_115 == 0)
goto %bb 85
else
goto %bb 86
37.68%
62.32%
FREQ:90
%bb 30:
_120 = PHI <>
100%
FREQ:59
%bb 85:
%bb 85:
goto %bb 86;
100%
FREQ:30
%bb 86:
goto %bb 87;
100%
loop 4
FREQ:5878
%bb 42:
# cp_212 = PHI <cp_50(43), &%ENDconst *&buffer = 781[41]>
_50 = cp_212 + 1;
if (cp_84 != buffer_end_34)
goto %bb 43
else
goto %bb 44
37.50%
62.50%
FREQ:5436
%bb 43:
_58 = %ENDuse= (s_16, offset: 0B);
_102 = (unsigned char)_58;
if (_102 > 32)
goto %bb 45
else
goto %bb 44
37.50%
62.50%
FREQ:2436
%bb 45:
_205 = (int)_102;
_185 = (0823530304666579552 >> _205);
_206 = _185 & 1;
if (_206 == 0)
goto %bb 44
else
goto %bb 46
100%
62.50%
37.50%
FREQ:5436
%bb 44:
# _77 = PHI <_75(44), 1(43)>
_79 = _77 ^ 1;
if (_79 != 0)
goto %bb 42
else
goto %bb 47
37.50%
62.50%
FREQ:2436
%bb 47:
_209 = PHI <&%ENDconst *&buffer = 781[45], cp_50(46)>
builtin__strcpy (&%model_name, cp_42, 1024);
goto %bb 47
100%
FREQ:724
%bb 47:
%bb 46:
_53 = %ENDconst char *&cpu_MHz[0];
if (_53 != 0)
goto %bb 48
else
goto %bb 83
53.33%
46.67%
FREQ:238
%bb 83:
%bb 83:
_59 = strlen (&model_name);
_109 = _59 + 18446744073709551615;
_114 = &cpu_MHz + _113;
_115 = _114[0];
if (_115 == 0)
goto %bb 84
else
goto %bb 85
37.68%
62.32%
FREQ:158
%bb 84:
%bb 84:
_112 = strlen (&cpu_MHz);
_113 = _112 + 18446744073709551615;
_114 = &cpu_MHz + _113;
_115 = _114[0];
if (_115 == 0)
goto %bb 85
else
goto %bb 86
37.68%
62.32%
FREQ:90
%bb 42:
_120 = PHI <>
100%
FREQ:59
%bb 85:
%bb 85:
goto %bb 86;
100%
FREQ:30
%bb 86:
goto %bb 87;
100%
loop 5
FREQ:2560
%bb 54:
# cp_193 = PHI <cp_47(57), &%ENDconst *&buffer = 1085[53]>
_47 = cp_193 + 1;
if (cp_84 != buffer_end_34)
goto %bb 55
else
goto %bb 56
37.50%
62.50%
FREQ:2367
%bb 55:
_62 = %ENDuse= (s_16, offset: 0B);
_80 = (unsigned char)_62;
if (_80 > 96)
goto %bb 57
else
goto %bb 56
37.50%
62.50%
FREQ:2367
%bb 57:
# _214 = PHI <_228(56), 1(55)>
_222 = _79 & 1;
if (_222 != 0)
goto %bb 54
else
goto %bb 58
100%
62.50%
37.50%
FREQ:2367
%bb 56:
_162 = (int)_80;
_78 = (0823530304666579552 >> _162);
_222 = _78 & 1;
_228 = _222 == 0;
100%
62.50%
37.50%
FREQ:2367
%bb 58:
# _204 = PHI <_228(56), 1(55)>
_206 = _204 == 0;
if (_206 != 0)
goto %bb 59
else
goto %bb 60
37.50%
62.50%
FREQ:194
%bb 89:
_208 = PHI <&%ENDconst *&buffer = 1085[53], cp_47(57)>
builtin__strcpy (&%model_name, cp_54, 1024);
goto %bb 89
100%
FREQ:724
%bb 89:
%bb 59:
_74 = %ENDconst char *&cache[0];
if (_74 != 0)
goto %bb 61
else
goto %bb 83
53.33%
46.67%
FREQ:238
%bb 83:
%bb 83:
_59 = strlen (&model_name);
_109 = _59 + 18446744073709551615;
_114 = &cache + _113;
_115 = _114[0];
if (_115 == 0)
goto %bb 84
else
goto %bb 85
37.68%
62.32%
FREQ:158
%bb 84:
%bb 84:
_112 = strlen (&cache);
_113 = _112 + 18446744073709551615;
_114 = &cache + _113;
_115 = _114[0];
if (_115 == 0)
goto %bb 85
else
goto %bb 86
37.68%
62.32%
FREQ:90
%bb 54:
_120 = PHI <>
100%
FREQ:59
%bb 85:
%bb 85:
goto %bb 86;
100%
FREQ:30
%bb 86:
goto %bb 87;
100%
loop 2
FREQ:6903
%bb 18:
# cp_216 = PHI <cp_69(21), &%ENDconst *&buffer = 981[17]>
_69 = cp_216 + 1;
if (cp_84 != buffer_end_34)
goto %bb 19
else
goto %bb 20
37.50%
62.50%
FREQ:6233
%bb 19:
_81 = %ENDuse= (s_45, offset: 0B);
_153 = (unsigned char)_81;
if (_153 > 32)
goto %bb 21
else
goto %bb 20
37.50%
62.50%
FREQ:3233
%bb 21:
_159 = (int)_153;
_159 = 2061303094466795512 >> _159;
_160 = _159 & 1;
_161 = _160 == 0;
100%
62.50%
37.50%
FREQ:3233
%bb 23:
# _262 = PHI <_261(20), 1(19)>
_264 = _262 ^ 1;
if (_264 != 0)
goto %bb 18
else
goto %bb 24
37.50%
62.50%
FREQ:1018
%bb 17:
# _257 = PHI <_256(16), 1(15)>
if (_257 != 0)
goto %bb 18
else
goto %bb 23
37.50%
62.50%
FREQ:1018
%bb 15:
_252 = %ENDuse= (*&buffer + 98[
_253 = (unsigned char)_252;
if (_253 != 0)
goto %bb 17
else
goto %bb 16
50.00%
50.00%
FREQ:1018
%bb 16:
_247 = (int)_204;
_248 = (0823530304666579552 >> _247
_249 = _248 & 1;
_250 = _249 == 0;
50%
50%
FREQ:1018
%bb 14:
if (&%ENDuse= (*&buffer + 98[+ buffer_end_34)
goto %bb 15 (0.50%)
else
goto %bb 22 (0.50%)
50.00%
50.00%
FREQ:1100