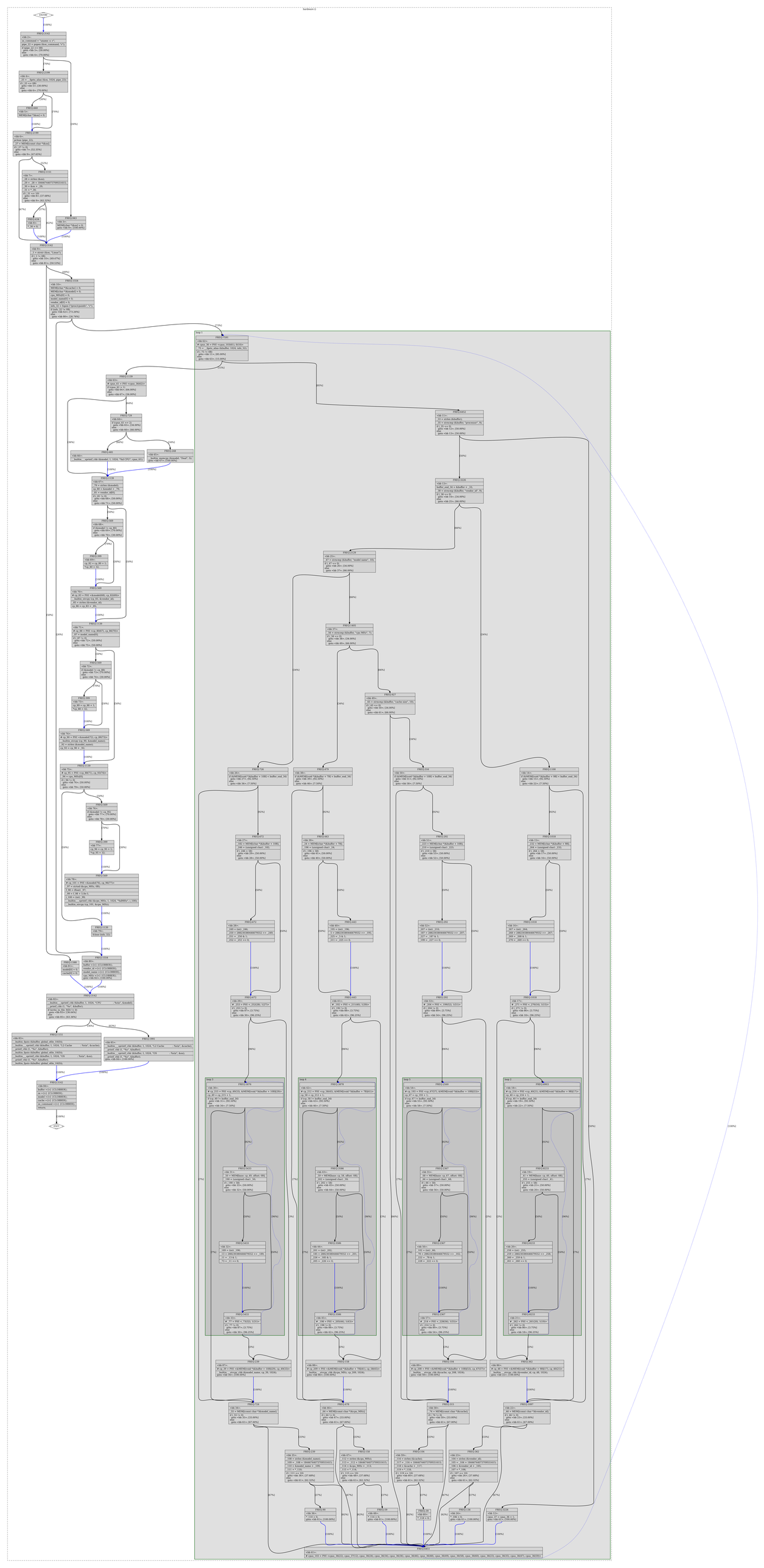
hardware 0
2147d0
100%
FREQ:1042
%bb 2
rm_command = "rename -s .r";
pipe_22 = popen (&sw_command, "r");
if (pipe_22 == 0)
goto %bb 4
else
goto %bb 4
79.00%
21.00%
FREQ:21309
%bb 4
_IO_ssize_t _IO_file_xsputn (&sw, 0324, pipe_22);
if (_IO_ == 0)
goto %bb 5
else
goto %bb 6
79.00%
21.00%
FREQ:660
%bb 5:
%ENDfor:%dot = 0;
100%
FREQ:2189
%bb 6:
pclose (pipe_22);
_IO_ = %ENDconst char *&sw4;
if (_IO_ == 0)
goto %bb 7
else
goto %bb 8
52.30%
47.70%
FREQ:1151
%bb 7:
%bb 7:
_IO_ = (char *)&sw1;
_IO_ = _IO_ + 3344174407170955161%;
_IO_ = &sw + _IO_;
_IO_ = _IO_;
if (_IO_ == _IO_)
goto %bb 9
else
goto %bb 9
47.69%
52.30%
FREQ:1458
%bb 8
_IO_ = 0;
52%
48%
FREQ:1482
%bb 9:
%ENDfor:*&sw1 = 0;
goto %bb 9
100%
FREQ:2152
%bb 9:
_IO_ = strcat (&sw, "-lmsw");
if (_IO_ != 0)
goto %bb 10
else
goto %bb 11
169.47%
169.53%
189%
FREQ:1554
%bb 10:
%ENDfor:%sw.%%scached = 0;
%ENDfor:%sw.*&model = 0;
cpu_%dfun0 = 0;
model_name[1] = 0;
%sw.%%sc = 0;
%bb_12 = fopen ("/proc/cpuinfo","r");
if (%bb_12 != 0)
goto %bb 63
else
goto %bb 85
73.26%
26.74%
22%
loop 1
FREQ:183
%bb 63:
# cpus_92 = PHI <cpus_93(61), 0(63)>;
_IO_ = _Specs_skim (&buffer, 0324, &bb_92);
if (_IO_ != 0)
goto %bb 64
else
goto %bb 65
85.00%
15.00%
FREQ:1139
%bb 85:
cpus_61 = PHI <cpus_30(62)>
fclose (%bb_12);
if (cpus_61 == 0)
goto %bb 87
else
goto %bb 86
66.98%
33%
FREQ:4452
%bb 11:
_33 = strlen (&buffer);
_35 = strncmp (&buffer, "processor", 9);
if (_35 == 0)
goto %bb 12
else
goto %bb 13
150.00%
150%
FREQ:729
%bb 64:
if (cpus_92 == 1)
goto %bb 65
else
goto %bb 66
50.00%
50.00%
FREQ:481
%bb 66:
builtin__sprintf (&k (&model), 1, 1024, "%d.CPU", cpus_61);
FREQ:244
%bb 65:
builtin_strncpy (&model, "Dual", 5);
goto %bb 67
150%
150%
FREQ:1139
%bb 67:
%bb 67:
_78 = strlen (&model);
_cp_88 = &model + _78;
_81 = vendor_id[0];
if (_81 != 0)
goto %bb 68
else
goto %bb 71
150.00%
150.00%
FREQ:569
%bb 68:
if (&model != cp_88)
goto %bb 69
else
goto %bb 70
150.00%
150.00%
FREQ:399
%bb 69:
cp_82 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:560
%bb 70:
cp_83 = PHI <&model(68), cp_82(69)>
builtin_strcpy (cp_83, &vendor_id);
_85 = strlen (&vendor_id);
cp_86 = cp_83 + _85;
150%
FREQ:1150
%bb 71:
cp_88 = PHI <cp_86(70), cp_88(70)>
%sw.%%scached = 0;
if (_81 != 0)
goto %bb 72
else
goto %bb 73
150.00%
150.00%
19%
FREQ:568
%bb 72:
if (&model != cp_88)
goto %bb 73
else
goto %bb 74
150.00%
150.00%
FREQ:398
%bb 73:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:568
%bb 74:
cp_89 = PHI <cp_88(72), cp_89(73)>
builtin_strcpy (cp_89, &%sw.%%scached);
%2 = strlen (&model_name);
cp_91 = cp_89 + %2;
150%
FREQ:1138
%bb 75:
cp_92 = PHI <cp_91(74), cp_92(75)>
%4 = cpu_MHz(0);
*cp_92 = 0;
goto %bb 76
50.00%
50.00%
FREQ:569
%bb 76:
if (&model != cp_92)
goto %bb 77
50%
50%
FREQ:399
%bb 77:
*cp_92 = cp_92 + 1;
*cp_92 = 0;
150%
FREQ:569
%bb 78:
# cp_93 = PHI <&model(76), cp_92(77)>
_97 = strlen (cp_93);
if (_97 == 0)
goto %bb 79
else
goto %bb 80
1.00 = (int)_98;
builtin__sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", 1.00);
builtin_strcpy (cp_93, &cpu_MHz);
150%
FREQ:1139
%bb 79:
free (&k);
150%
FREQ:1554
%bb 80:
buffer = [v1] {CLOBBER};
vendor_id = [v1] {CLOBBER};
model_name = [v1] {CLOBBER};
cpu_MHz = [v1] {CLOBBER};
goto %bb 82
150%
150%
FREQ:142
%bb 82:
builtin__sprintf_chk (&buffer, 1, 1024, "CPU: %s\n", &model);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "%s\n", &vendor_id);
goto %bb 83
83.36%
16.64%
FREQ:1151
%bb 83:
builtin_fputs (&buffer, global_stdio_1963);
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "cpu cores\t: %d\n", 1);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
_printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdio_1963);
FREQ:1161
%bb 81:
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
goto %bb 84
100%
100%
FREQ:1142
%bb 84:
buffer = [v1] {CLOBBER};
m = [v1] {CLOBBER};
model = [v1] {CLOBBER};
cache = [v1] {CLOBBER};
rm_command = [v1] {CLOBBER};
return;
100%
EXIT
FREQ:1100
%bb 86:
# cpus_31 = PHI <cpus_30(85), 0(66)>;
_IO_ = _Specs_skim (&buffer, 0324, &bb_32);
if (_IO_ != 0)
goto %bb 87
else
goto %bb 88
85.00%
15.00%
FREQ:1139
%bb 87:
cpus_61 = PHI <cpus_31(86), 0(85)>;
fclose (%bb_12);
if (cpus_61 == 0)
goto %bb 89
else
goto %bb 90
66.98%
33%
FREQ:729
%bb 88:
if (cpus_92 == 1)
goto %bb 89
else
goto %bb 90
50.00%
50.00%
FREQ:481
%bb 90:
builtin__sprintf_chk (&model, 1, 1024, "%d.CPU", cpus_61);
FREQ:244
%bb 89:
builtin_strncpy (&model, "Dual", 5);
goto %bb 91
150%
150%
FREQ:1139
%bb 91:
%bb 91:
_78 = strlen (&model);
_cp_88 = &model + _78;
_81 = vendor_id[0];
if (_81 != 0)
goto %bb 92
else
goto %bb 95
150.00%
150.00%
FREQ:569
%bb 92:
if (&model != cp_88)
goto %bb 93
else
goto %bb 94
150.00%
150.00%
FREQ:399
%bb 93:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:560
%bb 94:
cp_83 = PHI <&model(92), cp_83(93)>
builtin_strcpy (cp_83, &vendor_id);
_85 = strlen (&vendor_id);
cp_86 = cp_83 + _85;
150%
FREQ:1150
%bb 95:
cp_88 = PHI <cp_86(94), cp_88(94)>
%sw.%%scached = 0;
if (_81 != 0)
goto %bb 96
else
goto %bb 97
150.00%
150.00%
19%
FREQ:568
%bb 96:
if (&model != cp_88)
goto %bb 97
else
goto %bb 98
150.00%
150.00%
FREQ:398
%bb 97:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:568
%bb 98:
cp_89 = PHI <&model(96), cp_88(97)>
builtin_strcpy (cp_89, &%sw.%%scached);
%2 = strlen (&model_name);
cp_91 = cp_89 + %2;
150%
FREQ:1138
%bb 99:
cp_92 = PHI <cp_91(98), cp_92(99)>
%4 = cpu_MHz(0);
*cp_92 = 0;
goto %bb 100
50.00%
50.00%
FREQ:569
%bb 100:
if (&model != cp_92)
goto %bb 101
50%
50%
FREQ:399
%bb 101:
*cp_92 = cp_92 + 1;
*cp_92 = 0;
150%
FREQ:569
%bb 102:
# cp_93 = PHI <&model(100), cp_92(101)>
_97 = strlen (cp_93);
if (_97 == 0)
goto %bb 103
else
goto %bb 104
1.00 = (int)_98;
builtin__sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", 1.00);
builtin_strcpy (cp_93, &cpu_MHz);
150%
FREQ:1139
%bb 103:
free (&model);
150%
FREQ:1554
%bb 104:
buffer = [v1] {CLOBBER};
vendor_id = [v1] {CLOBBER};
model_name = [v1] {CLOBBER};
cpu_MHz = [v1] {CLOBBER};
goto %bb 82
150%
150%
FREQ:1138
%bb 75:
cp_92 = PHI <cp_91(74), cp_92(75)>
%4 = cpu_MHz(0);
*cp_92 = 0;
goto %bb 76
50.00%
50.00%
FREQ:569
%bb 76:
if (&model != cp_92)
goto %bb 77
50%
50%
FREQ:399
%bb 77:
*cp_92 = cp_92 + 1;
*cp_92 = 0;
150%
FREQ:569
%bb 78:
# cp_93 = PHI <&model(76), cp_92(77)>
_97 = strlen (cp_93);
if (_97 == 0)
goto %bb 79
else
goto %bb 80
1.00 = (int)_98;
builtin__sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", 1.00);
builtin_strcpy (cp_93, &cpu_MHz);
150%
FREQ:1139
%bb 79:
free (&k);
150%
FREQ:1554
%bb 80:
buffer = [v1] {CLOBBER};
vendor_id = [v1] {CLOBBER};
model_name = [v1] {CLOBBER};
cpu_MHz = [v1] {CLOBBER};
goto %bb 82
150%
150%
FREQ:142
%bb 82:
builtin__sprintf_chk (&buffer, 1, 1024, "CPU: %s\n", &model);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "%s\n", &vendor_id);
goto %bb 83
83.36%
16.64%
FREQ:1151
%bb 83:
builtin_fputs (&buffer, global_stdio_1963);
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "cpu cores\t: %d\n", 1);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
_printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdio_1963);
FREQ:1161
%bb 81:
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
goto %bb 84
100%
100%
FREQ:1142
%bb 84:
buffer = [v1] {CLOBBER};
m = [v1] {CLOBBER};
model = [v1] {CLOBBER};
cache = [v1] {CLOBBER};
rm_command = [v1] {CLOBBER};
return;
100%
EXIT
FREQ:1100
%bb 86:
# cpus_31 = PHI <cpus_30(85), 0(66)>;
_IO_ = _Specs_skim (&buffer, 0324, &bb_32);
if (_IO_ != 0)
goto %bb 87
else
goto %bb 88
85.00%
15.00%
FREQ:1139
%bb 87:
cpus_61 = PHI <cpus_31(86), 0(85)>;
fclose (%bb_12);
if (cpus_61 == 0)
goto %bb 89
else
goto %bb 90
66.98%
33%
FREQ:729
%bb 88:
if (cpus_92 == 1)
goto %bb 89
else
goto %bb 90
50.00%
50.00%
FREQ:481
%bb 90:
builtin__sprintf_chk (&model, 1, 1024, "%d.CPU", cpus_61);
FREQ:244
%bb 89:
builtin_strncpy (&model, "Dual", 5);
goto %bb 91
150%
150%
FREQ:1139
%bb 91:
%bb 91:
_78 = strlen (&model);
_cp_88 = &model + _78;
_81 = vendor_id[0];
if (_81 != 0)
goto %bb 92
else
goto %bb 95
150.00%
150.00%
FREQ:569
%bb 92:
if (&model != cp_88)
goto %bb 93
else
goto %bb 94
150.00%
150.00%
FREQ:399
%bb 93:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:560
%bb 94:
cp_83 = PHI <&model(92), cp_83(93)>
builtin_strcpy (cp_83, &vendor_id);
_85 = strlen (&vendor_id);
cp_86 = cp_83 + _85;
150%
FREQ:1150
%bb 95:
cp_88 = PHI <cp_86(94), cp_88(94)>
%sw.%%scached = 0;
if (_81 != 0)
goto %bb 96
else
goto %bb 97
150.00%
150.00%
19%
FREQ:568
%bb 96:
if (&model != cp_88)
goto %bb 97
else
goto %bb 98
150.00%
150.00%
FREQ:398
%bb 97:
*cp_88 = cp_88 + 1;
*cp_88 = 32;
150%
150%
FREQ:568
%bb 98:
cp_89 = PHI <&model(96), cp_88(97)>
builtin_strcpy (cp_89, &%sw.%%scached);
%2 = strlen (&model_name);
cp_91 = cp_89 + %2;
150%
FREQ:1138
%bb 99:
cp_92 = PHI <cp_91(98), cp_92(99)>
%4 = cpu_MHz(0);
*cp_92 = 0;
goto %bb 100
50.00%
50.00%
FREQ:569
%bb 100:
if (&model != cp_92)
goto %bb 101
50%
50%
FREQ:399
%bb 101:
*cp_92 = cp_92 + 1;
*cp_92 = 0;
150%
FREQ:569
%bb 102:
# cp_93 = PHI <&model(100), cp_92(101)>
_97 = strlen (cp_93);
if (_97 == 0)
goto %bb 103
else
goto %bb 104
1.00 = (int)_98;
builtin__sprintf_chk (&cpu_MHz, 1, 1024, "%dMHz", 1.00);
builtin_strcpy (cp_93, &cpu_MHz);
150%
FREQ:1139
%bb 103:
free (&model);
150%
FREQ:1554
%bb 104:
buffer = [v1] {CLOBBER};
vendor_id = [v1] {CLOBBER};
model_name = [v1] {CLOBBER};
cpu_MHz = [v1] {CLOBBER};
goto %bb 82
150%
150%
FREQ:142
%bb 82:
builtin__sprintf_chk (&buffer, 1, 1024, "CPU: %s\n", &model);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "%s\n", &vendor_id);
goto %bb 83
83.36%
16.64%
FREQ:1151
%bb 83:
builtin_fputs (&buffer, global_stdio_1963);
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "cpu cores\t: %d\n", 1);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
_printf_chk (1, "%s", &buffer);
builtin_fputs (&buffer, global_stdio_1963);
FREQ:1161
%bb 81:
builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t: %s\n", &cache);
_printf_chk (1, "%s", &buffer);
builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
goto %bb 84
100%
100%
FREQ:1142
%bb 84:
buffer = [v1] {CLOBBER};
m = [v1] {CLOBBER};
model = [v1] {CLOBBER};
cache = [v1] {CLOBBER};
rm_command = [v1] {CLOBBER};
return;
100%
EXIT
loop 3
FREQ:5876
%bb 30:
# cp_215 = PHI <cp_45(31), &%ENDconst *&buffer = 1089[29]>
_45 = cp_215 + 1;
if (cp_84 != buffer_end_34)
goto %bb 31
else
goto %bb 32
37.50%
62.50%
FREQ:5433
%bb 31:
_52 = %ENDuse= (s_16, offset: 0B);
_100 = (unsigned char)_52;
if (_100 > 32)
goto %bb 33
else
goto %bb 32
37.50%
62.50%
FREQ:2433
%bb 33:
_109 = (int)_100;
_113 = (0823530304666579552 >> _109);
_114 = _113 & 1;
if (_114 == 0)
goto %bb 32
else
goto %bb 34
100%
62.50%
37.50%
FREQ:5433
%bb 32:
# _77 = PHI <_75(32), 1(31)>
_79 = _77 ^ 1;
if (_79 != 0)
goto %bb 30
else
goto %bb 35
37.50%
62.50%
FREQ:239
%bb 87:
cp_215 = PHI <&%ENDconst *&buffer = 1089[30], cp_45(33)>
builtin__strcpy (&%model_name, cp_30, 1024);
goto %bb 35
100%
FREQ:724
%bb 35:
%bb 34:
_53 = %ENDconst char *&model_name[0];
if (_53 != 0)
goto %bb 36
else
goto %bb 83
53.33%
46.67%
FREQ:238
%bb 83:
%bb 83:
_118 = strlen (&model_name);
_119 = _118 + 18446744073709551615;
_120 = &model_name + _119;
_121 = *_120;
if (_121 == 10)
goto %bb 38
else
goto %bb 84
57.68%
42.32%
FREQ:158
%bb 84:
%bb 84:
_122 = strlen (&cpu_MHz);
_123 = _122 + 18446744073709551615;
_124 = &cpu_MHz + _123;
_125 = *_124;
if (_125 == 10)
goto %bb 85
else
goto %bb 86
57.68%
42.32%
FREQ:90
%bb 38:
*_120 = 0;
goto %bb 84
100%
FREQ:59
%bb 85:
*_124 = 0;
goto %bb 86
100%
FREQ:30
%bb 86:
%bb 86:
_126 = strlen (&%model_name);
_127 = _126 + 18446744073709551615;
_128 = &%model_name + _127;
_129 = *_128;
if (_129 == 10)
goto %bb 87
else
goto %bb 88
57.68%
42.32%
FREQ:30
%bb 87:
*_128 = 0;
goto %bb 88
100%
loop 4
FREQ:5878
%bb 42:
# cp_212 = PHI <cp_50(43), &%ENDconst *&buffer = 781[41]>
_50 = cp_212 + 1;
if (cp_84 != buffer_end_34)
goto %bb 43
else
goto %bb 44
37.50%
62.50%
FREQ:5435
%bb 43:
_58 = %ENDuse= (s_16, offset: 0B);
_102 = (unsigned char)_58;
if (_102 > 32)
goto %bb 45
else
goto %bb 44
37.50%
62.50%
FREQ:2435
%bb 45:
_111 = (int)_102;
_115 = (0823530304666579552 >> _111);
_116 = _115 & 1;
if (_116 == 0)
goto %bb 44
else
goto %bb 46
100%
62.50%
37.50%
FREQ:5435
%bb 44:
# _81 = PHI <_79(44), 1(43)>
_83 = _81 ^ 1;
if (_83 != 0)
goto %bb 42
else
goto %bb 47
37.50%
62.50%
FREQ:239
%bb 89:
_209 = PHI <&%ENDconst *&buffer = 781[41], cp_50(45)>
builtin__strcpy (&%model_name, cp_42, 1024);
goto %bb 47
100%
FREQ:724
%bb 47:
%bb 46:
_54 = %ENDconst char *&cpu_MHz[0];
if (_54 != 0)
goto %bb 48
else
goto %bb 85
53.33%
46.67%
FREQ:238
%bb 85:
%bb 85:
_130 = strlen (&cpu_MHz);
_131 = _130 + 18446744073709551615;
_132 = &cpu_MHz + _131;
_133 = *_132;
if (_133 == 10)
goto %bb 86
else
goto %bb 87
57.68%
42.32%
FREQ:158
%bb 86:
%bb 86:
_134 = strlen (&%model_name);
_135 = _134 + 18446744073709551615;
_136 = &%model_name + _135;
_137 = *_136;
if (_137 == 10)
goto %bb 88
else
goto %bb 89
57.68%
42.32%
FREQ:59
%bb 87:
*_132 = 0;
goto %bb 88
100%
FREQ:30
%bb 88:
*_136 = 0;
goto %bb 89
100%
loop 5
FREQ:2560
%bb 54:
# cp_193 = PHI <cp_47(57), &%ENDconst *&buffer = 1085[53]>
_47 = cp_193 + 1;
if (cp_84 != buffer_end_34)
goto %bb 55
else
goto %bb 56
37.50%
62.50%
FREQ:2367
%bb 55:
_62 = %ENDuse= (s_16, offset: 0B);
_80 = (unsigned char)_62;
if (_80 > 96)
goto %bb 57
else
goto %bb 56
37.50%
62.50%
FREQ:2367
%bb 57:
_114 = (int)_80;
_117 = (0823530304666579552 >> _114);
_118 = _117 & 1;
if (_118 == 0)
goto %bb 56
else
goto %bb 58
100%
62.50%
37.50%
FREQ:2367
%bb 58:
_102 = (int)_80;
_79 = (0823530304666579552 >> _102);
_103 = _79 & 1;
if (_103 == 0)
goto %bb 56
else
goto %bb 59
37.50%
62.50%
FREQ:2367
%bb 59:
# _214 = PHI <_229(58), 1(55)>
_229 = _214 ^ 1;
if (_229 != 0)
goto %bb 54
else
goto %bb 60
37.50%
62.50%
FREQ:239
%bb 91:
_208 = PHI <&%ENDconst *&buffer = 1085[53], cp_47(57)>
builtin__strcpy (&%model_name, cp_48, 1024);
goto %bb 60
100%
FREQ:724
%bb 60:
%bb 59:
_74 = %ENDconst char *&cache[0];
if (_74 != 0)
goto %bb 61
else
goto %bb 87
53.33%
46.67%
FREQ:238
%bb 87:
%bb 87:
_138 = strlen (&cache);
_139 = _138 + 18446744073709551615;
_140 = &cache + _139;
_141 = *_140;
if (_141 == 10)
goto %bb 89
else
goto %bb 90
57.68%
42.32%
FREQ:158
%bb 89:
%bb 89:
_142 = strlen (&%model_name);
_143 = _142 + 18446744073709551615;
_144 = &%model_name + _143;
_145 = *_144;
if (_145 == 10)
goto %bb 91
else
goto %bb 92
57.68%
42.32%
FREQ:59
%bb 90:
*_140 = 0;
goto %bb 91
100%
FREQ:30
%bb 91:
*_144 = 0;
goto %bb 92
100%
loop 2
FREQ:6003
%bb 18:
# cp_216 = PHI <cp_49(21), &%ENDconst *&buffer = 981[17]>
_49 = cp_216 + 1;
if (cp_84 != buffer_end_34)
goto %bb 19
else
goto %bb 20
37.50%
62.50%
FREQ:6233
%bb 19:
_64 = %ENDuse= (s_16, offset: 0B);
_153 = (unsigned char)_64;
if (_153 > 32)
goto %bb 21
else
goto %bb 20
37.50%
62.50%
FREQ:6233
%bb 21:
_158 = (int)_153;
_159 = (0823530304666579552 >> _158);
_160 = _159 & 1;
if (_160 == 0)
goto %bb 20
else
goto %bb 22
100%
62.50%
37.50%
FREQ:6233
%bb 22:
_168 = (int)_153;
_169 = (0823530304666579552 >> _168);
_170 = _169 & 1;
if (_170 == 0)
goto %bb 20
else
goto %bb 23
37.50%
62.50%
FREQ:6233
%bb 23:
# _262 = PHI <_263(20), 1(19)>
_263 = _262 ^ 1;
if (_263 != 0)
goto %bb 18
else
goto %bb 24
37.50%
62.50%
FREQ:239
%bb 93:
cp_48 = PHI <&%ENDconst *&buffer = 981[17]>
builtin__strcpy (&%vendor_id, cp_48, 1024);
goto %bb 24
100%
FREQ:724
%bb 24:
%bb 23:
_66 = %ENDconst char *&vendor_id[0];
if (_66 != 0)
goto %bb 25
else
goto %bb 89
53.33%
46.67%
FREQ:238
%bb 89:
%bb 89:
_146 = strlen (&vendor_id);
_147 = _146 + 18446744073709551615;
_148 = &vendor_id + _147;
_149 = *_148;
if (_149 == 10)
goto %bb 91
else
goto %bb 92
57.68%
42.32%
FREQ:158
%bb 91:
%bb 91:
_150 = strlen (&%vendor_id);
_151 = _150 + 18446744073709551615;
_152 = &%vendor_id + _151;
_153 = *_152;
if (_153 == 10)
goto %bb 93
else
goto %bb 94
57.68%
42.32%
FREQ:59
%bb 92:
*_148 = 0;
goto %bb 93
100%
FREQ:30
%bb 93:
*_152 = 0;
goto %bb 94
100%
FREQ:30
%bb 94:
%bb 94:
_154 = %ENDconst char *&model_name[0];
if (_154 != 0)
goto %bb 95
else
goto %bb 96
53.33%
46.67%
FREQ:238
%bb 96:
%bb 96:
_155 = strlen (&model_name);
_156 = _155 + 18446744073709551615;
_157 = &model_name + _156;
_158 = *_157;
if (_158 == 10)
goto %bb 97
else
goto %bb 98
57.68%
42.32%
FREQ:158
%bb 97:
%bb 97:
_159 = strlen (&cpu_MHz);
_160 = _159 + 18446744073709551615;
_161 = &cpu_MHz + _160;
_162 = *_161;
if (_162 == 10)
goto %bb 99
else
goto %bb 100
57.68%
42.32%
FREQ:59
%bb 98:
*_157 = 0;
goto %bb 99
100%
FREQ:30
%bb 99:
*_161 = 0;
goto %bb 100
100%
FREQ:30
%bb 100:
%bb 100:
_163 = strlen (&%model_name);
_164 = _163 + 18446744073709551615;
_165 = &%model_name + _164;
_166 = *_165;
if (_166 == 10)
goto %bb 101
else
goto %bb 102
57.68%
42.32%
FREQ:59
%bb 101:
*_165 = 0;
goto %bb 102
100%
FREQ:30
%bb 102:
%bb 102:
_167 = strlen (&%vendor_id);
_168 = _167 + 18446744073709551615;
_169 = &%vendor_id + _168;
_170 = *_169;
if (_170 == 10)
goto %bb 103
else
goto %bb 104
57.68%
42.32%
FREQ:59
%bb 103:
*_169 = 0;
goto %bb 104
100%
FREQ:30
%bb 104:
%bb 104:
_171 = strlen (&%cpu_MHz);
_172 = _171 + 18446744073709551615;
_173 = &%cpu_MHz + _172;
_174 = *_173;
if (_174 == 10)
goto %bb 105
else
goto %bb 106
57.68%
42.32%
FREQ:59
%bb 105:
*_173 = 0;
goto %bb 106
100%
FREQ:30
%bb 106:
%bb 106:
_175 = strlen (&%cache);
_176 = _175 + 18446744073709551615;
_177 = &%cache + _176;
_178 = *_177;
if (_178 == 10)
goto %bb 107
else
goto %bb 108
57.68%
42.32%
FREQ:59
%bb 107:
*_177 = 0;
goto %bb 108
100%
FREQ:30
%bb 108:
%bb 108:
_179 = strlen (&%cpu_MHz);
_180 = _179 + 18446744073709551615;
_181 = &%cpu_MHz + _180;
_182 = *_181;
if (_182 == 10)
goto %bb 109
else
goto %bb 110
57.68%
42.32%
FREQ:59
%bb 109:
*_181 = 0;
goto %bb 110
100%
FREQ:30
%bb 110:
%bb 110:
_183 = strlen (&%model_name);
_184 = _183 + 18446744073709551615;
_185 = &%model_name + _184;
_186 = *_185;
if (_186 == 10)
goto %bb 111
else
goto %bb 112
57.68%
42.32%
FREQ:59
%bb 111:
*_185 = 0;
goto %bb 112
100%
FREQ:30
%bb 112:
%bb 112:
_187 = strlen (&%vendor_id);
_188 = _187 + 18446744073709551615;
_189 = &%vendor_id + _188;
_190 = *_189;
if (_190 == 10)
goto %bb 113
else
goto %bb 114
57.68%
42.32%
FREQ:59
%bb 113:
*_189 = 0;
goto %bb 114
100%
FREQ:30
%bb 114:
%bb 114:
_191 = strlen (&%cache);
_192 = _191 + 18446744073709551615;
_193 = &%cache + _192;
_194 = *_193;
if (_194 == 10)
goto %bb 115
else
goto %bb 116
57.68%
42.32%
FREQ:59
%bb 115:
*_193 = 0;
goto %bb 116
100%
FREQ:30
%bb 116:
%bb 116:
_195 = strlen (&%cpu_MHz);
_196 = _195 + 18446744073709551615;
_197 = &%cpu_MHz + _196;
_198 = *_197;
if (_198 == 10)
goto %bb 117
else
goto %bb 118
57.68%
42.32%
FREQ:59
%bb 117:
*_197 = 0;
goto %bb 118
100%
FREQ:30
%bb 118:
%bb 118:
_199 = strlen (&%model_name);
_200 = _199 + 18446744073709551615;
_201 = &%model_name + _200;
_202 = *_201;
if (_202 == 10)
goto %bb 119
else
goto %bb 120
57.68%
42.32%
FREQ:59
%bb 119:
*_201 = 0;
goto %bb 120
100%
FREQ:30
%bb 120:
%bb 120:
_203 = strlen (&%vendor_id);
_204 = _203 + 18446744073709551615;
_205 = &%vendor_id + _204;
_206 = *_205;
if (_206 == 10)
goto %bb 121
else
goto %bb 122
57.68%
42.32%
FREQ:59
%bb 121:
*_205 = 0;
goto %bb 122
100%
FREQ:30
%bb 122:
%bb 122:
_207 = strlen (&%cache);
_208 = _207 + 18446744073709551615;
_209 = &%cache + _208;
_210 = *_209;
if (_210 == 10)
goto %bb 123
else
goto %bb 124
57.68%
42.32%
FREQ:59
%bb 123:
*_209 = 0;
goto %bb 124
100%
FREQ:30
%bb 124:
%bb 124:
_211 = strlen (&%cpu_MHz);
_212 = _211 + 18446744073709551615;
_213 = &%cpu_MHz + _212;
_214 = *_213;
if (_214 == 10)
goto %bb 125
else
goto %bb 126
57.68%
42.32%
FREQ:59
%bb 125:
*_213 = 0;
goto %bb 126
100%
FREQ:30
%bb 126: