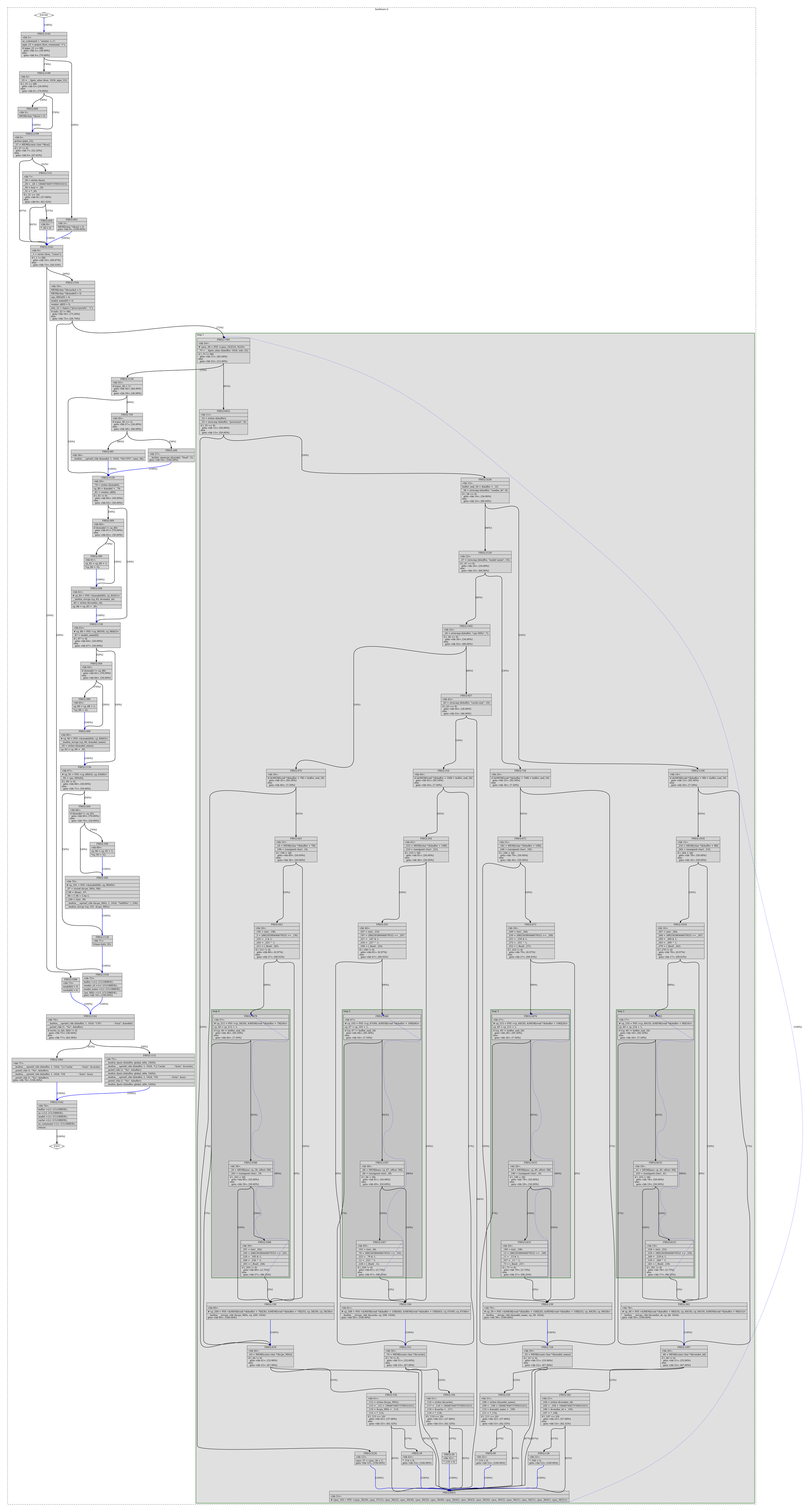
hardware ()
ENTRY
100%
FREQ:3142
%in = constant u = constant u[...
%pw_32 = popen (%cm_command, "r");
if (pipe_32 == 0)
goto <bb 3>; [10.00%]
else
goto <bb 4>; [90.00%]
90%
10%
FREQ:2199
<bb 4>:
_33 = __fgets_alias (&cm, 1024, pipe_32);
if (_33 == 0B)
goto <bb 5>; [30.00%]
else
goto <bb 6>; [70.00%]
30%
70%
FREQ:660
<bb 5>:
MEM[(char *)&cm] = 0;
100%
FREQ:2199
<bb 6>:
pclose (pipe_32);
_37 = MEM[(const char *)&cm];
if (_37 == 0)
goto <bb 7>; [52.35%]
else
goto <bb 9>; [47.65%]
47%
53%
FREQ:1151
<bb 7>:
_28 = strlen (&cm);
_29 = _28 + 18446744073709551615;
_30 = &cm + _29;
_31 = *_30;
if (_31 == 10)
goto <bb 8>; [37.68%]
else
goto <bb 9>; [62.32%]
62%
38%
FREQ:434
<bb 8>:
*_30 = 0;
FREQ:943
<bb 3>:
MEM[(char *)&cm] = 0;
goto <bb 9>; [100.00%]
100%
100%
FREQ:2142
<bb 9>:
_1 = strlen (&cm, "Linux");
if (_1 != 0B)
goto <bb 10>; [49.47%]
goto <bb 72>; [50.53%]
49%
51%
FREQ:1554
<bb 10>:
MEM[(char *)&cache] = 0;
MEM[(char *)&model] = 0;
cpu_MHz01 = 0;
model_name01 = 0;
vendor_id01 = 0;
_2 = popen ("/proc/cpuinfo", "r");
if (pipe_32 == 0B)
goto <bb 14>; [73.26%]
else
goto <bb 72>; [26.74%]
73%
27%
loop 1
FREQ:1594
<bb 16>:
# cpus_36 = PHI <cpus_163(53), 0(10)>
_75 = __fgets_alias (&buffer, 1024, pipe_32);
if (_75 != 0B)
goto <bb 17>; [95.00%]
else
goto <bb 15>; [5.00%]
95%
5%
FREQ:1139
<bb 17>:
_76 = strlen (&buffer);
if (_76 == 1)
goto <bb 54>; [64.00%]
goto <bb 53>; [36.00%]
64%
36%
FREQ:729
<bb 54>:
if (cpus_36 == 0)
goto <bb 57>; [54.00%]
goto <bb 56>; [46.00%]
46%
54%
FREQ:1481
<bb 58>:
__builtin__sprintf_chk (&model, 1, 1024, "%d CPU", cpus_36);
100%
FREQ:1139
<bb 59>:
_79 = strlen (&model);
if (_79 == 0)
goto <bb 60>; [10.00%]
else
goto <bb 61>; [90.00%]
10%
90%
FREQ:569
<bb 60>:
if (&model != cpu_80)
goto <bb 61>; [20.00%]
else
goto <bb 62>; [80.00%]
20%
80%
FREQ:599
<bb 63>:
cpu_82 = cpu_80 + 1;
*cpu_80 = 32;
100%
FREQ:569
<bb 62>:
cpu_83 = PHI <&model(60), cpu_82(63)>
__builtin_strcpy (cpu_83, &vendor_id);
_92 = strlen (&vendor_id);
cpu_80 = cpu_83 + _92;
100%
FREQ:1158
<bb 55>:
# cpu_86 = PHI <cpu_80(55), cpu_80(62)>
_87 = model_name01;
if (_87 != 0)
goto <bb 64>; [10.00%]
else
goto <bb 67>; [90.00%]
10%
90%
FREQ:569
<bb 64>:
if (&model != cpu_86)
goto <bb 65>; [70.00%]
else
goto <bb 66>; [30.00%]
70%
30%
FREQ:599
<bb 65>:
cpu_88 = cpu_86 + 1;
*cpu_86 = 32;
100%
FREQ:569
<bb 66>:
cp_90 = PHI <&model(64), cp_89(65)>
__builtin_strcpy (cp_90, &model_name);
_93 = strlen (&model_name);
cp_91 = cp_90 + _93;
100%
FREQ:1139
<bb 67>:
# cp_95 = PHI <cp_89(65), cp_93(66)>
_96 = cpu_MHz01;
if (_96 != 0)
goto <bb 68>; [50.00%]
else
goto <bb 72>; [50.00%]
50%
50%
FREQ:569
<bb 68>:
if (cp_95 != cp_91)
goto <bb 69>; [70.00%]
else
goto <bb 70>; [30.00%]
70%
30%
FREQ:569
<bb 69>:
cp_98 = cp_95 + 1;
*cp_95 = 32;
100%
FREQ:569
<bb 70>:
cp_101 = PHI <&model(68), cp_98(69)>
__builtin_strcpy (cp_101, &cpu_MHz);
_97 = strlen (&cpu_MHz);
cp_99 = cp_101 + _97;
*cp_99 = 58;
_94 = cp_99 + 1;
__builtin__sprintf_chk (cp_94, 1, 1024, "%dMHz", 1, 100);
__builtin_strcpy (cp_105, &cpu_MHz);
100%
FREQ:1139
<bb 71>:
cpu_106 = cpu_105 + 5;
*cpu_105 = 10;
100%
FREQ:1554
<bb 72>:
buffer.0 = (U) CCG88E81;
model_name.1 = (U) CCG88E82;
cpu_MHz.2 = (U) CCG88E81;
vendor_id.3 = (U) CCG88E83;
cache.4 = (U) CCG88E84;
if (model == 0B)
goto <bb 74>; [100.00%]
else
goto <bb 73>; [0.00%]
100%
0%
FREQ:3142
<bb 74>:
__builtin__sprintf_chk (&buffer, 1, 1024, "CPU\t\t: %s\n", &model);
_107 = strlen (&buffer);
if (vector_in_file_3(2) != 0)
goto <bb 75>; [30.36%]
goto <bb 77>; [69.64%]
30%
70%
FREQ:1991
<bb 75>:
__builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t\t: %s\n", &cache);
_108 = strlen (&buffer);
__builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
_109 = strlen (&buffer);
goto <bb 76>; [100.00%]
100%
FREQ:1153
<bb 73>:
__builtin_fputs (&buffer, global_stfile_19203);
__builtin__sprintf_chk (&buffer, 1, 1024, "L2 Cache\t\t: %s\n", &cache);
_110 = strlen (&buffer);
__builtin__sprintf_chk (&buffer, 1, 1024, "OS\t\t: %s\n", &os);
_111 = strlen (&buffer);
__builtin_fputs (&buffer, global_stfile_19203);
100%
FREQ:3142
<bb 76>:
buffer.5 = (U) CCG88E85;
os.6 = (U) CCG88E81;
cache.7 = (U) CCG88E84;
cm_command.8 = (U) CCG88E81;
return;
100%
EXIT
100%
FREQ:1139
<bb 11>:
_112 = strlen (&cpu_MHz);
_113 = _112 + 18446744073709551615;
_114 = &cpu_MHz + _113;
_115 = *_114;
if (_115 == 10)
goto <bb 12>; [37.68%]
else
goto <bb 53>; [62.32%]
62%
38%
FREQ:1226
<bb 12>:
cpus_37 = cpus_36 + 1;
goto <bb 53>; [100.00%]
100%
FREQ:6652
<bb 11>:
_53 = strlen (&buffer);
_55 = strncmp (&buffer, "processor", 9);
if (_55 == 0)
goto <bb 12>; [50.00%]
else
goto <bb 13>; [50.00%]
50%
50%
FREQ:1226
<bb 13>:
buffer_end_34 = &buffer + _53;
_58 = strncmp (&buffer, "vendor_id", 9);
if (_58 == 0)
goto <bb 14>; [34.00%]
else
goto <bb 23>; [66.00%]
34%
66%
FREQ:2129
<bb 23>:
_67 = strncmp (&buffer, "model name", 10);
if (_67 == 0)
goto <bb 24>; [34.00%]
else
goto <bb 33>; [66.00%]
34%
66%
FREQ:1405
<bb 33>:
_56 = strncmp (&buffer, "cpu MHz", 7);
if (_56 == 0)
goto <bb 34>; [34.00%]
else
goto <bb 43>; [66.00%]
34%
66%
FREQ:927
<bb 43>:
_69 = strncmp (&buffer, "cache size", 10);
if (_69 == 0)
goto <bb 44>; [34.00%]
else
goto <bb 53>; [66.00%]
34%
66%
FREQ:478
<bb 38>:
if (&MEM[(void *)&buffer + 78] < buffer_end_34)
goto <bb 40>; [92.00%]
else
goto <bb 40>; [7.50%]
92%
8%
FREQ:1516
<bb 44>:
if (&MEM[(void *)&buffer + 108] < buffer_end_34)
goto <bb 45>; [92.00%]
else
goto <bb 50>; [7.50%]
92%
8%
FREQ:758
<bb 24>:
if (&MEM[(void *)&buffer + 108] < buffer_end_34)
goto <bb 25>; [92.00%]
else
goto <bb 30>; [7.50%]
92%
8%
FREQ:1100
<bb 14>:
if (&MEM[(void *)&buffer + 981] < buffer_end_34)
goto <bb 15>; [92.00%]
else
goto <bb 20>; [7.50%]
92%
8%
FREQ:483
<bb 41>:
_116 = MEM[(char *)&buffer + 78];
_106 = (unsigned char)_116;
if (_106 == 58)
goto <bb 42>; [50.00%]
else
goto <bb 39>; [50.00%]
50%
50%
FREQ:292
<bb 45>:
_213 = MEM[(char *)&buffer + 108];
_210 = (unsigned char)_213;
if (_210 == 58)
goto <bb 46>; [50.00%]
else
goto <bb 48>; [50.00%]
50%
50%
FREQ:472
<bb 25>:
_142 = MEM[(char *)&buffer + 108];
_166 = (unsigned char)_142;
if (_166 == 58)
goto <bb 26>; [50.00%]
else
goto <bb 28>; [50.00%]
50%
50%
FREQ:1018
<bb 15>:
_212 = MEM[(char *)&buffer + 981];
_204 = (unsigned char)_212;
if (_204 == 58)
goto <bb 16>; [50.00%]
else
goto <bb 18>; [50.00%]
50%
50%
FREQ:443
<bb 39>:
_191 = (int) _196;
_3 = 2082130304844079552 >> _191;
_225 = _3 & 1;
_45 = _225 != 0;
_213 = (_Bool) _45;
if (_213 != 0)
goto <bb 40>; [0.97%]
else
goto <bb 37>; [99.03%]
99%
1%
FREQ:252
<bb 48>:
_207 = (int) _210;
_57 = 2082130304844079552 >> _207;
_217 = _57 & 1;
_218 = _217 != 0;
_199 = (_Bool) _218;
if (_199 != 0)
goto <bb 49>; [0.97%]
else
goto <bb 47>; [99.03%]
99%
1%
FREQ:872
<bb 26>:
_249 = (int) _246;
_250 = 2082130304844079552 >> _249;
_151 = _250 & 1;
_272 = _151 != 0;
_252 = (_Bool) _272;
if (_252 != 0)
goto <bb 27>; [0.97%]
else
goto <bb 27>; [99.03%]
99%
1%
FREQ:1058
<bb 16>:
_207 = (int) _204;
_208 = 2082130304844079552 >> _207;
_269 = _208 & 1;
_263 = _269 != 0;
_270 = (_Bool) _263;
if (_270 != 0)
goto <bb 17>; [0.97%]
else
goto <bb 17>; [99.03%]
99%
1%
loop 4
FREQ:2676
<bb 37>:
# _39 = PHI <_39(37), 0(39)>
# cp_212 = PHI <cp_58(39), &MEM[(void *)&buffer + 78](38)>
_40 = (int) _39;
if (cp_58 != buffer_end_34)
goto <bb 38>; [95.50%]
else
goto <bb 40>; [4.50%]
95%
5%
FREQ:2086
<bb 39>:
_59 = MEM[(unsigned char *)cp_58];
_60 = (unsigned char)_59;
if (_60 == 58)
goto <bb 40>; [50.00%]
else
goto <bb 37>; [50.00%]
50%
50%
FREQ:3586
<bb 40>:
# cp_61 = PHI <cp_58(38), cp_58(39), cp_58(39)>
# _62 = PHI <_40(38), _40(39), _40(39)>
_63 = (long unsigned int) _62;
_64 = _63 * 4;
_65 = cpu_MHz01 + _64;
_66 = *_65;
if (_66 == 0)
goto <bb 41>; [10.00%]
else
goto <bb 42>; [90.00%]
10%
90%
FREQ:158
<bb 42>:
# _67 = PHI <_66(40), 0(41)>
*_65 = _67;
goto <bb 43>; [100.00%]
100%
loop 5
FREQ:2660
<bb 47>:
# _41 = PHI <_41(47), 0(48)>
# cp_213 = PHI <cp_87(48), &MEM[(void *)&buffer + 108](46)>
_42 = (int) _41;
if (cp_87 != buffer_end_34)
goto <bb 48>; [95.50%]
goto <bb 50>; [4.50%]
95%
5%
FREQ:2367
<bb 49>:
_68 = MEM[(unsigned char *)cp_87];
_69 = (unsigned char)_68;
if (_69 == 58)
goto <bb 50>; [50.00%]
else
goto <bb 47>; [50.00%]
50%
50%
FREQ:158
<bb 50>:
# cp_209 = PHI <cp_87(48), cp_87(49), &MEM[(void *)&buffer + 108](44)>
# _208 = PHI <_42(48), _42(49), 0(44)>
_210 = (long unsigned int) _208;
_211 = _210 * 4;
_212 = cpu_MHz01 + _211;
_213 = *_212;
if (_213 == 0)
goto <bb 51>; [10.00%]
else
goto <bb 52>; [90.00%]
10%
90%
FREQ:158
<bb 52>:
# _214 = PHI <_213(50), 0(51)>
*_212 = _214;
goto <bb 53>; [100.00%]
100%
loop 3
FREQ:2676
<bb 27>:
# _43 = PHI <_43(27), 0(28)>
# cp_253 = PHI <cp_49(28), &MEM[(void *)&buffer + 108](26)>
_44 = (int) _43;
if (cp_49 != buffer_end_34)
goto <bb 28>; [95.50%]
goto <bb 30>; [4.50%]
95%
5%
FREQ:2086
<bb 28>:
_50 = MEM[(unsigned char *)cp_49];
_51 = (unsigned char)_50;
if (_51 == 58)
goto <bb 30>; [50.00%]
else
goto <bb 27>; [50.00%]
50%
50%
FREQ:3433
<bb 30>:
# cp_52 = PHI <cp_49(28), cp_49(29), cp_49(29)>
# _53 = PHI <_44(28), _44(29), _44(29)>
_54 = (long unsigned int) _53;
_55 = _54 * 4;
_56 = cpu_MHz01 + _55;
_57 = *_56;
if (_57 == 0)
goto <bb 31>; [10.00%]
else
goto <bb 32>; [90.00%]
10%
90%
FREQ:158
<bb 32>:
# _58 = PHI <_57(30), 0(31)>
*_56 = _58;
goto <bb 33>; [100.00%]
100%
loop 2
FREQ:2676
<bb 17>:
# _217 = PHI <_217(17), 0(18)>
# cp_214 = PHI <cp_69(18), &MEM[(void *)&buffer + 981](16)>
_218 = (int) _217;
if (cp_69 != buffer_end_34)
goto <bb 18>; [95.50%]
goto <bb 20>; [4.50%]
95%
5%
FREQ:2086
<bb 18>:
_41 = MEM[(unsigned char *)cp_69];
_215 = (unsigned char)_41;
if (_215 == 58)
goto <bb 20>; [50.00%]
else
goto <bb 17>; [50.00%]
50%
50%
FREQ:3233
<bb 20>:
# cp_216 = PHI <cp_69(18), cp_69(19), cp_69(19)>
# _217 = PHI <_218(18), _218(19), _218(19)>
_219 = (long unsigned int) _217;
_220 = _219 * 4;
_221 = cpu_MHz01 + _220;
_222 = *_221;
if (_222 == 0)
goto <bb 21>; [10.00%]
else
goto <bb 22>; [90.00%]
10%
90%
FREQ:158
<bb 22>:
# _223 = PHI <_222(20), 0(21)>
*_221 = _223;
goto <bb 23>; [100.00%]
100%
FREQ:3555
<bb 53>:
cpus_163 = PHI <cpus_36(20), cpus_37(22), cpus_36(24), cpus_36(25), cpus_36(40), cpus_36(42), cpus_36(43), cpus_36(50), cpus_36(52), cpus_36(27), cpus_36(33), cpus_36(45), cpus_36(53)>
100%