SCHEMATIC DIAGRAMS

PRODUCT NAME AP04A STM32 MCU 2Bus

UNIT NAME _____

UNIT NO. DTNSTM32MCU2B1

PWA NAME _____

PWA NO. _____

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STM32L452REI6

MCU
[DU32L452A]
IC750

751 752 RESET

100nF(K)25V
C75B

GND

E1

NRST

PA0   G2   A1   752
PA1   H2   A2   752
PA2   F3   D1   752
PA3   G3   D0   752
PA4   H3   A0   752
PA5   F4   D13 752
PA6   G4   D12 752
PA7   H4   D11 752
PA8   D7   D6   752
PA9   C7   D8   752
PA10   C6   PA10   751
PA11   C8   B-D3 752
PA12   B8   B-D2 752
PA13(JTMS/SWDIO) A8   SWDIO 752
PA14(JTCK/SWCLK) A7   SWCLK 752
PA15(JTDI)   A6   NC

PB0   F5   A3   752
PB1   G5   B-A4 752
PB2   G6   B-A5 752
PB3(JTDO/TRACESWO) A5   D3   752
PB4(NJTRST)   A4   D5   752
PB5   C4   D4   752
PB6   D3   D10 752
PB7   C3   B-D4 752
PB8   B3   SCL   750 752
PB9   A3   SDA   750 752
PB10   G7   B-SCL 750 752
PB11   H7   B-SDA 750 752
PB12   H8   D7   752
PB13   G8   B-D13 752
PB14   F8   B-D12 752
PB15   F7   B-D11 752

PC0   E3   A5   752
PC1   E2   A4   752
PC2   F2   B-A0 752
PC3   G1   B-A3 752
PC4   H5   B-A1 752
PC5   H6   B-A2 752
PC6   F6   B-D10 752
PC7   E7   D2   752
PC8   E8   B-D5 752
PC9   D8   B-D6 752
PC10   B7   B-D8 752
PC11   B6   PC11   751
PC12   C5   B-D7 752
PC13   A2   B-D1 752
PC14-OSC32_IN(PC14)   A1
PC15-OSC32_OUT(PC15) B1

PD2   B5   B-D0 752

PH0-OSC_IN(PH0)   C1 NC
PH1-OSC_OUT(PH1)   D1 NC
PH3-BOOT0(BOOT0)   B4   BOOT0 751

VDDUSB   E5

VBAT   B2

VDDA/VREF+   H1   VSSA/VREF-   F1

VDD   E4   VSS   C2
VDD   E6   VSS   D4
VDD   D2   VSS   D5
                   VSS   D6

P3R3V
O(X)
R750
O(X)
NOT MOUNT R751   O(X)
752 AREF
NOT MOUNT R752   O(X)
752 B-AREF
FL750

P3R3V

C750
100nF(K)25V
GND

C754
1uF(K)16V
GND

P3R3V

C751   10uF(M)25V
C752   100nF(K)25V
C753   100nF(K)25V
C755   100nF(K)25V
GND

GND

R753
10K(J)
GND

P3R3V
10K(J) R754
10K(J) R755
10K(J) R756
10K(J) R757

750 752 SCL
750 752 SDA

750 752 B-SCL
750 752 B-SDA

NX2012SA-32.768KHZ-EXS00A-MU00530
X750
32.768kHz

6pF(D)50V C756
6pF(D)50V C757
GND

DESIGNED BY
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TITLE
AP04A STM32 MCU 2Bus

FUNCTION
STM32 MCU

SH.NO. 750
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Reset

EVQP7B01P
[DSW28]
S750

750 751 752 RESET

GND

BOOT0

CUS-12TB
[DSW23A]
S751

P3R3V

750 751 BOOT0

BOOT

3
2
1

ON

P3R3V

10k(J)
R759

[MFNGR2]
Q750

GND

TS3A4751RUCR
ANALOG SW
[DL4751]
IC751

IN1 13
IN2 5
IN3 6
IN4 12

750 PA10
750 PC11

100(J)
R762

C763
47pF(J)50V

GND

NO1 1   COM1 2   D9 752
NO2 3   COM2 4   B-D9 752
NO3 8   COM3 9
NO4 11   COM4 10

VCC 14   GND 7

P3R3V

GND

C760
100nF(K)25V

GND

GND

P3R3V

100k(J)
R760

C759
470nF(K)

0(X)
R761

P3R3V

D750
1SS387

RESET 750 751 752

750 751 BOOT0

1K(J)
R758

DS750
SML-A12U8TT86

GND

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
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Leafony Bus-A

VBUS   P3R3V

[DC58E]
CN750

Pin	Signal
F1	3.3V
F2	SWDIO
F3	VBUS
F4	AREF
F5	RESET
F6	A0
F7	D6
F8	A1
F9	D7
F10	A2
F11	D8
F12	A3
F13	D9
F14	A4
F15	D10
F16	A5
F17	D11
F18	D0
F19	D12
F20	D1
F21	D13
F22	D2
F23	SDA
F24	D3
F25	SCL
F26	D4
F27	GND
F28	D5
F29	SWCLK
B1	3.3V
B2	SWDIO
B3	VBUS
B4	AREF
B5	RESET
B6	A0
B7	D6
B8	A1
B9	D7
B10	A2
B11	D8
B12	A3
B13	D9
B14	A4
B15	D10
B16	A5
B17	D11
B18	D0
B19	D12
B20	D1
B21	D13
B22	D2
B23	SDA
B24	D3
B25	SCL
B26	D4
B27	GND
B28	D5
B29	SWCLK

Net labels (source 750 / 751 / 752):
SWDIO, AREF, RESET, A0, D6, A1, D7, A2, D8, A3, D9, A4, D10, A5, D11, D0, D12, D1, D13, D2, SDA, D3, SCL, D4, D5, SWCLK

GND

Leafony Bus-B

VBUS   P3R3V

[DC58E]
CN751

Pin	Signal
F1	3.3V
F2	SWDIO
F3	VBUS
F4	AREF
F5	RESET
F6	A0
F7	D6
F8	A1
F9	D7
F10	A2
F11	D8
F12	A3
F13	D9
F14	A4
F15	D10
F16	A5
F17	D11
F18	D0
F19	D12
F20	D1
F21	D13
F22	D2
F23	SDA
F24	D3
F25	SCL
F26	D4
F27	GND
F28	D5
F29	SWCLK
B1	3.3V
B2	SWDIO
B3	VBUS
B4	AREF
B5	RESET
B6	A0
B7	D6
B8	A1
B9	D7
B10	A2
B11	D8
B12	A3
B13	D9
B14	A4
B15	D10
B16	A5
B17	D11
B18	D0
B19	D12
B20	D1
B21	D13
B22	D2
B23	SDA
B24	D3
B25	SCL
B26	D4
B27	GND
B28	D5
B29	SWCLK

Net labels (source 750 / 751 / 752):
B-AREF, RESET, B-A0, B-D6, B-A1, B-D7, B-A2, B-D8, B-A3, B-D9, B-A4, B-D10, B-A5, B-D11, B-D0, B-D12, B-D1, B-D13, B-D2, B-SDA, B-D3, B-SCL, B-D4, B-D5

GND

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