





Fast Client-Driven CFL-Reachability via Regularization-Based Graph Simplification

CHENGHANG SHI, Institute of Computing Technology, CAS, China and University of Chinese Academy of Sciences, China

DONGJIE HE, Chongqing University, China

HAOFENG LI*, Institute of Computing Technology, CAS, China

JIE LU, Institute of Computing Technology, CAS, China

LIAN LI*, Institute of Computing Technology, CAS, China, University of Chinese Academy of Sciences, China, and Zhongguancun Laboratory, China

JINGLING XUE, University of New South Wales, Australia

Context-free language (CFL) reachability is a critical framework for various program analyses, widely adopted despite its computational challenges due to cubic or near-cubic time complexity. This often leads to significant performance degradation in client applications. Notably, in real-world scenarios, clients typically require reachability information only for specific source-to-sink pairs, offering opportunities for targeted optimization.

We introduce MoYE, an effective regularization-based graph simplification technique designed to enhance the performance of client-driven CFL-reachability analyses by pruning non-contributing edges—those that do not participate in any specified CFL-reachable paths. MoYE employs a regular approximation to ensure exact reachability results for all designated node pairs and operates linearly with respect to the number of edges in the graph. This lightweight efficiency makes MoYE a valuable pre-processing step that substantially reduces both computational time and memory requirements for CFL-reachability analysis, outperforming a recent leading graph simplification approach. Our evaluations with two prominent CFL-reachability client applications demonstrate that MoYE can substantially improve performance and reduce resource consumption.

CCS Concepts: \bullet Theory of computation \rightarrow Grammars and context-free languages.

Additional Key Words and Phrases: CFL-Reachability, Graph Simplification

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1 Introduction

CFL-reachability [Reps 1998; Yannakakis 1990] is a foundational framework used to formalize a wide array of program analysis tasks, including pointer analysis [He et al. 2023; Lu and Xue 2019; Sridharan et al. 2005], inter-procedural data flow analysis [Arzt et al. 2014; Reps et al. 1995], shape

Authors' Contact Information: Chenghang Shi, Institute of Computing Technology, CAS, Beijing, China and University of Chinese Academy of Sciences, Beijing, China, shichenghang21s@ict.ac.cn; Dongjie He, Chongqing University, Chongqing, China, dongjiehe@cqu.edu.cn; Haofeng Li, Institute of Computing Technology, CAS, Beijing, China, lihaofeng@ict.ac.cn; Jie Lu, Institute of Computing Technology, CAS, Beijing, China Li, Institute of Computing Technology, CAS, Beijing, China and University of Chinese Academy of Sciences, Beijing, China and Zhongguancun Laboratory, Beijing, China, lianli@ict.ac.cn; Jingling Xue, University of New South Wales, Sydney, Australia, j.xue@unsw.edu.au.



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^{*}Corresponding authors

analysis [Reps 1998], and program slicing [Li et al. 2016; Reps et al. 1994; Sridharan et al. 2007]. This approach models program analysis by utilizing a context-free language L and represents the analyzed program as an edge-labeled graph G. It determines if, for every potential source-to-sink pair, there exists a path p in G where the sequence of labels along p is a string in L.

CFL-reachability is computationally demanding, exhibiting classic cubic time complexity of $O(|V|^3)$, where V denotes the node set of G. Although a slightly subcubic algorithm with a complexity of $O(|V|^3/\log(|V|))$ exists [Chaudhuri 2008], achieving truly subcubic bounds of $O(|V|^{3-\varepsilon})$, where $\varepsilon > 0$, for general CFL-reachability is notably challenging [Melski and Reps 2000] and continues to be an unresolved problem [Chatterjee et al. 2017]. Despite the introduction of several recent acceleration techniques [Lei et al. 2022a; Shi et al. 2023, 2022; Wang et al. 2017], the performance gains provided still fall short of the needs of client applications that rely on CFL-reachability.

In this paper, we introduce MoYE, a novel approach to optimizing CFL-reachability analysis for client-specific applications. Our primary insight is that clients often need specific source-to-sink reachability information, rather than all possible pairs, which traditional CFL-reachability algorithms [Lei et al. 2022a; Shi et al. 2023, 2022; Zheng and Rugina 2008] process, causing considerable inefficiencies by including numerous extraneous paths. Conventional graph simplification methods [Hardekopf and Lin 2007; Rountev and Chandra 2000], although effective, still retain unnecessary reachability information. A recent state-of-the-art technique, GF [Lei et al. 2023b], improves efficiency by contracting trivial edges—those irrelevant to any reachable paths—and merging their incident nodes. Diverging from these methods, MoYE exclusively focuses on pruning irrelevant paths, substantially reducing unnecessary computations and enhancing performance.

To achieve this, MoYE uses a unique graph simplification strategy to enhance efficiency. During pre-processing for client-driven CFL-reachability, it removes *non-contributing* (or *useless*) edges—those not part of any source-to-sink paths specified by clients. CFL-reachability algorithms then analyze this simplified graph to determine reachability. By eliminating only these edges, MoYE improves analysis efficiency while maintaining result accuracy for clients.

Addressing the challenges of crafting an effective yet lightweight graph simplification technique, we employ a regular approximation, L', of the context-free language L. This approximation covers all path strings of L and strategically targets the removal of non-contributing edges. By leveraging the relationship between L'-reachability and the transitions of its associated finite automaton, we have developed a novel regularization-based graph simplification algorithm. Operating in linear time relative to the number of edges in the input graph G, this algorithm adeptly eliminates non-contributing edges, ensuring both efficiency and effectiveness in addressing pre-analysis tasks.

We have developed MoYE, a standalone tool that employs our regularization-based graph simplification technique for two significant client analyses: points-to analysis in Java and alias analysis in C/C++. Our extensive testing shows that MoYE significantly speeds up CFL-reachability resolution by removing many non-essential edges, outperforming GF [Lei et al. 2023b], the current leading method. In points-to analysis, MoYE achieves node and edge reductions of 63.82% and 70.81%, respectively, resulting in a speedup of 6.99× and a memory reduction of 71.65%. Similarly, in alias analysis, it reduces 59.14% of nodes and 65.36% of edges, leading to a speedup of 15.72× and a memory reduction of 80.95%. In comparison, GF achieves node and edge reductions of 40.22% (39.84%) and 24.44% (36.89%) respectively in these analyses, with speedups of 4.36× (4.21×) and memory reductions of 56.7% (44.28%). Additionally, integrating MoYE with GF enhances reductions and performance gains in both analyses. Our experiments in a batch setting also confirm that MoYE further enhances the performance of CFL-reachability analysis.

MoYE represents the first regularization-based graph simplification approach specifically designed to enhance CFL-reachability analysis by removing non-contributing edges related to client-specified source-to-sink pairs. In summary, this paper presents the following principal contributions:

- We introduce MoYE, a novel regularization-based graph simplification approach that enhances CFL-reachability analysis by selectively removing non-contributing edges from the input graph of a program, while delivering exact reachability results.
- We develop MoYE as a lightweight regularization-based pre-analysis technique that operates efficiently in linear time relative to the input graph size.
- We implement MoYE as a standalone tool and validate its utility through its application to two common CFL-reachability client analyses, covering both Java and C/C++ programs.
- We demonstrate that MoYE significantly reduces both computational time and memory overhead in CFL-reachability analysis by minimizing graph size through extensive testing, outperforming the current leading graph simplification technique, GF [Lei et al. 2023b]. Additionally, we demonstrate that MoYE complements existing simplification methods, especially when combined with GF, achieving even greater performance enhancements.

The rest of this paper is structured as follows. Section 2 provides background on CFL-reachability and introduces a motivating example. Section 3 outlines the specific problem this paper addresses. Section 4 elaborates on our regularization-based approach for identifying contributing edges in client-specified source-to-sink CFL-reachability. Section 5 details our regularization-based graph simplification algorithm. Section 6 presents our evaluation results. Section 7 reviews related work in the field. Section 8 concludes the paper and explores potential directions for future research.

2 Background and Motivation

We start with a review of CFL-reachability (Sections 2.1 and 2.2) and present an illustrative example (Section 2.3) that highlights our research motivation. In the final section on graph simplification (Section 2.4), we discuss prior work and introduce our regularization-based approach, emphasizing the challenges addressed and the innovative solutions proposed.

2.1 Context-Free Language

A context-free language (CFL) L is defined by the strings generated from a context-free grammar (CFG). A CFG is characterized by a 4-tuple (N, Σ, P, S) , where N and Σ are disjoint finite sets of nonterminals and terminals, respectively, with Σ also referred to as the *alphabet* of the language. The set P contains a finite number of production rules, each formatted as $N \to (N \cup \Sigma)^*$. Here, $S \in N$ serves as the start (nonterminal) symbol of the grammar.

Following [Mohri and Nederhof 2001], we define the *productions of A* as those productions whose left-hand side is $A \in N$. For any subset $N' \subseteq N$, the *productions of N'* are defined as the union of all rules whose left-hand sides belong to any $A \in N'$.

A grammar is *left-linear* (*right-linear*) if all its productions in P are of the form $A \to B \omega$ ($A \to \omega B$) or $A \to \omega$, where $A, B \in N$ and $\omega \in \Sigma^*$. A context-free language L is considered a *regular language* if its corresponding CFG is either left-linear or right-linear.

2.2 CFL-Reachability

CFL-reachability is a widely used framework for various program analyses [Reps 1998]. In this framework, a CFG is used to formalize an analysis problem, and the program being analyzed is represented as an edge-labeled graph G = (V, E), with V and E denoting the sets of nodes and edges, respectively. Each edge in G is labeled with a terminal $t \in \Sigma$.

Path Strings. Consider a path p in G, represented as $v_0 \xrightarrow{t_1} v_1 \xrightarrow{t_2} \dots \xrightarrow{t_k} v_k$, where each t_i for $i \in [1, k]$ is a terminal in Σ . The *realized path string*, R(p), is the sequence obtained by concatenating the labels of the edges along the path, i.e., $R(p) = t_1 \dots t_k$.

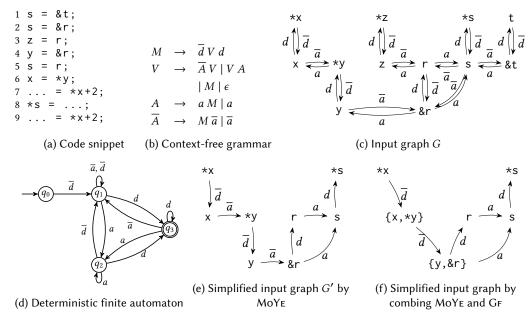


Fig. 1. A motivating example.

Reachable Paths/Pairs. A node v is said to be X-reachable from a node u if there exists a path p from u to v in G such that the path string R(p) belongs to the language generated by the nonterminal X in the grammar. Formally, this is expressed as $X \to^* R(p)$, where \to^* indicates zero or more applications of productions from P. Such a path, referred to as an X-path, is depicted as a summary edge in G, labeled by X to explicitly encode the reachability information. In the special case when X is the start symbol (i.e., X = S), node v is described as L-reachable from node u, with the path p termed an L-path. Consequently, the pair (u, v) is defined as an L-reachable pair. A summary edge can be derived multiple times through distinct reachable paths, with each path corresponding to a distinct derivation check [Lei et al. 2022a; Shi et al. 2023].

Essentially, CFL-reachability algorithms [Lei et al. 2022a; Shi et al. 2023, 2022] iteratively discover new reachable paths and generate summary edges in G. This process continues until no further paths can be discovered, indicating that a fixed point has been reached. The overall time complexity of this process is (sub)cubic relative to |V| [Chaudhuri 2008].

2.3 A Motivating Example

Consider the code snippet in Figure 1a as part of an available expression analysis. This analysis determines whether the expression *x+2 at line 9 can reuse the value computed at line 7 or if it requires recomputation. The reusability of *x+2 hinges on whether *x remains unchanged from line 7 to line 9. A crucial *alias query* then emerges: Are the memory locations *x and *s aliased? If they are, the assignment to *s at line 8 might modify *x, thereby rendering the expression *x+2, previously computed at line 7, unavailable at line 9.

This alias query can be addressed using CFL-reachability-based alias analysis [Zheng and Rugina 2008]. Figure 1b illustrates the grammar defining alias relations between program expressions. In this grammar, the terminals d, a, and ϵ symbolize pointer dereferencing, assignment, and the empty string, respectively. The nonterminals include M (memory aliasing), indicating expressions that may refer to the same memory location; V (value aliasing), signifying expressions that may

Step	Current String	Production
0	M	$M \to \overline{d} \ V \ d$
1	$\overline{d} V d$	$V \to \overline{A} V$
2	$\overline{d} \ \overline{A} \ V \ d$	$\overline{A} \to \overline{a}$
3	$\overline{d} \ \overline{a} \ V \ d$	$V \to V A$
4	$\overline{d} \overline{a} V A d$	$V \to M$
5	$\overline{d} \ \overline{a} \ M \ A \ d$	$M \to \overline{d} \ V \ d$
6	$\overline{d} \ \overline{a} \ \overline{d} \ V \ d \ A \ d$	$V \to \overline{A} V$
7	$\overline{d} \ \overline{a} \ \overline{d} \ \overline{A} \ V \ d \ A \ d$	$\overline{A} \to \overline{a}$
8	$\overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ V \ d \ A \ d$	$V \to \epsilon$
9	$\overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ d \ A \ d$	$A \rightarrow a$
10	$\overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ d \ a \ d$	

Table 1. Step-by-step derivation of $M \to^* \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ d \ a \ d$ for the motivating example in Figure 1.

evaluate to a common pointer; and A, which captures both direct $(A \to a)$ and indirect $(A \to aM)$ assignments. Additionally, for any symbol X, \overline{X} denotes the *inverse relation* of X. For additional details about this grammar, please refer to [Zheng and Rugina 2008].

Figure 1c displays the input graph G extracted from the code snippet in Figure 1a, where each node corresponds to an expression in the code. This graph consists of four types of edges: a-edges, \overline{a} -edges and \overline{d} -edges. The alias query, as discussed earlier, is resolved by determining the reachability from the source node *x to the sink node *s in G.

In this example, *x and *s are memory aliases because the following path exists in *G*:

$$*x \xrightarrow{\overline{d}} x \xrightarrow{\overline{a}} *y \xrightarrow{\overline{d}} y \xrightarrow{\overline{a}} &r \xrightarrow{d} r \xrightarrow{a} s \xrightarrow{d} *s$$
 (1)

The corresponding path string \overline{d} \overline{a} \overline{d} \overline{a} d a d can be derived from the nonterminal M, as shown in Table 1, indicating that *s is M-reachable from *x. Consequently, in Figure 1a, the assignment at line 8 invalidates the availability of the expression *x+2 at line 9.

2.4 Graph Simplification

We aim to develop a graph simplification technique that accelerates CFL-reachability analysis by reducing the input graph size. As shown in Figure 1, our alias analysis query requires establishing reachability only between *x and *s. Therefore, any simplification that maintains this specific reachability can effectively optimize the analysis for this client query. Below, we will review prior work and elaborate on our approach, focusing on the challenges and our innovative solution.

Prior Work. Existing graph simplification techniques often retain much irrelevant reachability information, leading to only modest reductions in graph size. For instance, the classic cycle elimination technique [Hardekopf and Lin 2007] does not apply in our motivating example because it may lead to missing *A*-edges produced by the production $A \rightarrow a M$, which relies on transitive *a*-edges, potentially compromising analysis soundness [Xu et al. 2024]. Moreover, *a*-edges do not form cycles in *G*. A recent method known as Graph Folding (GF) [Lei et al. 2023b], guided by recursive state machines, identifies foldable adjacent nodes by analyzing their incoming and outgoing edges. Adjacent node pairs are then merged, and all connecting edges considered trivial—primarily transitive edges in practical applications—are removed, since their contraction preserves CFL-reachability. In Figure 1, GF contracts only three *a*-edges (and their reverse edges): *y $\stackrel{a}{\rightarrow}$ x, &r $\stackrel{a}{\rightarrow}$ y, and r $\stackrel{a}{\rightarrow}$ z, achieving a 25% reduction in nodes and 25% reduction in edges. However, GF cannot contract r $\stackrel{a}{\rightarrow}$ s, &r $\stackrel{a}{\rightarrow}$ s, and &t $\stackrel{a}{\rightarrow}$ s due to the multiple incoming *a*-edges at s.

Our Work. Unlike existing graph simplification techniques [Hardekopf and Lin 2007; Lei et al. 2023b], MoYE focuses on simplifying the input graph G by preserving primarily the reachability information relevant to client needs. This strategy allows for the removal of many edges that do not contribute to the specific CFL-reachability paths of interest, though some redundancy may still remain. Figure 1e illustrates the resulting simplified graph G', which preserves those edges that form the L-reachable path from *x to *s (Equation (1)), removing 16 out 17 non-contributing edges for this particular example. However, the edge &r $\stackrel{a}{\rightarrow}$ s is conservatively retained by MoYE due to its approximation approach. Utilizing the simplified G' instead of the original G substantially enhances CFL-reachability analysis efficiency, applicable to both whole-program and demanddriven scenarios. For instance, an alias query starting at *x in G would unnecessarily traverse to *z, whereas in G', with *z, z, and associated edges removed, such irrelevant paths are eliminated.

Challenges. Designing MoYE as a pre-analysis presents two key challenges: maximizing the removal of non-contributing edges to minimize the computational and memory overhead of CFL-reachability analysis while ensuring the pre-analysis remains lightweight. Ideally, we would precisely identify contributing edges and remove all others. A naive approach would compute all CFL-reachable paths and mark non-contributing edges, but this would defeat the purpose, as it requires solving CFL-reachability upfront, incurring significant computational costs.

Key Idea. MoYE over-approximates the context-free language in Figure 1b with a regular language, which is then converted into a deterministic finite automaton (DFA) \mathcal{D} , as shown in Figure 1d. By analyzing the DFA's transition rules, it over-approximately identifies contributing edges and removes non-contributing edges from G under-approximately. The DFA has a small number of states, comparable to the nonterminals in the original CFG, ensuring efficient computation.

Let us summarize four key advantages offered by MoYE in the context of our motivating example:

- (1) **Effectiveness**. MoYE efficiently removes all non-contributing edges, achieving a reduction of 33.33% (4 out of 12) of nodes and 66.67% (16 out of 24) of edges, as shown in Figure 1e. Nodes such as z and &t are automatically removed as they no longer have incident edges after simplification. In contrast, GF [Lei et al. 2023b] only eliminates only 25% of nodes and 25% of edges through edge contraction. Notably, in the evaluation presented in Section 6, MoYE consistently outperforms GF in speeding up CFL-reachability analysis, often significantly.
- (2) **Preservation**. MoYE simplifies the input graph G into G' while preserving the reachability needed for the specific alias query considered. The M-path from *x to *s given in Equation (1) is retained in G' (Figure 1e), ensuring the reachability results remain unchanged.
- (3) **Efficiency**. MoYE operates with a time complexity linear to the number of edges in the input graph *G*, compared to the cubic complexity of CFL-reachability algorithms, making it highly efficient as a pre-analysis step to accelerate these algorithms.
- (4) **Compatibility**. MoYE is compatible with existing methods like GF [Lei et al. 2023b], enhancing graph simplification. GF can contract contributing edges (e.g., x → *y, y → &r) to shorten paths, while MoYE can remove non-contributing edges that GF cannot contract to prune irrelevant paths. Combining both methods, as shown in Figure 1f, results in a reduction of nodes by 50% and edges by 75%, surpassing the performance of using either technique alone. This synergy will be further analyzed in Section 6.

3 Problem Formulation

In this work, we focus on addressing client-driven CFL-reachability problems, in which the source and sink sets are defined by client applications, as formally stated below.

Definition 3.1 (Client-Driven CFL-Reachability Problem). An instance of a *client-driven CFL-reachability problem* is represented as a quadruple $(L, G, V_{\rm src}, V_{\rm snk})$, where L is a context-free language, G is an edge-labeled graph, and $V_{\rm src} \subseteq V$ and $V_{\rm snk} \subseteq V$ are the sets of source and sink nodes specified by a client. The goal is to compute all L-reachable node pairs $(u, v) \in V_{\rm src} \times V_{\rm snk}$ in G.

This scenario is typical in real-world program analysis tools, where queries are often processed in batches [Vedurada and Nandivada 2020]. For instance, in pointer analysis performed for constructing a call graph in a Java program, only the points-to sets of pointers that act as base variables for virtual invocation sites in the program are required.

Given a specific client analysis, we can formally define the "usefulness" of an edge in *G* by determining whether it contributes to particular source-to-sink reachable paths.

Definition 3.2 (*L*-Contributing Edges). Let $I = (L, G, V_{\rm src}, V_{\rm snk})$ be an instance of a client-driven CFL-reachability problem. An edge $u \stackrel{t}{\to} v \in G$ is defined as an *L*-contributing edge if and only if there exists an *L*-reachable path $v_{\rm src} \to \cdots \to u \stackrel{t}{\to} v \to \cdots \to v_{\rm snk}$ in G, where $v_{\rm src} \in V_{\rm src}$ and $v_{\rm snk} \in V_{\rm snk}$. If no such path exists, the edge is classified as a non-contributing edge of I.

Therefore, not all edges in G are necessary for calculating the desired L-reachable pairs. By removing non-contributing edges, we can improve the performance of CFL-reachability analysis on a simplified graph G', while maintaining exactly the same reachability results for clients.

Example 3.3. Revisiting our motivating example in Figure 1, the edges in the path p in Equation (1) are contributing edges, as they form the M-path from the source node *x to the sink node *s. However, the remaining edges are non-contributing. Although there is an M-path from *x to *z, it is irrelevant to the specific alias analysis considered.

We formulate our graph simplification problem as follows:

Given an instance $(L, G, V_{\text{src}}, V_{\text{snk}})$ of a client-driven CFL-reachability problem, the goal is to produce a simplified graph G' by removing non-contributing edges from G.

The technique proposed in this paper can be used as a pre-processing step for any client-driven CFL-reachability problem, whether applied in a demand-driven manner [Shi et al. 2022; Sridharan et al. 2005; Zheng and Rugina 2008] or for all-pairs reachability [Lei et al. 2022a; Shi et al. 2023].

4 Identifying L-Contributing Edges

Given $(L, G, V_{\text{src}}, V_{\text{snk}})$, we over-approximately identify L-contributing edges by first regularizing L into L' (Section 4.1), and then determining L'-contributing edges for V_{src} and V_{snk} in G (Section 4.2).

4.1 Regularizing L to L'

We first introduce a standard regular approximation technique for a CFL (Section 4.1.1) and then explain how to use it to soundly over-approximate the set of *L*-contributing edges (Section 4.1.2).

4.1.1 MN-Transformation. This represents a simple yet effective algorithm for converting a CFL L into a regular language L' [Mohri and Nederhof 2001].

Given a CFG (N, Σ, P, S) , let \mathcal{R} be the relation defined on its nonterminals $A, B \in N$:

$$A \mathcal{R} B \Leftrightarrow (\exists \alpha, \beta \in (\Sigma \cup N)^*, A \to^* \alpha B \beta) \land (\exists \alpha, \beta \in (\Sigma \cup N)^*, B \to^* \alpha A \beta)$$

Intuitively, \mathcal{R} defines an equivalence relation that partitions the set N of nonterminals into subsets of *mutually recursive nonterminals*. For each partition \mathscr{P} , two steps are applied to convert each production into right-linear form if its productions are not all left-linear or right-linear:

Fig. 2. The regular grammar derived from the context-free grammar given in Figure 1b.

- (1) *Introduce new nonterminal symbols.* For each nonterminal $A \in \mathcal{P}$, introduce a new nonterminal $A' \in N$, and add the ϵ -production $A' \to \epsilon$ to the grammar.
- (2) *Replace productions.* For each production of the form $A \to \alpha_0 B_1 \alpha_1 B_2 \alpha_2 \dots B_m \alpha_m$, where $B_1, \dots, B_m \in \mathcal{P}, \alpha_0, \dots, \alpha_m \in (\Sigma \cup (N \mathcal{P}))^*$, and $m \ge 0$, replace it with:

$$A \to \alpha_0 B_1$$

$$B'_1 \to \alpha_1 B_2$$

$$\dots$$

$$B'_{m-1} \to \alpha_{m-1} B_m$$

$$B'_m \to \alpha_m A'$$

In the case of m = 0 (where the production is $A \to \alpha_0$), replace it with $A \to \alpha_0 A'$.

Strongly regular grammars generate regular languages and can be converted into equivalent finite automata using the standard algorithm outlined in [Mohri and Nederhof 2001] and further detailed in [Nederhof 2000]. Strongly regular grammars are characterized by productions within each partition $\mathscr P$ being exclusively left-linear or right-linear. When determining the linearity of a production in $\mathscr P$, nonterminals not within $\mathscr P$ are treated as terminals.

By construction, L' is a superset of L, meaning any string derivable in L is also derivable in L'. For each nonterminal X in L, at most one new nonterminal X' is added to L', where X and X' represent the start and end of string recognition generated by X in L. This ensures that the number of nonterminals in L' is at most double that of L, which is beneficial in practice.

As an optimization, Step (1) can be refined by adding the ϵ -production $A' \to \epsilon$ only when $A \in \mathscr{P}$ is directly reachable from another set of mutually recursive nonterminals \mathscr{P}' [Mohri and Nederhof 2001]. In this case, \mathscr{P}' uses \mathscr{P} to generate strings in the language of A. This refinement ensures that \mathscr{P} begins with A and concludes with the production $A' \to \epsilon$, effectively preventing undesirable strings from being introduced into the transformed grammar.

Example 4.1. In Figure 1b, the CFG contains a single partition of mutually recursive nonterminals, $\{M, V, A, \overline{A}\}$. The corresponding regular grammar obtained via the MN-transformation is shown in Figure 2. Since we focus on the memory alias relation in this example, M is the start symbol. With the refinement for Step (1) mentioned above, only $M' \to \epsilon$ is introduced in Step (1) during the MN-transformation. This ensures that string generation terminates exclusively with $M' \to \epsilon$.

For the M-path given in Equation (1), denoted here as p, the path string R(p) can obviously be derived from this regular grammar as follows: $M \to \overline{d} \ V \to \overline{d} \ \overline{d} \ A \to \overline{d} \ \overline{a} \ A' \to \overline{d} \ \overline{a} \ V \to \overline{d} \ \overline{a} \ M \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{d$

$$*x \xrightarrow{\overline{d}} x \xrightarrow{\overline{a}} *y \xrightarrow{\overline{d}} y \xrightarrow{\overline{a}} &r \xrightarrow{a} s \xrightarrow{d} *s$$
 (2)

R(p') represents an unwanted string derived as follows: $M \to^* \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ V' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ A \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ V' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ A \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ a \ A' \to \overline{d} \ \overline{a} \ \overline{d} \ \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ \overline{d} \ \overline{d} \ \overline{a} \ \overline{d} \ \overline{a} \ \overline{d} \ \overline{d} \ \overline{a} \ \overline{d} \$

- d. Without the refinement in Step (1), the production $\overline{A}' \to \epsilon$ would be included, allowing R(p)'s derivation to prematurely terminate at $M \to^* \overline{d} \ \overline{a} \ \overline{A'}$, resulting in the incomplete string $\overline{d} \ \overline{a}$, which is also not part of the original language.
- 4.1.2 Correctness for Approximating L-Contributing Edges. We now examine the correctness of our graph simplification technique. Given a CFL L and its regular approximation L', L' "relaxes" language reachability (Lemma 4.2), allowing us to over-approximate the set of L-contributing edges in G (Lemma 4.3) and ensure the correctness of the graph simplification (Theorem 4.6).

Lemma 4.2 (Relaxed Reachability). Consider any two nodes u and v in G. If v is L-reachable from u via a path p in G, then v must also be L'-reachable from u via p.

Proof Sketch. Let the set of strings generated by L be $\mathcal{L}(L)$. Without loss of generality, let us assume v is L-reachable from u via path p such that $R(p) \in \mathcal{L}(L)$, and since L' over-approximates $L(\mathcal{L}(L) \subseteq \mathcal{L}(L'))$, it follows that $R(p) \in \mathcal{L}(L')$. Therefore, v is L'-reachable from u via p.

Lemma 4.3. In G, if an edge $u \xrightarrow{t} v$ is L-contributing, then it is also L'-contributing.

Proof Sketch. Assume that $u \xrightarrow{t} v$ contributes to an L-reachable path p. According to Lemma 4.2, p must also be an L'-reachable path, which $u \xrightarrow{t} v$ contributes to.

Corollary 4.4. In G, if an edge $u \xrightarrow{t} v$ is non-contributing to L'-reachability, then it is also non-contributing to L-reachability.

Example 4.5. Continuing from Example 4.1, where L is provided in Figure 1b and L' in Figure 2, the M-reachable path from *s to *x under L is also M-reachable under L'. Evidently, any path that qualifies as an M-path in L-reachability also qualifies in L'-reachability. However, if we instead select *x and r as the source and sink, and let p' be the sub-path from *x to r in Equation (1), we find that p' qualifies as an M-path in L'-reachability but not in L-reachability.

By removing edges in G that do not contribute to L', we create a simplified graph G'. Since these edges do not contribute to any L-reachable pair in $V_{\rm src} \times V_{\rm snk}$, verifying L-reachability on both G and G' should yield the same set of L-reachable pairs. This confirms the correctness of this regular approximation as a graph simplification technique, as formally stated below.

Theorem 4.6 (Correctness of Regular Approximation). Given an instance $(L, G, V_{\text{src}}, V_{\text{snk}})$ of a client-driven CFL-reachability problem, solving L-reachability from any source in V_{src} to any sink in V_{snk} on G and its simplified version G' yields the same set of L-reachable source-to-sink pairs.

Proof Sketch. Let E_C and E_C' represent the sets of L-contributing and L'-contributing edges, respectively. According to Lemma 4.3, $E_C \subseteq E_C'$, making E_C' an over-approximation of E_C . Therefore, the edges that are not in E_C' are non-contributing to L-reachability (Corollary 4.4) and can be safely removed from G, resulting in the simplified graph G'. This removal does not affect the L-reachability of any source-to-sink paths from $V_{\rm src}$ to $V_{\rm snk}$, ensuring the simplification maintains the accuracy of CFL-reachability analysis on G'.

It is important to note that the graph simplification process reduces not only the edge set but also the node set of G. Intuitively, a node u with only non-contributing incoming and outgoing edges can be safely removed. This effect is demonstrated in Figure 1e from our motivating example, where four extraneous nodes—*z, z, t, and &t—are removed from the input graph G to obtain G'.

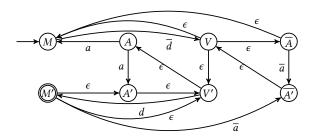


Fig. 3. NFA derived from the regular grammar in Figure 2, with the initial state M and as the final state M'.

4.2 Identifying L'-Contributing Edges

After regularizing L to L' as described in Section 4.1, we proceed to identify L'-contributing edges as an over-approximation of L-contributing edges. To achieve this, we examine the transition rules of a finite automaton, which is equivalent to the regular language L'. Specifically, we track the state transitions of nodes in G, using them to guide the identification of L'-contributing edges.

4.2.1 From L' to Finite Automaton. L', obtained by regularizing L in Section 4.1.1, is strongly regular, enabling its straightforward conversion into a finite automaton [Nederhof 2000].

Finite Automata. A deterministic finite automaton (DFA) is a 5-tuple $\mathcal{A}=(Q,\Sigma,\delta,q_{\text{init}},F)$, where Q is a finite set of states, Σ a finite alphabet, $\delta:Q\times\Sigma\to Q$ the transition function, $q_{\text{init}}\in Q$ the initial state, and $F\subseteq Q$ the set of final states. A nondeterministic finite automaton (NFA) extends a DFA by allowing $\delta:Q\times(\Sigma\cup\{\epsilon\})\to\mathcal{P}(Q)$, where ϵ permits transitions without consuming an input symbol and $\mathcal{P}(Q)$ allows multiple possible next states.

Definition 4.7 (Transition Sequence). Consider a string $\omega = t_1 t_2 \dots t_k$, where $t_i \in \Sigma$. If there exists a chain of state transitions: $q_0 \stackrel{t_1}{\longrightarrow} q_1 \stackrel{t_2}{\longrightarrow} \dots \stackrel{t_k}{\longrightarrow} q_k$ in a finite automaton \mathcal{A} , such that each transition $q_{i-1} \stackrel{t_i}{\longrightarrow} q_i$ is defined in the transition function δ for all i from 1 to k, then this sequence of transitions represents the *transition sequence* for string ω in \mathcal{A} .

Let us examine this definition in more detail. If \mathcal{A} is deterministic, the transition sequence for a string is unique, provided it exists. Note that q_0 in this definition may not necessarily be the initial state q_{init} . The transition sequence of a string serves as a witness to the computation of \mathcal{A} as it processes the string. A string is *accepted* by the automaton if, and only if, its transition sequence begins at the initial state q_{init} and ends at a final state $q_f \in \mathcal{F}$.

Translation to NFA. Given a strongly regular grammar L' (Section 4.1.1), we convert it into an equivalent NFA $\mathcal N$ following [Mohri and Nederhof 2001]. We set S as the initial state and S' as the final state of $\mathcal N$, where S is the start nonterminal of the original grammar L, and S' is a new nonterminal introduced in the MN-transformation (Section 4.1). The production $S' \to \epsilon$ allows S' to generate the empty string, completing string recognition. Hence, a string in L' is accepted by $\mathcal N$ if it follows a transition sequence from S to S'.

Conversion to DFA. The NFA \mathcal{N} is converted into an equivalent DFA \mathcal{D} using the standard subset construction algorithm [Aho et al. 2006]. We then employ Hopcroft's algorithm [Hopcroft 1971] for state minimization to eliminate redundant states. The resulting DFA accurately simulates the behavior of the original NFA, facilitating predictable and efficient computations.

Example 4.8. Figure 3 shows the NFA derived from the regular grammar L' in Figure 2, with the corresponding DFA in Figure 1d. Revisiting the M-path from *x to *s (Equation (1)) in our motivating example, Figure 4 illustrates the relationship of this path (reproduced in Figure 4a) with

$$\begin{array}{c} *x \overset{\overline{d}}{\longrightarrow} x \overset{\overline{a}}{\longrightarrow} *y \overset{\overline{d}}{\longrightarrow} y \overset{\overline{a}}{\longrightarrow} \&r \overset{d}{\longrightarrow} r \overset{a}{\longrightarrow} s \overset{d}{\longrightarrow} *s \\ & (a) \text{ The M-path from $*x$ to $*t$ in Equation (1)} \\ M \overset{\overline{d}}{\longrightarrow} V \overset{\epsilon}{\longrightarrow} \overline{A} \overset{\overline{a}}{\longrightarrow} \overline{A'} \overset{\epsilon}{\longrightarrow} V \overset{\epsilon}{\longrightarrow} M \overset{\overline{d}}{\longrightarrow} V \overset{\epsilon}{\longrightarrow} \overline{A} \overset{\overline{a}}{\longrightarrow} \overline{A'} \\ M' \overset{d}{\longleftarrow} V' \overset{\epsilon}{\longleftarrow} A' \overset{a}{\longleftarrow} A \overset{\epsilon}{\longleftarrow} V' \overset{\epsilon}{\longleftarrow} M' \overset{d}{\longleftarrow} V' \overset{\epsilon}{\longleftarrow} V \\ & (b) \text{ Transition sequence on the NFA in Figure 3} \\ q_0 \overset{\overline{d}}{\longrightarrow} q_1 \overset{\overline{a}}{\longrightarrow} q_1 \overset{\overline{d}}{\longrightarrow} q_1 \overset{\overline{a}}{\longrightarrow} q_1 \overset{\overline{d}}{\longrightarrow} q_3 \overset{\overline{a}}{\longrightarrow} q_2 \overset{\overline{d}}{\longrightarrow} q_3 \\ & (c) \text{ Transition sequence on the DFA in Figure 1d} \end{array}$$

Fig. 4. The M-path from Figure 1 (as given in Equation (1)) and the transition sequences of its path string.

the transition sequences in both the NFA and DFA. The NFA moves from state M to M' through 16 transitions (Figure 4b), while the DFA progresses from state q_0 to q_3 in just 7 transitions (Figure 4c), illustrating the DFA's efficiency. Both sequences correspond to the M-path defined by L'.

4.2.2 Determining L'-Contributing Edges. Since the regular language L' and the DFA \mathcal{D} are equivalent, we can utilize the DFA's state transitions to identify L'-contributing edges.

The DFA \mathcal{D} provides an alternative representation of the regular language L'. Consider a path p in $G: v_0 \xrightarrow{t_1} v_1 \xrightarrow{t_2} \dots \xrightarrow{t_k} v_k$, where the path string $R(p) = t_1 t_2 \dots t_k$ forms a string $\omega \in \Sigma^*$. Since \mathcal{D} is deterministic, the transition sequence for ω is unique, if it exists. Although determinism is not crucial for identifying L'-contributing edges, converting the original NFA \mathcal{N} to a state-minimal DFA \mathcal{D} often reduces the number of states practically, despite no theoretical guarantee—a well-established fact. This reduction, demonstrated in Figures 7 and 8 for our two key client analyses, accelerates the identification process, as explained in Example 4.8.

Correspondence between L'-Reachable Paths and Transition Sequences. Since L' and \mathcal{D} are equivalent, it is well-known that a string ω belongs to L' if and only if it is accepted by \mathcal{D} .

Lemma 4.9. Let L' be a regular language and $\mathcal{A} = (Q, \Sigma, \delta, q_{\text{init}}, F)$ its equivalent finite automaton. A path $p = v \to \cdots \to v'$ in G is L'-reachable if and only if the path string R(p) has a transition sequence starting at q_{init} and ending at a final state $q_f \in F$.

Proof Sketch. Given the equivalence between L' and \mathcal{A} , this proof is derived directly from the definitions of L'-path (Section 2.2) and transition sequence (Definition 4.7).

Configurations. This lemma illustrates that comparing a path p with its transition sequence R(p) aligns each automaton state with a specific node in G, forming a sequence of configurations: $(v_0, q_0) \xrightarrow{t_1} (v_1, q_1) \xrightarrow{t_2} \dots \xrightarrow{t_k} (v_k, q_k)$. Each pair (v_i, q_i) from $V \times Q$ constitutes a *configuration* for node v_i . A node in G can have multiple configurations based on its paths. We focus on identifying realizable configurations, which are crucial for establishing L'-reachable pairs $(u, v) \in V_{\rm src} \times V_{\rm snk}$.

Definition 4.10 (Realizable Configurations). A configuration $(v, q) \in V \times Q$ is called *realizable* if the following two conditions are both satisfied:

- (1) There exists a path in G from some node $v_{\rm src} \in V_{\rm src}$ to v that corresponds to a transition sequence from the initial state $q_{\rm init}$ to q, and
- (2) There exists a path in G from v to some node $v_{\rm snk} \in V_{\rm snk}$ that corresponds to a transition sequence from q to a final state $q_{\rm f} \in F$.

Note that $v_{\rm src}$ and $v_{\rm snk}$ may not be necessarily distinct from v.

Intuitively, this definition implies the existence of an L'-path from $v_{\rm src}$ to $v_{\rm snk}$ via node v. According to the two conditions, there exists a path p in G of the form $v_{\rm src} \to \cdots \to v \to \cdots \to v_{\rm snk}$. The corresponding path string R(p) has an associated transition sequence from $q_{\rm init}$ to $q_{\rm f}$, passing through state q at node v. By Lemma 4.9, this means that $v_{\rm snk}$ is L'-reachable from $v_{\rm src}$.

Finally, we present a necessary and sufficient condition based on realizable configurations that enables us to identify L'-contributing edges in G efficiently.

Theorem 4.11 (Determining L'-Contributing Edges). In G, an edge $u \xrightarrow{t} v$ is L'-contributing if and only if there exist realizable configurations (u, q_1) and (v, q_2) , such that $q_1 \times t \to q_2 \in \delta$.

Proof Sketch. To prove the implication " \Longrightarrow ", assume that $u \xrightarrow{t} v$ is part of an L'-path $v_{\rm src} \to \cdots \to u \xrightarrow{t} v \to \cdots \to v_{\rm snk}$. According to Lemma 4.9, there exists a configuration chain $(v_{\rm src}, q_{\rm init}) \to \cdots \to (u, q_1) \xrightarrow{t} (v, q_2) \to \cdots \to (v_{\rm snk}, q_{\rm f})$. By Definition 4.10, the configurations (u, q_1) and (v, q_2) are realizable. Furthermore, the transition from (u, q_1) to (v, q_2) is effected by the edge $u \xrightarrow{t} v$, verifying that this edge contributes to the reachability from $v_{\rm src}$ to $v_{\rm snk}$.

To prove the implication " \Leftarrow ", assume (u, q_1) and (v, q_2) are realizable configurations. This implies there exist (1) a path from $v_{\rm src}$ to u corresponding to a transition sequence $q_{\rm init} \to \cdots \to q_1$, and (2) a path from v to $v_{\rm snk}$ corresponding to a transition sequence $q_2 \to \cdots \to q_{\rm f}$. The transition $q_1 \times t \to q_2$ effectively connects these two paths, establishing an L'-path as outlined in Lemma 4.9. This linkage confirms that $u \to v$ is an L'-contributing edge, completing our proof.

Example 4.12. Returning to our motivating example in Figure 4, based on the relationship between the *M*-path in Figure 4a and the transition sequence in Figure 4c, we deduce a configuration chain:

$$(\mathbf{*x},q_0) \rightarrow (\mathbf{x},q_1) \rightarrow (\mathbf{*y},q_1) \rightarrow (\mathbf{y},q_1) \rightarrow (\mathbf{\&r},q_1) \rightarrow (\mathbf{r},q_3) \rightarrow (\mathbf{s},q_2) \rightarrow (\mathbf{*s},q_3)$$

All configurations in this chain are realizable, and according to Theorem 4.11, the edges in this M-path are L'-contributing (thus L-contributing since L' over-approximates L). Let us now consider the path p' in Equation (2). While all its edges are identified as L'-contributing due to $(*x, q_0) \rightarrow \cdots \rightarrow (\$r, q_1) \rightarrow (\$s, q_2) \rightarrow (*s, q_3)$, the edge $\&r \xrightarrow{a} s$ is deemed L-contributing spuriously.

5 Graph Simplification Algorithm

We introduce our regularization-based graph simplification algorithm designed to identify L'contributing edges, following the guidelines set forth in Theorem 4.11. The algorithm utilizes the
DFA \mathcal{D} , derived from L', to iteratively compute realizable configurations during the traversal of G.

5.1 Computing Realizable Configurations

Algorithm 1 details the algorithm for computing the set C of realizable configurations. This algorithm operates by intersecting two sets of reachable configurations: one set generated in the forward phase (lines 5-13), originating from sources in $V_{\rm src}$ and the initial state, and the other set in the backward phase (lines 14-23), starting from sinks in $V_{\rm snk}$ and the final states. The intersection ensures that each realizable configuration (v,q) forms part of a configuration chain from $(v_{\rm src},q_{\rm init})$ through (v,q) to $(v_{\rm snk},q_{\rm f})$, where $v_{\rm src}\in V_{\rm src},v_{\rm snk}\in V_{\rm snk}$, and $q_{\rm f}\in F$.

Initially, three sets are initialized as empty: the worklist W, FC for storing visited configurations during the forward phase, and C for storing realizable configurations (lines 2-4).

In the forward phase (lines 5-13), pairs from $V_{\rm src} \times \{q_{\rm init}\}$ are added to W and FC as initial configurations (lines 5-7). The loop from lines 8-13 continuously processes configurations that are reachable during forward traversal until no new configurations can be reached. A configuration (v', q') is added to FC and W if it has not been visited before $((v', q') \notin FC)$.

Algorithm 1: Computing realizable configurations.

```
Input: \mathcal{A} = (Q, \Sigma, q_{\text{init}}, \delta, F), G = (V, E), V_{\text{src}}, \text{ and } V_{\text{snk}}
Output: the set C of all realizable configurations

1 Proc ComputeRLZConf(\mathcal{A}, G, V_{\text{src}}, V_{\text{snk}}):
14 for v \in V_{\text{snk}} do
2 W = \emptyset;

15 for q_f \in F do
```

```
for q_f \in F do
 _{2} W = \emptyset;
                                                                                 if (v, q_f) \in FC \land (v, q_f) \notin C then
                                                                   16
 3 FC = \emptyset; /* Configurations in forward phase */
                                                                                   add (v, q_f) to C and W;
                        /* Realizable configurations */
 5 for v \in V_{\rm src} do
                                                                   18 while W \neq \emptyset do
        if (v, q_{\text{init}}) \notin FC then
                                                                            pop (v, q) from W;
                                                                   19
            add (v, q_{init}) to FC and W;
                                                                            for v' \xrightarrow{t} v \in E do
                                                                   20
   while W \neq \emptyset do
                                                                                 for q' \times t \rightarrow q \in \delta do
                                                                   21
        pop (v, q) from W;
                                                                                      if (v', q') \in FC \land (v', q') \notin C then
 9
                                                                   22
                                                                                           add (v', q') to C and W;
        for v \xrightarrow{t} v' \in E do
                                                                   23
10
             for q \times t \rightarrow q' \in \delta do
11
                  if (v', q') \notin FC then
12
                                                                   24 return C;
                       add (v', q') to FC and W;
```

The backward phase (lines 14-23) operates similarly to the forward phase but starts with configurations in $V_{\rm snk} \times F$ (lines 14-17). During this traversal (lines 18-23), a configuration (v', q') is considered only if previously visited in the forward phase $((v', q') \in FC)$ and not yet visited in the backward phase $((v', q') \notin C)$. This selective process efficiently performs the intersection, minimizing unnecessary traversal and focusing on potentially realizable configurations.

Time Complexity. Algorithm 1 operates with a time complexity of $O(|Q|^2 \times |E|)$, where Q represents the states of \mathcal{A} . The sets W, FC, and C are managed using hash tables, enabling operations like insertions and lookups in lines 6-7, 12-13, 16-17, and 22-23 to occur in O(1) amortized time.

The forward phase, delineated in lines 5-13, initializes initial configurations based on $V_{\rm src}$, resulting in $O(|V_{\rm src}|)$ complexity. The main loop from lines 8-13 processes the worklist until no new configurations are found, iterating over potential configurations bounded by $|Q| \times |V|$. Each node v can have up to $|V| \times |\Sigma|$ outgoing edges. As $\mathcal A$ is deterministic, there is at most one output state from state q via any terminal t. Given these dynamics, the while loop in lines 8-13 might seem to have a complexity of $O(|Q| \times |V|^2 \times |\Sigma|)$ in a naive analysis. However, since each node v in G can host at most |Q| configurations and each configuration (v,q) can only initiate transitions corresponding to v's outgoing edges — each potentially yielding up to one resultant state — an amortized analysis leads us to a complexity of $O(|Q| \times |E|)$ for the forward phase.

The backward phase, processed in lines 14-23, contributes $O(|V_{\rm snk}| \times |F| + |Q|^2 \times |E|)$ to the overall time complexity, as an output state can have up to |Q| possible input states transited via a terminal t (line 21). Combining these, Algorithm 1 exhibits a complexity of $O(|V_{\rm src}| + |V_{\rm snk}| \times |F| + |Q| \times |E| + |Q|^2 \times |E|)$. Assuming $|V| \le |E|$, and since $V_{\rm src}$ and $V_{\rm snk}$ are subsets of V, and F is a subset of Q, the complexity simplifies to $O(|Q|^2 \times |E|)$, covering all phases of the algorithm.

Space Complexity. The space complexity of Algorithm 1 is $O(|Q| \times |V|)$, reflecting the maximum potential number of configurations that can be stored or processed within the algorithm.

Algorithm 2: Graph simplification.

5.2 Overall Algorithm

Algorithm 2 describes the overall regularization-based graph simplification process for the input graph G. It starts by identifying realizable configurations using the ComputeRLZConf procedure from Algorithm 1. The loop in lines 4-6 processes the edge set E to determine if each edge contributes to E'-reachability, as specified by Theorem 4.11 and implemented in line 5. The algorithm completes by creating a simplified graph E', where non-contributing edges are removed.

For an edge $u \xrightarrow{t} v$, label t can result in up to |Q| transitions, since the output state is uniquely determined by the input state provided it exists, giving the loop at lines 4-6 in Algorithm 2 a time complexity of $O(|Q| \times |E|)$. Including the complexity of the ComputeRLZConf procedure, the total time complexity of Algorithm 2 is $O(|Q|^2 \times |E|)$. The space complexity of Algorithm 2, primarily determined by ComputeRLZConf, is $O(|Q| \times |V|)$.

In practice, the DFA $\mathcal D$ derived from L' typically features only a few states; for instance, the DFAs in our study in Section 6 contain just 3 and 4 states each. Consequently, the time complexity of Algorithm 2 is linear to the number of edges in the input graph G. Since G is generally sparse in real-world program analysis, where |E| = O(|V|), this translates to approximately linear complexity with respect to |V|. Therefore, MoYE is highly efficient, making it an effective pre-processing technique to streamline the input graph for CFL-reachability, as evaluated below.

6 Evaluation

We evaluate MoYE with two major clients: field-sensitive points-to analysis for Java [Sridharan et al. 2005] and alias analysis for C/C++ [Zheng and Rugina 2008], both extensively studied [Lei et al. 2022a; Shi et al. 2023; Wang et al. 2017; Xu et al. 2024]. Our evaluation confirms MoYE's efficiency and effectiveness in enhancing CFL-reachability tasks. Notably, MoYE accelerates a recent CFL-reachability algorithm [Lei et al. 2022a] and outperforms the leading graph simplification method GF [Lei et al. 2023b]. Combined with GF, MoYE delivers even greater performance gains, proving its value in optimizing CFL-reachability. In a batch setting, MoYE further boosts performance.

Our experiments are designed to address the following four research questions (RQs):

- RQ1: How efficient is MoYE when used as a pre-analysis tool in the all-queries setting?
- RQ2: To what extent does MoYE reduce the size of input graphs in the all-queries setting?
- RQ3: How much does MoYE accelerate CFL-reachability analysis in terms of reduced runtime overhead and memory footprint in the all-queries setting?
- RQ4: How significantly does MoYE accelerate CFL-reachability analysis in a batch setting?

```
FlowsTo
                                                                                 → new | FlowsTo Assign
                                                                 Assign
                                                                                 \rightarrow assign | PutAlias<sub>f</sub> get<sub>f</sub>
FlowsTo → new | FlowsTo Assign
                                                                 PutAlias_f \rightarrow put_f Alias
                                                                 FlowsTo
                                                                                 \rightarrow \overline{new} \mid \overline{Assign} \overline{FlowsTo}
FlowsTo → new | Assign FlowsTo
                                                                 Assign
                                                                                 \rightarrow \overline{assign} \mid \overline{get_f} \overline{PutAlias_f}
Assign \rightarrow assign | put<sub>f</sub> Alias get<sub>f</sub>
\overline{Assign} \rightarrow \overline{assign} \mid \overline{get_f} A lias \overline{put_f}
                                                                 PutAlias_f \rightarrow Alias \overline{put_f}
              → FlowsTo FlowsTo
                                                                                  → FlowsTo FlowsTo
Alias
                                                                 Alias
                                                                             (b) Normalized grammar
              (a) Original grammar
```

Fig. 5. Original and normalized CFGs specifying points-to analysis for Java, with FlowsTo as the start symbol.

Fig. 6. Original and normalized CFGs specifying alias analysis for C/C++, with V as the start symbol.

We assess MoYe's performance in two distinct settings. First, for RQ1, RQ2, and RQ3, we adopt the all-queries approach—resolving every source–sink pair in $V_{\rm src} \times V_{\rm snk}$ via a single pass, following Cfl's and Gf's evaluation methodology [Lei et al. 2022a, 2023b]. This method efficiently handles large sets of queries by establishing all-pairs reachability, allowing constant-time solutions for each query once computed, and thus provides an approximate lower bound on MoYe's performance gains in accelerating Cfl-reachability analysis. Additionally, it offers an upper bound on MoYe's resource usage and a lower bound on its graph reduction potential. In this setting, a demand-driven approach, which handles each query individually, has no theoretical advantage: answering a single query can take $O(|V|^3)$ [Yannakakis 1990], and many queries share overlapping paths—resulting in redundant work [Vedurada and Nandivada 2020; Zheng and Rugina 2008].

For RQ4, we use a batch-based approach, where queries are grouped into smaller batches of source–sink pairs from $V'_{\rm src} \times V'_{\rm snk}$ ($V'_{\rm src} \subseteq V_{\rm src}$ and $V'_{\rm snk} \subseteq V_{\rm snk}$). This method is particularly effective for handling a moderate number of queries in a demand-driven manner.

6.1 Implementation

Field-Sensitive Points-to Analysis for Java. Figure 5a displays the standard CFG used for specifying points-to analysis in Java. This CFL-reachability analysis designates allocation and virtual-invocation statements as sources and sinks, respectively, focusing only on those virtual-invocation statements within application code and new statements where allocated objects are utilized within the application [Ali and Lhoták 2012]. This setup facilitates the construction of a call graph by establishing CFL-reachability for all source-to-sink pairs. The standard CFL-reachability algorithm [Melski and Reps 2000] mandates that the CFG be normalized, ensuring that each production's right-hand side contains at most two symbols. The normalized version is depicted in Figure 5b. The alphabet Σ includes four types of terminals: new, assign, put_f , and get_f , representing allocation, assignment, field write, and field read, respectively. In points-to analysis graphs G [He et al. 2023, 2022; Sridharan et al. 2005; Xu et al. 2009], as described by [Reps 1998] and constructed using Tai-E [Tan and Li 2023], each edge is bidirectional; for any edge $v \to u$, a corresponding inverse edge $u \to v$ exists. This setup utilizes terminals like \overline{new} , \overline{assign} , $\overline{put_f}$, and $\overline{get_f}$ to denote

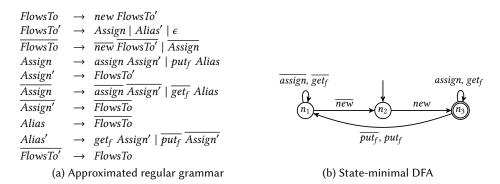


Fig. 7. Field-sensitive points-to analysis for Java.

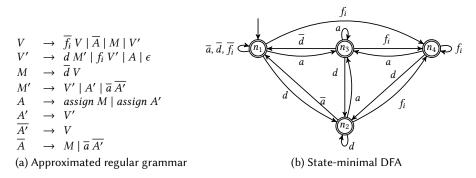


Fig. 8. Field-sensitive alias analysis for C/C++.

these inverse edge labels. The start symbol FlowsTo indicates the flow of an object to a pointer, while its inverse, $\overline{FlowsTo}$, represents standard points-to relations. Additionally, the normalization step often introduces new nonterminals, such as $PutAlias_f$ and $\overline{PutAlias_f}$, as seen in Figure 5b.

Field-Sensitive Alias Analysis for C/C++. Figure 6 presents both the original and normalized CFGs utilized for alias analysis in C/C++. This CFL-reachability analysis designates store and load statements as sources and sinks, respectively, and determines the value alias relation (denoted by the start symbol V) between sources and sinks, facilitating the tracking of indirect value flows through memory accesses [Li et al. 2011; Shi et al. 2018; Sui and Xue 2018; Yao et al. 2024]. The original CFG includes a field-sensitive production $V \rightarrow f_i V f_i$, where f_i represents the i-th object field. Alias analysis graphs G are constructed by the open-source SVF tool [Sui and Xue 2016].

CFL-Reachability Solver. To address CFL-reachability problems, we use Pocr, a recent CFL-reachability algorithm [Lei et al. 2022a], which reduces transitive redundancy via ordered derivations, along with bit-vector set operations for subcubic performance [Chaudhuri 2008] implemented by us. The tool is sourced from the Pocr artifact [Lei et al. 2022b]. We refer to this solver as CFL, which requires a normalized CFG as input, like those shown in Figures 5b and 6b.

MoYE. We developed MoYE in two components: The first component, a Python3 module, transforms a CFG into a DFA via an intermediary regular grammar, producing identical DFAs from original or normalized grammars in under a second. This one-time process per CFG is illustrated in Figures 7 and 8, with resulting DFAs for points-to and alias analysis having 3 and 4 states, respectively (Figures 7b and 8b). The second component, which handles graph simplification, uses these DFAs and is implemented in LLVM-14.0.0 (Algorithms 1 and 2).

Validation of Correctness. We empirically validated the correctness of our graph simplification technique MoYE on all Java and C/C++ programs where CFL is scalable, listed in Tables 2 and 3, by confirming that CFL produces the same set of L-reachable pairs in $V_{\rm src} \times V_{\rm snk}$ on both the original graph G and the simplified graph G' generated by MoYE.

6.2 Experimental Setup

Baselines. We evaluate MoYe's effectiveness in improving CFL-reachability analysis via graph size reduction and compare it against GF, the current leading approach [Lei et al. 2023b]. Because their methods are orthogonal, we also test Combined, applying GF first to contract edges, then MoYe to remove non-contributing edges. Running MoYe before GF is infeasible because GF assumes bidirectional graphs for points-to/alias analysis, a key to optimizing edge scanning [Lei et al. 2023a]; doing so would disrupt GF's foundational assumptions (Figure 1e). Furthermore, GF is faster, making it more practical to apply before MoYe, as demonstrated in our results (Tables 2 and 3). We measure performance gains by executing the CFL solver on both the original and simplified graphs.

Experimental Settings. All experiments are conducted on a server with dual 12-core Intel(R) Xeon(R) Gold 5317 CPUs at 3.00GHz and 2 TB of RAM, with each run capped at 6 hours and 512 GB of memory. We report the mean (μ) time and memory usage over 6 runs for each experiment. Following standard evaluation guidelines [Georges et al. 2007], we calculated the *standard variation* σ , observing a maximum *coefficient of variation* ($CV = \frac{\sigma}{\mu}$) of 4.98%, indicating minimal variation [Westgard nd]. We then calculate statistics on average metrics across benchmarks—such as speedups and reduction rates—using *geometric means*.

Benchmarks and Graph Construction. Following the latest related work [He et al. 2024a], we have selected 13 Java programs from the well-known DACAPO benchmark [Blackburn et al. 2006] (version 6cf0380) for points-to analysis, coupled with a large Java library (JRE1.8.0_31) and TamiFlex [Bodden et al. 2011] for reflection handling. The number of classes and reachable methods are displayed in Columns 2-3 in Table 2. Consistent with prior studies [He et al. 2024b; Thiessen and Lhoták 2017], we have excluded jython due to its overly conservative reflection log, which rendered it unscalable within our time budget. We employ TAI-E [Tan and Li 2023], an open-source static analysis framework, to translate these Java programs into bytecode for graph extraction. For alias analysis, we evaluate 10 C/C++ programs from the SPEC 2017 suite. They are compiled with Clang and linked into LLVM bitcode using wllvm¹, then analyzed with the SVF framework [Sui and Xue 2016] to generate input graphs. Sources and sinks are identified during graph generation. Detailed statistics on the number of nodes, edges, sources, and sinks in each program—tailored to points-to and alias analyses—are listed in Table 2 and Table 3, respectively.

6.3 RQ1: Evaluating MoYe's Efficiency as Pre-Analysis

In this first RQ, we measure the computational and memory overhead of MoYe as a pre-analysis technique in the all-queries setting by solving all source—sink pairs in $V_{\rm src} \times V_{\rm snk}$ in a single pass, which approximates an upper bound on MoYe's resource usage. Tables 2 and 3 show the analysis time and memory usage for the graph simplification techniques—MoYe, GF, and Combined—applied to each program under points-to and alias analyses, respectively. For comparison, we also run CFL on the original (unsimplified) graphs, providing a reference point.

Points-to Analysis for Java. Table 2 shows that MoYE is lightweight, taking about one minute for fop at its slowest, while GF typically completes in under ten seconds for all 13 Java programs. The analysis times for both tools are significantly shorter compared to CFL's. COMBINED, leveraging GF's efficiency and the pre-simplified graph it provides, operates faster than MoYE. In terms of

¹https://github.com/travitch/whole-program-llvm

Table 2. Performance results for points-to analysis of Java programs using CFL for CFL-reachability analysis and three graph simplification methods (GF, MOYE, COMBINED) in the all-queries setting. Columns show analysis time (seconds), peak memory (MBs), number of classes, reachable methods (cols 2–3), and the graph's nodes, edges, sources, and sinks (cols 4–7). A "-" indicates the analysis did not finish within 6 hours.

Bench.	#Cls	#DM	#Nodes	#Edges	#Srcs	#Snks	Cfl		GF		МоҮе		Combined	
Bench.	#CIS	#KWI					Time	Mem	Time	Mem	Time	Mem	Time	Mem
avrora	8,737	12,643	86,457	314,544	8,282	4,529	4,712.79	4,267.74	0.27	54.50	1.89	81.88	1.08	54.47
batik	11,858	29,378	199,790	902,310	16,863	17,366	-	-	1.62	128.13	14.91	210.16	8.74	127.57
eclipse	10,134	15,992	119,123	488,122	10,020	12,027	-	-	0.53	72.65	4.46	118.61	2.57	72.53
fop	12,871	49,629	370,007	1,856,600	32,099	62,416	-	-	9.08	250.27	68.15	402.78	38.62	249.95
h2	10,712	21,343	151,070	668,916	10,924	22,685	-	-	0.76	98.46	8.18	153.59	4.66	98.14
luindex	8,433	16,994	119,191	430,822	8,855	12,564	15,485.27	8,830.43	0.43	71.85	3.93	110.75	2.28	71.78
lusearch	8,433	8,719	62,110	229,392	5,391	897	1,756.85	2,343.93	0.15	36.71	0.93	57.82	0.57	36.75
pmd	9,954	24,918	185,812	973,112	13,368	31,232	-	-	1.69	126.74	16.58	209.78	9.25	126.42
sunflow	7,399	16,626	115,608	430,460	10,664	2,090	17,582.17	8,883.26	0.41	69.02	3.58	108.21	2.17	68.92
tomcat	8,433	9,486	66,045	244,086	5,773	747	2,146.93	2,649.05	0.16	38.85	1.07	61.46	0.66	38.94
tradebeans	9,954	10,330	74,438	273,734	6,713	747	3,238.06	3,443.98	0.19	43.12	1.36	69.46	0.86	43.17
tradesoap	9,954	10,330	74,438	273,734	6,713	747	3,195.30	3,446.23	0.19	43.23	1.40	69.48	0.87	43.15
xalan	10,841	15,062	115,361	455,242	9,189	13,317	11,493.15	6,788.44	0.45	69.84	3.86	114.31	2.41	69.90
Geo. Mean	9,715	16,333	117,027	472,990	9,828	5,611	5,248.16	4,497.75	0.52	72.24	4.18	115.10	2.48	72.17

Table 3. Performance results for alias analysis of C/C++ programs using CFL for CFL-reachability and GF, MoYE, and COMBINED for graph simplification in the all-queries setting, with column definitions from Table 2.

Bench.	#Nodes	#Edges	#Srcs	#Snks	Cfl		GF		MoYE		Combined	
Deffell.	#INOUES				Time	Mem	Time	Mem	Time	Mem	Time	Mem
nab	18,151	40,910	867	1,501	68.38	343.80	0.04	21.19	0.07	15.18	0.05	21.38
XZ	12,795	27,848	377	568	30.38	195.86	0.03	14.94	0.04	10.81	0.03	15.00
cactus	157,319	335,256	8,211	11,337	-	-	0.45	175.96	5.80	129.58	3.42	175.85
leela	22,861	51,958	1,884	2,282	140.97	423.57	0.05	27.44	0.12	20.24	0.09	27.31
x264	68,316	157,582	4,748	6,768	792.87	2,655.51	0.18	78.24	0.63	56.93	0.51	78.33
povray	78,052	185,288	3,228	5,138	-	-	0.23	89.97	1.60	63.11	1.17	90.38
parest	120,601	259,762	8,290	13,610	9,536.01	6,985.11	0.37	136.27	4.46	101.29	2.31	136.37
imagick	120,056	322,144	3,120	4,700	-	-	0.40	144.19	3.00	100.18	2.96	144.12
omnetpp	244,498	521,674	12,061	29,887	-	-	0.85	272.49	14.39	201.42	8.59	272.54
perlbench	160,837	424,272	5,906	17,600	-	-	0.61	194.42	9.47	140.38	6.60	194.48
Geo. Mean	69,249	159,613	3,277	5,510	294.46	880.44	0.20	80.34	1.14	58.17	0.79	80.45

memory usage, both MoYE and GF consume only a fraction of what CFL requires, with MoYE using $1.38\times$ more than GF. For Combined, the total pre-analysis time is t_1+t_2 : GF operates on the original graph (t_1 corresponds to the data in Table 2, 10th column), and MoYE operates on the simplified graph (t_2 is usually less than the data in the 12th column). Thus, Combined's pre-analysis time (14th column) does not equal the sum of the 10th and 12th columns. Combined's peak memory usage is the higher of GF's or MoYE's. Since the peak occurs during GF's stage, the values in the 15th column are very close to those of GF (11th column). Finally, it is important to note that CFL failed to complete analyses for five benchmarks—batik, eclipse, fop, h2, and pmd, highlighting the critical role of graph simplification techniques in enhancing performance.

Alias Analysis for C/C++. Table 3 reveals that CFL failed to complete the analysis for half of the C/C++ programs tested, including cactus, povray, imagick, omnetpp, and perlbench, within the 6-hour time limit. These programs also required more time and resources for MoYE, GF, and COMBINED. Despite these demands, all three pre-analysis techniques finished much faster than CFL, emphasizing their efficiency and lower asymptotic time complexity in comparison.

Compared to Cfl, both MoYe and Gf consume significantly less analysis time and memory, making them well-suited as pre-analysis techniques to accelerate Cfl-reachability analysis. Specifically, for benchmarks where Cfl completes within 6 hours, MoYe and Gf require only 0.037% (0.085%)

and 0.005% (0.028%) of the analysis time, while using 1.809% (3.249%) and 1.147% (4.453%) of the memory consumed by CFL in points-to (alias) analysis, respectively. Combined also shows similar benefits, underscoring the value of integrating these two graph simplification methods.

6.4 RQ2: Evaluating MoYE's Effectiveness in Graph Size Reduction as Pre-Analysis

In this second RQ, we assess MoYe's effectiveness in reducing input graph sizes relative to GF in the all-queries setting, thus establishing an approximate lower bound on MoYe's graph reduction potential. Section 6.6 demonstrates that MoYe achieves even greater edge reduction in the batch setting than shown here. We find MoYe to be both lightweight and highly effective, which enhances the advantages of Combined—integrating MoYe and GF together.

Figures 9 and 10 show the reduction rates for nodes and edges in the input graphs for points-to and alias analyses, respectively. In points-to analysis, GF reduces nodes by 40.22% and edges by 24.44% on average, while MoYE achieves higher reductions of 63.82% (nodes) and 70.81% (edges). The combined approach, Combined, yields the best performance, cutting nodes by 77.79% and edges by 78.32%. In alias analysis, GF, MoYE, and Combined reduce nodes by 39.84%, 59.14%, and 76.71%, and edges by 36.89%, 65.36%, and 79.0%, respectively.

Combined attains higher reduction rates than MoYe or GF alone because they reduce the input graph based on distinct principles: MoYe prunes non-contributing edges to eliminate irrelevant paths, while GF contracts trivial edges to shorten paths (see [Lei et al. 2023b] for details on GF). In our analyses, GF only contracts transitive edges—such as assign-edges in points-to analysis and a-edges in alias analysis—which generally represent assignments in CFL-based program analyses. In contrast, MoYe removes non-contributing edges of any type from the input graph. Let $E_{\rm GF}$ be the set of edges contracted by GF, and $E_{\rm MoYe}$ the set removed by MoYe. The two methods are compatible for graph simplification. GF may contract edges that MoYe identifies as necessary for preserving CFL-reachability; contracting these edges retains reachability, whereas removing them may cause unsoundness. Such edges lie in $E_{\rm GF}$ – $E_{\rm MoYe}$. Conversely, edges deemed non-contributing by MoYe may not appear trivial to GF due to its approximations, so these edges are in $E_{\rm MoYe}$ – $E_{\rm GF}$. With respect to node reduction, GF consolidates two nodes by contracting the edges between them, while MoYe removes a node only if all its incident edges are non-contributing.

To further demonstrate the effectiveness of MoYE, we conducted an additional cast-may-fail analysis for Java, utilizing source-to-sink pairs distinct from those in the call graph construction client. Specifically, we selected new statements as sources and cast statements as sinks, reusing the grammars and DFA presented in Figures 5 and 7. This analysis focuses on determining the potential failure of cast statements, like "a = (A) b;", based on the points-to set of the pointer b. Figure 11 shows MoYE's reduction rates of nodes and edges in the input graphs, averaging 69.91% and 72.62%, respectively. These rates are comparable to those observed in the call graph construction client (Figure 11), highlighting MoYE's robustness across different analysis scenarios.

6.5 RQ3: Evaluating MoYe's Impact on CFL-Reachability in the All-Queries Setting

In this third RQ, we assess MoYe's ability to enhance CFL (CFL-reachability analysis) by reducing analysis time and memory usage relative to GF in the all-queries setting. We also demonstrate how Combined, which integrates MoYe and GF, achieves even greater performance, underscoring MoYe's pivotal role in significantly boosting analysis efficiency.

As standard practice dictates [Reps et al. 1995], summary edges derived from grammar productions are added during CFL-reachability solving (Section 2.2). By reducing the input graph size, the number of summary edges decreases, enabling CFL to run faster and use less memory. Hereafter, edges in the input graph are referred to as *graph edges* to distinguish them from summary edges.

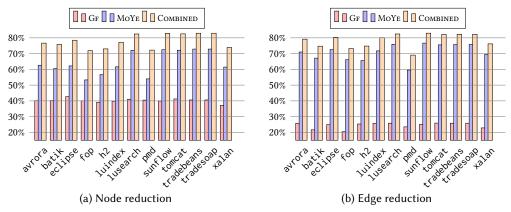


Fig. 9. Reduction rates for nodes and edges in points-to analysis input graphs in the all-queries setting.

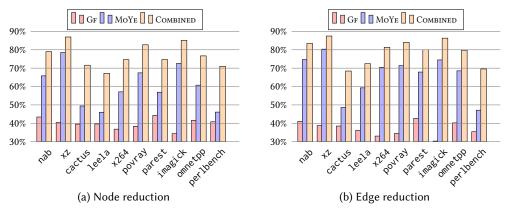


Fig. 10. Reduction rates of nodes and edges in alias analysis input graphs in the all-queries setting.

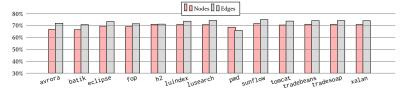
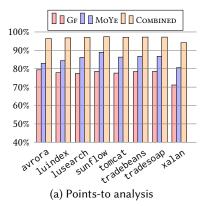


Fig. 11. Reduction rates for nodes and edges in cast-may-fail points-to analysis, a significant client analysis for Java, achieved by MoYE in the all-queries setting.

Reduction of Summary Edges. Figure 12 shows the summary edge reduction rates achieved by MoYe, Gf, and Combined, focusing on programs Cfl can analyze within 6 hours on the original graphs. While higher input graph reductions often correlate with greater summary edge reductions, this is not always the case, as summary edge count also depends on factors like edge types.

In points-to analysis, the average summary edge reductions for MoYE and GF are 85.31% and 77.34%, respectively. Although MoYE removes significantly more graph edges (70.81%) than GF (24.44%), their summary edge reduction rates are comparable because GF specifically targets transitive edges, which propagate reachability and lead to new summary edges. By focusing on transitive edges, GF effectively eliminates summary edges despite a relatively small reduction in graph edges, leaving room for orthogonal optimizations such as those proposed in this work. In



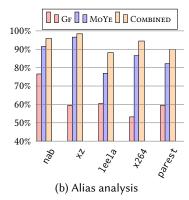


Fig. 12. Reduction rates of summary edges achieved by GF, MoYE and COMBINED in the all-queries setting.

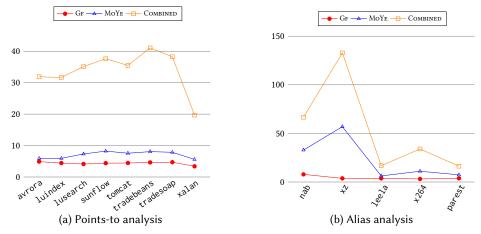


Fig. 13. Speedups in CFL-reachability analysis achieved by GF, MoYE and COMBINED in the all-queries setting.

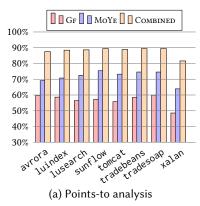
alias analysis, MoY ϵ substantially outperforms GF, achieving 86.47% in summary edge reduction versus 61.38% for GF.

Comparing GF and MoYE individually with their combination, Combined, we observe that Combined achieves significantly greater summary edge reductions across all programs, averaging 96.64% for points-to analysis and 93.28% for alias analysis. Additionally, Combined's performance trends closely mirror that of the better-performing of the two graph simplification techniques.

Performance Improvement. Figures 13 and 14 present the performance speedups and memory reduction rates in CFL-reachability analysis achieved by GF, MOYE, and COMBINED. Across different programs, the trends in these two figures align with the summary edge reduction rates, as both GF and MoYE enhance CFL-reachability performance by limiting the number of inserted summary edges, leading to shorter analysis time and lower memory usage.

In points-to analysis for Java, MoYE outperforms GF with an average speedup of 6.99× (vs. 4.36×) and a memory reduction of 71.65% (vs. 56.7%). For C/C++ alias analysis, GF achieves an average speedup of 4.21× and a memory reduction of 44.28%, while MoYE reaches 15.72× and 80.95%, respectively. Meanwhile, Combined attains an average speedup of 33.17× (38.28×) and a memory reduction of 87.89% (88.05%) for points-to (alias) analysis.

Improved Scalability. Table 4 demonstrates how graph simplification greatly improves the scalability of CFL-reachability analysis for difficult-to-analyze programs. Among the ten Java and



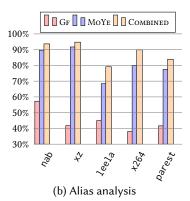


Fig. 14. Reduction rates of memory usage achieved by GF, MoYE and COMBINED in the all-queries setting.

Table 4. Scalability improvements of CFL for ten Java and C/C++ benchmarks that could not be analyzed on original graphs (Tables 2 and 3), using the simplified graphs G_{GF} , G_{MOYE} , and $G_{COMBINED}$ generated by GF, MOYE, and COMBINED, respectively, in the all-queries setting.

Benchmark	Language	G_0	Gr	$G_{ m Mo}$	οYE	G_{Combined}		
Delicilliark	Language	Time	Mem	Time	Mem	Time	Mem	
batik	Java	-	-	-	-	6,618.04	4,449.13	
eclipse	Java	5,602.95	4,645.72	4,435.76	3,345.78	1,128.10	1,552.13	
fop	Java	-	-	-	-	-	-	
h2	Java	18,605.60	9,589.94	11,160.27	6,285.78	3,579.64	3,208.65	
pmd	Java	-	-	-	-	8,127.69	5,307.03	
cactus	C/C++	-	-	-	-	14,307.28	6,274.87	
povray	C/C++	10,681.60	8,920.30	8,216.03	3,589.66	1,901.83	2,039.74	
imagick	C/C++	-	-	11,337.23	4,641.95	2,250.87	2,242.45	
omnetpp	C/C++	-	-	17,980.23	7,983.35	4,908.00	4,979.92	
perlbench	C/C++	-	-	-	-	-	-	

C/C++ programs CFL could not complete within six hours (Tables 2 and 3), using the simplified graphs, G_{GF} , G_{MoYE} , or G_{Combined} , enabled CFL to finish analyzing 3, 5, and 8 of these programs, respectively—underscoring MoYE's significant impact.

6.6 RQ4: Evaluating MoYe's Impact on CFL-Reachability in a Batch Setting

In this final RQ, we evaluate MoYE's performance gains over CFL and GF in a demand-driven batch setting. For each Java or C++ program, we randomly sample 10 query groups Q_i ($i \in [1, 10]$), each containing 1% sources ($V_{\rm src}$) and 1% sinks ($V_{\rm snk}$). For each Q_i , MoYE generates a set of contributing edges E_i . We then invoke CFL to resolve Q_i , starting with a simplified graph $G_{\rm MoYE}^{i,{\rm start}}$ and ending with $G_{\rm MoYE}^{i,{\rm end}}$, which incorporates the summary edges created by CFL. Here, $G_{\rm MoYE}^{i,{\rm start}}$ is built incrementally from $G_{\rm MoYE}^{i-1,{\rm end}}$ by adding $E_i \setminus \bigcup_{j \in [1,i-1]} E_j$ and their incident nodes (with $G_{\rm MoYE}^{0,{\rm end}}$ being empty). This incremental construction reuses reachability results (i.e., summary edges) from $G_{\rm MoYE}^{i-1,{\rm end}}$, obtained while answering Q_{i-1} , to assist with Q_i through a well-known caching mechanism [Reps et al. 1995; Zheng and Rugina 2008]. We define the *contributing edge rate* for the first i batches as $\frac{|\bigcup_{j \in [1,i]} E_j|}{|E|}$, where E is the set of edges in the original graph G of the program being analyzed.

In the batch setting, CFL, GF, and MOYE handle queries differently, allowing us to evaluate MOYE's performance gains below. Both CFL and GF are designed for all-pairs reachability with sources and sinks defined independently of queries (Section 6.1). Consequently, CFL operates directly on

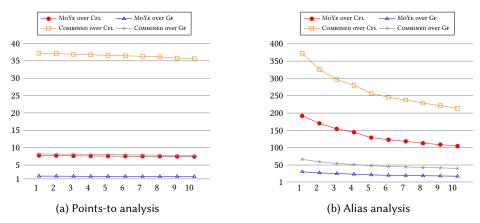


Fig. 15. Speedups achieved by MoYE and COMBINED over CFL (w/o simplification) and GF in the batch setting .

the original graph G, answering all queries $\bigcup_{i \in [1,10]} Q_i$ in one run, taking time t_{CFL} . Meanwhile, GF contracts the same assign-edges for points-to analysis (or a-edges for alias analysis) for every batch Q_i , so it runs once per program, incurring a pre-analysis time p_{GF} to produce the simplified graph G_{GF} . We then invoke CFL on G_{GF} to answer all queries $\bigcup_{i \in [1,10]} Q_i$, incurring time t_{GF} . In contrast, MoYE allows queries to be handled on demand. For each query Q_i , p_{MoYE}^i is the pre-analysis time, and t_{MoYE}^i is the main CFL analysis time (including incrementally adding new contributing edges and their incident nodes to $G_{\text{MoYE}}^{i,\text{start}}$). Therefore, MoYE's speedups over CFL and GF for Q_i are conservatively calculated, including time for previous queries, as follows:

$$\text{Speedup over CFL} = \frac{t_{\text{CFL}}}{\sum_{j \in [1,i]} (p_{\text{MoYE}}^j + t_{\text{MoYE}}^j)}, \quad \text{Speedup over GF} = \frac{p_{\text{GF}} + t_{\text{GF}}}{\sum_{j \in [1,i]} (p_{\text{MoYE}}^j + t_{\text{MoYE}}^j)}.$$

Figure 15 gives MoYe's speedups over CFL and GF. For points-to analysis (Figure 15a), we compare MoYe with CFL on eight benchmarks and with GF on ten benchmarks, where CFL completes within six hours on G and G_{GF} , respectively. For alias analysis (Figure 15b), we similarly compare MoYe with CFL on five benchmarks and with GF on six benchmarks, under the same time constraints.

MoYE achieves significant speedups over both CFL and GF, though these gains gradually decrease as the number of batches increases, as expected (due to the way these speedups are calculated). The rate of decline slows over time. Additionally, MoYE's pre-analysis overhead remains minimal compared to CFL's main analysis (Section 6.3). In Figure 15, MoYE substantially accelerates CFL in both points-to and alias analyses, with performance declining from $7.72\times$ to $7.41\times$ and from $191.93\times$ to $104.66\times$, respectively (red line with circle markers). Similarly, MoYE consistently outperforms GF, with speedups decreasing from $1.8\times$ to $1.69\times$ and from $30.5\times$ to $17.77\times$ in points-to and alias analyses, respectively (blue line with triangle markers). These results underscore MoYE's strong effectiveness in enhancing CFL-reachability in the batch setting, especially for alias analysis.

In Figure 15, Combined substantially outperforms Moye, which had been the best performer among Moye, Gf, and Cfl, in both points-to and alias analyses (orange lines with square markers; gray lines with star markers). The speedups of Combined over Moye range from 4.55× to 4.57× in points-to analysis and from 2.19× to 2.27× in alias analysis, over 10 batches. These results highlight the compatibility of Moye with Gf in further boosting the performance of CFL-reachability analysis.

Table 5 presents the batch-wise contributing edge rates for MOYE and COMBINED. Table 5a shows these rates for points-to analysis, while Table 5b covers alias analysis. In each analysis, for $i \in [1, 10]$, M_i represents the contributing edge rate of the first i batches under MOYE, compared to the all-queries setting (M_{max}). Similarly, C_i and C_{max} are the corresponding metrics for COMBINED.

Table 5. Contributing edge rates by MoYE and Combined. M_i (C_i) represents the contributing edge rate for the first i batches by MoYE (Combined), while M_{\max} (C_{\max}) denotes the rates when considering all queries $(V_{\operatorname{Src}} \times V_{\operatorname{Snk}})$. To save space, $M_{i/\ldots/j}$ $(C_{i/\ldots/j})$ denotes the sequence $M_i/\cdots/M_j$ $(C_i/\cdots/C_j)$. Only programs on which CFL terminates on G or G_{GF} within 6 hours are included.

Batch	avrora	eclipse	h2	luindex	lusearch	sunflow	tomcat	tradebeans	tradesoap	xalan
$M_{1/2}$	26.27/26.30	23.00/23.09	26.12/26.27	24.16/24.21	23.52/23.53	22.40/22.41	23.99/23.99	23.75/23.75	23.75/23.75	24.07/24.15
$M_{3/4}$	26.33/26.35	23.13/23.42	26.48/26.64	24.25/24.30	23.54/23.54	22.53/22.54	24.00/24.00	23.76/23.77	23.76/23.76	24.22/24.29
$M_{5/6}$	26.38/26.40	23.46/23.49	26.77/26.85	24.35/24.40	23.54/23.55	22.55/22.56	24.00/24.01	23.77/23.77	23.76/23.76	24.42/24.50
$M_{7/8}$	26.42/26.44	23.53/23.57	27.01/27.17	24.45/24.50	23.55/23.56	22.57/22.59	24.01/24.02	23.77/23.78	23.77/23.77	24.56/24.60
$M_{9/10}$	26.52/26.55	23.62/23.66	27.30/27.39	24.54/24.57	23.56/23.57	22.60/22.60	24.02/24.03	23.78/23.78	23.78/23.78	24.68/24.76
$M_{\rm max}$	28.98	27.44	34.42	28.27	24.08	23.35	24.46	24.18	24.18	30.53
$C_{1/2}$	19.08/19.10	16.98/17.01	19.60/19.69	17.38/17.40	17.28/17.28	16.59/16.59	17.62/17.62	17.52/17.52	17.52/17.52	18.81/18.86
$C_{3/4}$	19.12/19.13	17.02/17.25	19.82/19.93	17.43/17.45	17.28/17.28	16.60/16.60	17.62/17.62	17.52/17.53	17.52/17.52	18.91/18.96
$C_{5/6}$	19.14/19.15	17.26/17.27	19.99/20.05	17.47/17.50	17.28/17.28	16.60/16.61	17.62/17.62	17.53/17.53	17.52/17.52	19.07/19.12
$C_{7/8}$	19.16/19.17	17.29/17.32	20.16/20.28	17.53/17.56	17.28/17.29	16.61/16.62	17.63/17.63	17.53/17.53	17.53/17.53	19.16/19.19
$C_{9/10}$	19.24/19.25	17.34/17.35	20.37/20.43	17.58/17.60	17.29/17.29	16.62/16.63	17.63/17.63	17.53/17.53	17.53/17.53	19.25/19.30
C_{max}	20.84	19.64	25.21	19.89	17.54	17.04	17.85	17.73	17.73	23.82

(a) Points-to analysis

Batch	nab xz		leela x264		povray	parest
$M_{1/2/3}$	8.30/8.44/8.57	5.44/5.64/5.72	7.39/8.15/9.11	3.17/3.95/4.16	15.75/15.98/16.10	5.81/6.09/6.52
$M_{4/5/6}$	8.82/8.97/9.07	5.93/6.23/6.24	9.63/10.25/10.52	4.44/4.68/4.89	16.27/16.37/16.63	6.68/6.88/7.12
$M_{7/8/9}$	9.21/9.35/9.49	6.27/6.31/6.36	10.74/10.87/11.18	5.37/5.58/5.78	16.69/16.76/16.84	7.30/7.51/7.68
$M_{10/\text{max}}$	9.55/25.32	6.41/19.66	11.55/40.72	5.96/29.50	16.93/28.61	7.89/32.11
$C_{1/2/3}$	5.12/5.18/5.28	3.43/3.56/3.61	3.46/5.23/5.91	1.85/2.05/2.19	8.74/8.89/8.96	3.53/3.71/3.97
$C_{4/5/6}$	5.45/5.53/5.60	3.75/3.93/3.94	6.28/6.74/6.92	2.40/2.56/2.69	9.08/9.14/9.31	4.07/4.21/4.36
$C_{7/8/9}$	5.69/5.77/5.88	3.95/3.98/4.02	7.07/7.16/7.40	2.84/2.98/3.12	9.35/9.40/9.44	4.48/4.61/4.71
$C_{10/\text{max}}$	5.92/16.56	4.05/12.53	7.66/27.58	3.24/18.69	9.50/15.95	4.85/19.97

(b) Alias analysis

Query batches often share many L'-contributing edges due to the regular approximation of L into L' (Section 4.1), enhancing the caching mechanism for summary edges [Reps et al. 1995; Zheng and Rugina 2008]. For points-to analysis (Table 5a), M_i (C_i) are slightly lower than $M_{\rm max}$ ($C_{\rm max}$). Consequently, Figure 15a shows marginally higher speedups in the batch setting compared to the all-queries setting. Since most L'-contributing edges appear in the first batch (M_1 , C_1), speedups decline only slightly over 10 batches. For alias analysis (Table 5b), the batch-based contributing edge rates for MoYE and Combined are noticeably lower than $M_{\rm max}$ ($C_{\rm max}$). As a result, both achieve more significant speedups in the batch setting compared to the all-queries setting (Figure 15b), with speedups declining more visibly across the 10 batches.

7 Related Work

In this section, we review work closely related to our graph simplication approach, focusing on CFL-reachability, graph simplification, and grammar regularization.

7.1 CFL-Reachability

Originally developed for Datalog chain queries in the database community [Bravenboer and Smaragdakis 2009; Jordan et al. 2016; Yannakakis 1990], CFL-reachability has become a key framework in program analysis [Reps 1998]. Recent advancements include subcubic algorithms [Chaudhuri 2008; Zhang et al. 2014], disk-based parallel computation [Wang et al. 2017], transitive redundancy elimination [Lei et al. 2022a], multi-derivation [Shi et al. 2023, 2024b], skewed tabulation [Lei et al. 2024], online cycle elimination [Xu et al. 2024], and staged solving [Shi et al. 2024a]. Efficient algorithms have also been proposed for specialized cases like bidirected Dyck-CFL reachability [Chatterjee et al. 2017; Xu et al. 2009; Zhang et al. 2013]. These efforts focus on boosting efficiency and scalability. MoYE is orthogonal to these online optimizations and can act as a pre-processing step to further improve their performance.

7.2 Graph Simplification

Graph simplification techniques, aiming to optimize language reachability by reducing graph size, must approximate to some degree to minimize pre-analysis runtime overhead, thus preventing performance declines. Guided by recursive state machines, GF approximates the states of nodes to contract trivial edges, extending offline variable substitution methods for general CFL-reachability problems [Alur et al. 2005; Lei et al. 2023b; Rountev and Chandra 2000]. For interleaved-Dyck language reachability, a non-context-free scenario, Li et al. [2020] suggest eliminating specific parenthesis-labeled edges that contribute nothing to interleaved-Dyck paths. This method uses bidirectionality and independent resolutions of multiple CFLs for efficiency and is not directly comparable with MoYE. The mutual refinement approach [Ding and Zhang 2023] generalizes this by intersecting graphs for multiple CFL reachabilities. MoYE, employing regular approximation, uniquely accelerates client-driven CFL-reachability analysis.

7.3 Regular Approximation and Its Applications

Regular approximation of context-free languages is critical for applications like language recognition and efficient parsing [Mohri and Pereira 1998; Nederhof 1998]. MN-transformation [Mohri and Nederhof 2001] is a straightforward and practical technique that maintains grammar readability and modifiability. While more complex methods for tighter approximations exist [Eğecioğlu 2009], our experience shows that they often lead to combinatorial explosions with minimal precision gains. In context-sensitive pointer analysis, CFL-reachability helps identify precision-critical variables or objects. These are treated context-sensitively to retain analysis precision, while others are handled context-insensitively to reduce performance costs [He et al. 2021; Lu and Xue 2019]. This involves verifying conditions against multiple interleaved CFLs, often using regular approximations for efficiency. In this work, we utilize regular approximation to prune non-contributing edges from the input graph, significantly enhancing CFL-reachability performance.

8 Conclusion

In this paper, we introduce MoYE, a conceptually simple yet highly effective regularization-based graph simplification technique for enhancing CFL-reachability analysis. MoYE uses a classic regular approximation to convert a context-free language into a regular one. By analyzing the transition rules of a deterministic finite automaton—an equivalent form of a regular language—we identify key contributing edges for CFL-reachability. Extensive experiments in field-sensitive points-to analysis for Java and alias analysis for C/C++ show that MoYE not only accelerates CFL-reachability analysis but also outperforms a leading graph simplification method. When combined with this method, MoYE delivers further performance gains, underscoring its utility in optimizing CFL-reachability analysis. In batch settings, MoYE continues to boost CFL-reachability analysis performance.

Future work could explore developing a tighter approximation that maximizes the removal of non-contributing edges while maintaining the efficiency of pre-analysis. Additionally, tailoring the approximation specifically for program analysis workloads could be beneficial, as the context-free grammars used in CFL-based analyses often exhibit simpler structures.

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Data Availability Statement

Our artifact is publicly available at [Shi et al. 2025].

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