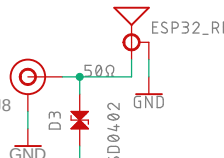


ESP32 WROVER

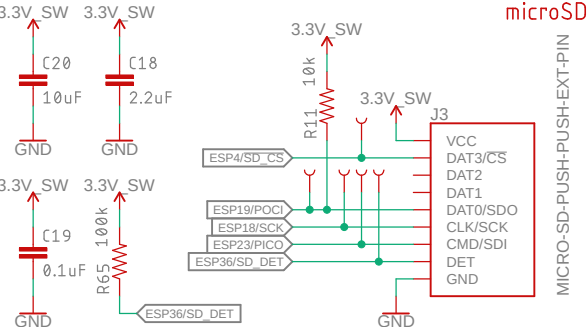
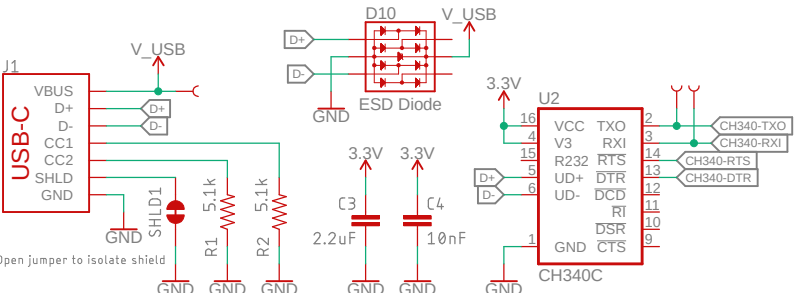
Use boot control pins with caution: 0, 2, 5, 12, 15

I00: Avoid device connections. Can be used as a status LED.
I02: Avoid external pullups – will cause bootloader fail.
I05: Has built-in pullup at POR.
I012: Avoid external pullups – will cause bootloader fail.
I015: Has built-in pullup at POR.

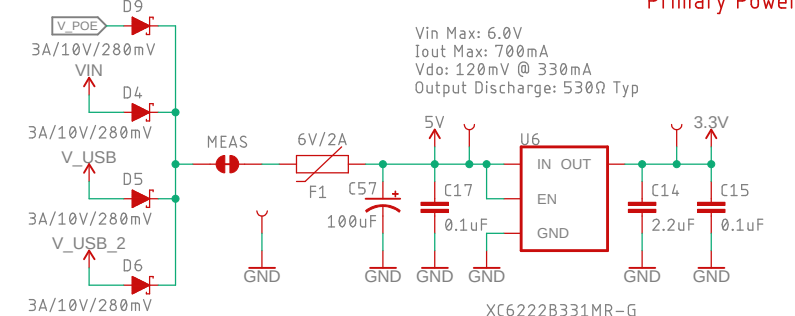
ADC2 is unavailable when WiFi is enabled.



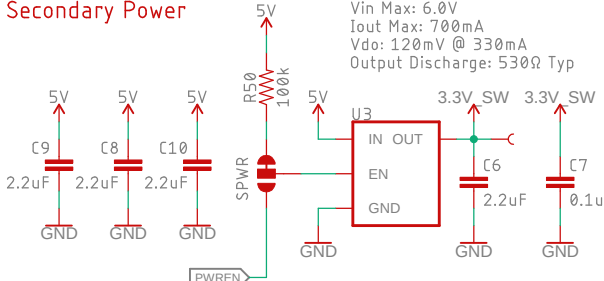
ESP32 USB-C



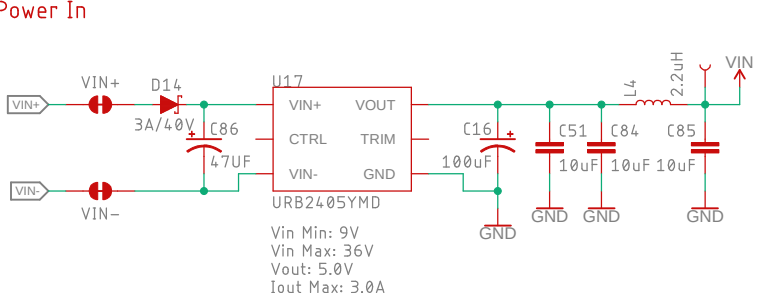
microSD



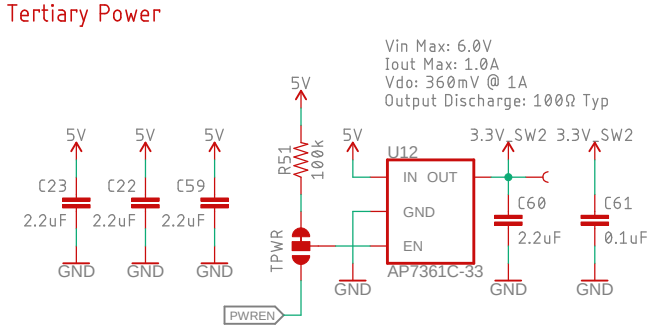
Primary Power



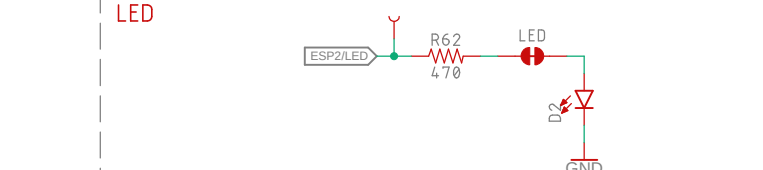
Secondary Power



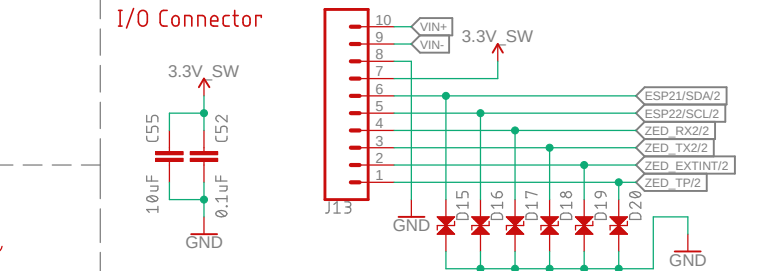
Power In



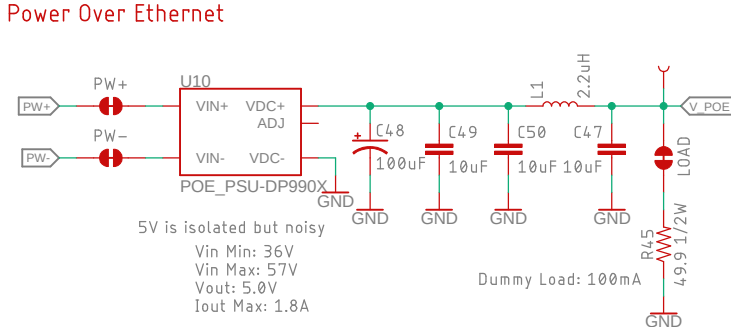
Tertiary Power



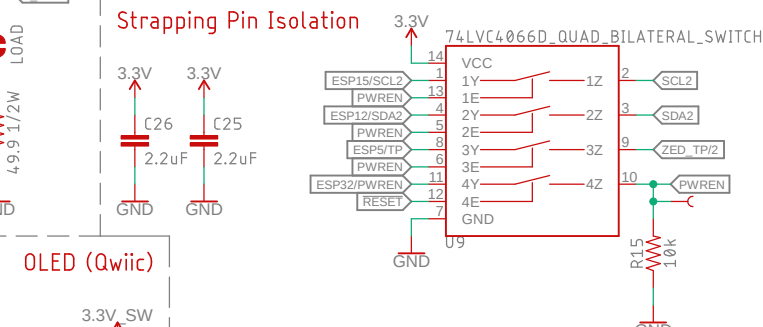
ED



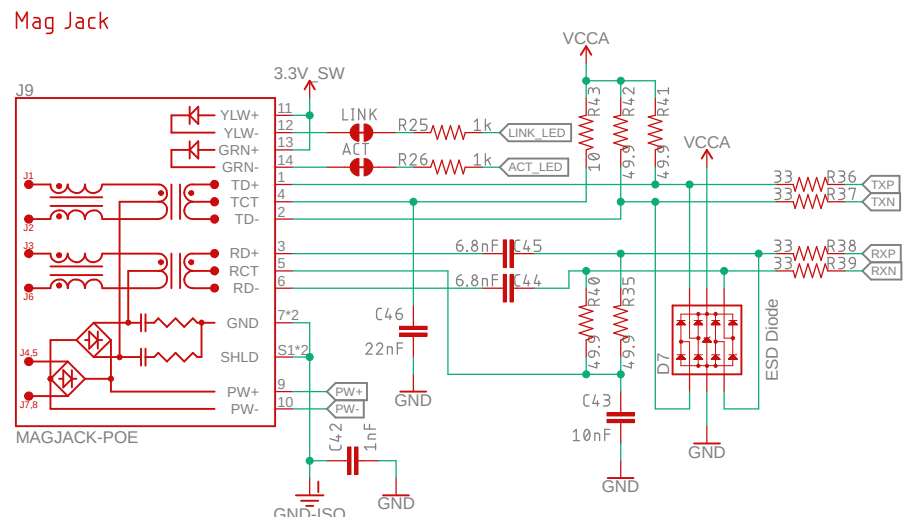
I/O Connecto



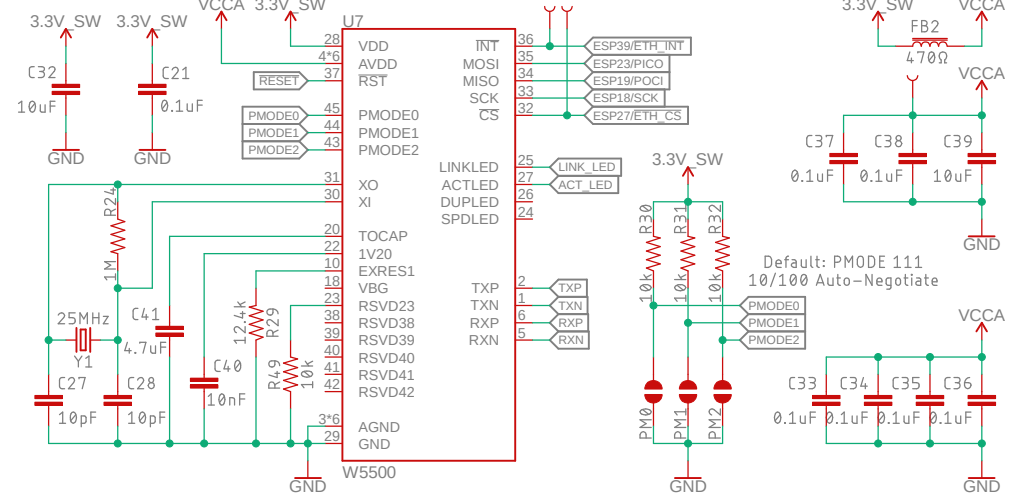
Power Over Ethernet



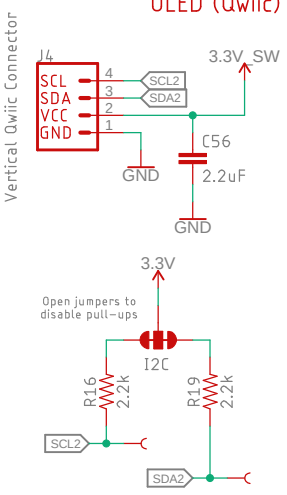
Trapping Pin Isolation



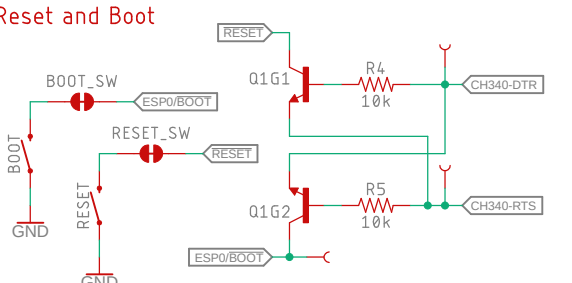
Mag lack



W5500



LED (QWHE)



Reset and Boot

Boot Mode Configuration			
Pin	Run	Bootloader	Default
I00	1	0	1
I02	x	0	0
I05	1	x	1
I012	0	0	0
I015	x	x	1

I012 – MDIO I015 – MDIO

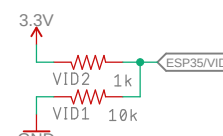
If DTR is LOW,
toggling RTS from HIGH to
LOW resets to run mode.

If RTS is HIGH,
toggling DTR from LOW to
HIGH resets to bootloader.

USB Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
 Prepreg thickness: 0.2mm (7.87 mil). Er = 4.6
 10.5 mil track with 9.5 mil gap (20 mil center to center) = 90 Ohms

RF Track Impedance: Coplanar Waveguide with Ground Calculations
 Ground is on layer 2. Prepreg thickness: 0.2mm. Er = 4.6
 13.74 mil track with 8 mil gap = 50 Ohms
<https://saturnpcb.com/saturn-pcb-toolkit/>

Product Identification



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Special Instructions



TITLE: SparkFun_RTK_EVK	
Design by: Paul Clark	REV: v11
Date: 08/02/2024 16:06	Sheet: 1/2

