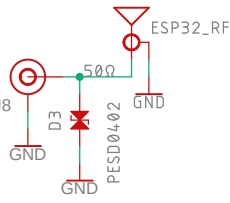


ESP32 WROVER

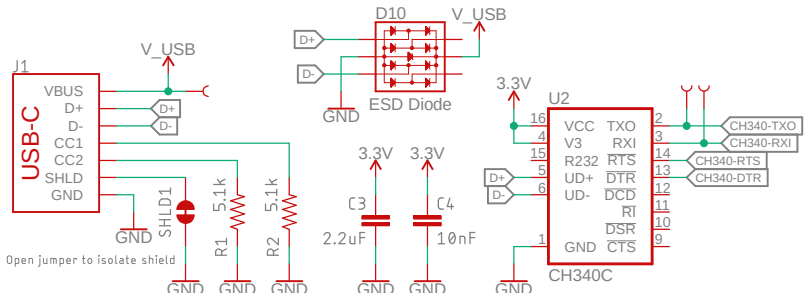
Use boot control pins with caution: 0, 2, 5, 12, 15

I00: Avoid device connections. Can be used as a status LED.
I02: Avoid external pullups – will cause bootloader fail.
I05: Has built-in pullup at POR.
I012: Avoid external pullups – will cause bootloader fail
I015: Has built-in pullup at POR.

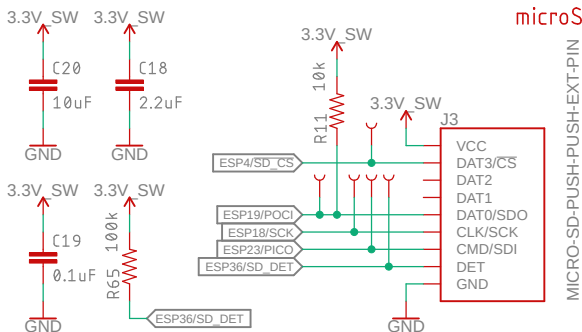
ADC2 is unavailable when WiFi is enabled.



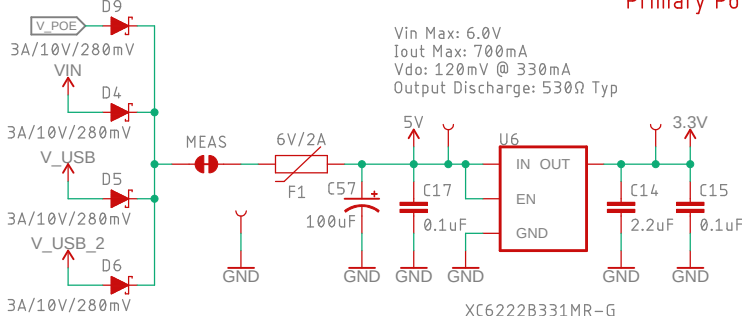
ESP32 USB-C



microSD

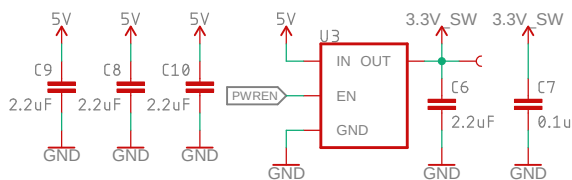


Primary Power

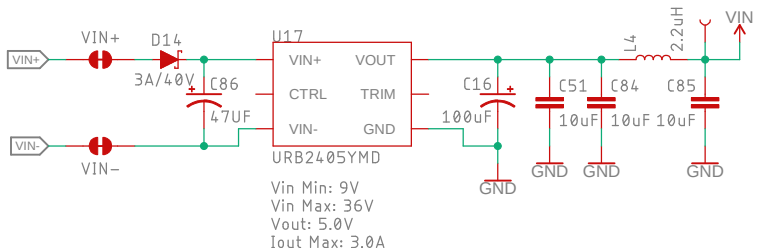


Secondary Power

Vin Max: 6.0V
Iout Max: 700mA
Vdo: 120mV @ 330mA
Output Discharge: 530Ω Typ

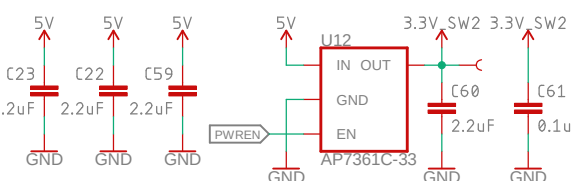


Power In

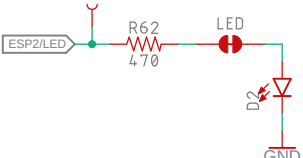


Tertiary Power

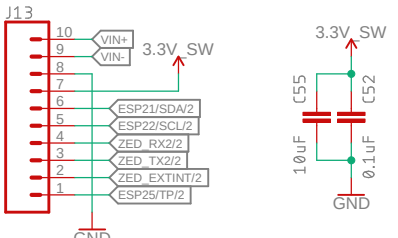
Vin Max: 6.0V
Iout Max: 1.0A
Vdo: 360mV @ 1A
Output Discharge: 1000 Typ



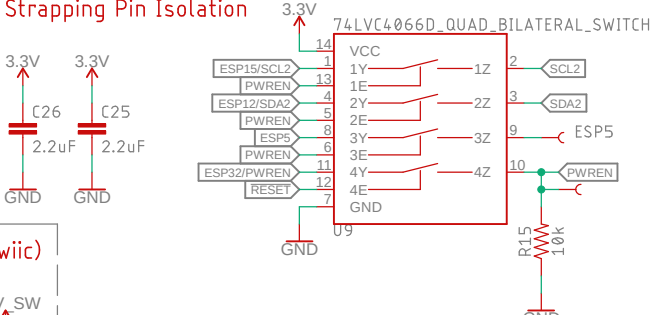
LED



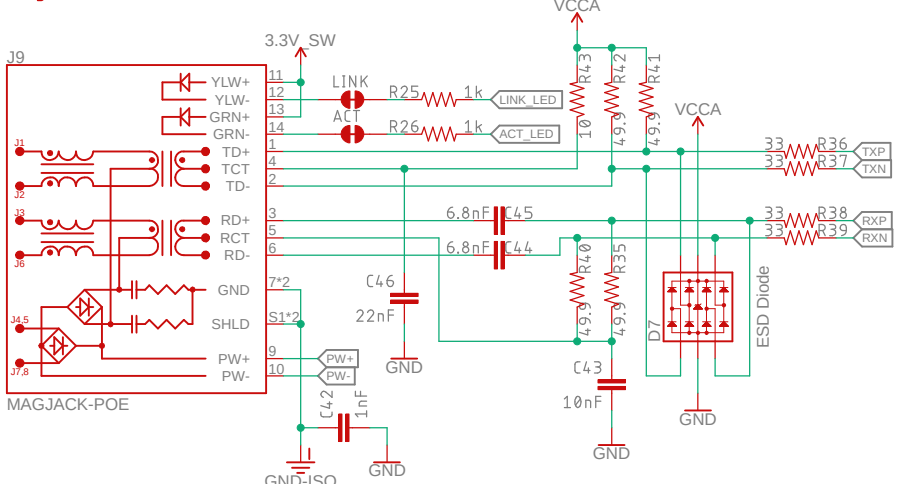
I/O Connector



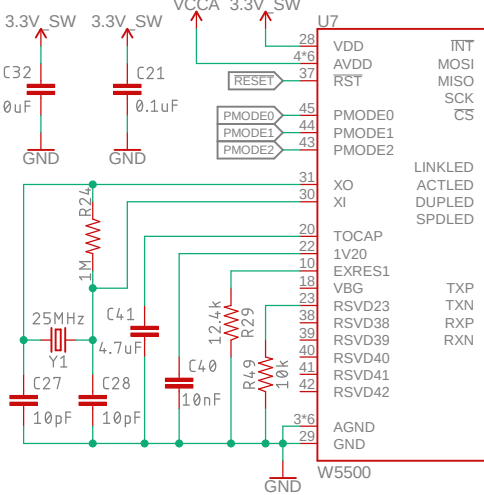
Strapping Pin Isolation



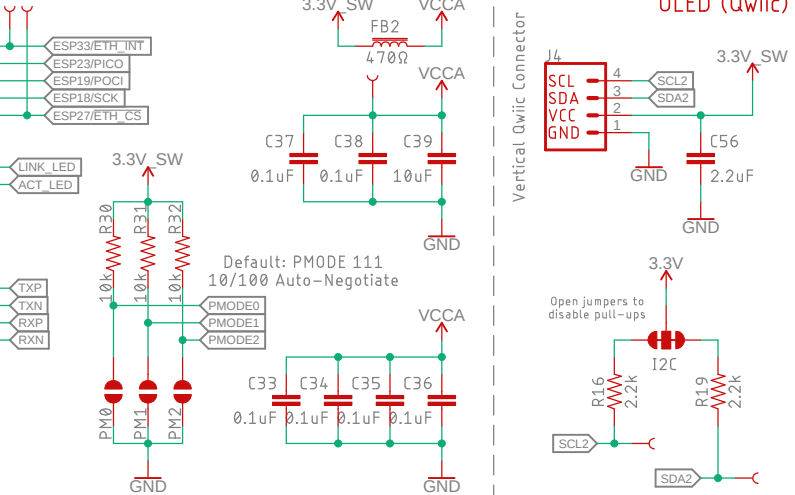
Mag Jack



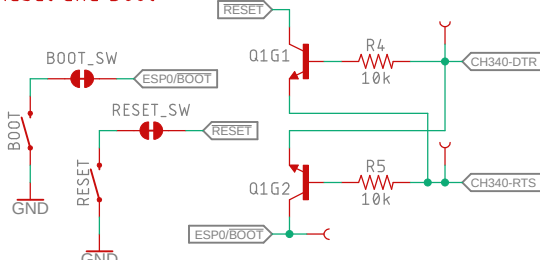
W5500



OLED (Qwiiir)



Reset and Boot



Boot Mode Configuration			
Pin	Run	Bootloader	Default
I00	1	0	1
I02	x	0	0
I05	1	x	1
I012	0	0	0
I015	x	x	1

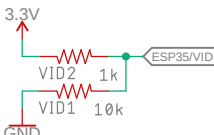
If DTR is LOW,
toggling RTS from HIGH to
LOW resets to run mode.

If RTS is HIGH,
toggling DTR from LOW to
HIGH resets to bootloader

USB Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
 Prepreg thickness: 0.2mm (7.87 mil). Er = 4.6
 10.5 mil track with 9.5 mil gap (20 mil center to center) = 90 Ohms

RF Track Impedance: Coplanar Waveguide with Ground Calculations
Ground is on layer 2. Prepreg thickness: 0.2mm. Er = 4.6
13.74 mil track with 8 mil gap = 50 Ohms
<https://saturnpcb.com/saturn-pcb-toolkit/>

Product Identifier



RTK Product Voltage IDs:

Surveyor: Pin 35 floats at 0.18V approx.
 Express: 3.3 / 13.3 = 0.82V (0.74 - 0.90)
 Reference Station: 10 / 30 = 1.1V (0.99 - 1.21)
 Facet: 10 / 20 = 1.65V (1.49 - 1.82)
 Facet L-Band: 20 / 30 = 2.2V (1.98 - 2.42)
 Express Plus: 10 / 13.3 = 2.48V (2.23 - 2.73)
 Everywhere: 10 / 11 = 3.00V (2.70 - 3.30)



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Special Instructions



TITLE: RTK Everywhere L-Band Cellular

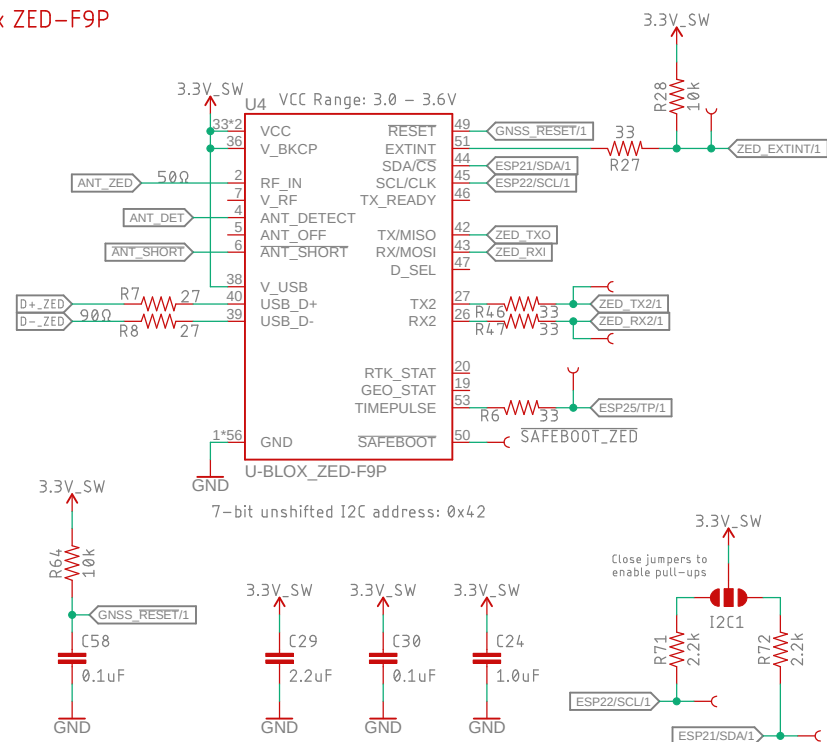
Design by: Paul Clark

REV:
v01

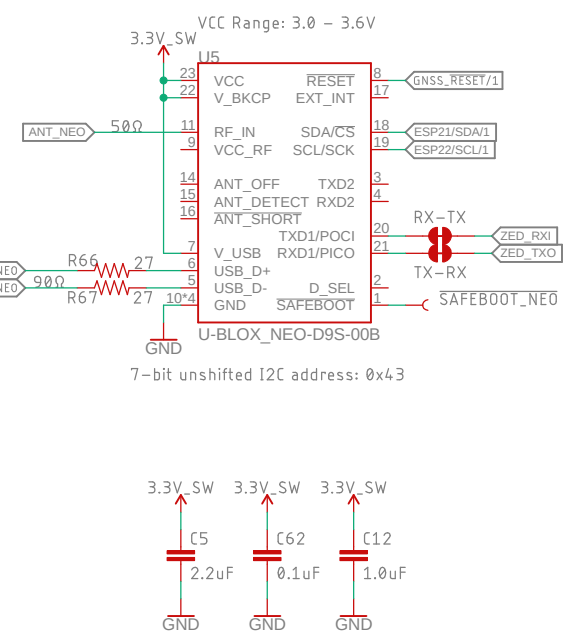
Date: 27/07/2023 10:49

Sheet: 1/2

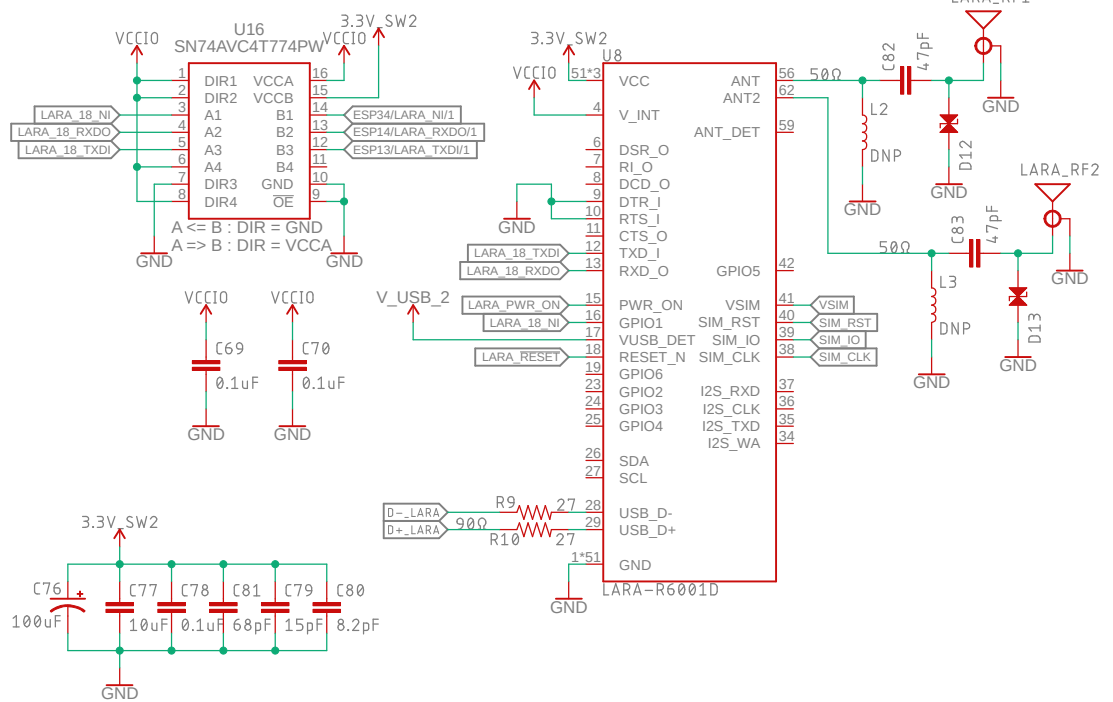
u-blox ZED-F9P



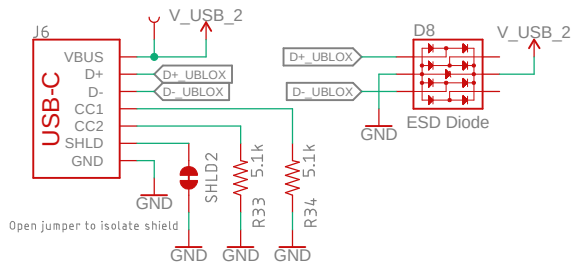
u-blox NEO-D9S



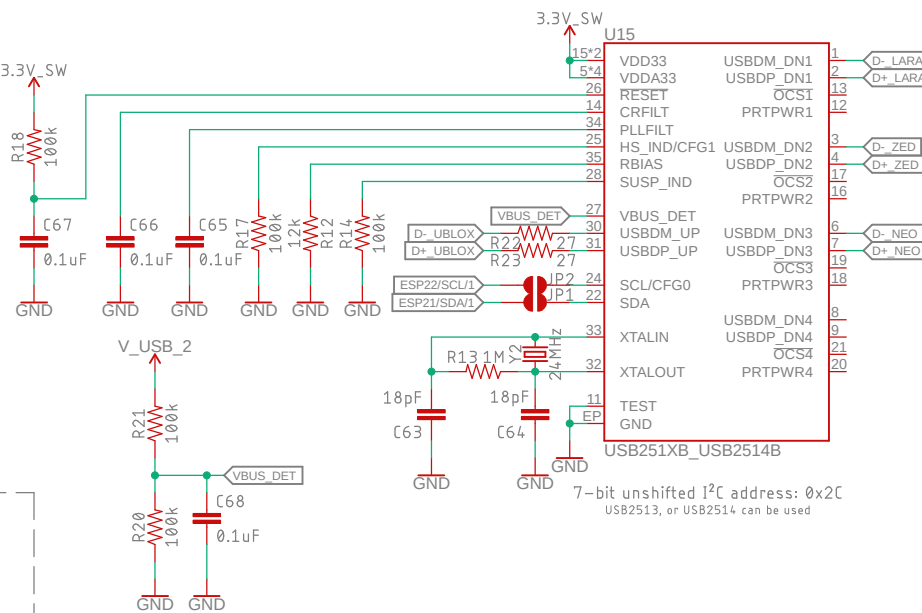
u-blox LARA-R6001



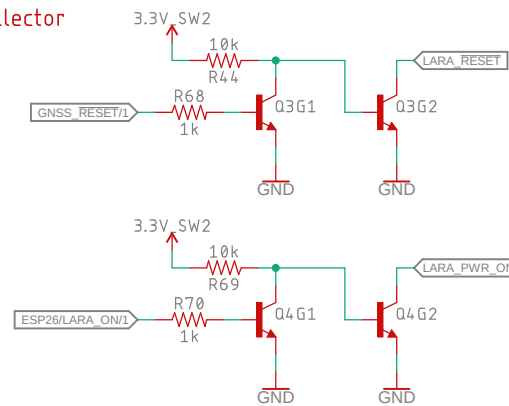
USB Hub USB-C



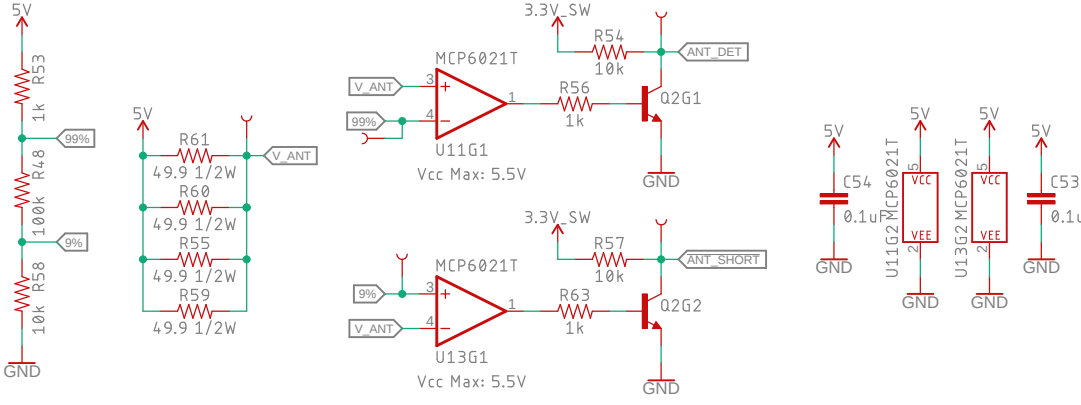
USB Hub



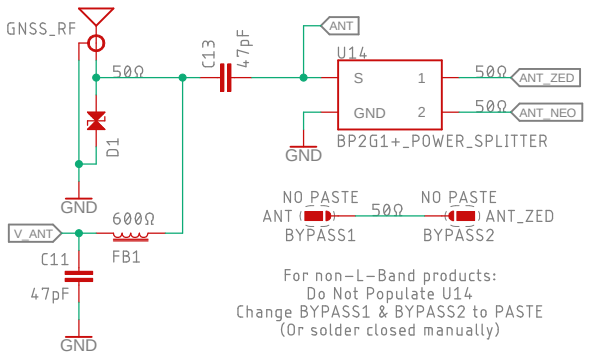
Open Collector Outputs



Antenna Power



Antenna Splitter



SIM

