[EE303A] Digital System Design

Lab #4

- 반드시 마감 기한을 지켜주세요.
 Please keep the due date in mind.
- 질문이 있으면 (홍성민, seongminhong@kaist.ac.kr)로 연락주세요. If you have any question, feel free to mail TA (seongminhong@kaist.ac.kr).
- This project's purpose is to understand the sequential logic.
 This project is to design MAC (Multiply-Accumulate) as below figure.
- Write your code on "MAC.v".
- Testbench code tests 1000 cases. Each case is 0.01 point and full score is 10 points (Decimal rounding up, e.g. 5.5 points is 6 point)



