

Instruction 1 SS1

Instruction 2 sahanabranch = Branch1 leenabranch = Branch2

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Model Control (1997) School (1
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MNOWWeb/c/Users/#0/Destrop/Sahana/TeamI/DevOps (main)
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4 ptl checkout statis
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Instruction 2

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MANOVINEA//Uters/HP/Desktop/Sahara/Tearl/DevOps (main)
g it branch - Sahanabranch
Deleted branch sahanabranch
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Deleted branch sahanabranch
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Set of the Processor of the Processor
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Instruction 3

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MNOWS64/c/Users/HP/Desktop/Sahana/Teaml/DevOps/DevOps

— □ X

**PRIDESATTOP-3VCRRV8 MINOWS4 -/Desktop/Sahana/Teaml/DevOps/DevOps (Branch3)

**git e/bekout main

**git e/bekout main

**our branch is up to date with 'origin/main'.

**PRIDESATTOP-3VCRRV8 MINOWS4 -/Desktop/Sahana/Teaml/DevOps/DevOps (main)

**git add .

***PRIDESATTOP-3VCRRV8 MINOWS4 -/Desktop/Sahana/Teaml/DevOps/DevOps (main)

**git commit on "morae check"

**git changed, 1 insertion(*)

**git commit on "morae check"

**git changed, 1 insertion(*)

**green to the changed, 1 insertion(*)

**green
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| MINISTER | MINISTER
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Instruction 3 conflict resolve

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Months/Action/Products/State/plane/Team/Decogn/Decogns (branch)

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Instruction 5

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| March | Marc
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Instruction 6

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### PROSESTOP-TYCERV$ MINGNES -/Desktop/Sahana/TeamI/DevOps/DevOps (main)

1 git branch

1 penabranch

1 penabranch

1 penabranch

2 git branch

2 git branch

3 git checkout Teenabranch

5 git checkout Teenabranch

5 git checkout Teenabranch

5 git checkout Teenabranch

5 git checkout Teenabranch

1 penabranch

1 penabranch

1 penabranch

2 git menach 1 penabranch

2 git menach 1 penabranch

3 git reset 1 penabranch

2 git reset 1 penabranch

3 git reset 1 penabranch

4 penabranch

5 git reset 1 penabranch

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5 git reset 1 penabranch

5 git reset 1 penabranch

6 penabranch

1 penabranch

2 penabran
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