1. Main Objective
   1. To determine what, if any, effect radio-frequency emissions from external sources have on clock drift.
      1. What kind of clock?
      2. Is specifying external sources needed?
   2. To determine the effect of radio-frequency emissions from external sources on clock drift.
      1. For this one I removed the “if any”, since we are simply looking for the relationship. If there is no trend seen, that will be the relationship: no relationship.
   3. To establish the relationship between internal RC oscillator frequency drift and radio frequency emissions.
      1. Is this too indirect?
   4. To establish the relationship between internal RC oscillator frequency drift and the energy…blah blah
2. Secondary Objectives
   1. Preparation
      1. Choose 4 popular vendors
         1. Microchip Technologies (PIC), Texas Instruments, STMicroelectronics, NXP Semiconductor.
      2. Choose 5 part numbers
         1. What are they?
            1. What is the justification of our selection method
      3. To establish stable clock sources for each device being tested.
         1. Find a method to determine the stability of clock sources under test.
      4. To establish a stable system for each device
         1. External circuitry will not affect
   2. Controlling factors during the experiment to ensure valid data.
      1. To eliminate temperature factors while testing devices.
      2. To eliminate ambient radiation from the testing environment.
         1. Environment RF radiation, environment light radiation, EMI from power sources.
      3. To eliminate mechanical (sound) energy from affecting
      4. To prevent the RF radiation source from affecting test equipment to preserve the integrity of data.
   3. The experiment
      1. To determine a control for the experiment of internal RC oscillator drift by testing 3 units of 5 part numbers from 4 popular vendors
3. The problem and significance of proposed work
   1. The problem
      1. Without data on the effects of RF emissions on internal RC oscillators, clock source selection is based on a conservative approach to unknown.
   2. Significance of proposed work
      1. Collecting data on these effects will allow a method for effectively choosing a clock source based known risk and performance.
      2. Removing unknown aspects of MCU internal RC oscillators will increase their viability as a device clock source. This will ultimately lead to the potential of reducing cost and board space by removing the need for an external oscillator.
      3. To find new data on internal RC oscillators
         1. To determine the viability of the use of internal RC oscillators on devices potentially going into space.
         2. To determine the viability of the use of internal RC oscillators on devices operating near sources of RF emissions.

* Notes
  + For objectives
    - What the objective is
    - Then how to do it in the research plan
  + For objectives
    - We are testing MCU
      * “To measure clock drift by X”
      * “To establish an accurate method of measuring clock drift”