Primary OP	RDest	ALUOP/ImmHi	Rsrc/ImmLo		
Name	Operands	15-12	11-8	7-4	3-0
INSTR	RDest, RSrc	0000	RDest	OPCODE	RSrc
INSTRI	RDest, Imm	OPCODE	RDest	ImmHi	ImmLo
ADD	RDest, RDest	0000	RDest	0010	RSrc
SUB	RDest, RDest	0000	RDest	0011	RSrc
AND	RDest, RDest	0000	RDest	0110	RSrc
OR	RDest, RDest	0000	RDest	0111	RSrc
XOR	RDest, RDest	0000	RDest	1000	RSrc
NOT	RDest, RDest	0000	RDest	1001	RSrc
LSH	RDest, RDest	0000	RDest	1010	RSrc
RSH	RDest, RDest	0000	RDest	1011	RSrc
ARSH	RDest, RDest	0000	RDest	1100	RSrc
MUL	RSrc2, RSrc	0000	RSrc2	1101	RSrc
FMUL	RDest, RSrc	0000	RDest	1110	RSrc
MOVR	RDest, RSrc	0000	RDest	1111	RSrc
Compares					
CMP (A-B)	A, B	0000	A	0100	В
CMP (Imm-A)	A, Imm	0100	A	ImmHi	ImmLo
CMPR (B-A)	B, A	0000	A	0101	В
CMPR (A-Imm)	Imm, A	0101	A	ImmHi	ImmLo
,					
Primary OP	Data	OP	Address		
Name	Operands	15-12	11-8	7-4	3-0
MOVMR	RAddr, RDest	0001	RDest	0010	RAddr
MOVRM	RAddr, RSrc	0001	RSrc	0011	RAddr
OP	Imm				
Name	Operands	17-14	13-0		
CALL	Imm Addr	0100	Imm		
MOVMRI	Imm Addr	0101	Imm		
MOVRMI	Imm Addr	0110	Imm		
RET	n/a	0111	n/a		
OP	Imm				

Name	Operands	17-13
JL	Imm Addr	10000
JLE	Imm Addr	10001
JNE	Imm Addr	10010
JE	Imm Addr	10011
J	Imm Addr	10100
JBE	Imm Addr	10100
JB	Imm Addr	10101
30	Illim ricar	10110
OP	Operand	ImmLo
Name	Operands	17-14
POP	RDest	1100
PUSH	RSrc	1100
PUSHI	Imm	1110
r OSIII	1111111	1110
Name	Operands	17-12 (OP)
INCR	R	111100
DECR	R	111100
DDOIL		111101
Psuedoinstructions with special formatting		
Moves		
MOV [%R], %R		Move from a register in
MOV %R, [%R]		Move from memory at a
MOV [IMM/LABEL], %R		Move from a register in
MOV %R, [IMM/LABEL]		Move from memory at
MOV %R, [LABEL+constant]		Move from memory at
MOV [LABEL+constant], %R		Move to memory at a la
MOV %R, %R		Move a register to anot
MOV %R, IMM		Move a register to another Move an immediate up
1410 4 7010, 111111		IVIOVO dil Imiliodi
Special Jump Formats		
JUMP A, B, LABEL		Jump to label if the tw
Works with the following jump types		Jump to laser it the time
JE	JNE	JA*
(NOTE: jumps with asterisks are emulated using reverse compares	JILL	JA
(NOTE), jumps with assertions are emulated using reverse compared	I	I