

10 9 8 7 6 5 4 3 2 1

F

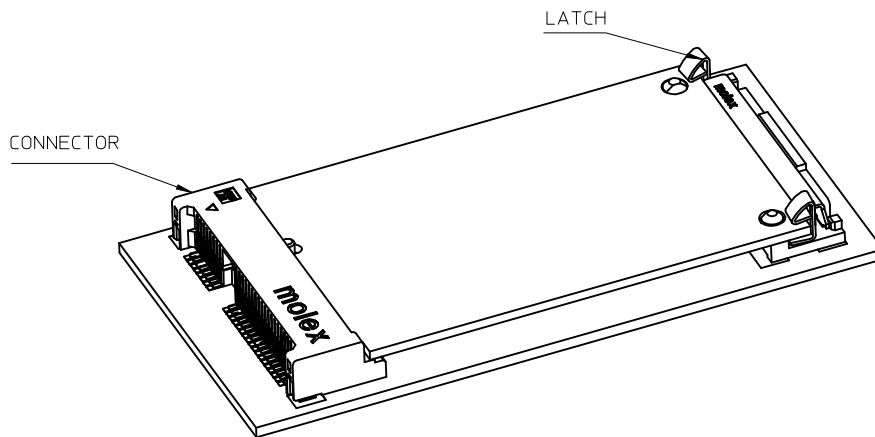
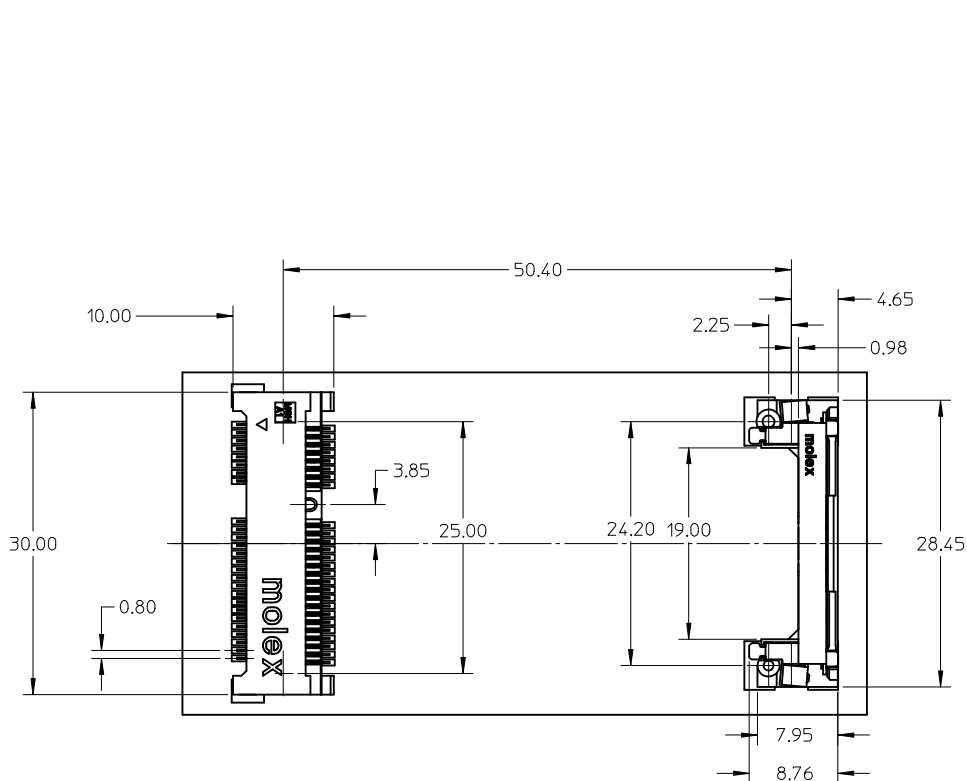
E

D

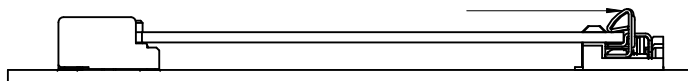
C



B

A

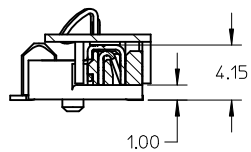


PUSH LATCH TO RELEASE MODULE

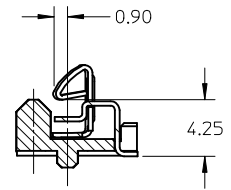
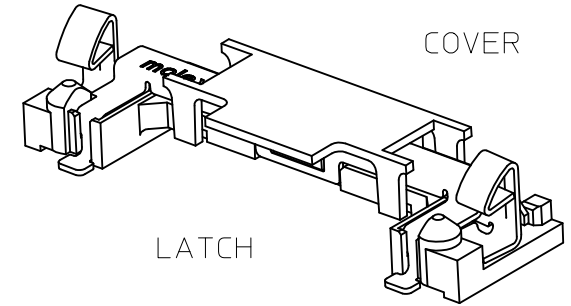
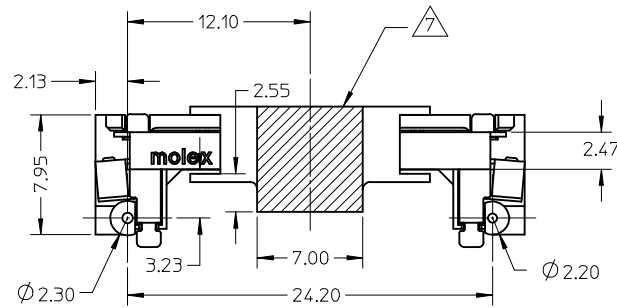


REVISED EC NO: SH2009-0388 DRW: ZHAO CHKD: APPR: HWANG	QUALITY SYMBOLS ▽=0 ◇=0	DESCRIPTION	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 1:1	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION		
				mm	INCH	DRAWN BY DAVID HU	DATE 2005/12/14	TITLE LATCH FOR MINIPCI-E 0.8MM PITCH 52 CIRCUIT LEAD-FREE			
			4 PLACES	± ---	± ---	CHECKED BY HARVEY	DATE 2005/12/14				
			3 PLACES	± ---	± ---	APPROVED BY GARY	DATE 2005/12/14			 MOLEX INCORPORATED	
			2 PLACES	± 0.25	± ---	MATERIAL NO. 48099-5701					DOCUMENT NO. SD-48099-006
1 PLACE	± 0.25	± ---	SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						
			ANGULAR ± 3 °								
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS								

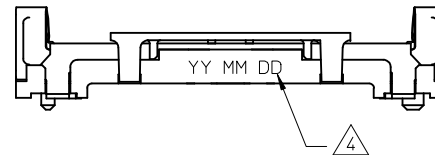
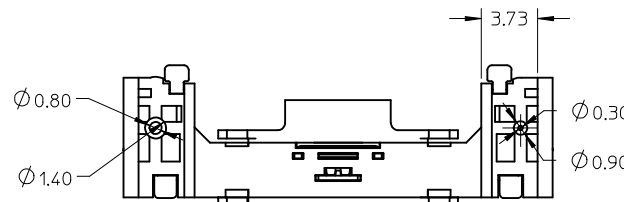
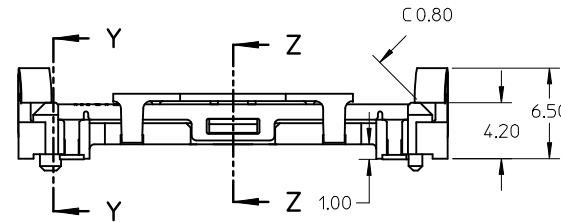
9 8 7 6 5 4 3 2 1



SECTION Z-Z
Scale 3:1

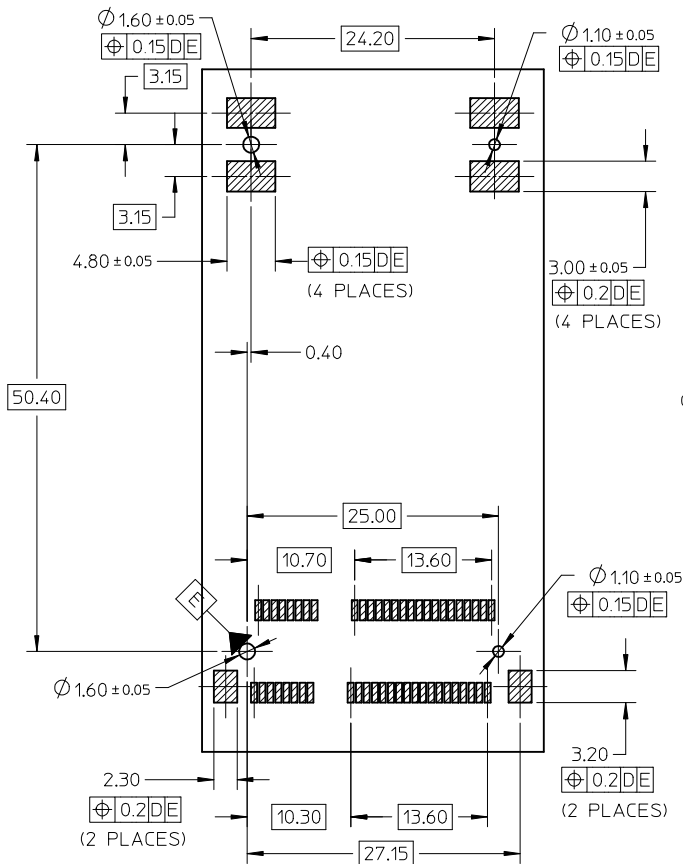


SECTION Y-Y
Scale 3:1



NOTES:

1. THIS LATCH USES WITH MINIPCI EXPRESS CONN.
2. HOUSING:PA46 GLASS FILLED,COLOR:BLACK UL94V-0
METAL SHELL:STAINLESS STEEL,THICKNESS:0.3MM
BEAM:STAINLESS STEEL,THICKNESS:0.3MM
3. FINISH:GOLD FLASH PLATED IN SOLDER AREA.
4. DATE CODE:YY:YEAR MM:MONTH DD:DATE
5. THE MOUDLE CARD REFERS TO MINI PCI EXPRESS CARD
ELECTROMECHANICAL SPECIFICATION.
6. MATCHED CONNECTOR:67910 SERIES.
7. FOR PICK AND PLACE AREA.
8. PART 48099-5701 COMPLIANT TO ROHS DIRECTIVE 2002/95/EC
AND ELV DIRECTIVE 2000/53/EC.



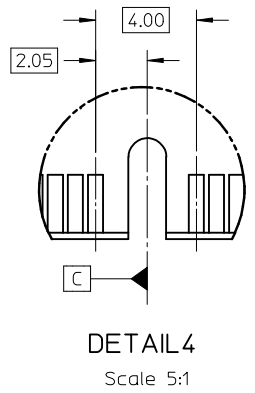
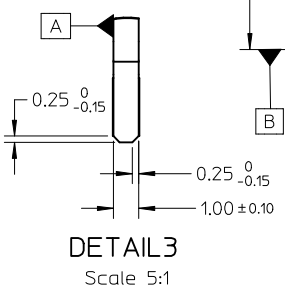
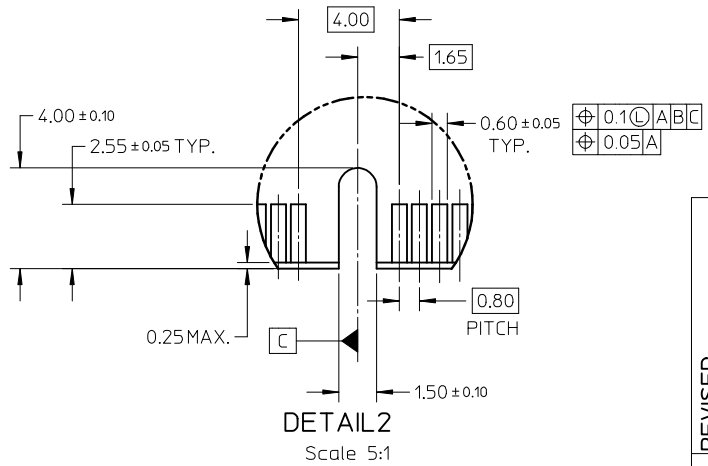
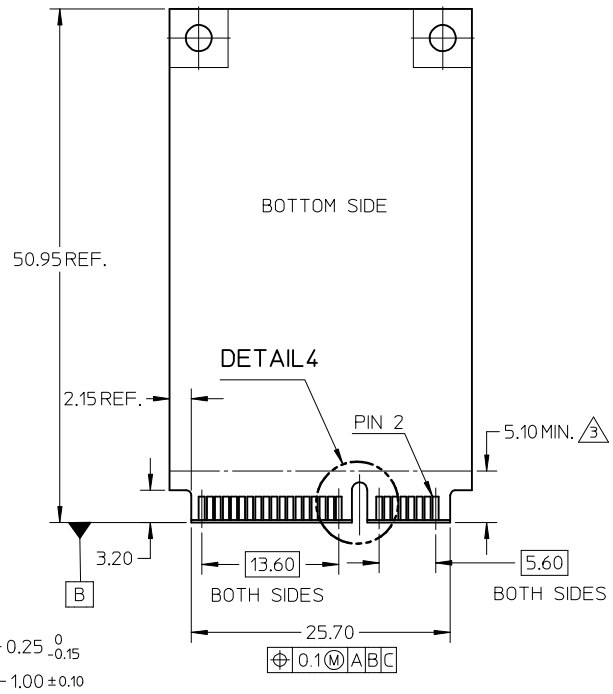
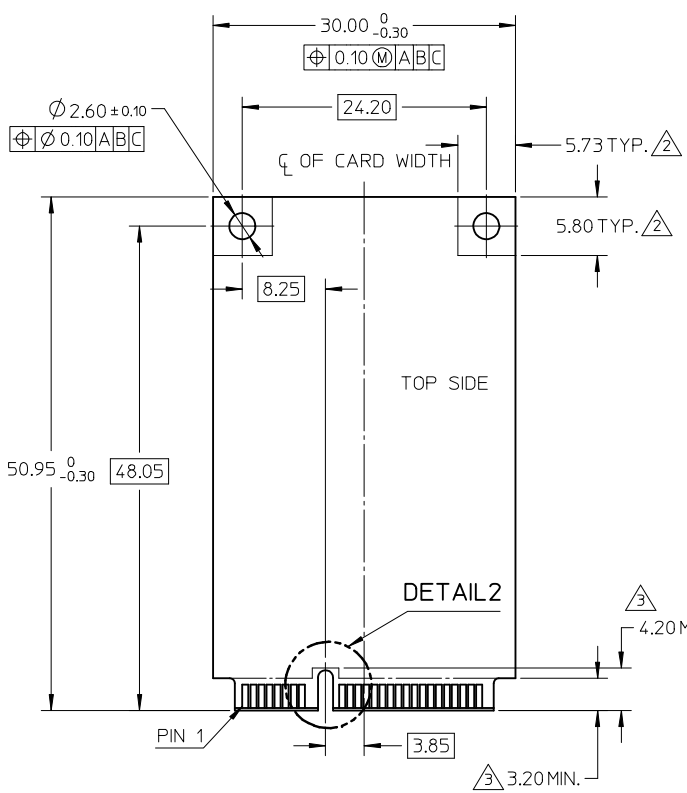
DATUM D IS THE TOP SURFACE OF PCB




RECOMMENDED PCB LAYOUT

REVISED EC NO: SH2009-0388 2008/12/03 DRW:NEZHAO CHKD: 2008/12/04 APP:HWANG 2008/12/09	DESCRIPTION	QUALITY SYMBOLS ▽=0 C=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 1:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
				mm	INCH	DRAWN BY DAVID HU	DATE 2005/12/14	TITLE LATCH FOR MINIPCI-E 0.8MM PITCH 52 CIRCUIT LEAD-FREE		
			4 PLACES	± ---	± ---	CHECKED BY HARVEY	DATE 2005/12/14			
			3 PLACES	± ---	± ---	APPROVED BY GARY	DATE 2005/12/14	MOLEX MOLEX INCORPORATED		
			2 PLACES	± 0.25	± ---	MATERIAL NO. 48099-5701	DOCUMENT NO. SD-48099-006		SHEET NO. 2 OF 3	
			1 PLACE	± 0.25	± ---	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		
			ANGULAR ± 3 °							

10 9 8 7 6 5 4 3 2 1

NOTES,
1. PIN NUMBERING REFERENCE:
ODD PINS-TOP SIDE
EVEN PINS-BOTTOM SIDE
2. COMPONENT AND ROUTING(ALL LAYERS)
KEEP OUT AREA FOR HOLD DOWN SOLUTIONS
3. COMPONENT KEEP OUT AREA FOR CONNECTOR



REVISED EC NO: SH2009-0388 DRAWN: EZHAO CHKD: APPR: HWANG	DESCRIPTION REV	QUALITY SYMBOLS		GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 1:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
		 =0  =0			mm	INCH	DRAWN BY DATE DAVID HU 2005/12/14		TITLE LATCH FOR MINIPCI-E 0.8MM PITCH 52 CIRCUIT LEAD-FREE  MOLEX INCORPORATED				
			4 PLACES	± ---	± ---	CHECKED BY DATE 2005/12/14							
			3 PLACES	± ---	± ---	HARVEY 2005/12/14							
			2 PLACES	± 0.25	± ---	APPROVED BY DATE 2005/12/14							
			1 PLACE	± 0.25	± ---	GARY 2005/12/14							
				ANGULAR ± 3 °		MATERIAL NO. 48099-5701		DOCUMENT NO. SD-48099-006		SHEET NO. 3 OF 3			
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

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