

Hanle Zhang

University of Chinese Academy of Sciences, Beijing, PRC, 100049
✉ Email: zhanghanle21@mailsucas.ac.cn / peter.zhang@berkeley.edu

EDUCATION

University of Chinese Academy of Sciences(UCAS) Bachelor of Computer Science and Technology (expected 2025)	Sept. 2021-Present Current GPA: 3.95/4 Ranking: 2/92
University of California, Berkeley Visiting Student in EECS Department	Jan. 2024-Present Courses: CS152, CS170, CS188

ACADEMIC EXPERIENCE

"One Student One Chip" Program Engineering Intern Beijing Institute of Open Source Chip	July 2022-Aug. 2022
<ul style="list-style-type: none">Completed some functions of a software ISA emulator NEMU using C language: achieved a simple debugger and implemented some functions of C library string.h and stdio.hDeveloped a multi-cycle RISC-V processor using Verilog	
FPGA Verification Platform Engineering Intern Institute of Computing Technology, Chinese Academy of Sciences	July 2023-Aug. 2023
<ul style="list-style-type: none">Finished an HLS version of LeNet running on an FPGA, which was served as one of the verification examples for a FPGA-based HLS verification platform ENCOREUsed Vitis to synthesize IP core from LeNet's HLS design, built boot and root, started Linux on ZYNQ Sidewinder board and wrote bit stream on configurable fabricTrained parameters using Python version LeNet, wrote a memory mapping program to load parameters to FPGA	
International Summer School Institute of Computing Technology, Chinese Academy of Sciences	Aug. 2023
<ul style="list-style-type: none">Attended lectures on cloud computing systems, SMT formal verification, neural networks and deep learningParticipated in seminars on FPGA, Emotion AI and NeRF	

PROJECTS

Computer Organization and Design Course Project Institute of Computing Technology, Chinese Academy of Sciences
<ul style="list-style-type: none">Developed a multi-cycle MIPS processor and a RISC-V processor with Instruction Cache and Data CacheImplemented some functions of a DNN accelerator
Computer Architecture Project Institute of Computing Technology, Chinese Academy of Sciences
<ul style="list-style-type: none">Co-developed a pipelined LoongArch processor using VerilogSupported exception handling, AXI bus interface, virtual memory, ICache

HONORS & AWARDS

Excellent Student in UCAS	June 2023
The First Prize of Scholarship in UCAS	Nov. 2022
Outstanding Student Leader in UCAS	June 2022

SKILLS AND HOBBIES

TOEFL: 113 (R29, L30, S25, W29)	Aug. 2023
Programming: C, Verilog, MIPS AL, X86 AL (Some experience with: Python, C++, Chisel, Golang, HTML, CSS)	
Music: Singing, playing traditional Chinese instrument Erhu	
Sports: Basketball	