

*The student must include the following 6 parts in the report.*

**The reports must be individual submission and the common parts for a group can be parts 1,2,3,4 below. The parts 5, 6 and 7 below are individual contribution and results.**

## 0. Cover Page

## 1. Table of Contents

- Include all the chapters/Sections with Page Numbers.

## 2. Introduction

- Description of the components in not more than 10 sentences.

## 3. Components: Latch1, Latch2, 4:16 Decoder, FSM

- Description of the components in not more than 5 sentences.
- Truth Tables
- Circuit Diagram and/or Block Diagram as required for the component.
- Waveform File for each component. (Latches, 4:16, FSM)

## 4. ALU\_1 for Problem Set 1 of the Lab 6 procedure

- Description outlining purpose and design of the component.
- Screen shot of the Block schematic file BDF (either hand written or using software).
- Mention the purpose of all inputs and outputs of this component.
- Table of Microcode's generated by decoder for ALU\_1.
- Complete Waveform File (using FSM student ID).

## 5. ALU\_2 for Problem Set 2 of the Lab6 procedure

- Description outlining purpose and design of the component. The
- Screen shot of the Block schematic file BDF (either hand written or using software).
- Mention the purpose of all inputs and outputs of this component.
- Table of Microcode's generated by decoder for ALU\_2.
- Complete Waveform File (using FSM student ID).

## 6. ALU\_3 for Problem Set 3 of the Lab6 procedure

- Description outlining purpose and design of the component.
- Screen shot of the Block schematic file BDF (either hand written or using software).
- Mention the purpose of all inputs and outputs of this component.
- Table of Microcode's generated by decoder for ALU\_3.
- Complete Waveform File (using FSM student ID).

## 7. Conclusion