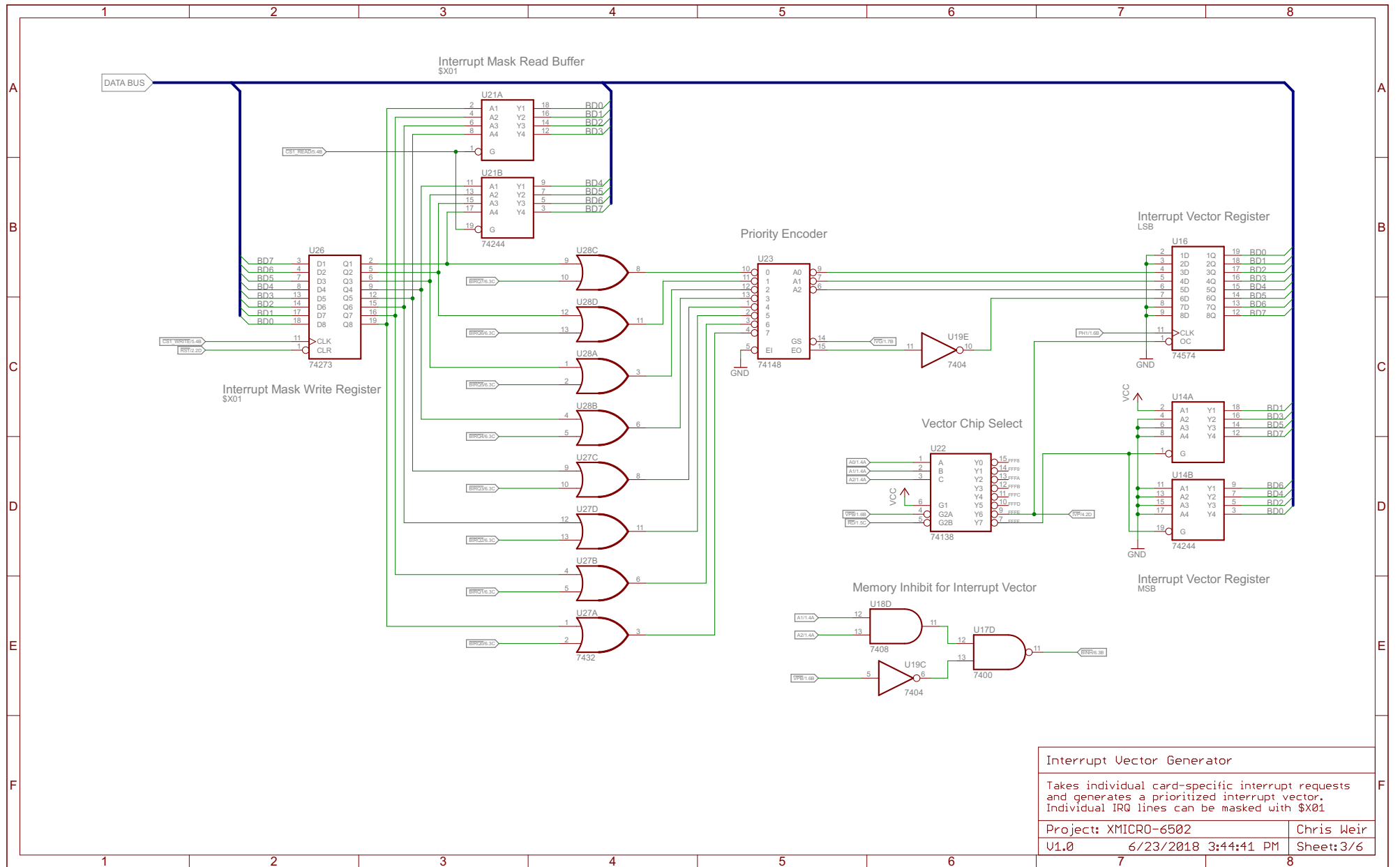
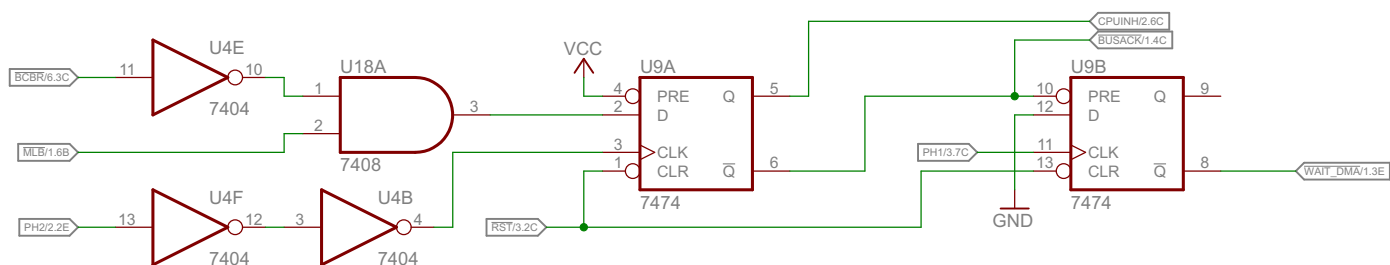


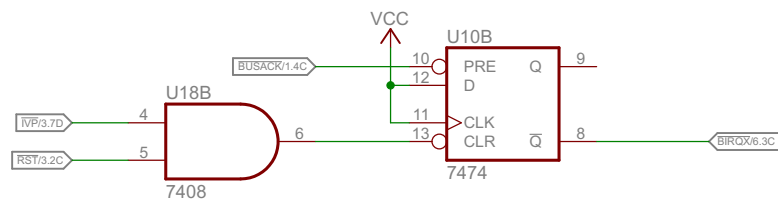
Bank Switcher		
Generates address bits A19-A16 based on current CPU address and the bank register.		
Project: XMICRO-6502	Chris Weir	
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### DMA Interrupt

Interrupt is triggered upon BUSACK.  
Interrupt is cleared on reset or on the interrupt vector pull.



### DMA System

Stops CPU operation on request and releases the bus when safe, then acknowledges the request. An interrupt is triggered for each request.

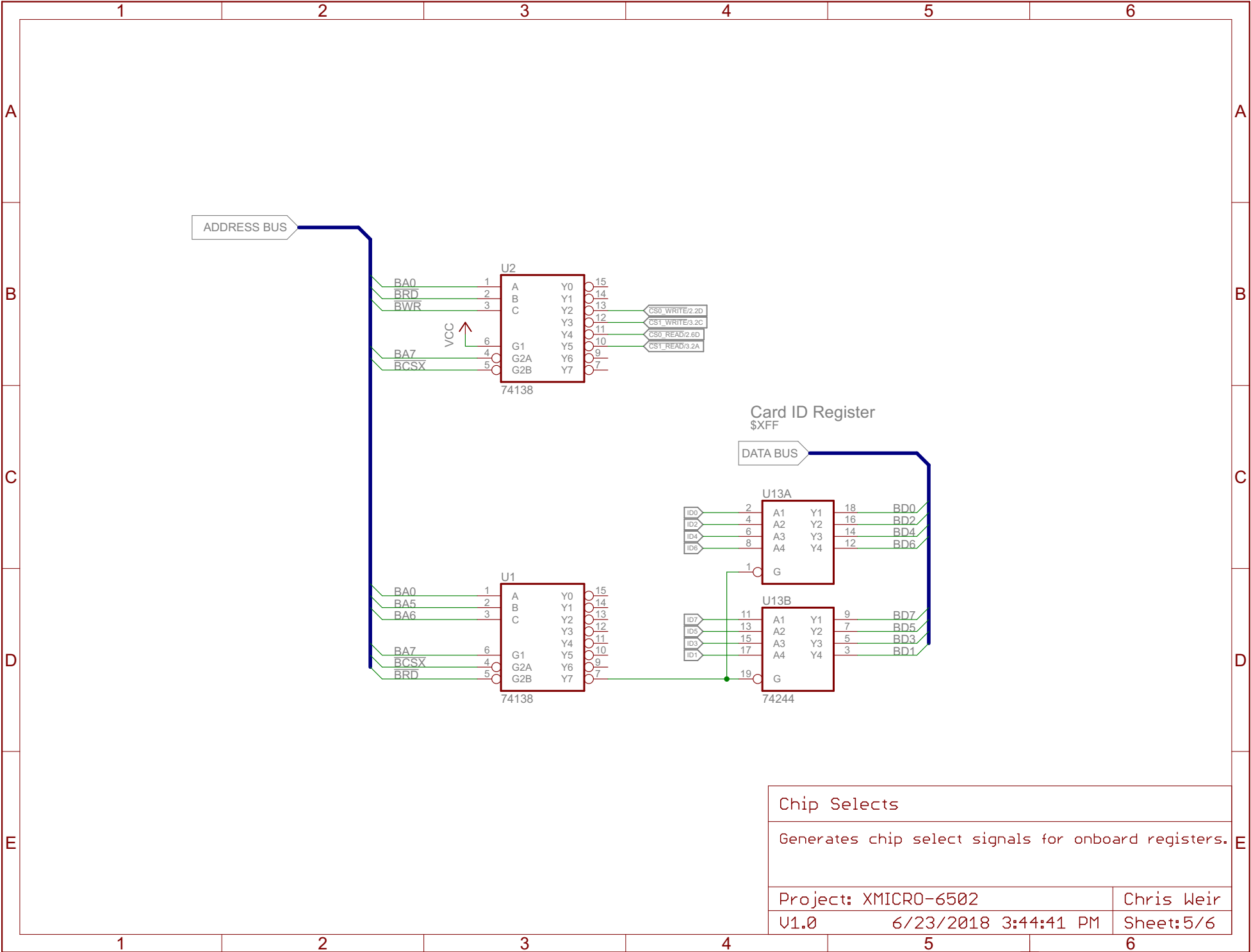
Project: XMICRO-6502

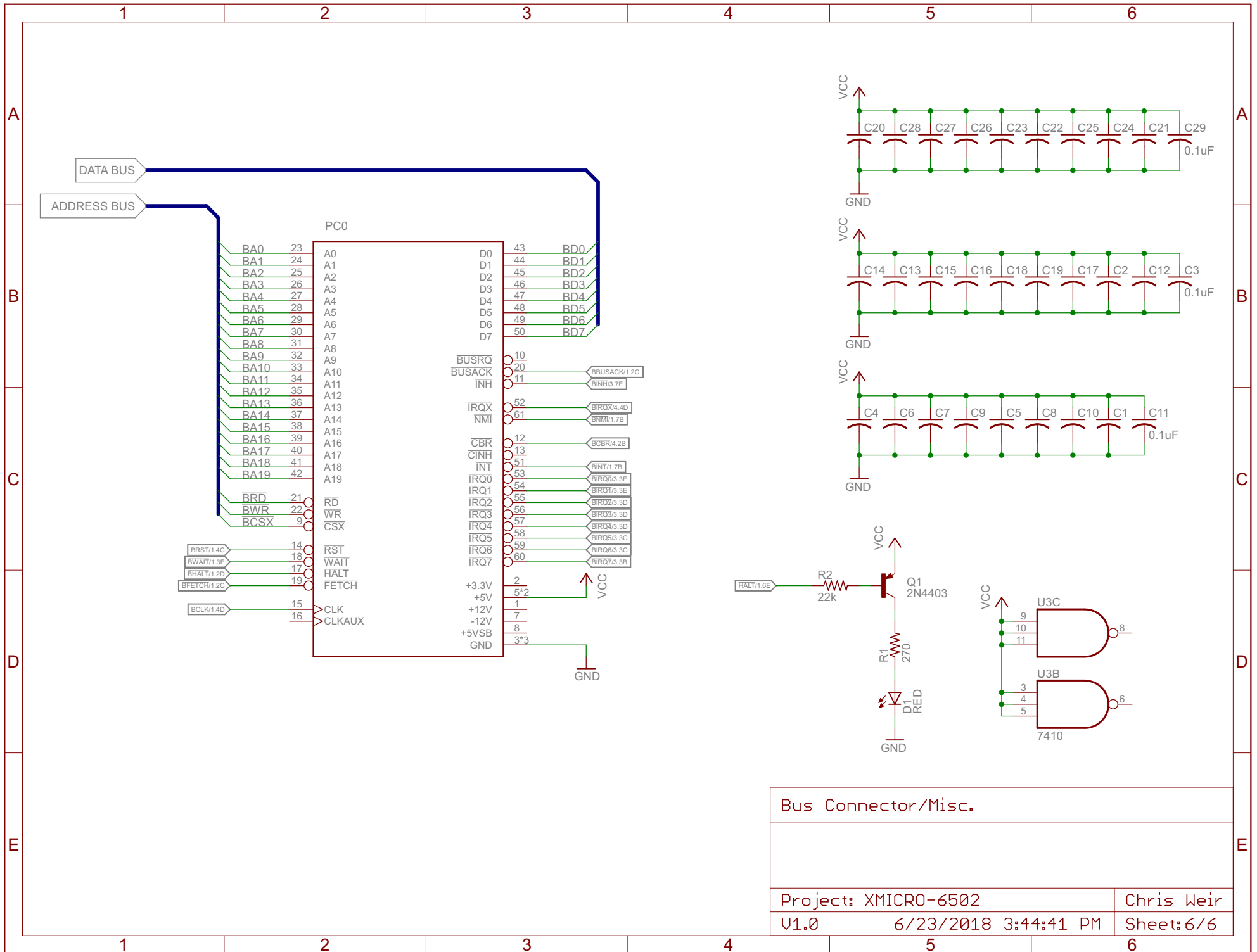
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Bus Connector/Misc.

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