

Interrupt Vector Generator

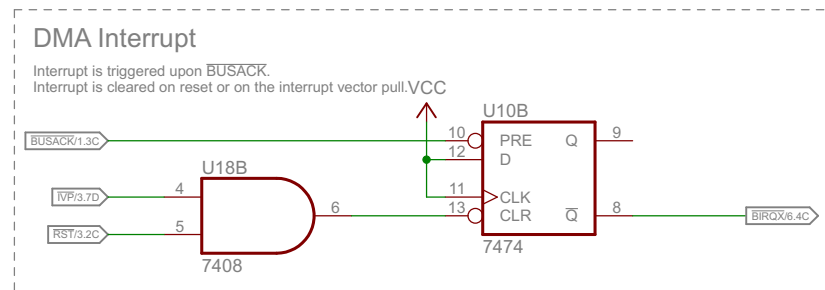
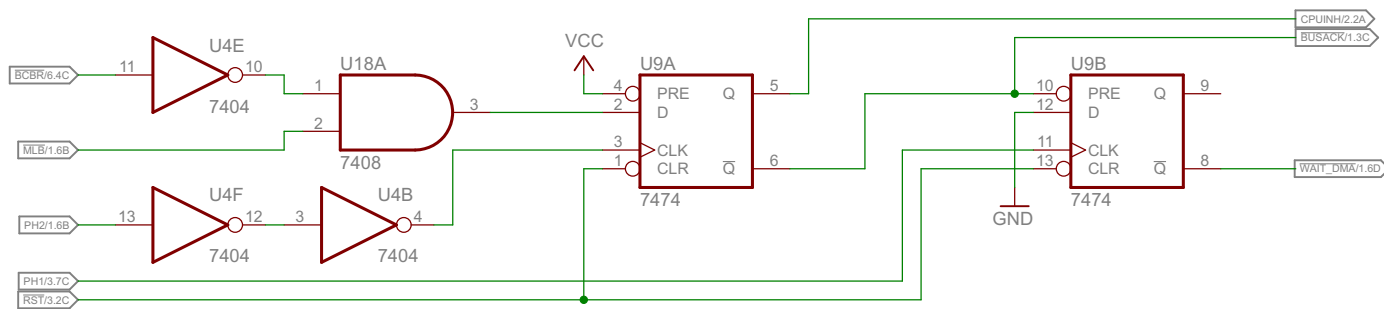
Takes individual card-specific interrupt requests and generates a prioritized interrupt vector. Individual IRQ lines can be masked with \$X01

Project: XMICRO-6502 V2.3

Chris Weir

V2.1

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DMA System

Stops CPU operation on request and releases the bus when safe, then acknowledges the request. An interrupt is triggered for each request.

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Chris Weir

V2.1

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