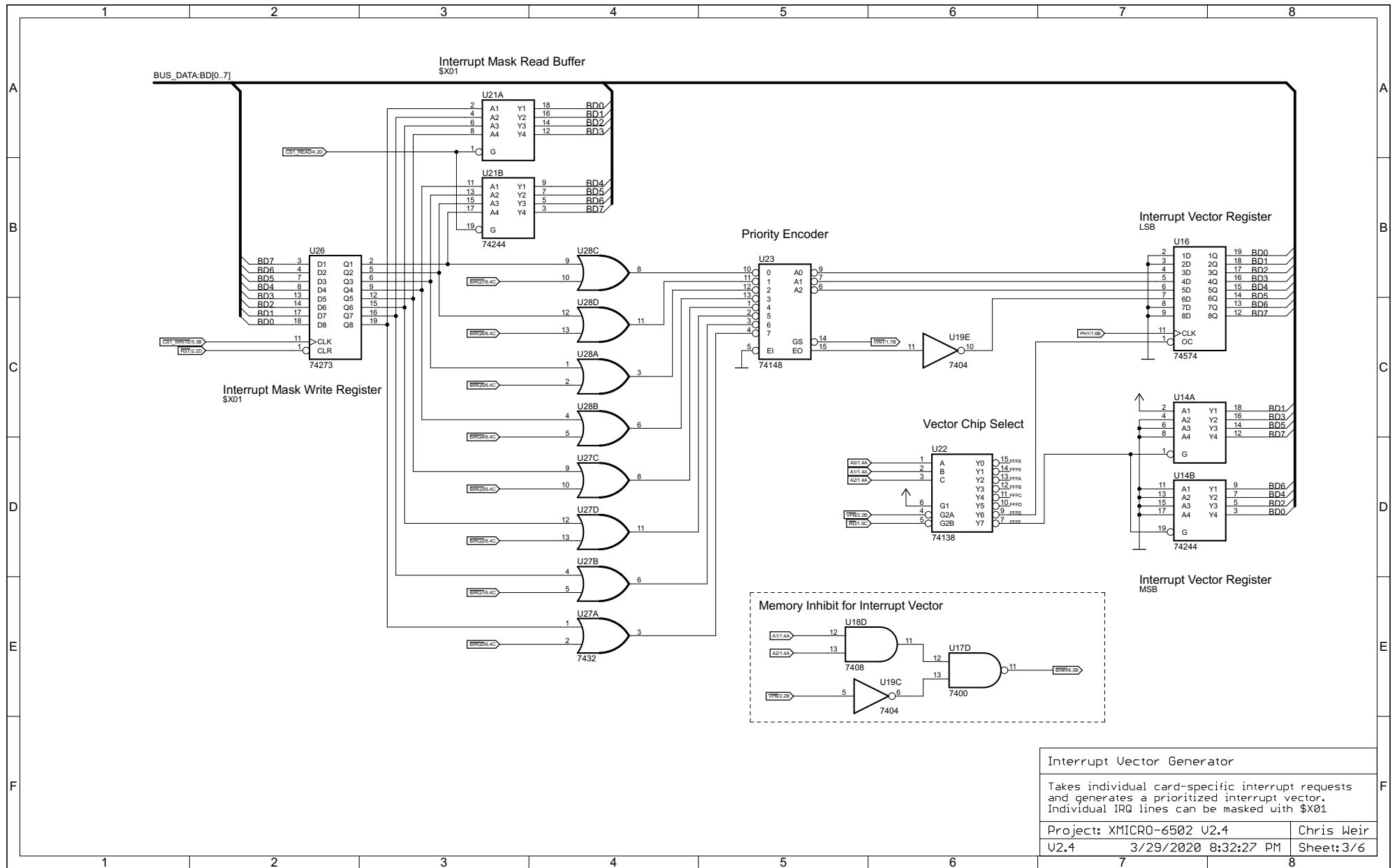
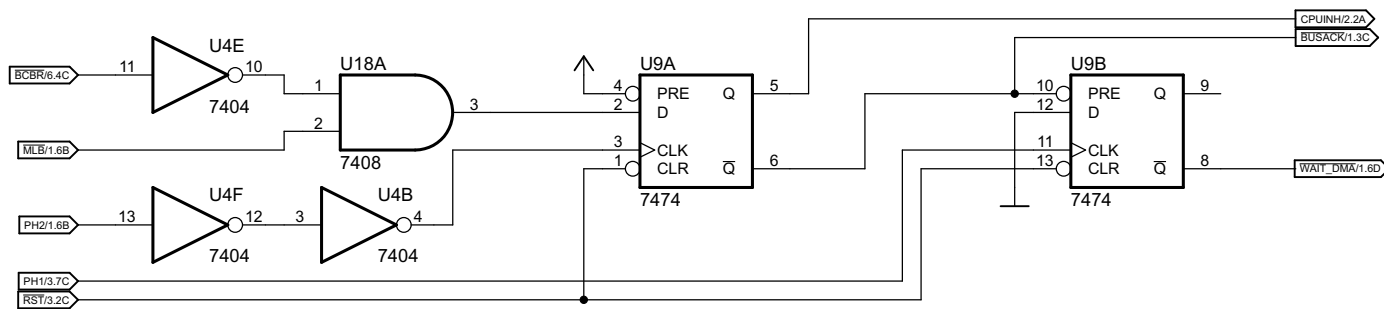


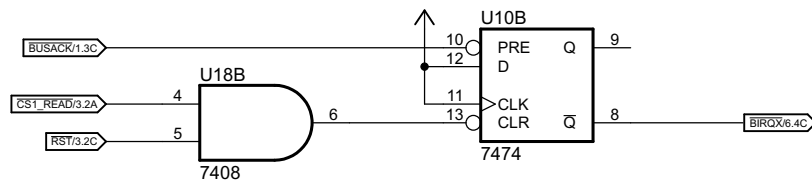
Bank Switcher		
Generates address bits A19-A15 based on current CPU address and the bank register.		
Project: XMICRO-6502 V2.4	Chris Weir	
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DMA Interrupt

Interrupt is triggered upon BUSACK.
Interrupt is cleared on reset or \$X01 read (Interrupt Mask Register)



DMA System

Stops CPU operation on request and releases the bus when safe, then acknowledges the request. An interrupt is triggered for each request.

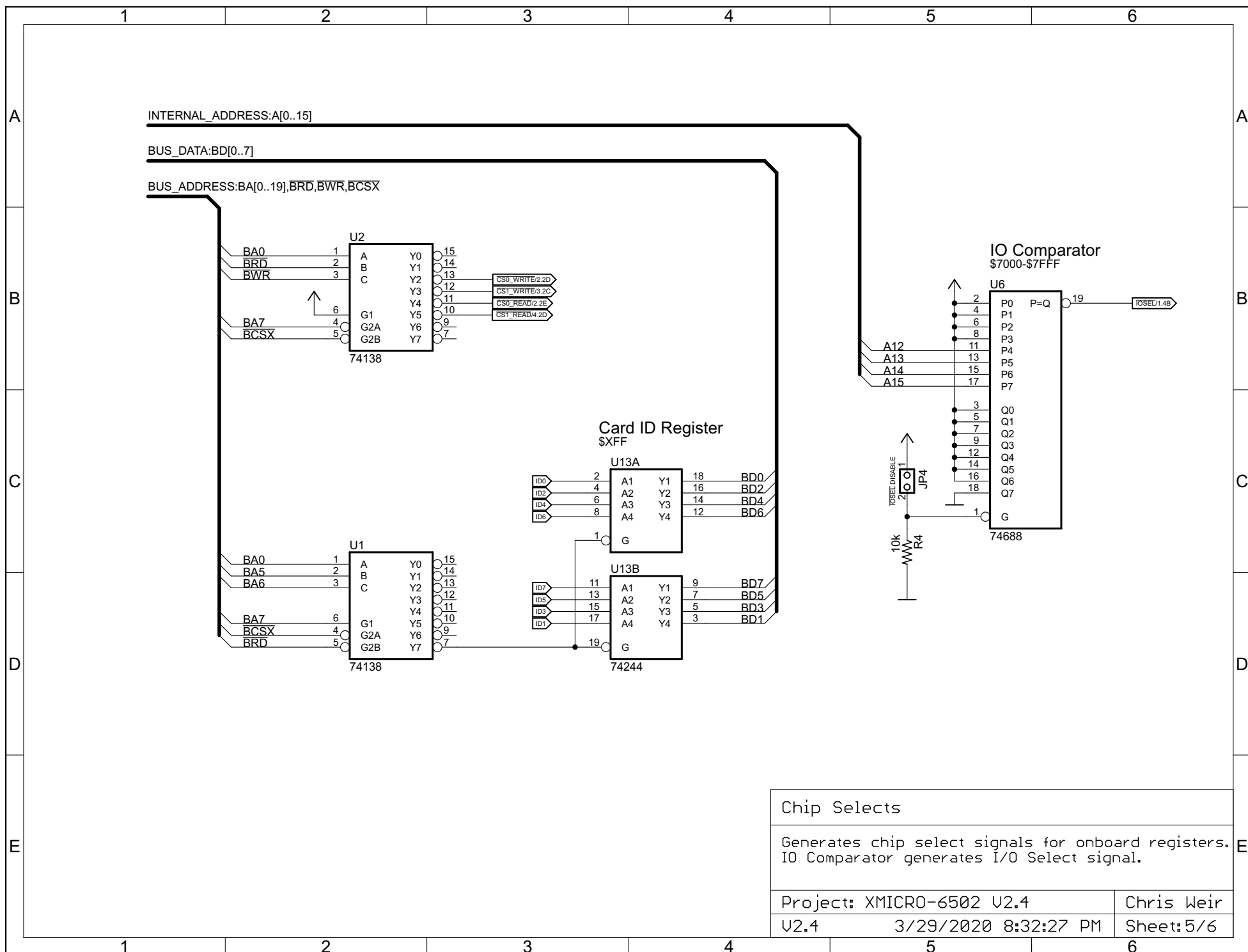
Project: XMICRO-6502 V2.4

Chris Weir

V2.4

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Chip Selects		
Generates chip select signals for onboard registers. IO Comparator generates I/O Select signal.		
Project: XMICRO-6502 V2.4	Chris Weir	
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