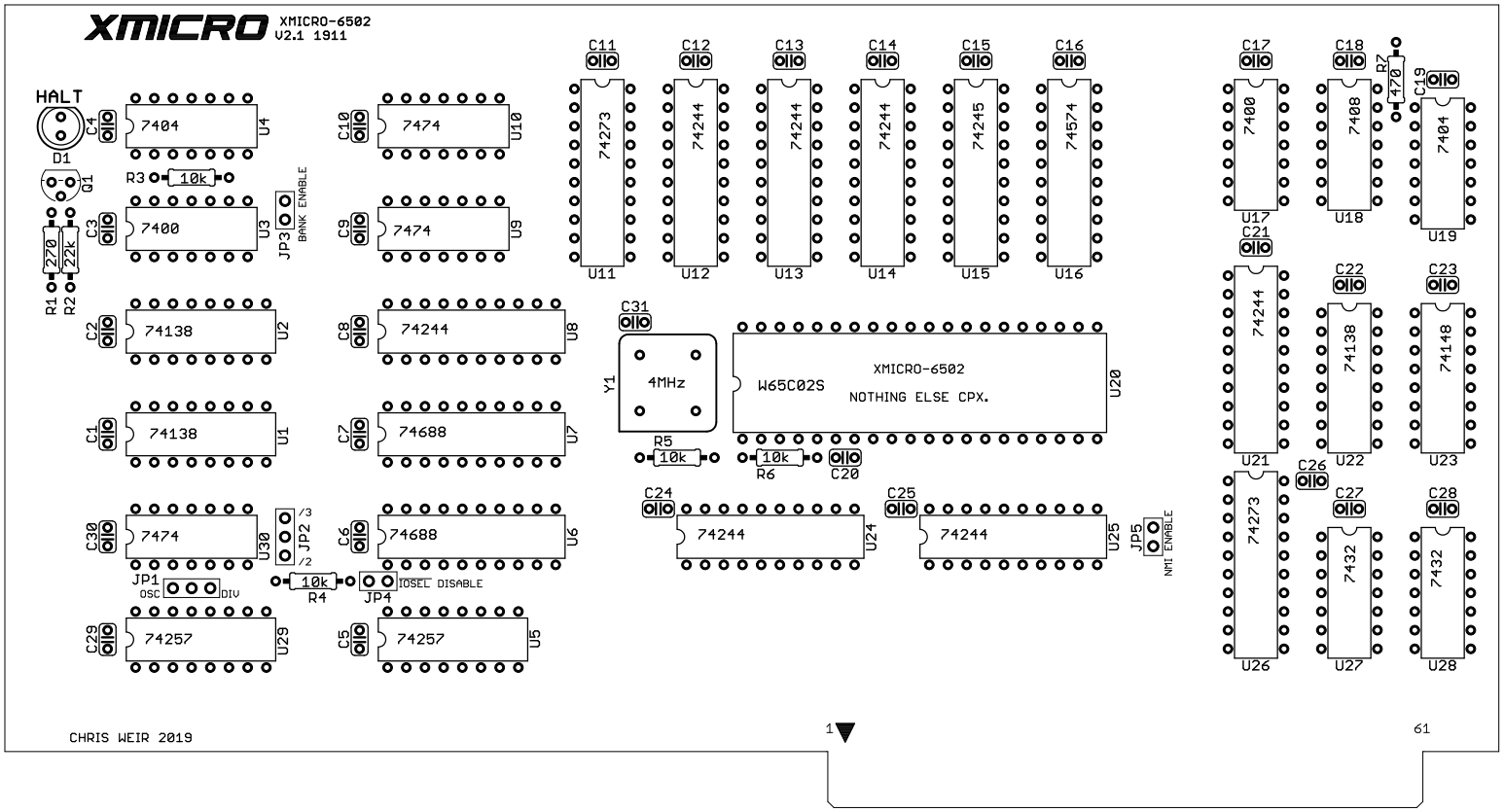
XMICRO
XMICRO-6502
V2.1 1911
HALT
D1
Q1
R1 270
R2 22k
C4 7404 U4
R3 10k
C3 7400 U3
JP3 BANK ENABLE
C2 74138 U2
C1 74138 U1
C30 7474 U30
JP2 /3 /2
JP1 OSC DIV
R4 10k
JP4 IOSEL DISABLE
C29 74257 U29
C10 7474 U10
C9 7474 U9
C8 74244 U8
C7 74688 U7
C6 74688 U6
C5 74257 U5
C11 74273 U11
C12 74244 U12
C13 74244 U13
C14 74244 U14
C15 74245 U15
C16 74574 U16
C31
Y1 4MHz
R5 10k
R6 10k
C20
W65C02S
XMICRO-6502
NOTHING ELSE CPX.
U20
C24 74244 U24
C25 74244 U25
JP5 NMI ENABLE
C17 7400 U17
C18 7408 U18
R7 470
C19 7404 U19
C21 74244 U21
C22 74138 U22
C23 74148 U23
C26 74273 U26
C27 7432 U27
C28 7432 U28
CHRIS WEIR 2019
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