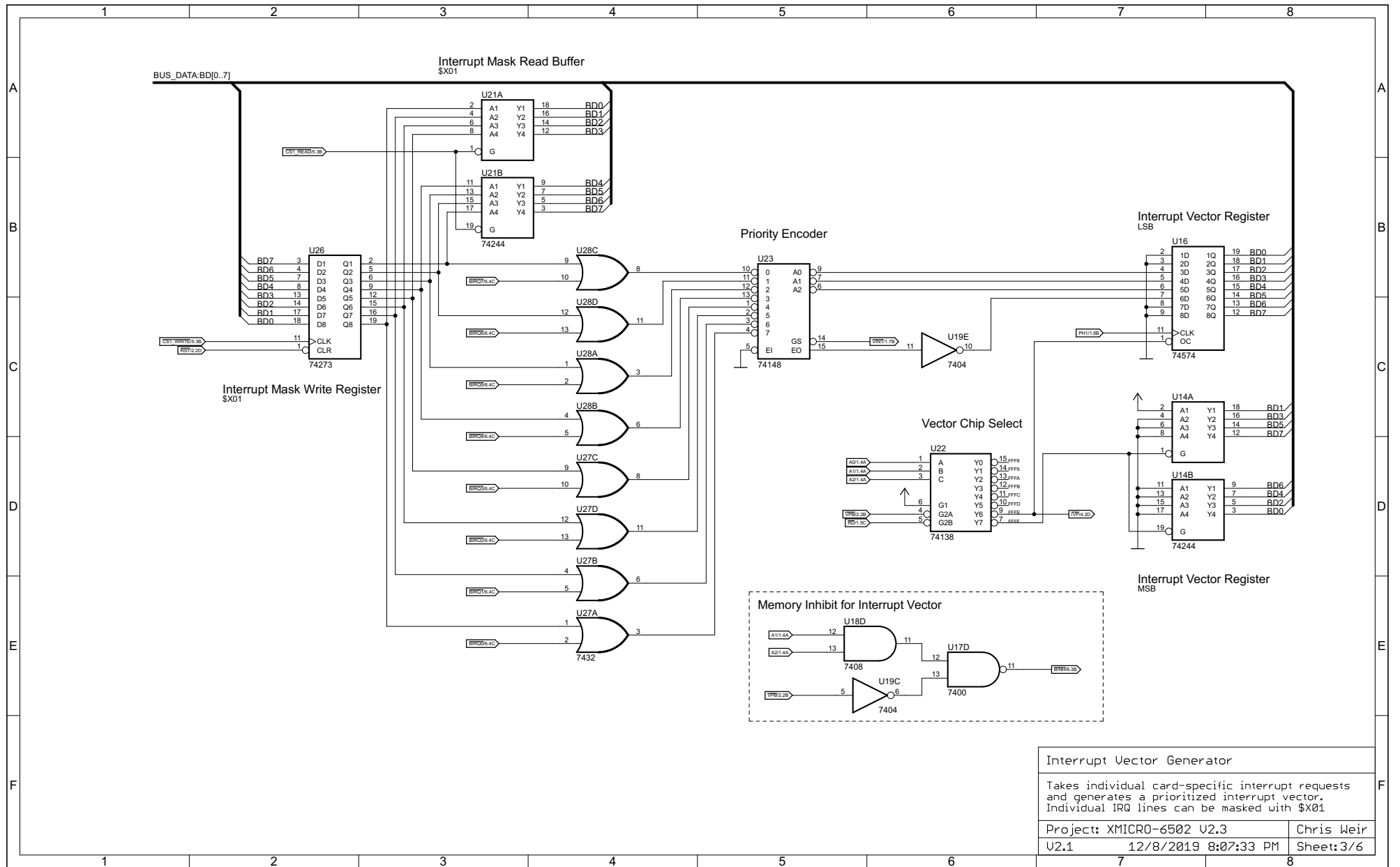
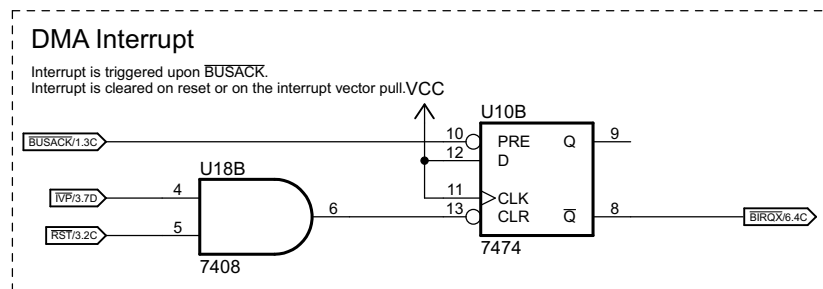
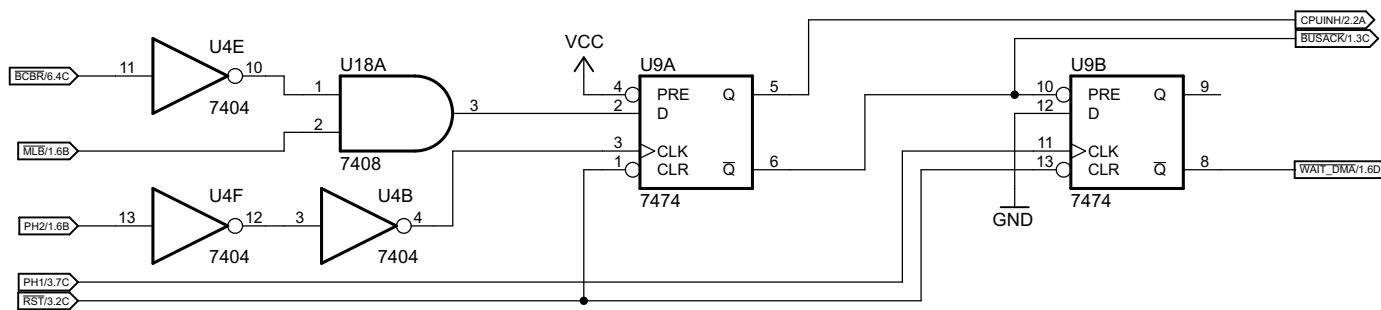


Bank Switcher	
Generates address bits A19-A15 based on current CPU address and the bank register.	
Project: XMICRO-6502 U2.3	Chris Weir
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DMA System

Stops CPU operation on request and releases the bus when safe, then acknowledges the request. An interrupt is triggered for each request.

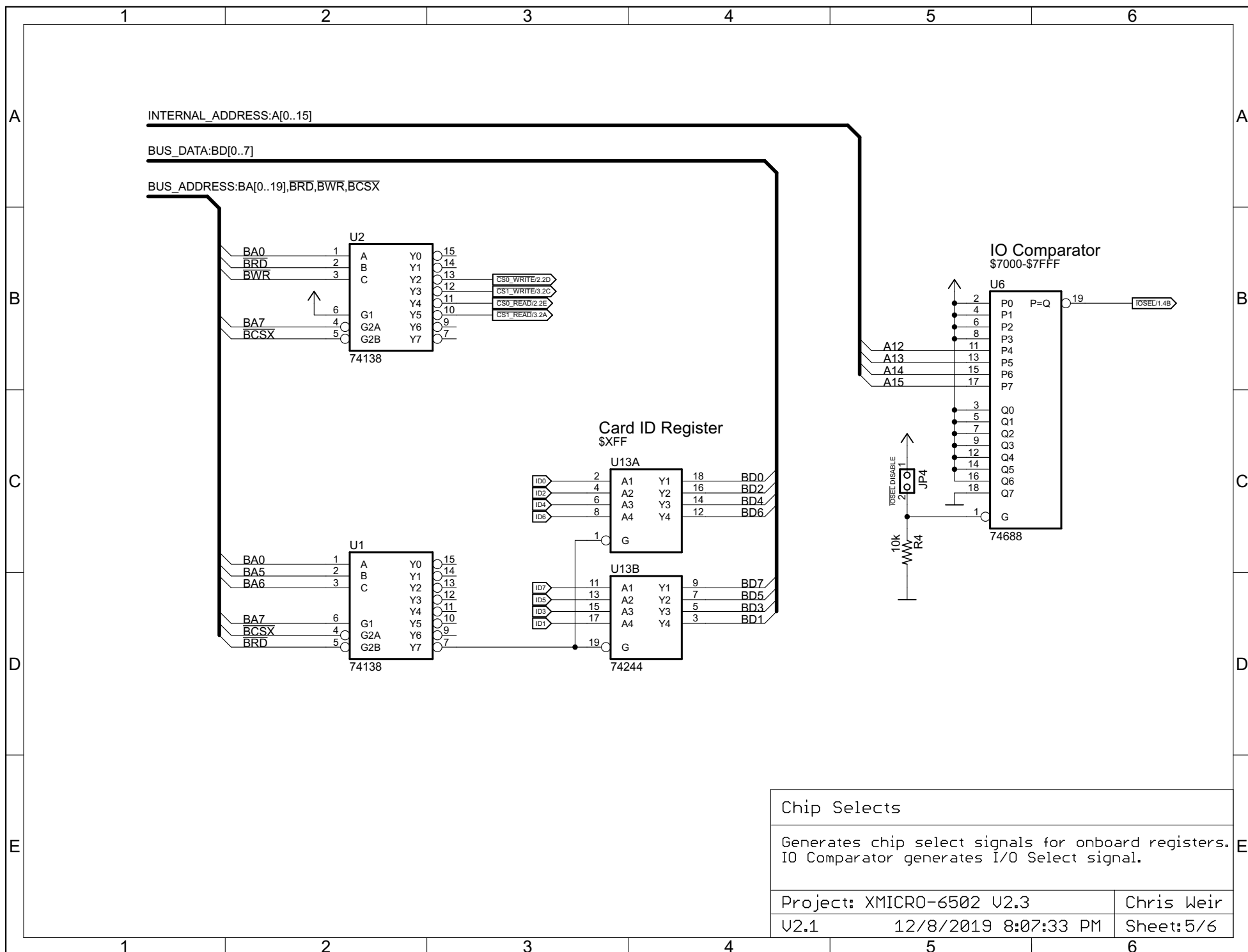
Project: XMICRO-6502 V2.3

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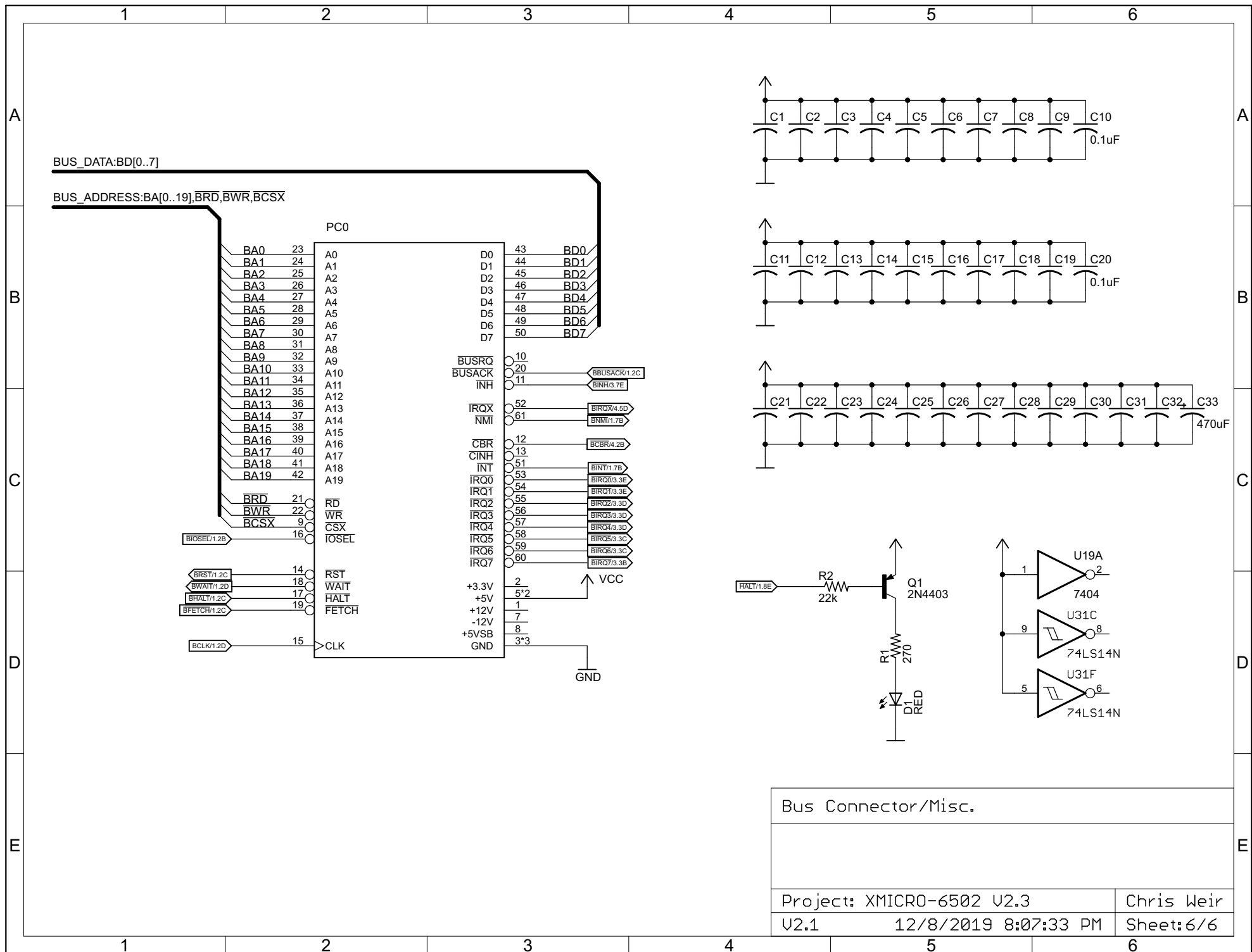
V2.1

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Chip Selects	
Generates chip select signals for onboard registers. IO Comparator generates I/O Select signal.	
Project: XMICRO-6502 V2.3	Chris Weir
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Bus Connector/Misc.

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V2.1

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