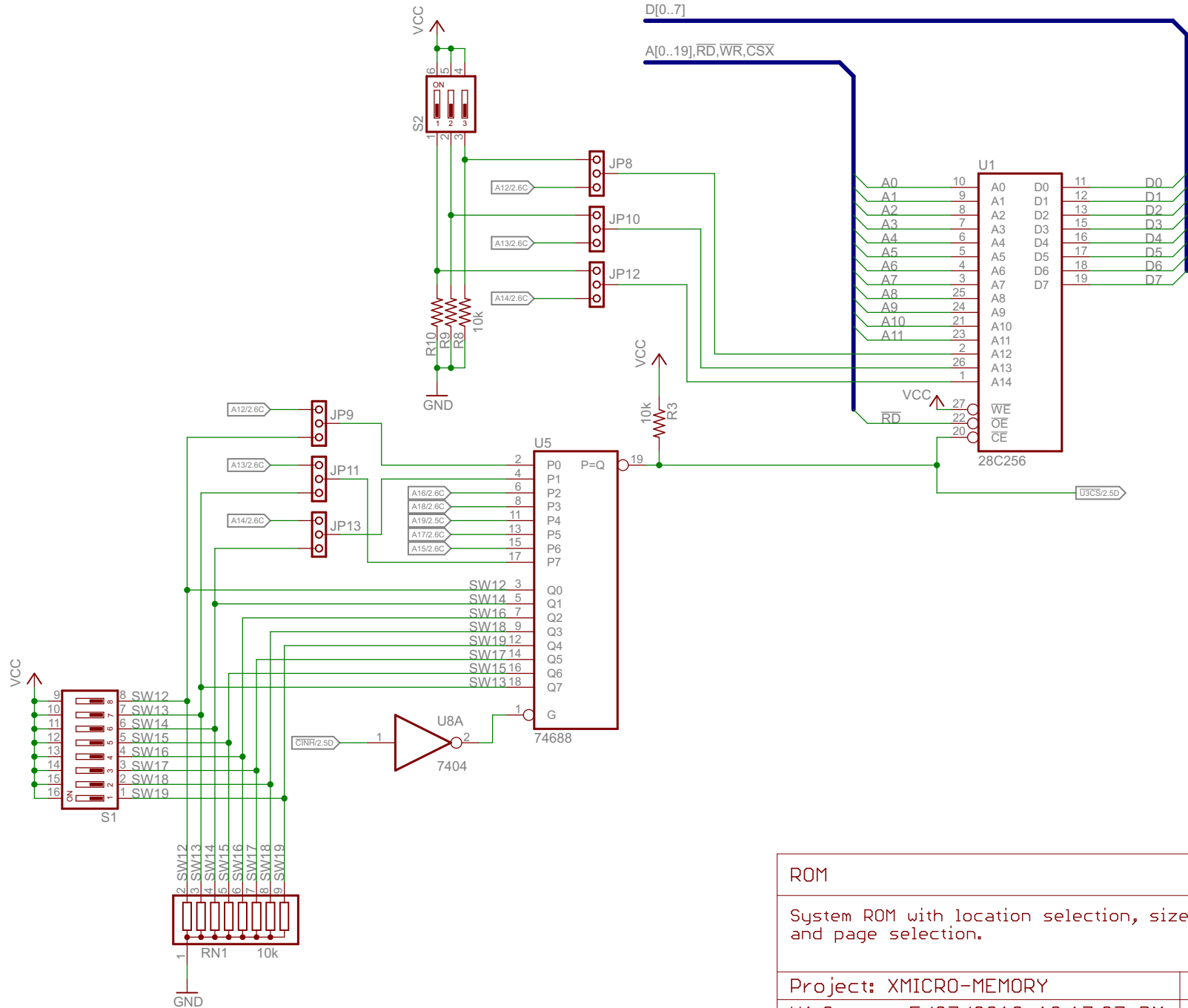


RAM		
Main system RAM		
Project: XMICRO-MEMORY	Chris Weir	
V1.0	5/27/2019 10:17:37 PM	Sheet: 2/7



ROM

System ROM with location selection, size selection, and page selection.

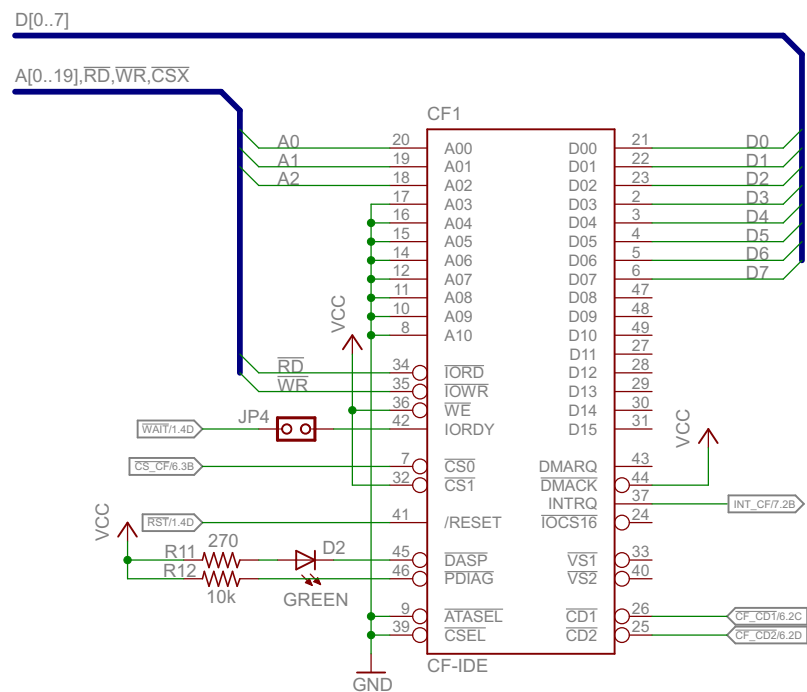
Project: XMICRO-MEMORY

Chris Weir

V1.0

5/27/2019 10:17:37 PM

Sheet: 3/7



CompactFlash Card

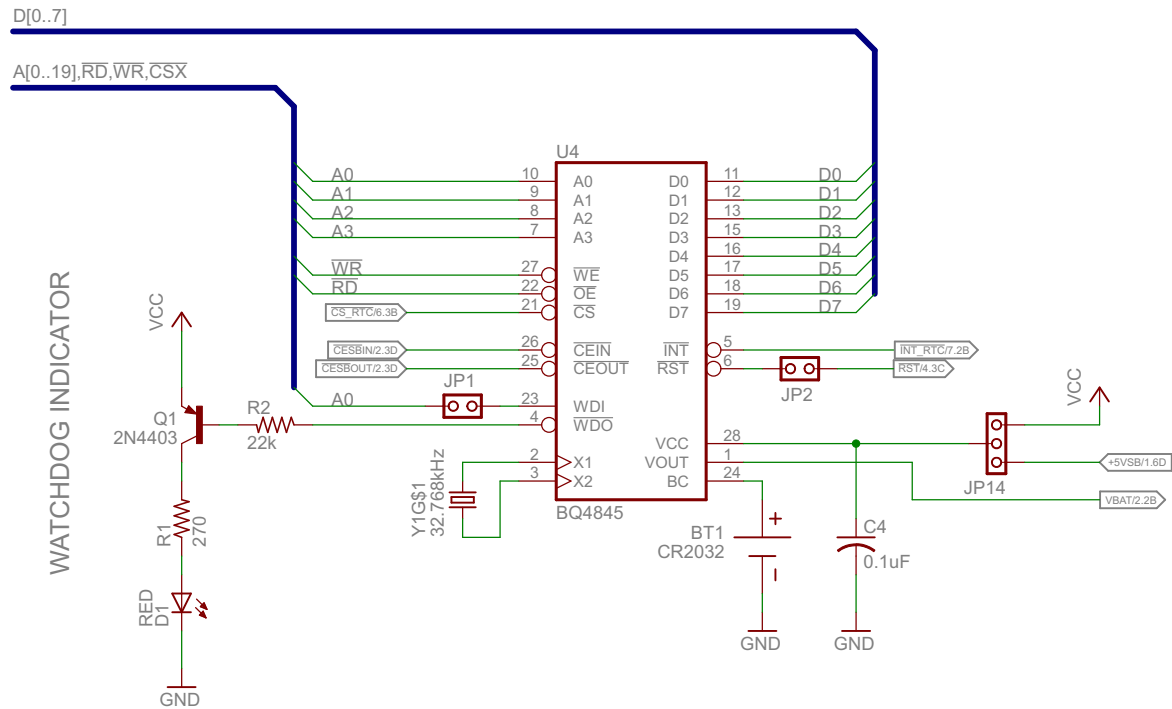
Project: XMICRO-MEMORY

Chris Weir

V1.0

5/27/2019 10:17:37 PM

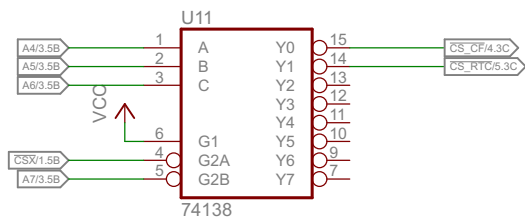
Sheet: 4/7



RTC		
Real Time Clock keeps track of time, provides programmable alarm interrupts, CPU watchdog, SRAM battery protection, power-on reset.		
Project: XMICRO-MEMORY	Chris Weir	
V1.0	5/27/2019 10:17:37 PM	Sheet:5/7

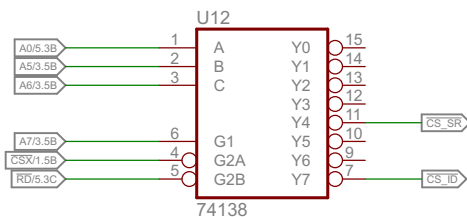
Chip Select (CF/RTC)

CS_CF = \$00-\$0F
CS_RTC = \$10-\$1F

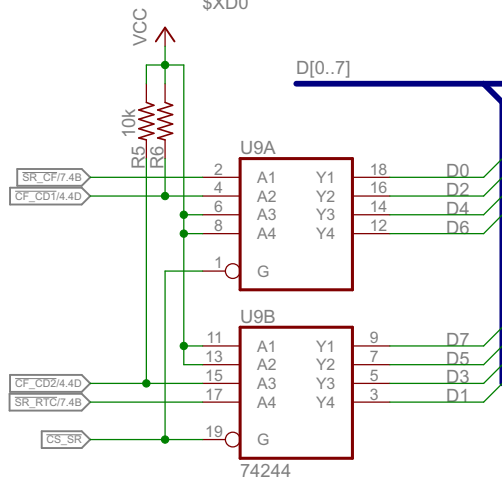


Chip Select (Onboard Registers)

CS_SR = \$D0
CS_ID = \$FF

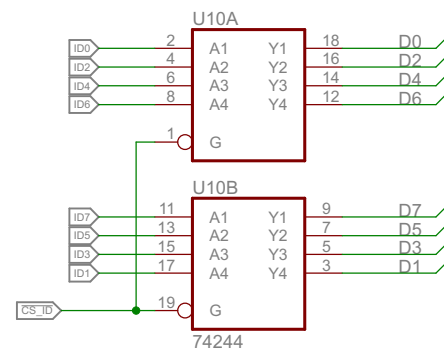


Card Status Register \$XD0



0: CF Interrupt Status
1: RTC Interrupt Status
2: CF Insertion Status 1
3: CF Insertion Status 2

Card ID Register \$XFF



Chip Selects/Onboard Registers

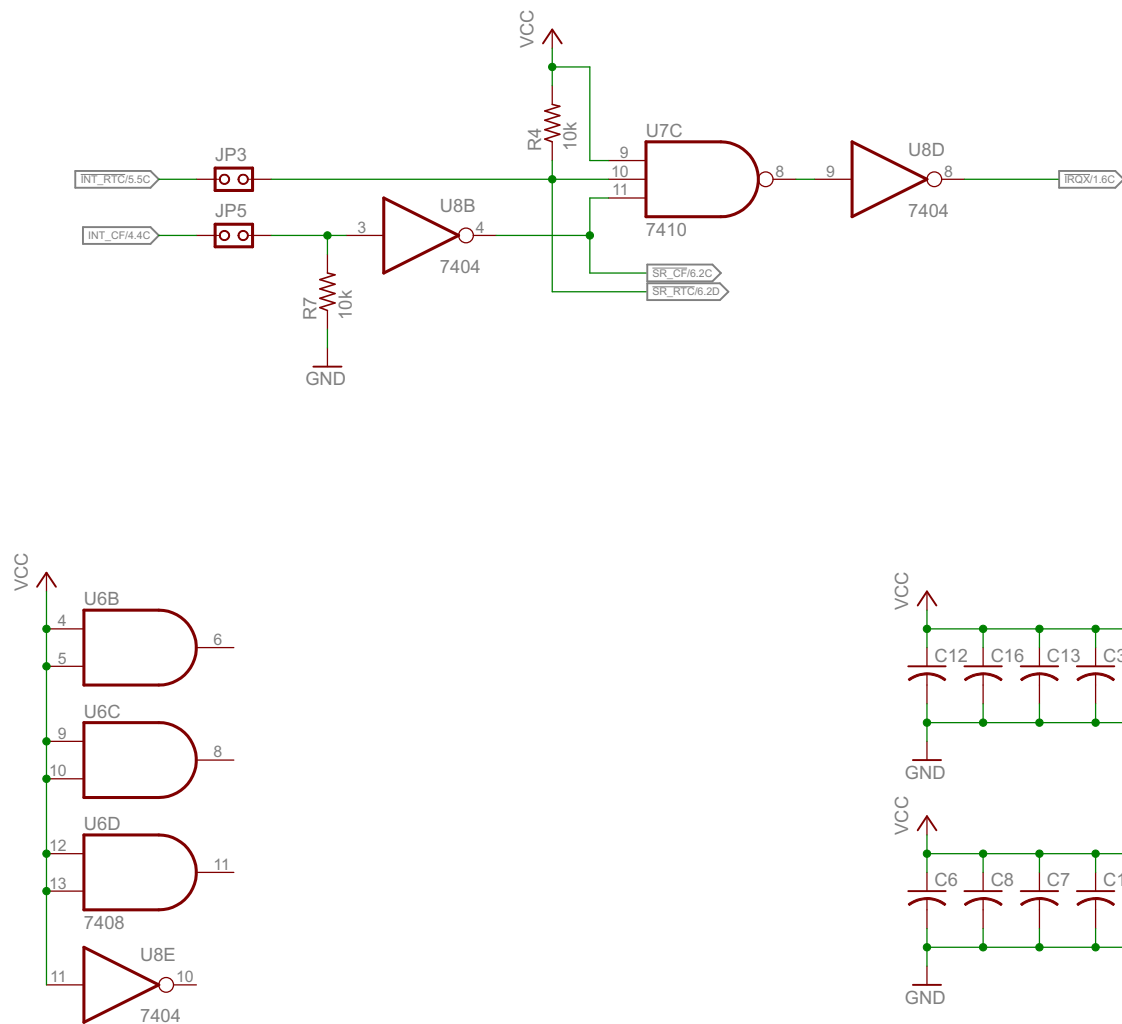
Project: XMICRO-MEMORY

V1.0

5/27/2019 10:17:37 PM

Chris Weir

Sheet: 6/7



Interrupt Combiner/Misc.

Combines interrupt signals from multiple devices.

Project: XMICRO-MEMORY

Chris Weir

V1.0

5/27/2019 10:17:37 PM

Sheet: 7/7