1. General Description

The XMICRO-LPA is an XMICRO development tool which breaks out all bus signals into high-density connectors compatible with HP/Agilent logic analyzers.

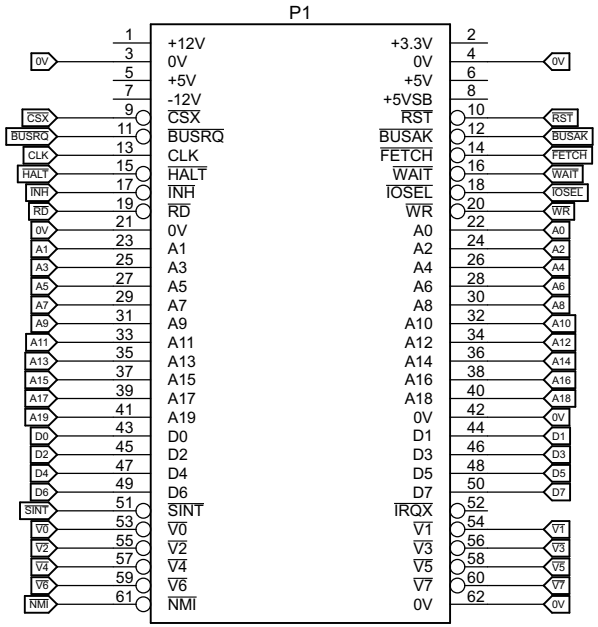
1.1 Features

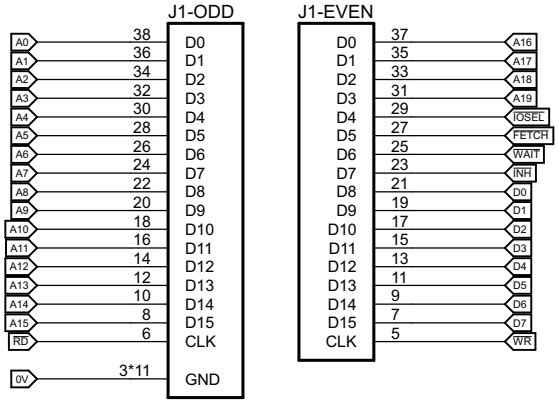
- For use with HP/Agilent E5346A High Density Probe Adapter
- Minimal skew on critical signals

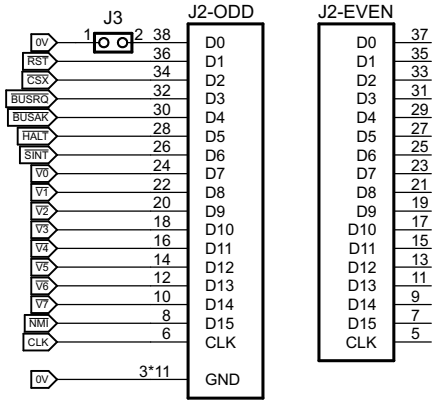
1.2 System Outline

SIGNAL	LENGTH (MM)	TOTAL DELAY (PS)	SKEW (PS)
D5	66.42	380.80	-7.74
D6	66.69	382.35	-6.19
D7	66.79	382.92	-5.62
D2	66.9	383.55	-4.99
$\overline{V7}$	67.04	384.36	-4.19
A11	67.13	384.87	-3.67
D3	67.17	385.10	-3.44
A14	67.18	385.16	-3.38
D4	67.21	385.33	-3.21
A13	67.29	385.79	-2.75
A5	67.29	385.79	-2.75
A15	67.29	385.79	-2.75
A1	67.38	386.30	-2.24
A6	67.4	386.42	-2.12
A10	67.4	386.42	-2.12
D0	67.43	386.59	-1.95
A2	67.49	386.94	-1.61
A9	67.51	387.05	-1.49
A7	67.51	387.05	-1.49
A4	67.55	387.28	-1.26
A12	67.55	387.28	-1.26
A0	67.58	387.45	-1.09
D1	67.65	387.85	-0.69
A3	67.66	387.91	-0.63
$\overline{NMI}$	67.77	388.54	0.00

SIGNAL	LENGTH (MM)	TOTAL DELAY (PS)	SKEW (PS)
$\overline{RD}$	67.77	388.54	0.00
$\overline{WR}$	67.77	388.54	0.00
A8	67.77	388.54	0.00
A18	67.89	389.23	0.69
A19	68.03	390.03	1.49
$\overline{V6}$	68.42	392.27	3.73
A17	68.56	393.07	4.53
A16	68.7	393.87	5.33
$\overline{INH}$	69.52	398.57	10.03
$\overline{V5}$	70.85	406.20	17.66
$\overline{WAIT}$	71.57	410.33	21.79
$\overline{V4}$	72.23	414.11	25.57
$\overline{IOSEL}$	72.54	415.89	27.35
$\overline{FETCH}$	74.38	426.44	37.90
$\overline{V3}$	74.66	428.04	39.50
$\overline{V2}$	76.04	435.95	47.41
$\overline{V1}$	78.47	449.89	61.35
$\overline{V0}$	79.85	457.80	69.26
$\overline{SINT}$	81.23	465.71	77.17
CLK	87.07	499.19	110.65
$\overline{HALT}$	90.02	516.10	127.56
$\overline{BUSAK}$	93.51	536.11	147.57
$\overline{BUSRQ}$	94.89	544.03	155.48
$\overline{CSX}$	97.32	557.96	169.42
$\overline{RST}$	98.7	565.87	177.33







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Project: XMICRO-LPA		Revision: 1.0
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