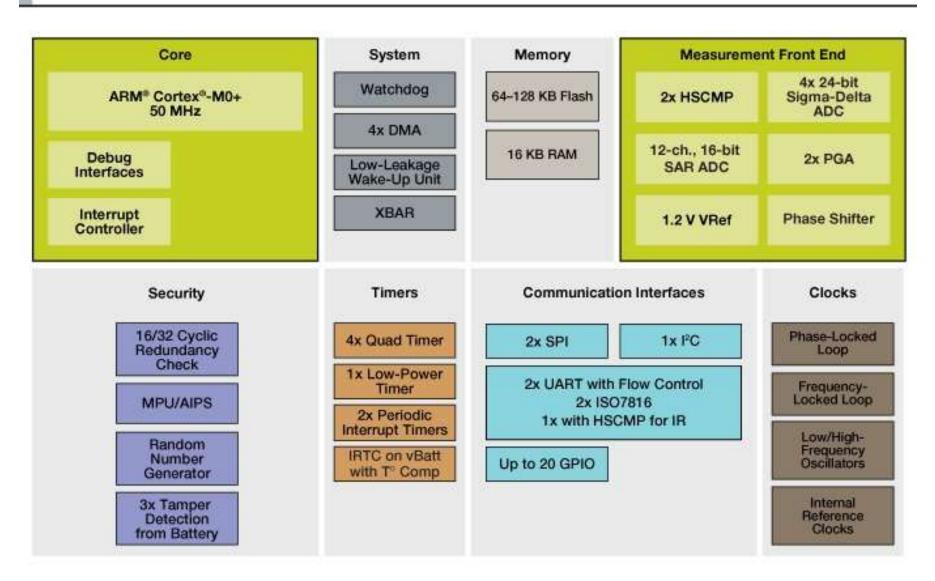
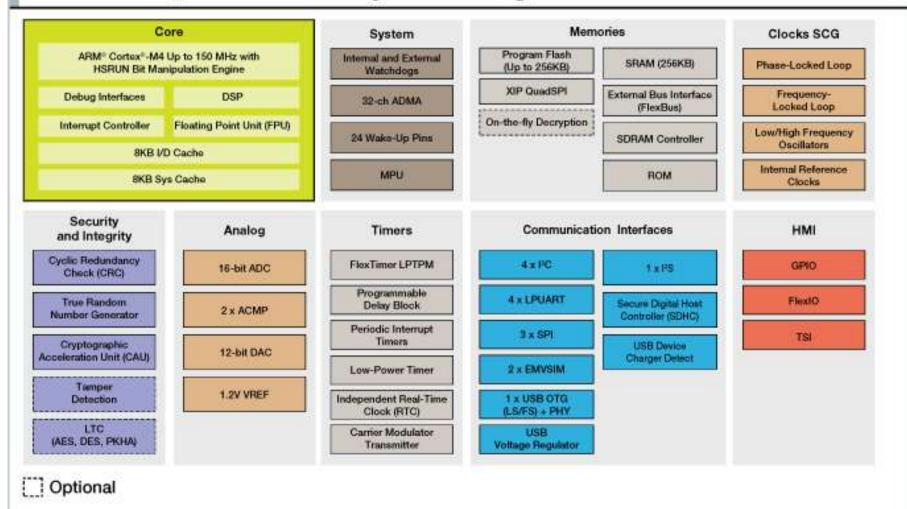
Arquitectura de Computadoras

Anexo Clase 9 Ejemplos comerciales

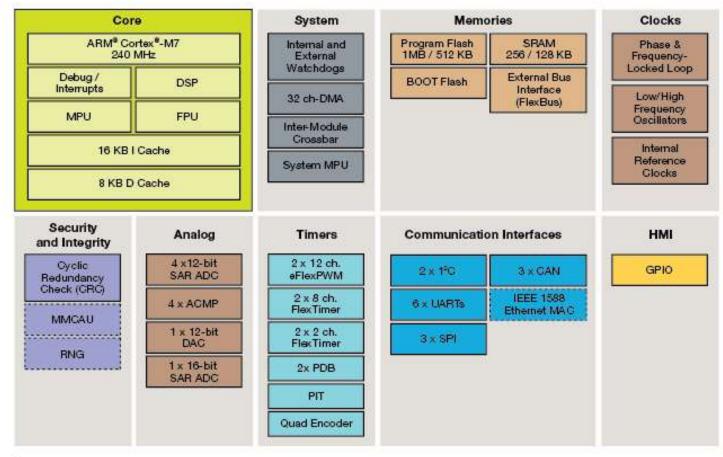
Kinetis KM1x Metering MCUs Block Diagram



Kinetis K8x_150 MCU Family Block Diagram

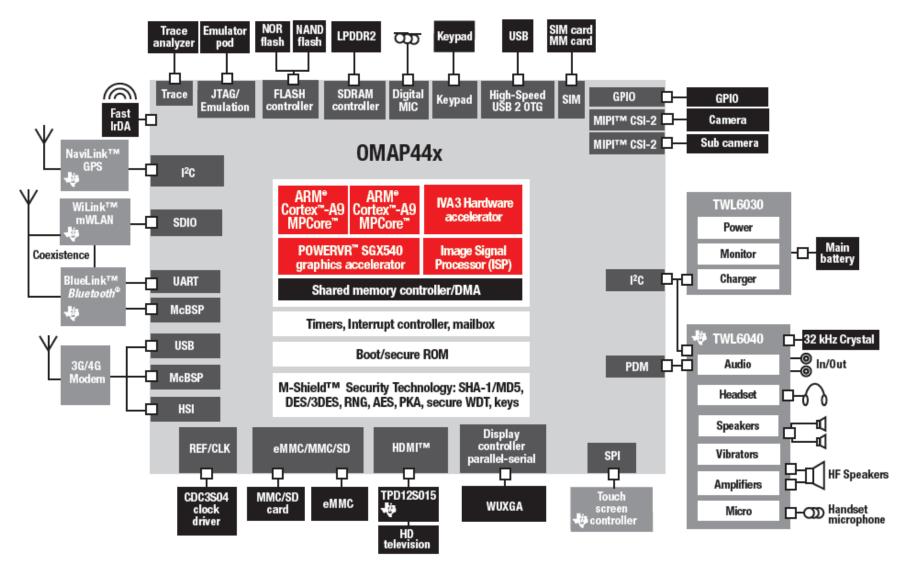


Kinetis KV5x MCU Family Block Diagram



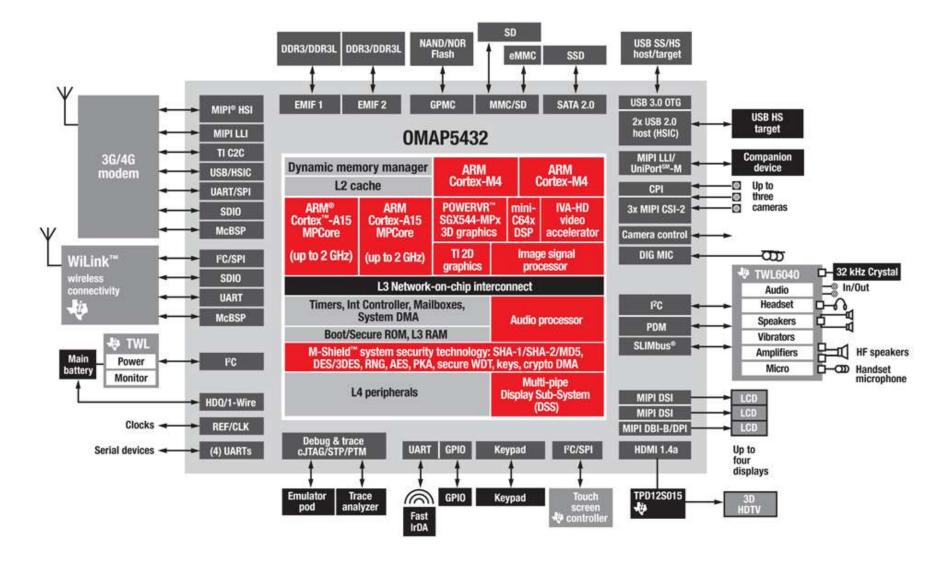
Optional

SoC con memoria compartida



anexo Clase 9

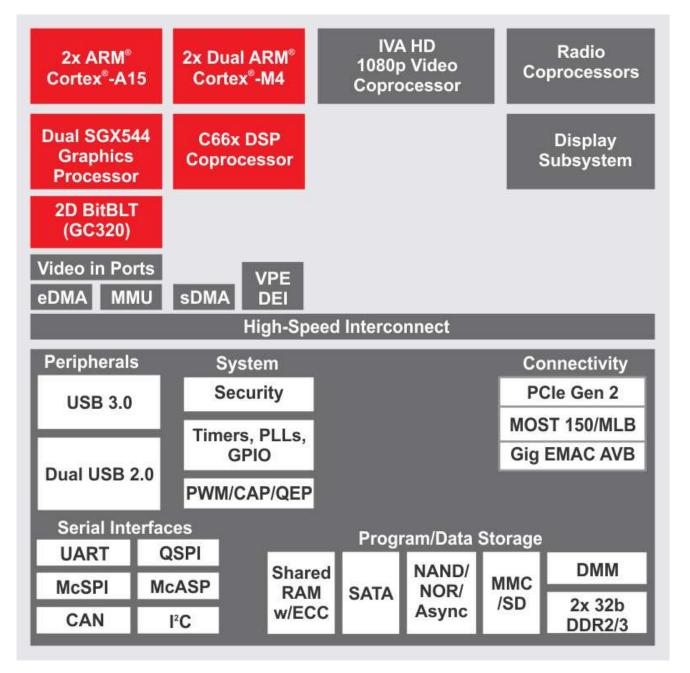
NoC



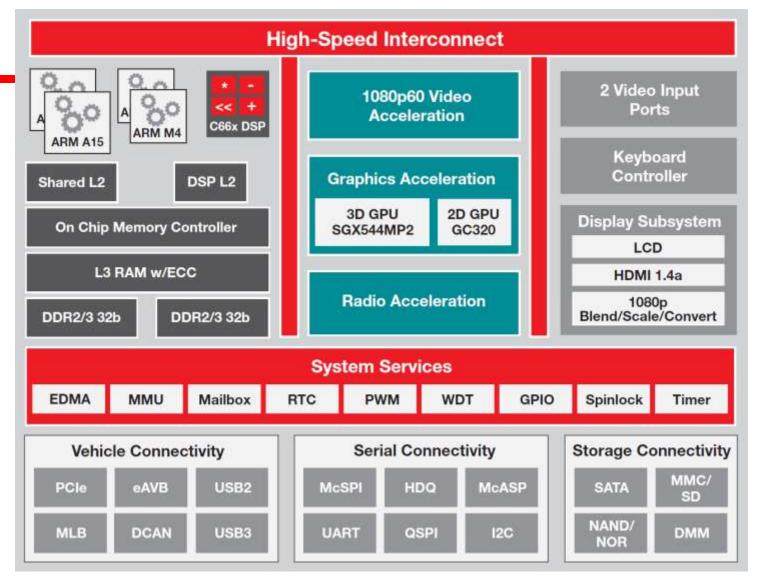
anexo Clase 9 6

DRA746 SoC

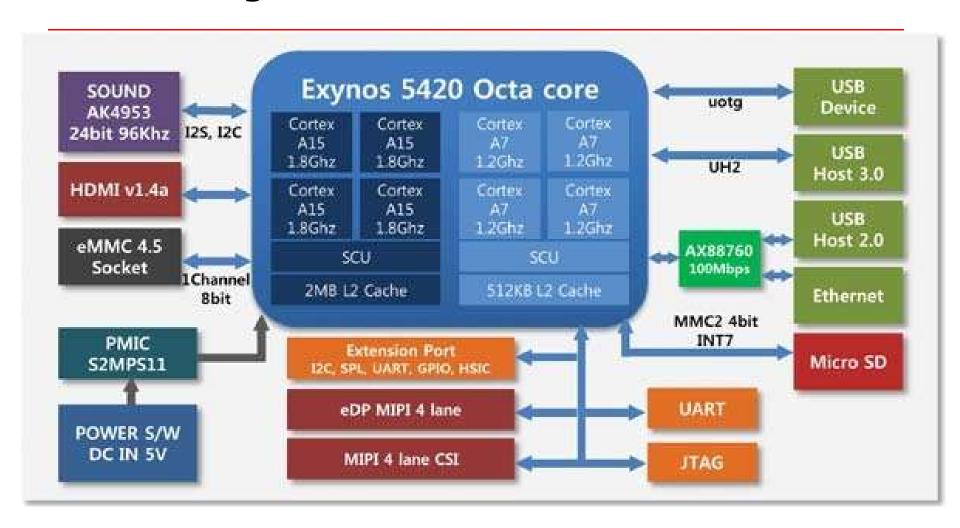
Jacinto 6



Jacinto 6 en bloques

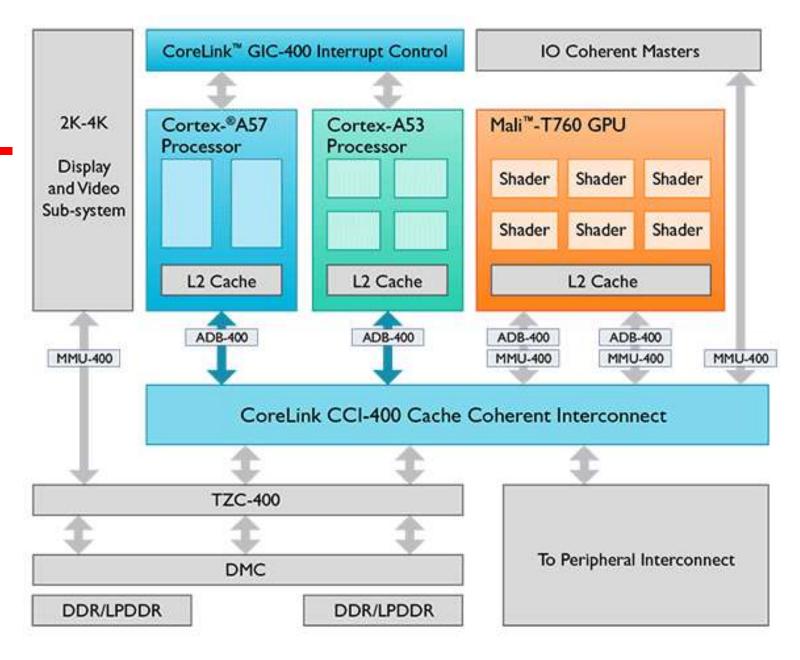


NoC Exynos 5420



Samsung Exynos 5 Octa (Exynos 5433) ARM® Cortex®-A53 ARM® Cortex®-A57 ARM CoreSight™ Multicore Debug and Trace ARM CoreSight™ Multicore Debug and Trace NEON" NEON" SIMD engine with SIMD engine with cypto ext. cypto ext ARMv8-A ARMv8-A 32b/64b CPU 32b/64b CPU Floating Point Floating Point Unit Unit 32k D-Cache Core 8-64k I-Cache 8-64k D-Cache Core 48k I-Cache w/parity w/ECC with DED parity w/ECC 3 3 ACP SCU SCU L2 Cache w/ECC (512kB ~ 2MB) L2 w/ECC (128kB - 2MB) ACP Configurable AMBA®4 ACE or Configurable AMBA®4 ACE or AMBA5 CHI Coherent Bus Interface AMBA5 CHI Coherent Bus Interface gpucpu.com

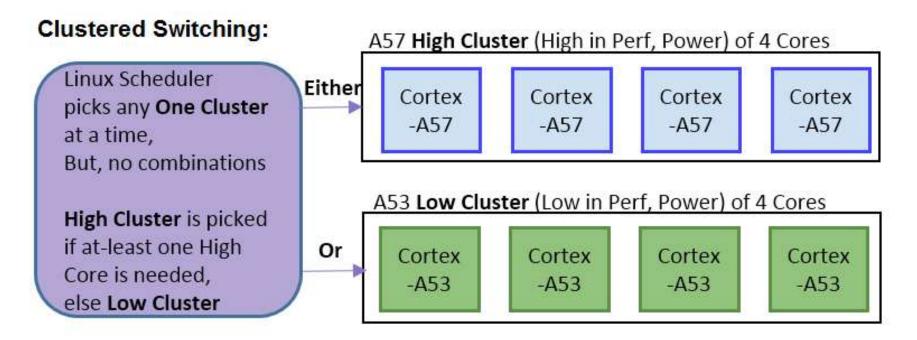
Samsung Galaxy S6



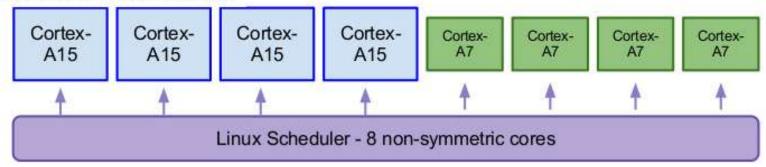
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Exynos octa 7420

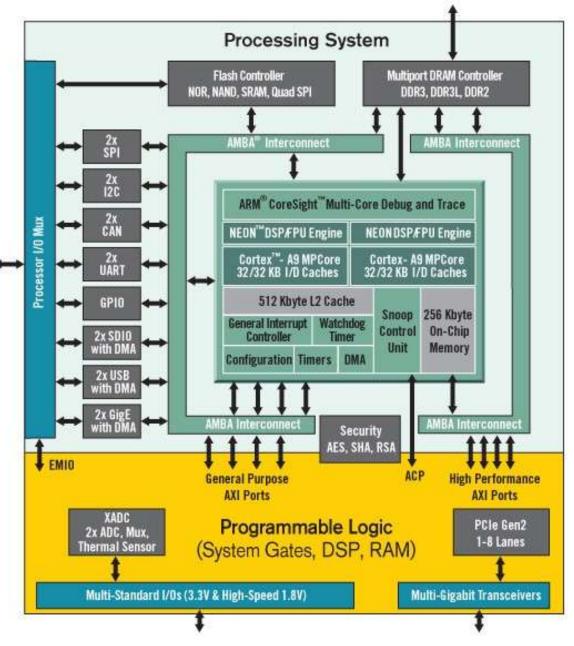


Global Task Scheduling:



Zynq-7000

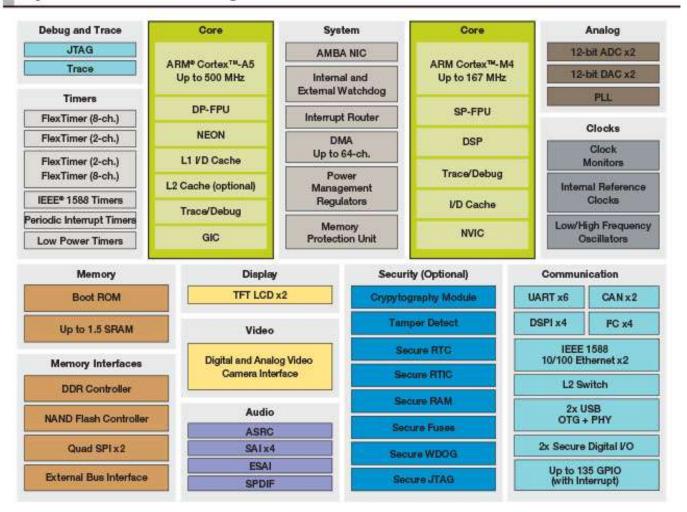
Xilinx



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Vybrid Freescale

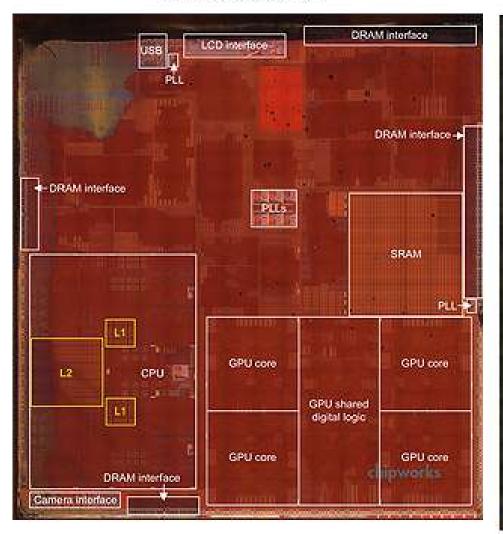
Vybrid VF6xx Block Diagram

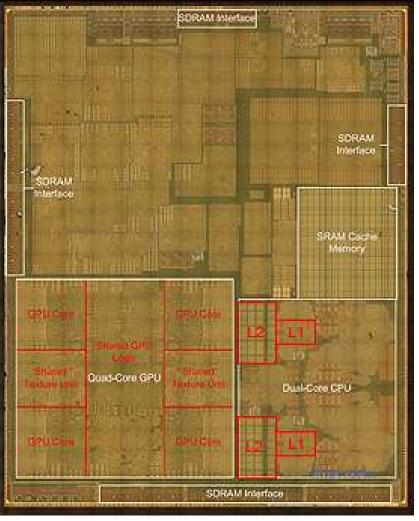


Apple A8 SoC

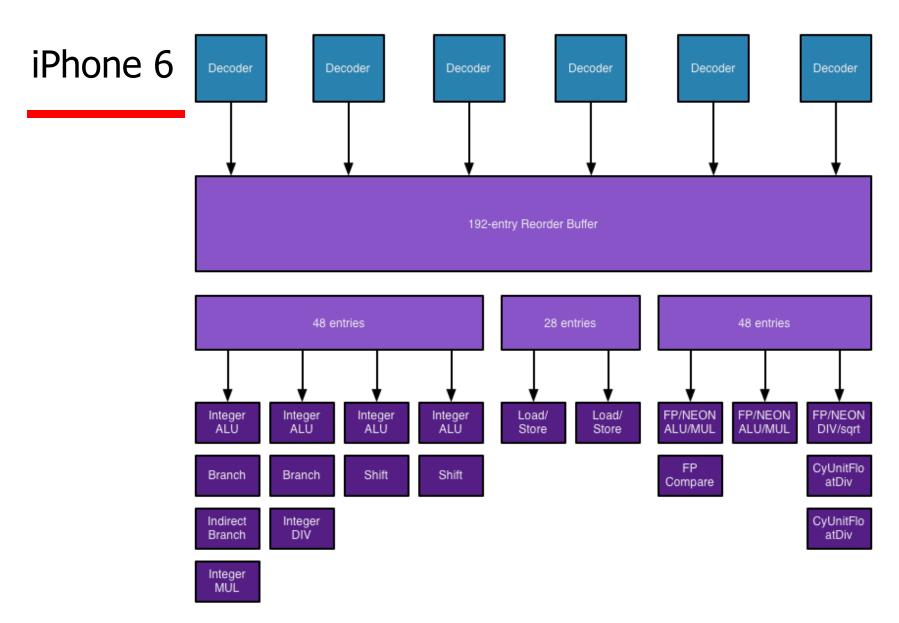
A7 9.83 x 10.45 mm

A8 8.47 x 10.50 mm



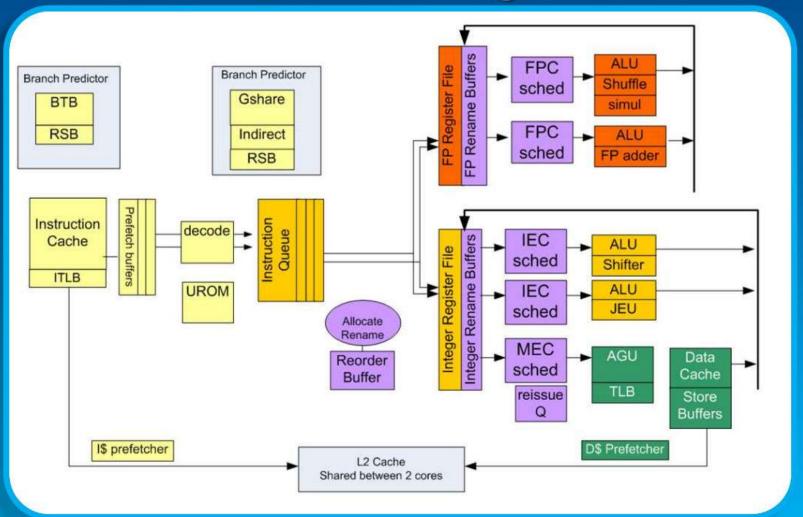


Apple Cyclone

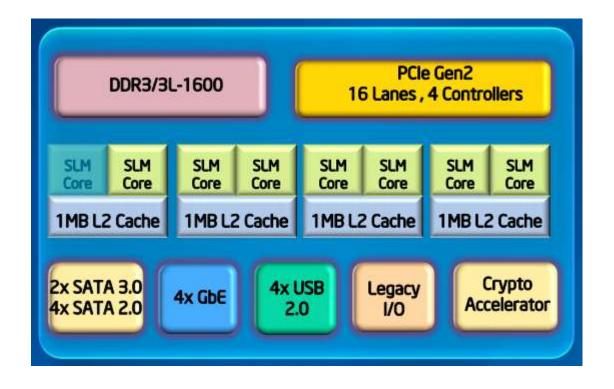


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Core Block Diagram



Intel Atom C2000 family



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Lecturas recomendadas

Fabricantes

- www.arm.com
- www.ti.com
- www.xilinx.com
- <u>www.altera.com</u>
- <u>www.freescale.com</u>
- www.intel.com