Realtek Ameba RTL8195AM DEV01 User Manual

This document define pin out of Ameba RTL8195AM DEV.

Version 1.10

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Table of Contents
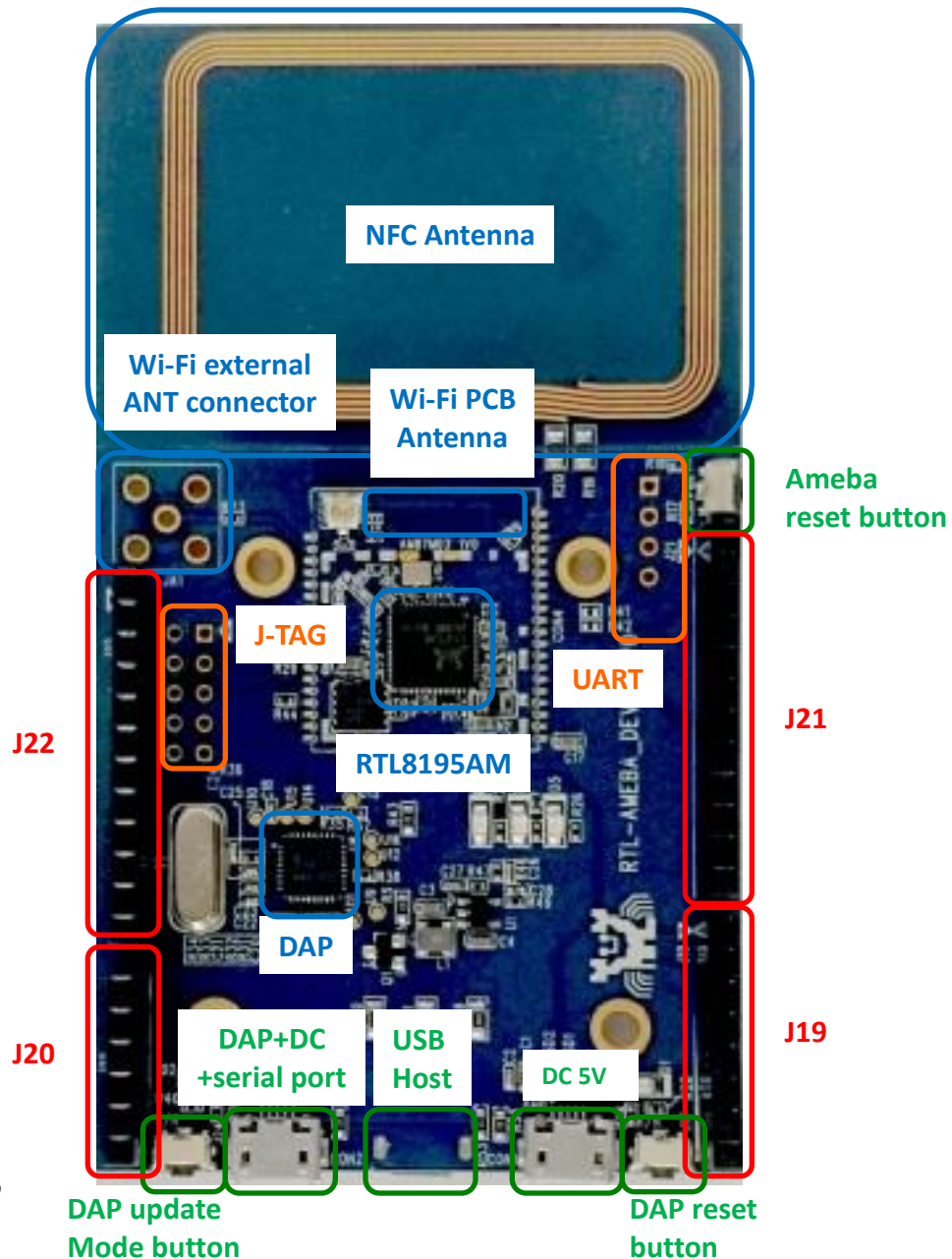
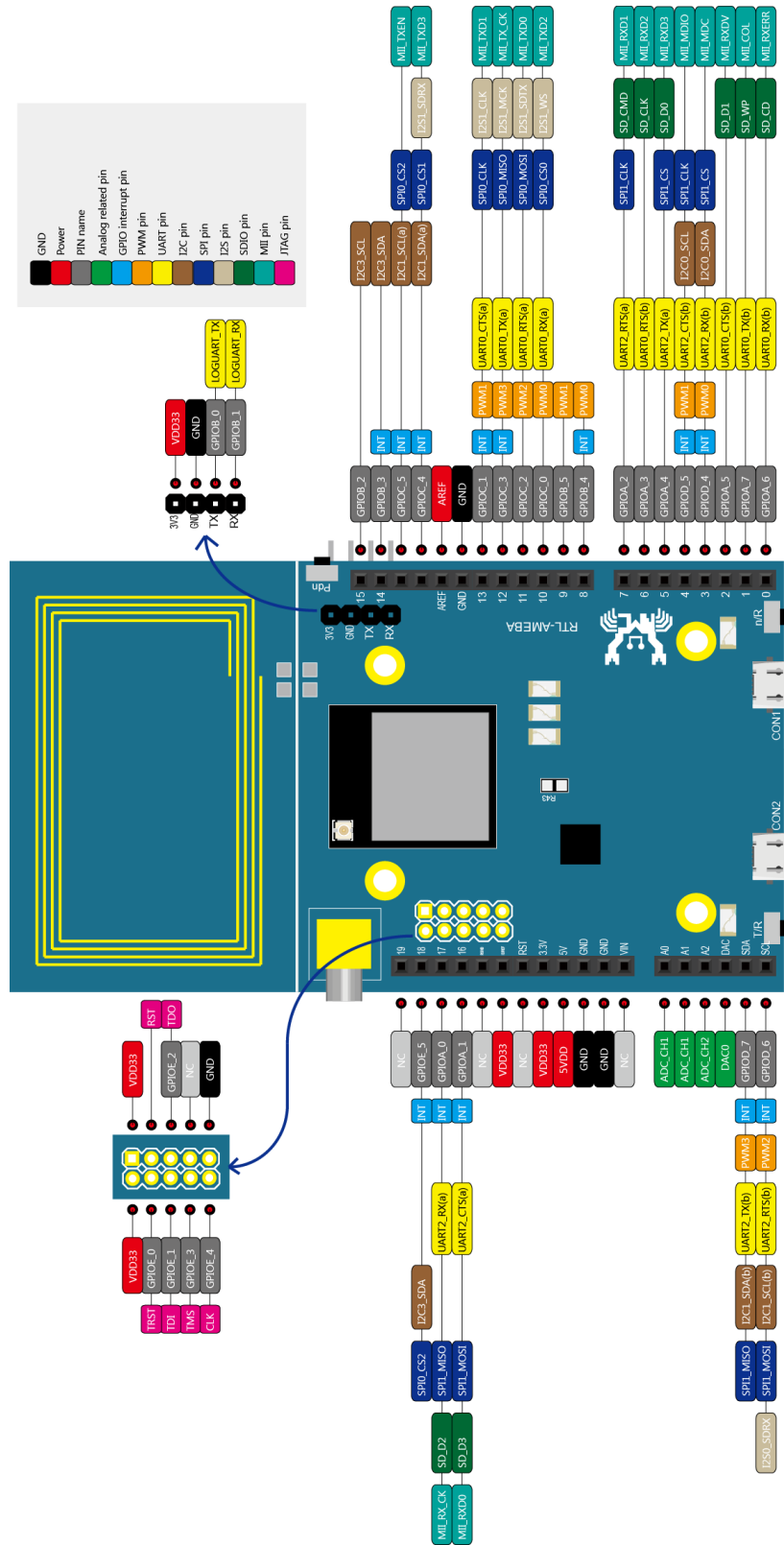# 1 System requirements

- Windows PC (XP, Vista, 7)
- USB type A to Micro-B USB cable x 1
- RS-232 to UART board(debug) x 1, JTAG cable x1 (option)

2 Hardware block diagram

- IC: RTL8195AM
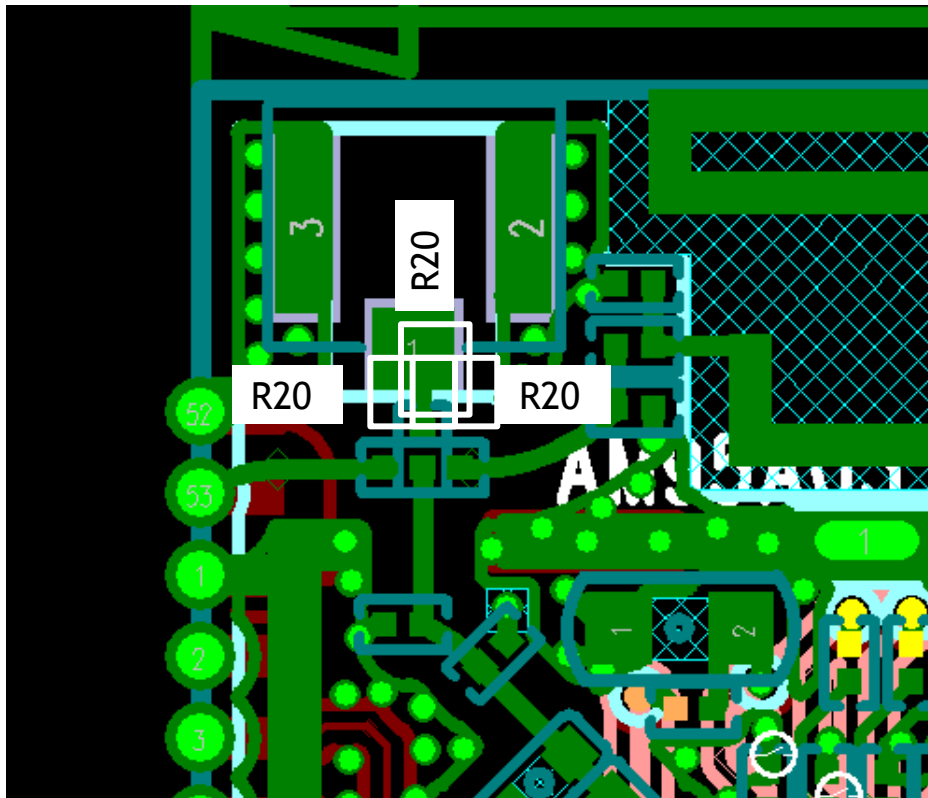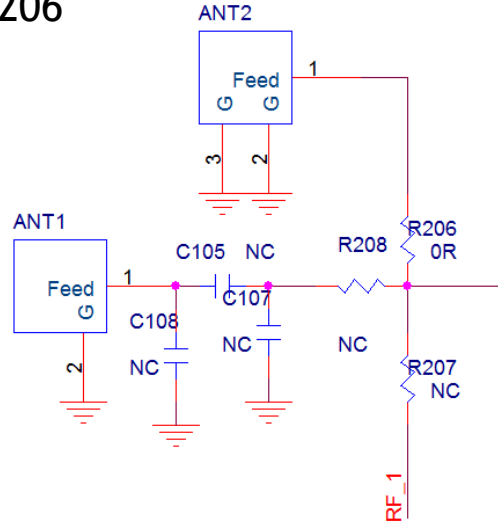- Module HDK version: HDK-AM95A03_1V0
- DEV HDK version: RTL-AMEBA_DEV01_1v1

3 Pin out reference

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4 Antenna hardware setup

- I-PEX/U.FL connector: R206
- External antenna: R207
- PCB antenna: R208

5 Peripherals support

- Debug UART: GPIOB_[0..1]
- JTAG: GPIOE_[0..4]

5.1 Pin function table setup

- Multiple functions are supported by group setup.
- For example: GPIOA_6(Rx), GPIOA_7(Tx), GPIOA_3(RTS) and GPIOA_5(CTS) are used if UART0 function. GPIOA_3(RTS) and GPIOA_5(CTS) can not be used as other functions.
- For example: GPIOC_0, GPIOC_1, GPIOC_2, GPIOC_2, GPIOC_3 are used if PWM is occupied. GPIOC_1(PWM1) and GPIOC_2(PWM2) can not be used as other functions.

PIN name	JTAG	SDD	SDH	MII	UART Group	I2C Group	SPI Group	I2S Group	PCM Group	WL_LED	PWM	ETE	WKDT	GPIO INT	Default State	SCHMT
GPIOA_0		D2	D2	RX_CK	UART2_IN		SPI1_MISO							GPIO_INT	PH	O
GPIOA_1		D3	D3	RXD0	UART2_CTS		SPI1_MOSI							GPIO_INT	HI	
GPIOA_2		CMD	CMD	RXD1	UART2_RTS		SPI1_CLK								PH	O
GPIOA_3		CLK	CLK	RXD2	UART0_RTS										PH	O
GPIOA_4		D0	D0	RXD3	UART2_OUT		SPI1_CS								PH	
GPIOA_5		D1	D1	RXDV	UART0_CTS								D_SBY0		PH	
GPIOA_6		INT	CD	RXERR	UART0_IN										PH	
GPIOA_7			WP	COL	UART0_OUT										HI	
GPIOB_0					LOG_OUT							ETE0	D_SLP0		HI	
GPIOB_1					LOG_IN					WL_LED0		ETE1			PH	
GPIOB_2						I2C3_SCL						ETE2			HI	O
GPIOB_3						I2C3_SDA						ETE3		GPIO_INT	PH	
GPIOB_4										WL_LED0	PWM0			GPIO_INT	PH	
GPIOB_5										WL_LED0	PWM1				PH	O
GPIOC_0				TXD2	UART0_IN		SPI0_CS0	I2S1_WS	PCM1_SYNC		PWM0	ETE0			HI	
GPIOC_1				TXD1	UART0_CTS		SPI0_CLK	I2S1_CLK	PCM1_CLK		PWM1	ETE1		GPIO_INT	HI	O
GPIOC_2				TXD0	UART0_RTS		SPI0_MOSI	I2S1_SD_TX	PCM1_OUT		PWM2	ETE2			HI	
GPIOC_3				TX_CK	UART0_OUT		SPI0_MISO	I2S1_MCK	PCM1_IN		PWM3	ETE3		GPIO_INT	HI	O
GPIOC_4				TXD3		I2C1_SDA	SPI0_CS1	I2S1_SD_RX						GPIO_INT	HI	
GPIOC_5				TXEN		I2C1_SCL	SPI0_CS2							GPIO_INT	HI	O
GPIOD_4				MDC	UART2_IN	I2C0_SDA	SPI1_CS		PCM1_SYNC		PWM0	ETE0		GPIO_INT	PH	O
GPIOD_5				MDIO	UART2_CTS	I2C0_SCL	SPI1_CLK		PCM1_CLK		PWM1	ETE1	D_SBY2	GPIO_INT	PH	O
GPIOD_6					UART2_RTS	I2C1_SCL	SPI1_MOSI	I2S0_SD_RX	PCM1_OUT		PWM2	ETE2		GPIO_INT	PH	O
GPIOD_7					UART2_OUT	I2C1_SDA	SPI1_MISO		PCM1_IN		PWM3	ETE3		GPIO_INT	PH	O
GPIOE_0	TRST				UART0_OUT	I2C2_SCL	SPI0_CS0	I2S0_WS	PCM0_SYNC		PWM0				PH	O
GPIOE_1	TDI				UART0_RTS	I2C2_SDA	SPI0_CLK	I2S0_CLK	PCM0_CLK		PWM1			GPIO_INT	PH	O
GPIOE_2	TDO				UART0_CTS	I2C3_SCL	SPI0_MOSI	I2S0_SD_TX	PCM0_OUT		PWM2			GPIO_INT	PH	O
GPIOE_3	TMS				UART0_IN	I2C3_SDA	SPI0_MISO	I2S0_MCK	PCM0_IN		PWM3		D_SBY3	GPIO_INT	PH	O
GPIOE_4	CLK					I2C3_SCL	SPI0_CS1								PH	O
GPIOE_5						I2C3_SDA	SPI0_CS2							GPIO_INT	PH	O

NOTE1: PH = Pull-High, HI = High-impedance

NOTE2: GPIOA_1 needs external Circuit to do the pull high control; others' pull control can be done by register setting (including GPIOA_1's PD).

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5.2 Peripheral Descriptions

		Baud rate
UART	UART_LOG	38400 Hz
	UART0	4 MHz
	UART2	4 MHz
		Clock rate
SPI	SPI0_Master	20.8 MHz
	SPI0_Slave_TRx	4.1 MHz
	SPI1_Master	41.6 MHz
	SPI1_Slave_TRx	--
		Clock rate
I2C	Standard mode	0~100 kb/s
	Fast mode	<400 kb/s
	High-speed mode	<3.4Mb/s

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6 Hardware configuration

6.1 CMSIS-DAP

RTL-AMEBA_DEV01 supports CMSIS-DAP debugger. It requires installing "serial to USB driver" at first. Serial to USB driver can be found in tools\serial_to_usb\mbedWinSerial_16466.

Connect board to the PC with micro-USB cable.

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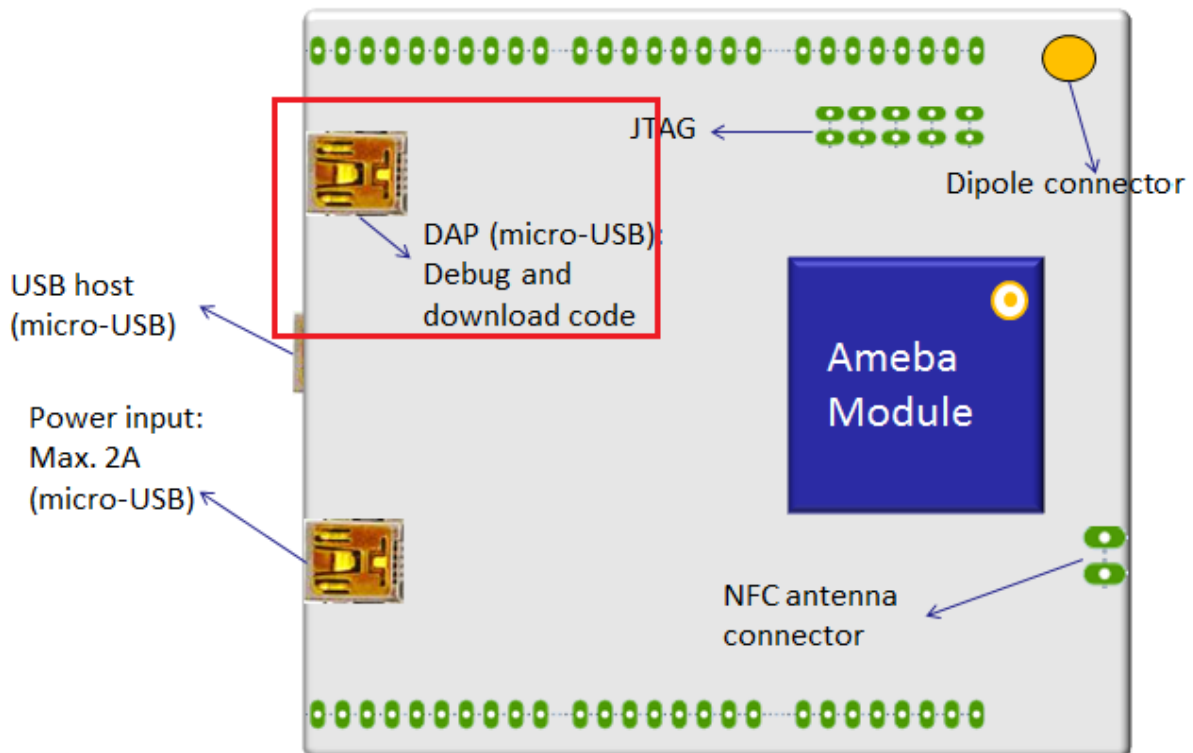
6.2 J-Link/JTAG

Weld JTAG and log UART connectors to HDK board and connect with pitch 2.54mm 2x5pins connector. It is recommended to weld the connector on the bottom side. Users can connect extension boards from top side.
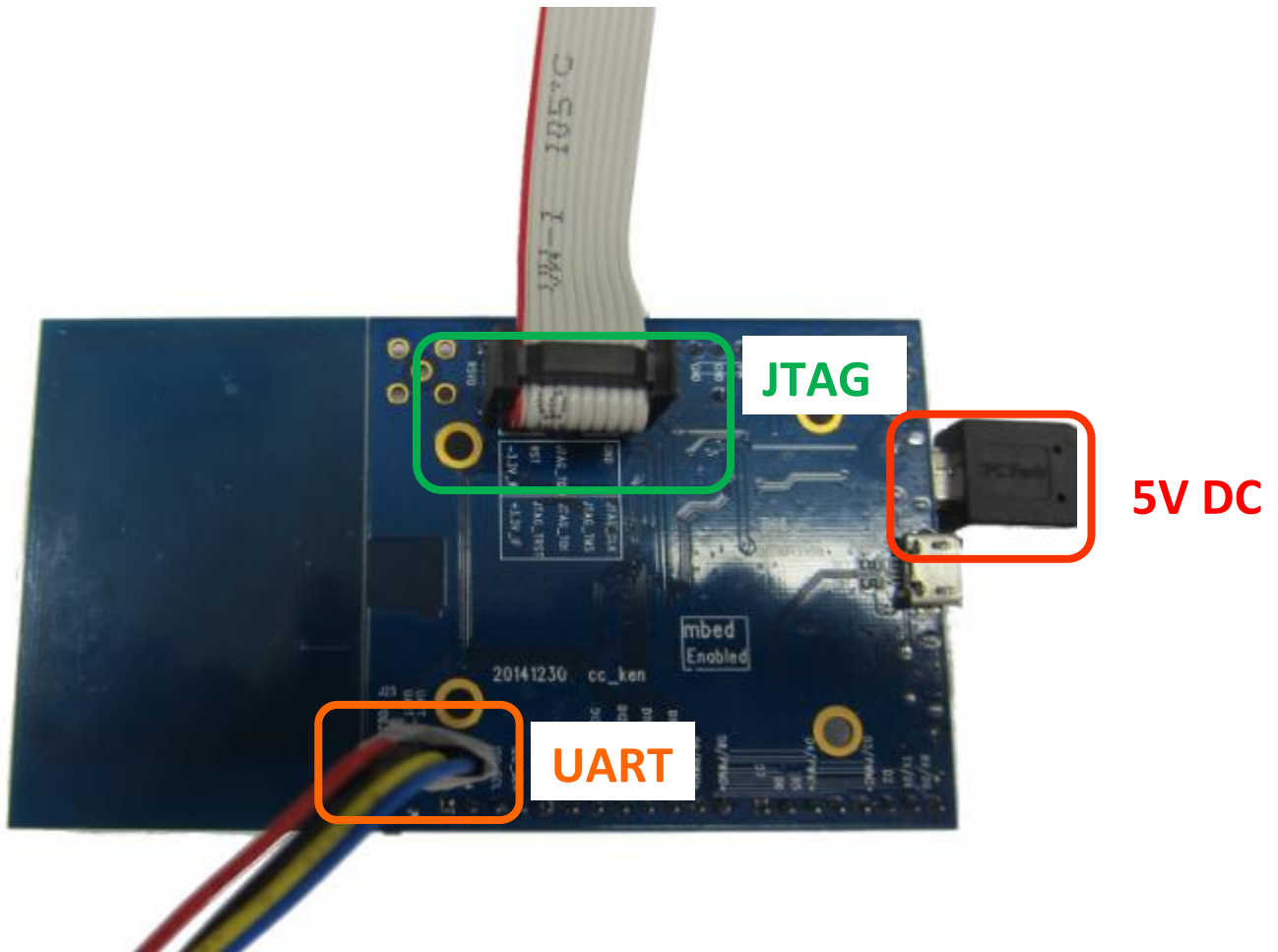
Dupont Line or 2.54mm 2x5 pins connector.

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Power On(Disable DAP mode)

Holding TGT_NRESET button (J24, red-circled) then press Pdn button (J13, blur-circled). Release the button after power on.
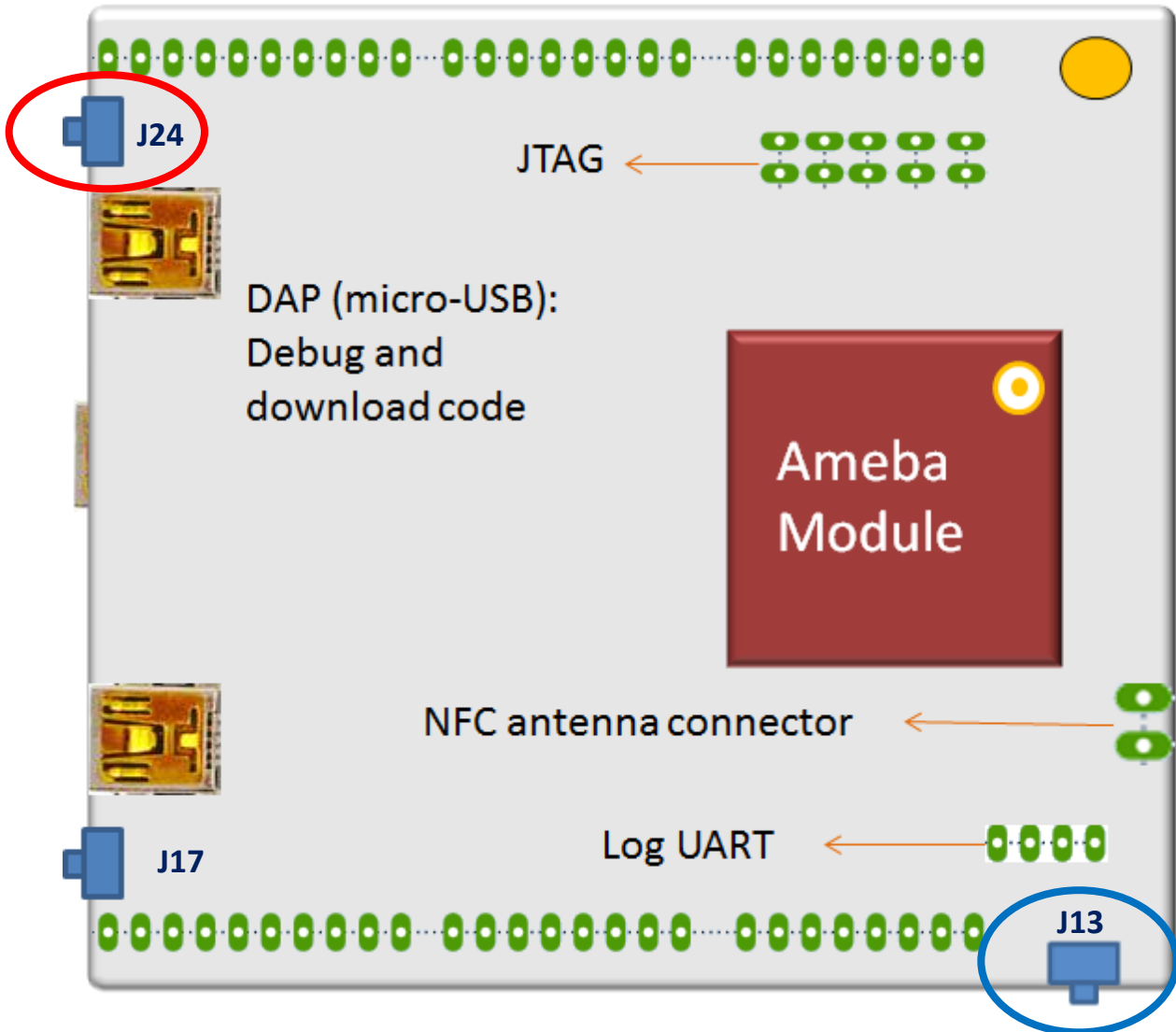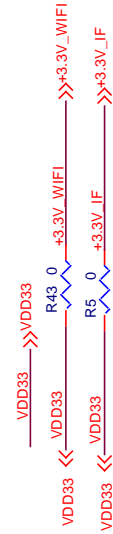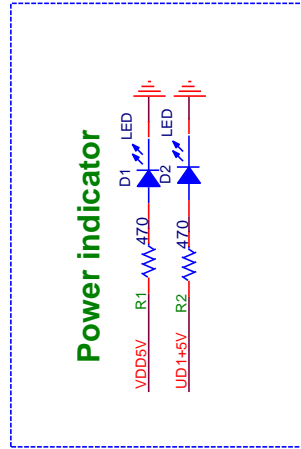
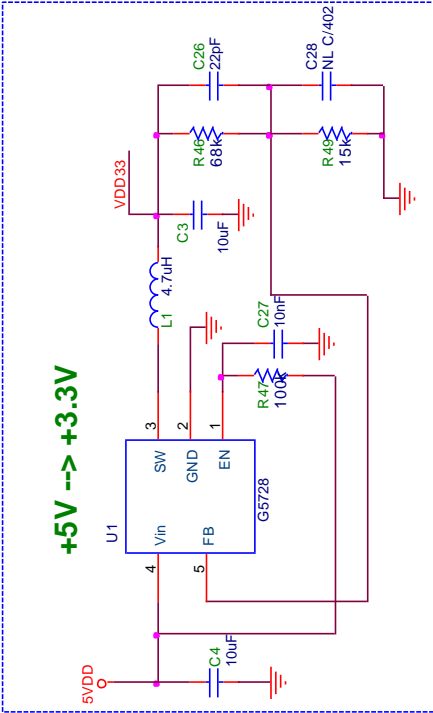# 6.3 DAP mode

In DAP mode, the DAP firmware can be updated.

Holding TGT_NRESET button (J24, red-circled) then press nRESET button (J17, blur-circled). Then the DAP mode window will show up.
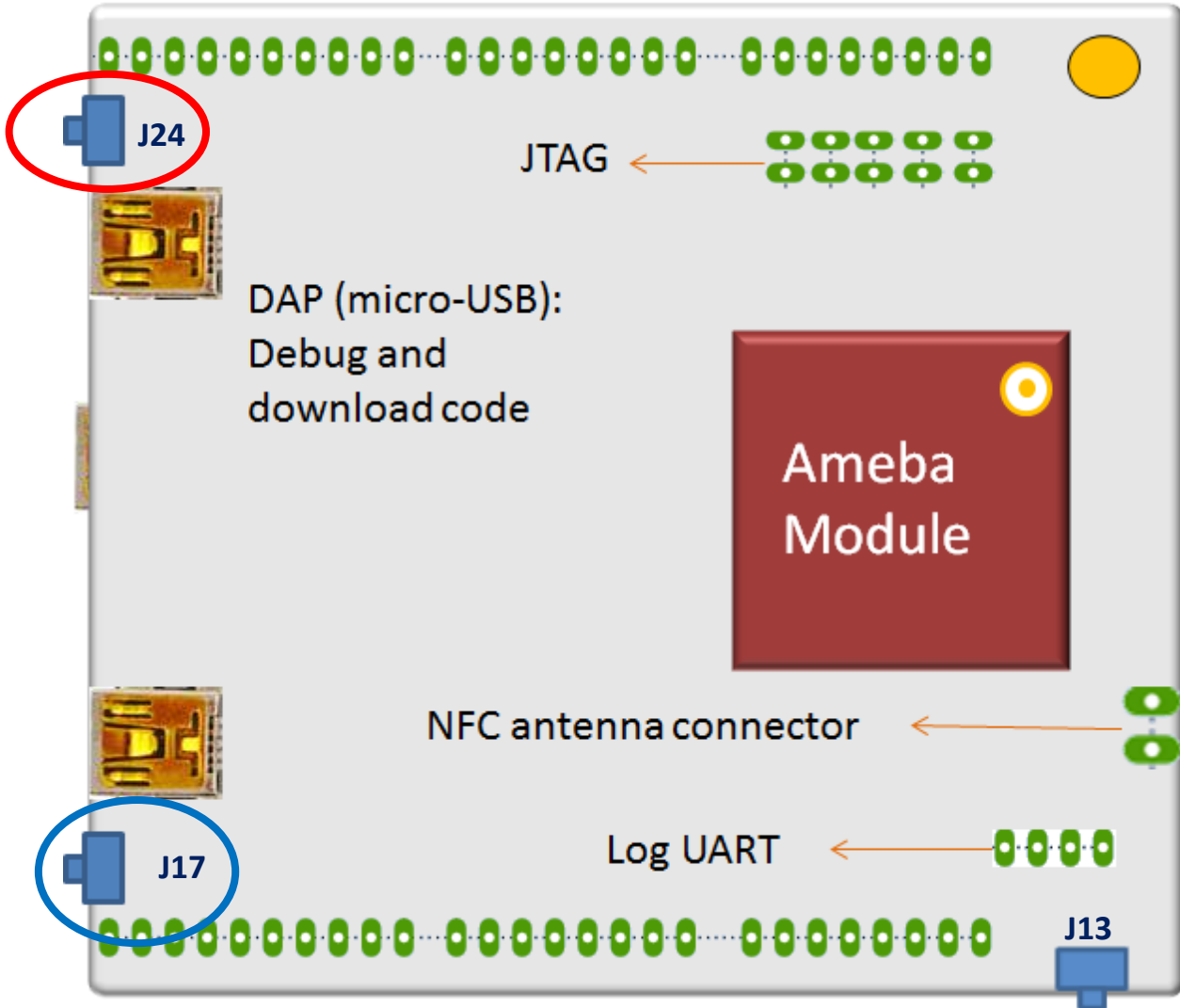
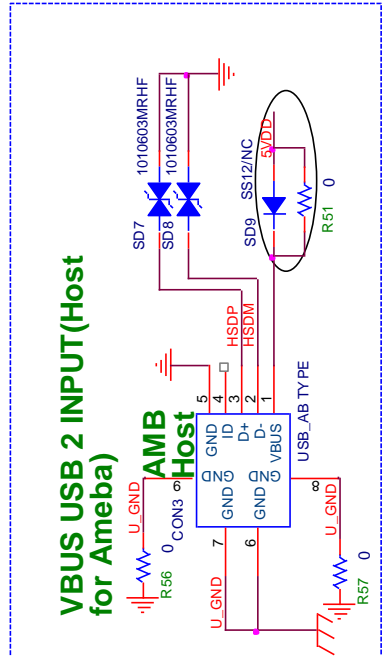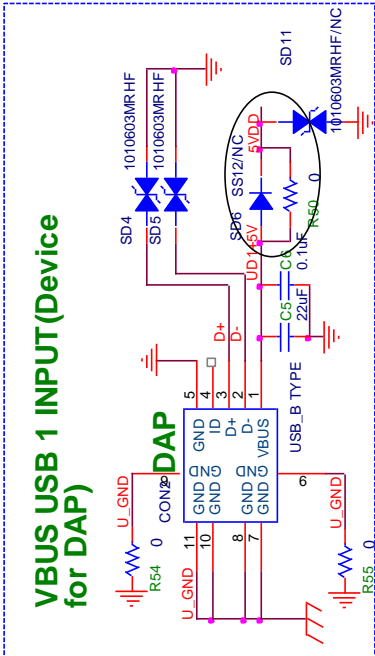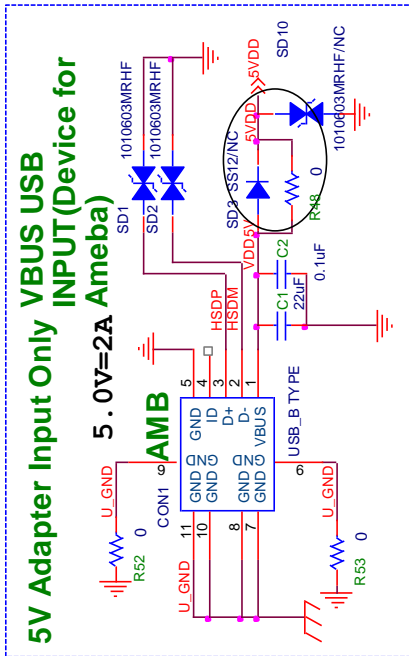DAP window will show up when entering DAP mode.

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7 Reference electrical schematics

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8 Sensor board

- Extension board: RTL-AMEBA_EXT B2_2V0