



> David Nicholls, Hitex (UK) Ltd.



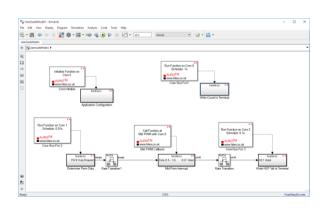


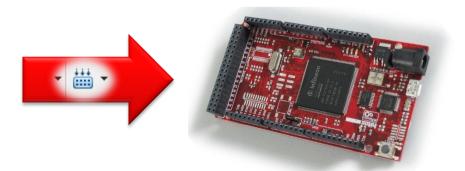
Targeted Modelling and Build Solution





- Build your complete multicore application in Simulink®
- Use Embedded Coder® to produce application C code
- Compile C code and download application to Target





Re-use existing control algorithms





- Existing control algorithms built in MATLAB®/Simulink® may now be reused
- Conversion to AURIXTM simpler than ever before
- Targets such as ShieldBuddy supported "out of the box"
- Adaption to customer Targets on demand











Setup the Simulink® Model

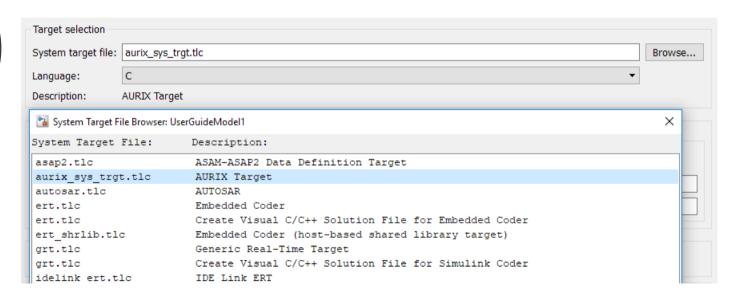
Note: Not all the following blocks are available in the free version

Simulink® Model Setup





Select the AURIX[™]
Target in Simulink®

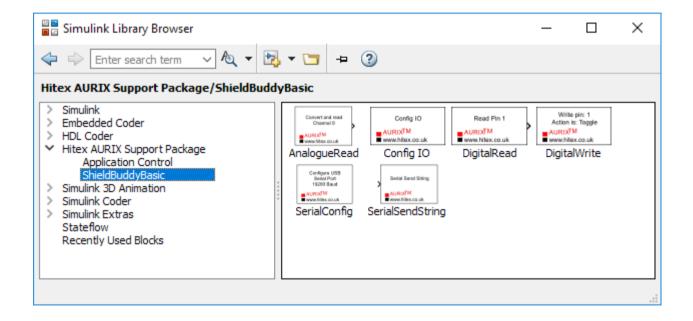


Simulink® Model Setup







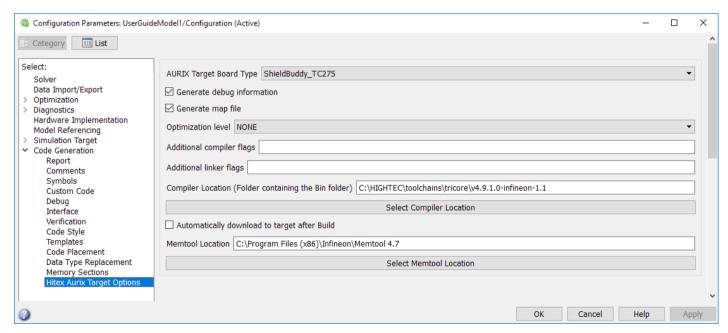


Simulink® Model Setup













Build Application Structural

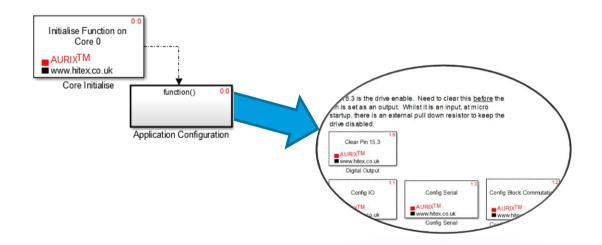
Build Application - Structural







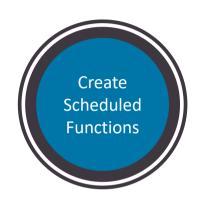
Build Startup Configuration and assign to the Required AURIX[™] Core



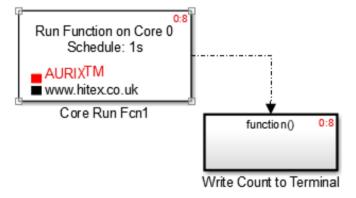
Build Application - Structural







Assign an AURIXTM Core and Scheduling Period







Build Application Functional Examples

Build Application – Functional Examples

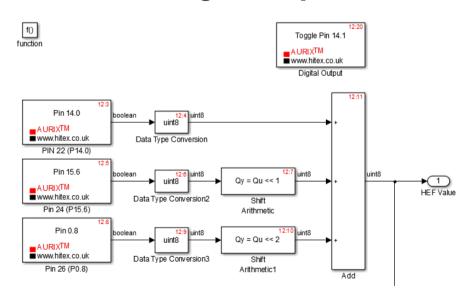






Read State of Digital Input

Set, Clear, & Toggle Digital Output



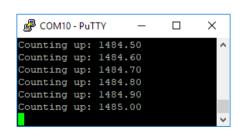
Build Application - Functional Examples

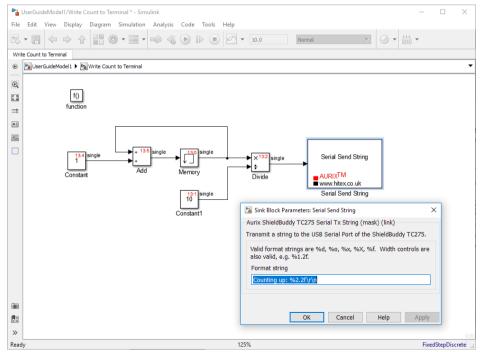






Use C Style Format String. Block adapts to number of required inputs.





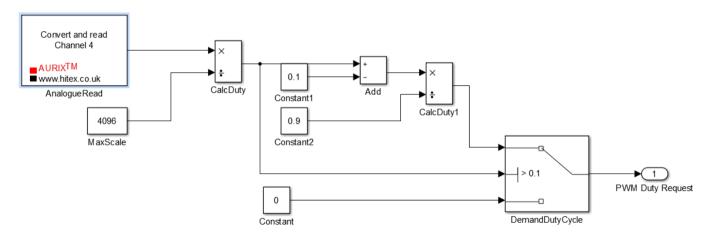








Trigger and Read VADC results







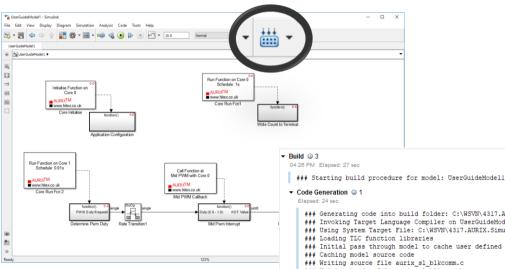
Build Application Send to Target

Build Application – Send to Target



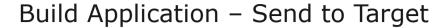






Single Click to Build, Compile and Download to Target

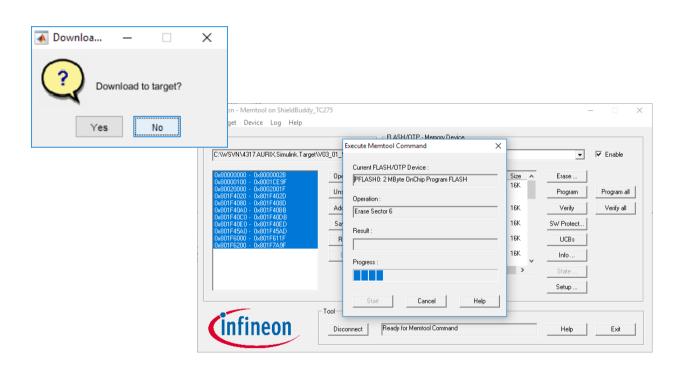
Generating code into build folder: C:\WSVN\4317.AURIX.Simulink.Target\V03 01 SimTargetBase ### Invoking Target Language Compiler on UserGuideModell.rtw ### Using System Target File: C:\WSVN\4317.AURIX.Simulink.Target\V03 01 SimTargetBase\V06 Impl-### Loading TLC function libraries ### Initial pass through model to cache user defined code ### Caching model source code ### Writing source file aurix sl blkcomm.c ### Writing header file aurix sl blkcomm.h ### Writing source file aurix sl asclin.c ### Writing header file aurix sl asclin.h C:\HIGHTEC\toolchains\tricore\v4.9.1.0-infineon-1.1/bin/tricore-gcc -W1,-T C:\WSVN\4317.AURIX. sections -nostartfiles -W1, --extmap="a" -W1, -Map, UserGuideModell .map -g2 -o UserGuideModell IfxAsclin Asc.o IfxAsclin Lin.o IfxAsclin PinMap.o IfxAsclin Spi.o IfxAsclin cfq.o IfxCpu.o If IfxGpt12.o IfxGpt12 PinMap.o IfxGtm.o IfxGtm Atom.o IfxGtm Atom Pwm.o IfxGtm Atom PwmH1.o IfxG IfxGtm_Tom_Pwm.o IfxGtm_Tom_PwmHl.o IfxGtm_Tom_Timer.o IfxGtm_Trig.o IfxGtm_cfg.o IfxPort.o If IfxOspi cfg.o IfxScuCcu.o IfxScuEru.o IfxScuWdt.o IfxScu PinMap.o IfxSrc.o IfxStdIf DPipe.o If. Ifx Fifo.o Ifx InternalMux.o SpiIf.o aurix sl asclin.o aurix sl blkcomm.o timersched.o UserGui "### Created "executable": UserGuideModell.elf" HEX file successfully created: UserGuideModell.hex ### Successful completion of build procedure for model: UserGuideModell Build process completed successfully











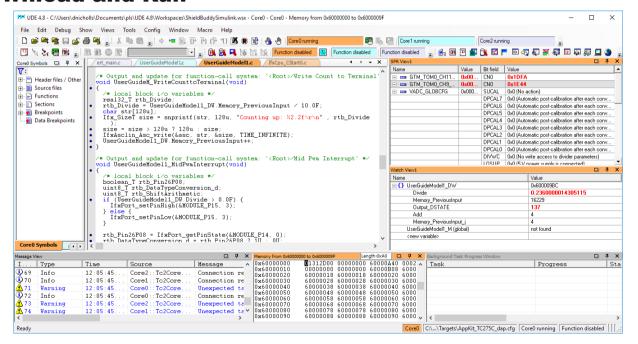
Build Application – Send to Target







Alternatively use fully featured Multicore Debugger to Download and Run



Further Information



For additional information about the free version, pricing and options, please contact Hitex (UK) Ltd.

sales@hitex.co.uk



