31-29	baanch og		Į.					
1(001) add immediate addiu setless than imm. than imm. 2(010) TLB FlPt unsigned	bearok og	I	1					
immediate than imm. than imm. 2(010) TLB F1Pt 3(011)	pranch eq	branch ne	blez	bgtz				
3(011)	andi	ori	xori	load upper immediate				
4(100) load byte load half lwl load word								
	load byte unsigned	load half unsigned	lwr					
5(101) store byte store half swl store word			swr					
6(110) load linked lwcl word								
7(111) store cond. swcl word								
op(31:26)=010000 (TLB), rs(25:21)								

op(31:26)=010000 (TLB), rs(25:21)								
23–21	0(000)	1(001)	2(010)	3(011)	4(100)	5(101)	6(110)	7(111)
25–24								
0(00)	mfc0		cfc0		mtc0		ctc0	
1(01)								
2(10)								
3(11)								

op(31:26)=000000 (R-format), funct(5:0)								
2-0	0(000)	1(001)	2(010)	3(011)	4(100)	5(101)	6(110)	7(111)
5–3								
0(000)	shift left logical		shift right logical	sra	sllv		srlv	srav
1(001)	jump register	jalr			syscal1	break		
2(010)	mfhi	mthi	mflo	mtlo				
3(011)	mult	multu	div	divu				
4(100)	add	addu	subtract	subu	and	or	xor	not or (nor)
5(101)			set 1.t.	set l.t. unsigned				
6(110)								
7(111)								