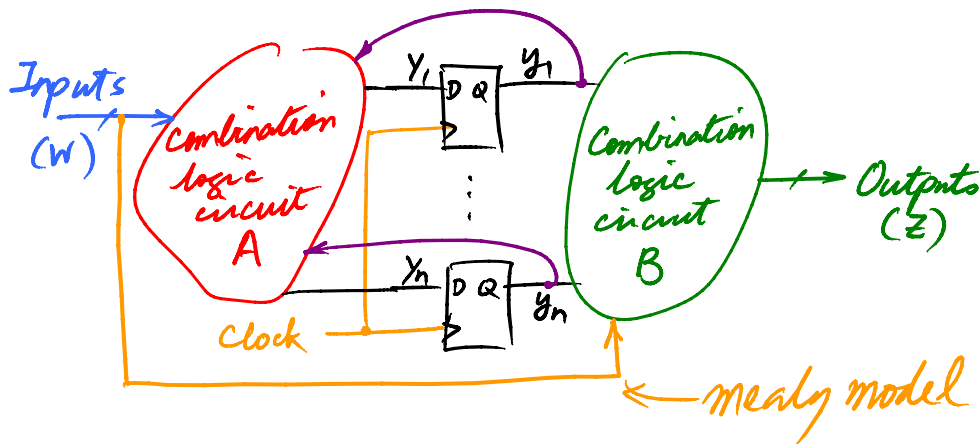
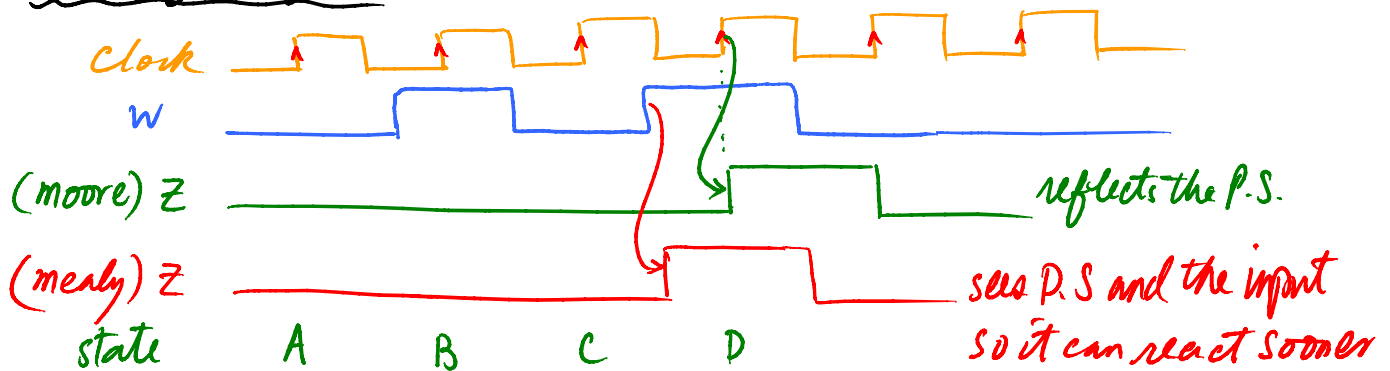


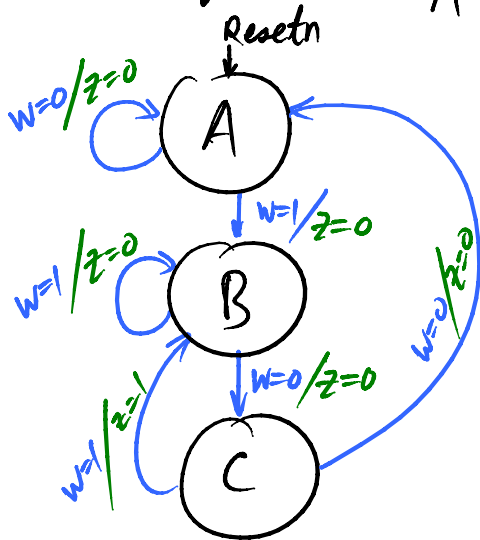
### 6.3 Mealy model of FSM



⇒ "101" pattern example  
Timing Diagram



State-diagram



A - waiting for "1"

B - seeing the first "1"

C - seeing the "1" followed by "0"

State Table

P.S	N.S		Output	
	w=0	w=1	w=0	w=1
A	A	B	0	0
B	C	B	0	0
C	A	B	0	1

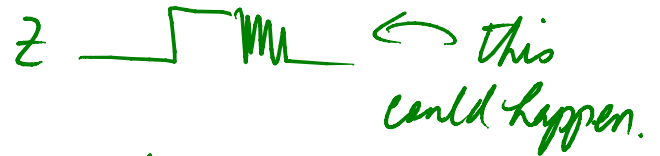
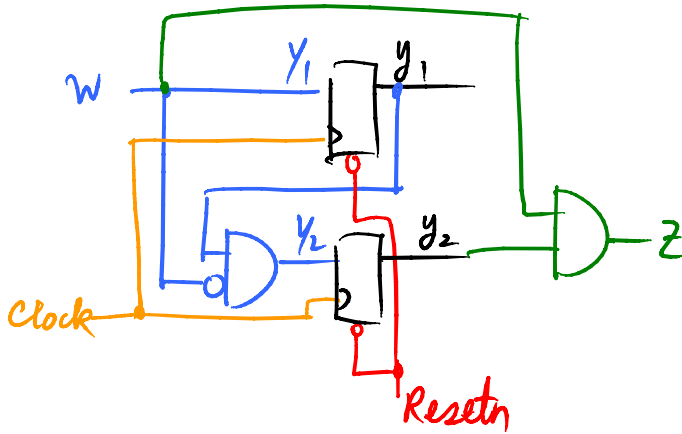
State-assignment

$y_2 y_1$	w=0		w=1		w=0		w=1	
	$y_2 y_1$	$y_2 y_1$	$y_2 y_1$	$y_2 y_1$	z	z	z	z
A	00	00	01	00	0	0	0	0
B	01	10	01	00	0	0	0	0
C	10	00	01	00	0	1	0	1

use as don't care!

Next-state expressions  $Y_2 = \bar{W}Y_1$ ,  $Y_1 = W$ , output  $Z = Y_2W$

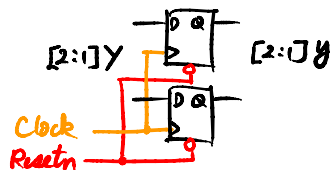
FSM. cct. diagram



reaction of Mealy model is slightly faster than the Moore model (by approx. 1 clock cycle)

```

module seq101-Mealy(input clock, W, Resetn, output Z);
  reg [2:1] y, Y; // y2, y1 are the present states, Y2, Y1 are the next states
  parameter A=2'b00, B=2'b01, C=2'b10, // state assignment
  // combinational circuit A, describing state transitions + output
  always@(W, y)
    case (y)
      A: if (!W)
          begin
            Y=A;
            Z=0;
          end
        else
          begin
            Y=B;
            Z=0;
          end
      B: if (!W)
          begin
            Y=B;
            Z=0;
          end
        else
          begin
            Y=C;
            Z=0;
          end
      C: if (!W)
          begin
            Y=A;
            Z=0;
          end
        else
          begin
            Y=B;
            Z=1;
          end
    endcase
  // FFs describing D-ff's with synchronous reset
  always@(posedge clock)
    if (Resetn==0)
      y <= A;
    else
      y <= Y;
endmodule
  
```



## 6.6 State minimization (Partitioning)

We'll use the state table for the "101" pattern recognition example as a starting point. for simplicity we will use A, B, C, D, E, F, G and H instead

	P.S	N.S		Output z
		W=0	W=1	
A	S000	S000	S100	0
B	S001	S000	S100	0
C	S010	S001	S101	0
D	S011	S001	S101	0
E	S100	S010	S110	0
F	S101	S010	S110	1
G	S110	S011	S111	0
H	S111	S011	S111	0

	P.S.	N.S		Output z
		W=0	W=1	
A	A	E		0
B	A	E		0
C	B	F		0
D	B	F		0
E	C	G		0
F	C	G		1
G	D	H		0
H	D	H		0

0-successors 1-successors

Step 1 We start with every state in the partition

$$P_1 = (ABCDEFGH)$$

Step 2 We will divide  $P_1$  into two sub partitions according to z.

$$P_2 = (\underbrace{ABCDEFGH}_{z=0})_0 (\underbrace{F}_{z=1})_1$$

Step 3 We will look at the 0-successors

$$ABCDEFGH \xrightarrow{0\text{-succ.}} AABBCDD$$

belongs to partition 0

Step 4

$$\text{looking at } ABCDEGH \xrightarrow{1\text{-succ.}} EEEFGHH$$

1-successor

partition 1

partition 2

$$(ABEGH)_0 (F)_1 (CD)_1$$

P.S.	N.S		Output z
	W=0	W=1	
A	A	E	0
B	A	E	0
C	B	F	0
D	B	F	0
E	C	G	0
F	C	G	1
G	D	H	0
H	D	H	0

0-successors 1-successors

Repeat Step 3 & 4 until no further partition can be found.

$(\underline{ABEGH})_1 \xrightarrow{\text{0-succ}} \underline{AACDD} \rightarrow \text{partition } ③$

$$(AB)_1 (F)_2 (CD)_3 (EGH)_4$$
$$AB \xrightarrow{0\text{-succ}} AA, \quad AB \xrightarrow{1\text{-succ}} EE \quad (\text{no need to divide})$$
$$CD \xrightarrow{0\text{-succ}} BB, \quad CD \xrightarrow{1\text{-succ}} FF \quad (\text{no need to divide})$$
$$EGH \xrightarrow{0\text{-succ}} CDD, EGH \xrightarrow{1\text{-succ}} GHH \text{ (no need to divide)}$$

minimum states = 4.  $AB \rightarrow A$

$$CD \rightarrow C$$
$$EGH \rightarrow E$$
$$F \rightarrow F$$

P.S	W.S		Output z
	W=0	W=1	
A	A	E	0
C	A	F	0
E	C	E	0
F	C	E	1