5
CS570 Summer 2016 Exam #2
6/15/2016
1. $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$ 2: $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$ 3: $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$ 4: $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$ 5: $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$ 7: $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$ 8: $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$ 9: $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$ = $\frac{\cancel{\cancel{0}}}{\cancel{\cancel{0}}}$
1. a. An O/S that uses a paged memory management scheme requires the hardware to include a PMMU in addition to the standard computer components.
b. Name two control bits used in both segmentation memory systems and Paging memory systems:
dray wit? 1. Modification
2. presen+
2. Given an O/S which has divided all of fast memory into 3 frames, and if this O/S uses the FIFO algorithm for page replacement/swapping, then how many page faults are generated if the following sequence of pages are requested for use (show your work!): 2 5 2 3 5 4 2 5 1 2 3 2 2 5 5 3 3 4 2 5 1 1 3 2 2 2 5 5 3 3 4 2 5 1 3 2 2 2 5 5 3 3 4 2 5 1 3
Page hult: * * * * * * * * = 9
3. Repeat #2, but use the LRU algorithm for page replacement/swapping instead. How many page faults? 2 5 2 3 5 4 2 5 1 2 3 2 2 5 2 3 5 4 2 5 1 2 3 2 2 5 2 3 5 4 2 5 1 2 3 2 5 2 3 5 4 2 5 1 2 3
Page fault: ** * * * * = 7
4. Consider a virtual memory system using paged memory with 32-bit physical addresses and 32-bit virtual addresses and 18 bits of the virtual address was for the offset. a. What size should the pages be (how big do they need to be)? 262144 b. How many pages can the process have (maximum number)? 16,384

