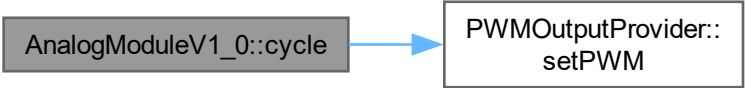


AnalogModuleV1_0::cycle



```
graph LR; A[AnalogModuleV1_0::cycle] --> B[PWMOutputProvider::setPWM]
```

The diagram consists of two rectangular boxes connected by a blue arrow. The left box is gray and contains the text 'AnalogModuleV1_0::cycle'. The right box is white with a black border and contains the text 'PWMOutputProvider::setPWM'. A blue arrow points from the right side of the gray box to the left side of the white box.

PWMOutputProvider::
setPWM