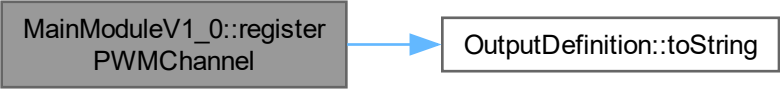


MainModuleV1\_0::register  
PWMChannel



```
graph LR; A["MainModuleV1_0::register  
PWMChannel"] --> B["OutputDefinition::toString"]
```

OutputDefinition::toString