

```
graph BT; AnalogOutput --> Interface;
```

Interface

A UML diagram illustrating an inheritance or implementation relationship. At the top is a white rectangular box with a thin gray border containing the text "Interface". Below it is a gray rectangular box with a thin black border containing the text "AnalogOutput". A blue arrow points vertically from the top center of the "AnalogOutput" box to the bottom center of the "Interface" box, indicating that "AnalogOutput" implements or inherits from "Interface".

AnalogOutput