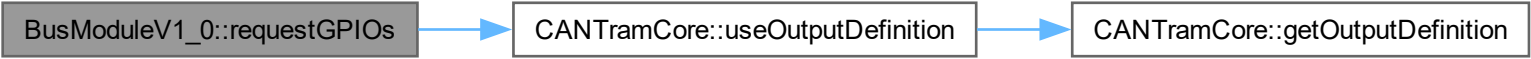


BusModuleV1_0::requestGPIOs



```
graph LR; A[BusModuleV1_0::requestGPIOs] --> B[CANTramCore::useOutputDefinition]; B --> C[CANTramCore::getOutputDefinition];
```

CANTramCore::useOutputDefinition

CANTramCore::getOutputDefinition