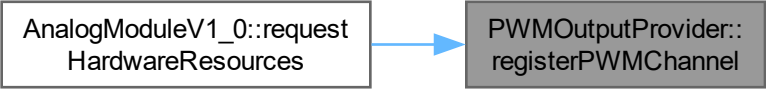


AnalogModuleV1\_0::request  
HardwareResources



```
graph LR; A[AnalogModuleV1_0::request  
HardwareResources] --> B[PWMOutputProvider::  
registerPWMChannel];
```

The diagram consists of two rectangular boxes connected by a blue arrow pointing from left to right. The left box is white with a black border and contains the text 'AnalogModuleV1\_0::request' and 'HardwareResources' on two lines. The right box is gray with a black border and contains the text 'PWMOutputProvider::' and 'registerPWMChannel' on two lines. The blue arrow originates from the right side of the left box and points to the left side of the right box.

PWMOutputProvider::  
registerPWMChannel