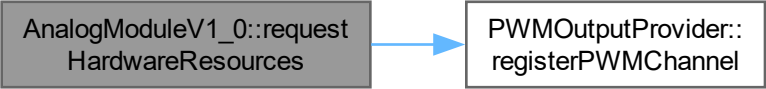


AnalogModuleV1\_0::request  
HardwareResources



```
graph LR; A["AnalogModuleV1_0::request  
HardwareResources"] --> B["PWMOutputProvider::  
registerPWMChannel"]
```

The diagram consists of two rectangular boxes connected by a blue arrow pointing from left to right. The left box is shaded gray and contains the text 'AnalogModuleV1\_0::request' followed by 'HardwareResources' on a new line. The right box is white with a black border and contains the text 'PWMOutputProvider::' followed by 'registerPWMChannel' on a new line.

PWMOutputProvider::  
registerPWMChannel