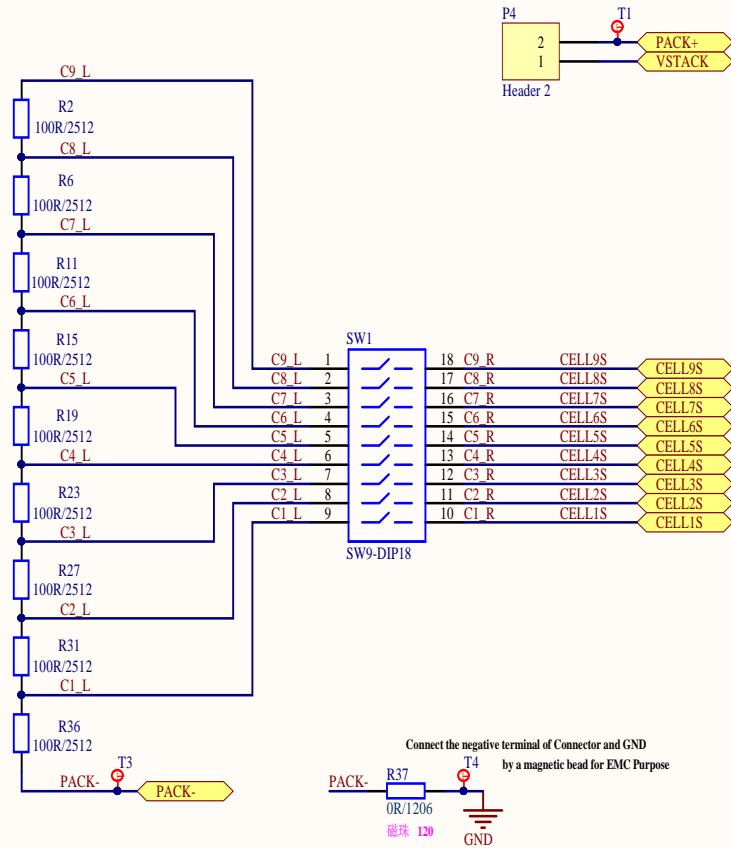
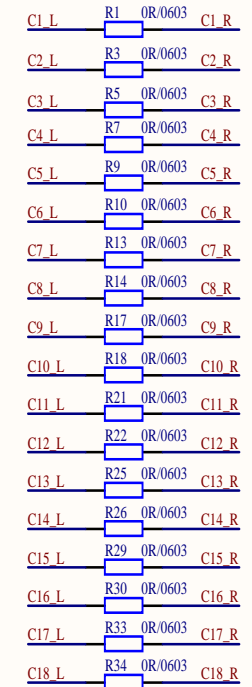


Cell Simulator by dial switch



Cell Simulator by 0 Resistor



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Date: 2022/10/9	Time: 9:29:15	Sheet * of *
File: C:\Users\Leo_Zhang\Desktop\ZX_PCB\BM20 Rev0\BM20_Rev0_CellSimulator.SchDoc		



A

A

B

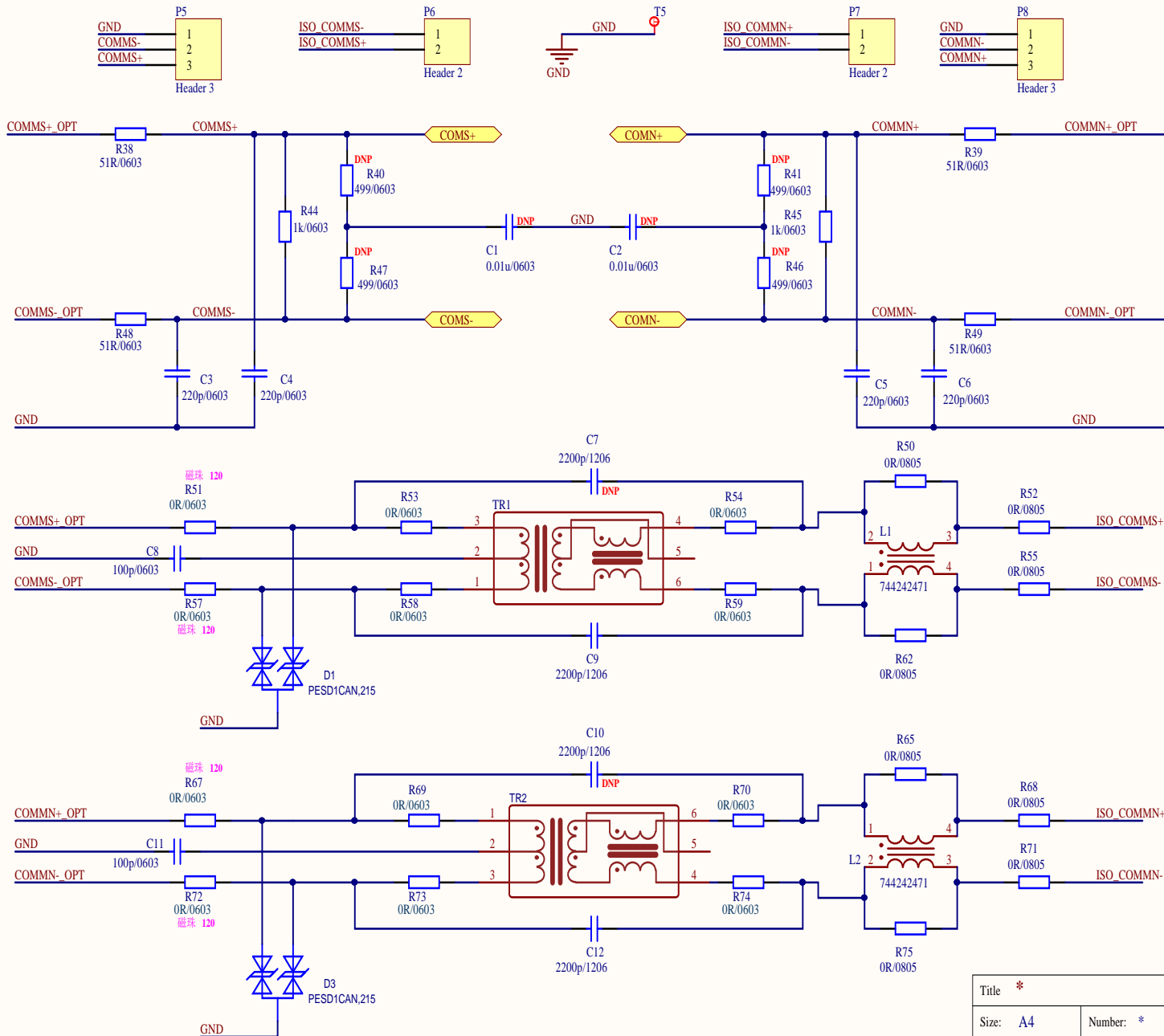
B

C

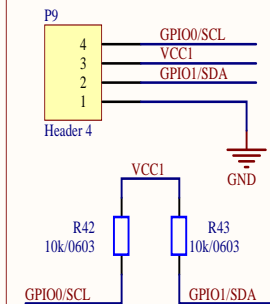
C

D

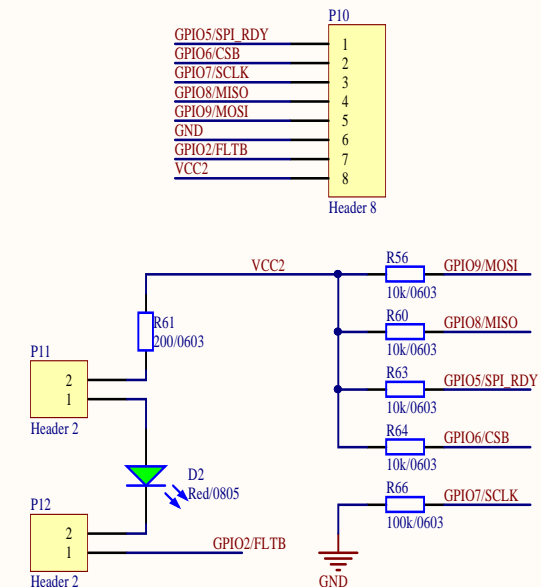
D



I2C



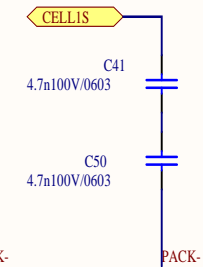
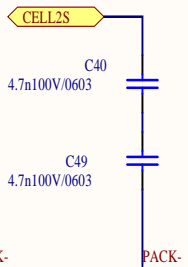
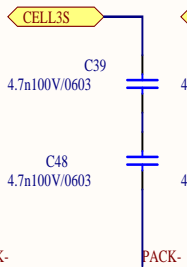
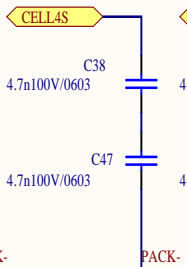
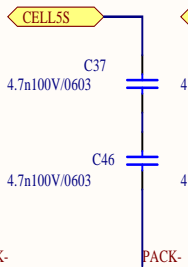
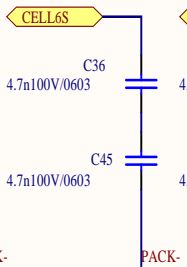
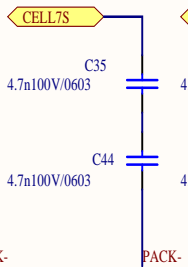
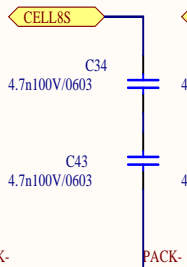
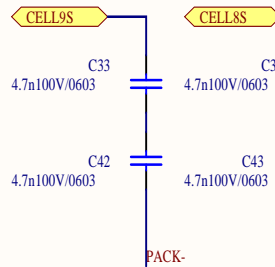
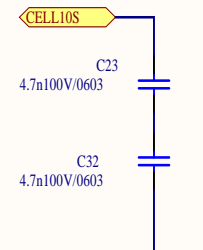
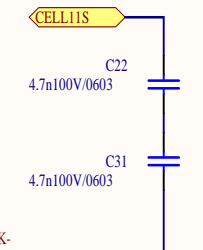
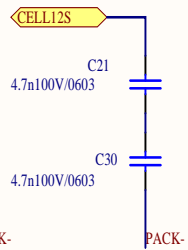
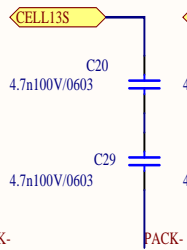
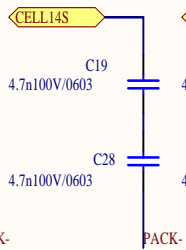
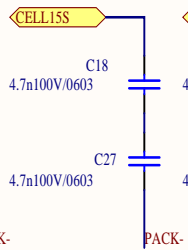
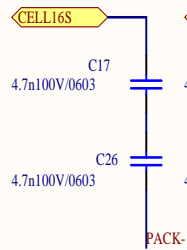
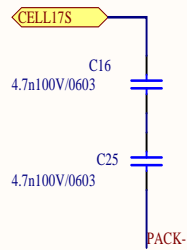
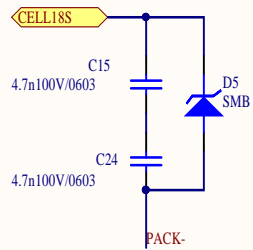
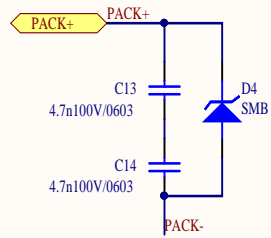
SPI




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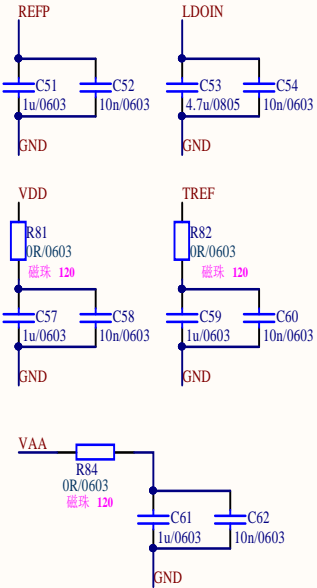


CM Caps and TVS

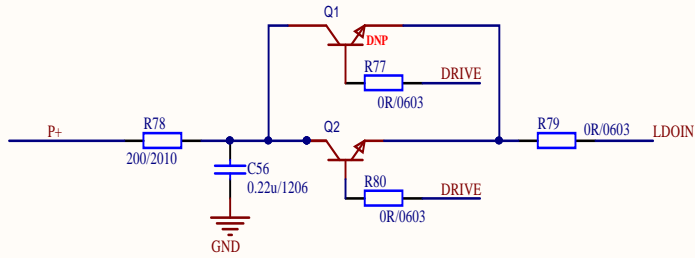


Title			Altium Limited L3, 12a Rodborough Rd		
Size: A4	Number:	Revision:	Frenchs Forest NSW		
Date: 2022/10/9	Time: 9:29:16	Sheet of	Australia 2086		
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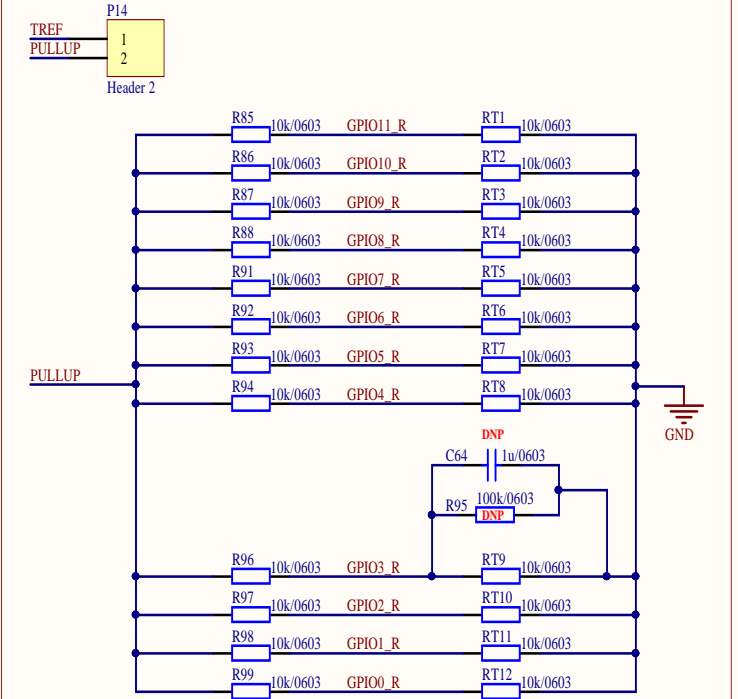
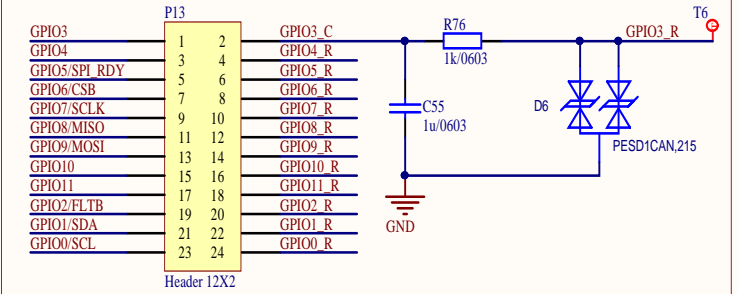
Decoupling CAPS



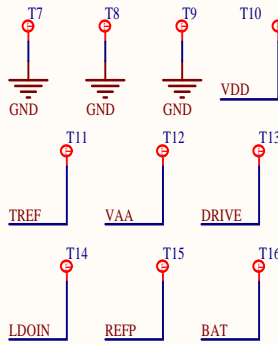
BJT Power Supply



GPIOs

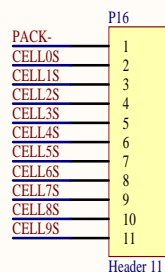
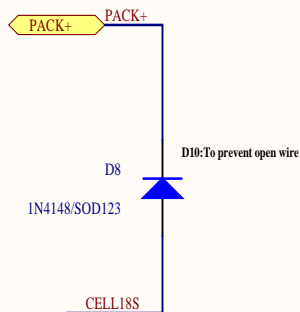


Test Points

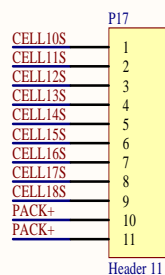


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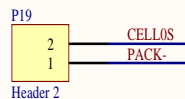
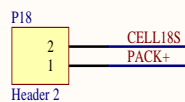




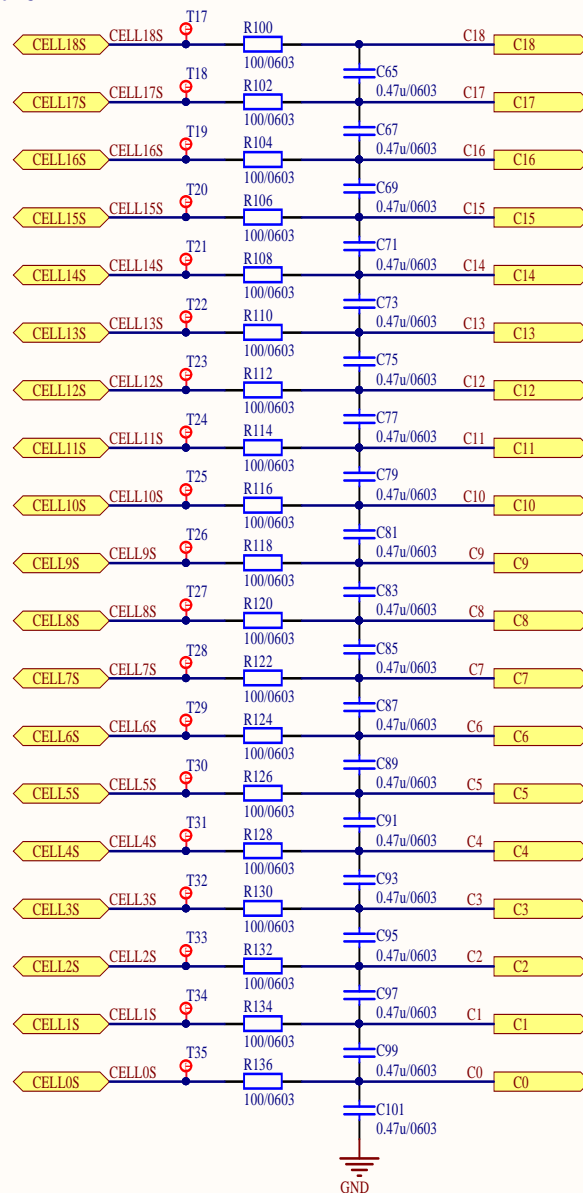
Pin Description:
1: the negative terminal of BAT connector
2-11: cell voltage ADC input



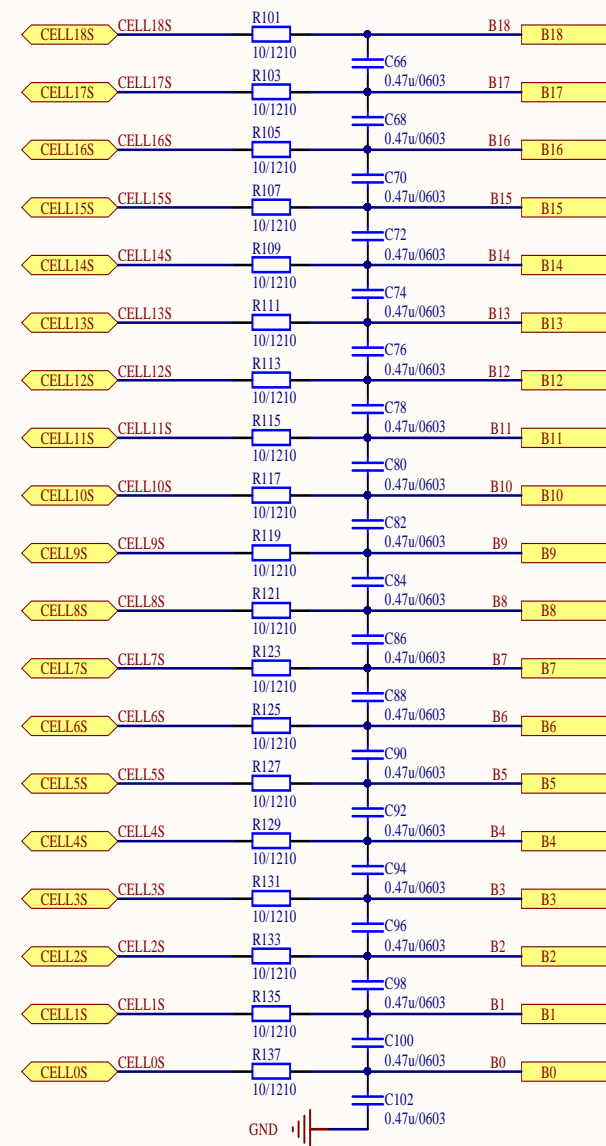
Pin Description:
1-9: cell voltage ADC input
10-11: the positive terminal of BAT connector



RC For Cn



RC For Bn



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