Test Case of Technical Requirement

WP as Prerequisites	Author of TC of TR
TR_v0.2	Shuo Xu

TC_TR ID	TC Description	Configuration	Pass/Fail Criteria	Traced to TR ID
TC_TR_00	Single measurement			
	Continuous measurement			

TC_TR ID	TC Description	Configuration	Pass/Fail Criteria	Traced to TR ID
	Automatic cell balance			
	Manual cell balance			
	Reversely wake up	B0 is chained to S1, S2 with C All B0, S1, S2 are initially in SD mode BAT=60V for B0, S1, S2 B0 generate WAKE tone from north port B0 generate ADDR CMD with 0x01 as ADDR B0 generate FLT_MSK CMD to mask other faults B0 generate MON_WAEK_GO CMD B0 generate TO_SLEEP CMD Force GPIO OT at S1		
	IO			
	I2C master			
	Ring communication			

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