Application Scenario

TR ID	Description	
TR016	If AFE is digital reset due to itself, such as TSD, UV and the reset, MCU needs to know these detailed caused to take proper actions if AFE is waked up again	
TR043	Auto-CB is seldom used during application, usually, only several cells need to be balanced	
TR046	Since the timer of AFE is not used for CATL for now, thus, default max time threshold for each channel	
TR048, TR063	Since the thermal sensors may be allocate to monitor two different areas: battery pack and PCB board, thus, two groups protection thresholds are neccessary	
TR010, TR083	Two kinds of digital reset shall be present for MCU flexible application: hard and soft. For hard digital reset, the whole chain shall be reset by wake tone For soft digital reset, only one device shall be reset by register bit SRST, besides, the ADDRESS shall be kept during soft digital reset	
TR085	To meet the mass demo requirement before BM04A is ready, two solutions are offered: 1.if customer has xx600, only 2 BM02As/1 BM02A+GUI are provided 2. if customer has no xx600, 2 BM02As/1 BM02A+USB to ANY+GUI are provide. For USB to ANY, ANY means 2Mbps UART.	
TR100	There is no bound relationship about SCLK of MCU and SPI_RDY. SPI_RDY just provide FIFO status information for MCU, MCU always has the decision-making choice to continue SCLK or not. BTW: FLTB shall indicate the underflow or overflow of FIFO as the bottom line for MCU	