





CHIN HUNG VUI

 B.Eng. (Hons) [Electrical – Electronics](#) (Major in [Telecommunication](#)), University of Technology Malaysia

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Experience

Senior Software Engineer (Software Integrator), [Continental Automotive Singapore](#) (Jan 2023 - Present)

C/C++, Python, C#, Java, CI/CD (Jenkins), Robot Framework

- continue working as the main graphical controller (GC) software integrator for Japan OEM (JOEM) project.

Software Engineer (Software Integrator), [Continental Automotive Singapore](#) (Apr 2021 – Jan 2023)

C/C++, Python, C#, Java, CI/CD (Jenkins), Robot Framework

- main graphical controller (GC) software integrator who work closely with architect to define software architecture for a new full digital cluster project which led by Continental Singapore team.
- develop and maintain system software components running on Green Hills Software (GHS) Integrity OS (POSIX-certified RTOS).
- deploy and integrate third-party software components based on project need.
- develop several tools using python and C# which greatly enhance the integration process.
- managing system configuration using CMake in the complex software system of a full digital cluster project.
- deploy and maintain CI/CD pipeline for new project using Jenkins, Git, and Jira.
- define software integration test strategy and acceptance criteria.
- setup and maintain automation test bench for integration test using Robot Framework.
- perform software integration test for sample release to ensure software maturity is ready for deliverable milestones.
- releasing and documentation of software.
- support, investigate and propose solution for production issue.

Software Engineer (Feature Responsible | GC Application Developer | HMI Developer), [Continental Automotive Singapore](#) (July 2019 – Apr 2021)

C/C++, Python, CGI Studio

- holding multiple roles simultaneously: software feature responsible, graphical controller (GC) application developer, and HMI developer.
- responsible for the complete function chain of Warning Subsystem (a critical software subsystem in Full Digital Cluster project).
- leading the design and development of modules in warning subsystem.
- manage to implement warning subsystem from scratch and successfully deliver to customer within a very tight schedule.
- analyse and clarify requirement with Japan OEM (JOEM).
- document software specification according to customer's requirement.
- involve in entire SDLC which include requirement analysis, system design, system realization, testing, and documentation phase.
- participant in review process to assure the quality of software and documentation.
- familiar with entire project's software architecture (AC - GC), IIP framework, Courier Framework, and Candera CGI Studio (Embedded HMI design tool).
- working in agile development team which utilizing scrum-based methodologies and tool suites (Jira).

SOC/CPU Silicon Design Engineer Intern, [Intel Malaysia](#) (June 2018 – Aug 2018)

- perform pre-Silicon Software verification of RTL design in System Verilog on X Window System.
- work with different teams to resolve the violations found in the RTL design.
- ramp up new team members on FEBE knowledge.

Education

- 2015 – 2019: Bachelor of Engineering (Honours) Electrical – Electronics, UTM, CGPA: 3.84 / 4.00
FYP: Real-Time Hand Gesture Recognition Using Deep Learning for Smart Home Systems
- 2013 – 2014: STPM, SMK Sungai Tapang, CGPA: 3.75 / 4.00
- 2008 – 2012: SPM, SMK Sungai Tapang, Result: 11A

Achievement & Personal Project

- awarded both [Dean's Award](#) and [Vice Chancellor's Award](#) in 2019 for outstanding academic and extracurricular achievements.
- represented Malaysia and secured the first-ever [championship](#) for Malaysian team in ABU ROBOCON 2016, a renowned international robotic competition held at Bangkok, Thailand.
- represented Malaysia and won the [first runner up](#) position in ABU ROBOCON 2017, held at Tokyo, Japan.
- awarded the [National Champion of Microsoft Imagine Cup Malaysia 2018](#) with our intelligence surveillance system that can understand and predict patients' movements and activities to prevent unwanted incidents.
- awarded the [Regional Winner of Innovate Malaysia \(SAS Track\) 2018](#), [Champion of The Great Lab \(TGL\) Grand Design Challenge 2017](#), [The Grand Lab \(TGL\) Grand Design Challenge 2017 Gold Award](#), and [Champion of Internet of Things \(IoT\) Hackathon 2017](#), where we created a machinery diagnosis and predictive maintenance system based on big data analytics and machine learning.
- awarded the [Theme Award](#) in the 2018 UTM Electrical Engineering Capstone Showcase for our real-time parking spot detection project.
- created a portfolio webpage (<https://leonardchin2017.github.io>) using HTML, CSS, and JavaScript.
- led the development of STM32 in UTM ROBOCON team and develop several drivers for robots which use STM32 as their microcontroller.
- led Android development in UTM ROBOCON team.
- organized Android workshop and mini-robot workshop in UTM.
- became judge of International Youth Robotic Competition (IYRC) in 2017.