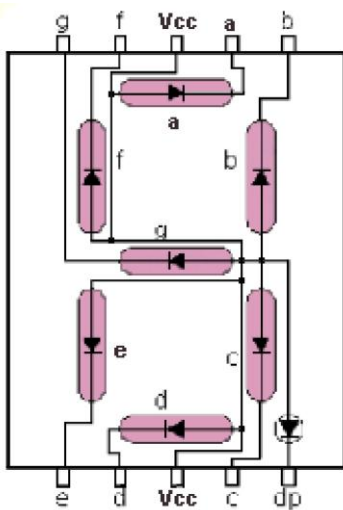# Lab 9:

Part I(50) – 7 Segment Display Driver

Using a 3-8 decoder and external gates, design a 7 segment display driver. It will display numbers 0-7.

Implement only in Logisim using logic gates. -Truth Table

-Logical expressions for each output

Part II(50) – 7 JK Flip Flop

Design a JK Flip Flop using D Flip Flop and external gates. Implement only on breadboard.

-Truth table -
Implementation