Instruction set

opcode bits (6 MSB)

28-26 / 31-29	000	001	010	011	100	101	110	111
000	R-format		Jump	Jump & link	Branch eq	Branch ne		
001	Add imm		Set less than imm		Andi	Ori	Xori	Load upper imm
010								
011	Load byte	Load half		Load word	Load byte unsigned	Load half unsigned		
100	Store byte	Store half		Store word				
101								
110								
111								

R-format (opcode = 000000)
Funct bits (6 LSB)

2-0 / 5-3	000	001	010	011	100	101	110	111
000	Shift left logical		Shift right logical					
001	Jump register							
010								
011								
100	Add		Subtract		And	Or		Nor
101			Set less than					
110								
111								