

EnclaveFuzz: Finding Vulnerabilities in SGX Applications

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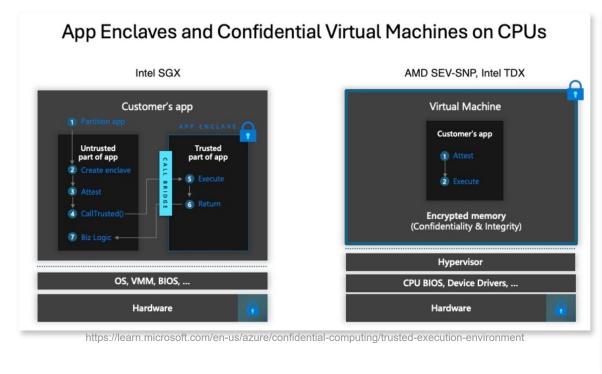
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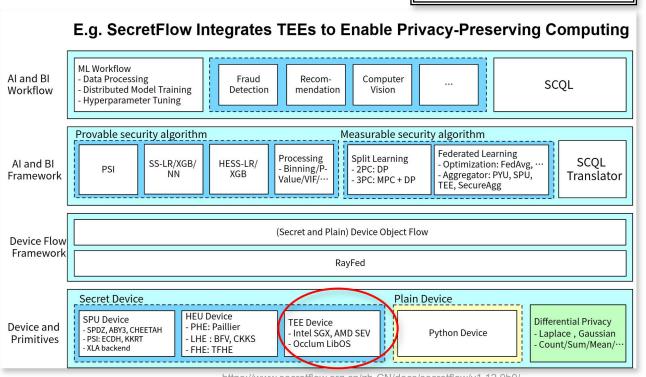
TEE and Its Applications



- Trusted Execution Environment: a segregated area of memory and CPU that's protected from the rest by using access control and encryption, e.g. enclaves and confidential VMs.
- Application Scenario: cloud computing, privacy-preserving computing...





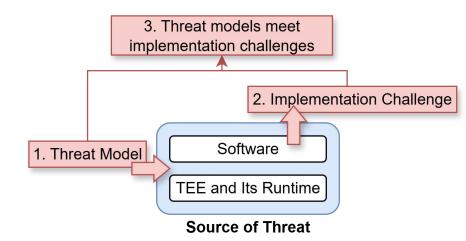


Why TEE ≠ Automatically Secure



- **Source of Threat:** Beyond the standard TEE threat model, TEE application security also faces implementation-level challenges.
 - Misaligned Trust Assumptions: Developers misunderstand/overlook TEEs' threat models.
 - 2. **Implementation Challenge:** Implementation issues are difficult to avoid (especially in C/C++).
 - 3. Threat Models Meet Implementation Challenge → SGX-specific vulnerability.
- (Side channels are out of our scope.)







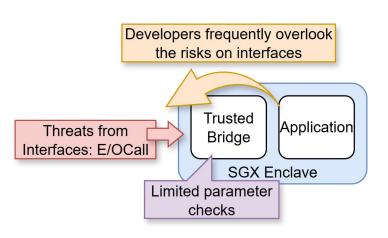
- **Risk:** SGX developers may overlook threat model, leaving issues such as stack overflow unchecked (left fig.), then we are able to hijack control flow etc.
- **Summary:** With ELRANGE-targeted shadow map, we discovered 162 vulnerabilities in 14 SGX applications, highlighting the threat model is often overlooked.

Lessons:

- 1. Cross-boundary data/pointer should be carefully handled.
- 2. Memory safe language (e.g. Rust) is important.

```
SQLITE_PRIVATE int sqlite3BtreeOpen(...) {
    unsigned char zDbHeader[100];
    rc = sqlite3PagerReadFileheader(..., zDbHeader);
    //unixRead is called, zDbHeader is passed to pBuf
}
static int unixRead(..., void *pBuf, ...) {
    got = seekAndRead(...);
    memset(&((char*)pBuf)[got],0,amt-got);//overflow
}
static int seekAndRead(...) {
    // OCALL to get got from host
    got = osRead(id->h, pBuf, cnt);
    return got;
}

Controlled by
Untrusted Host
```

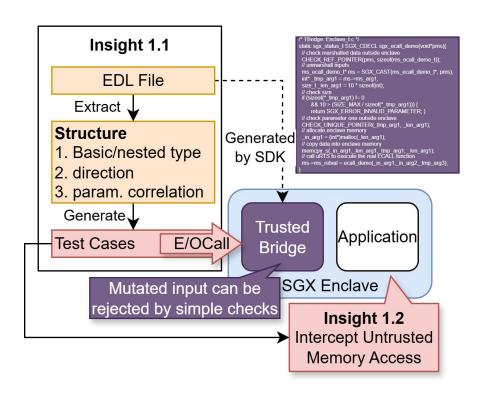




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• Insight:

- Test cases derived via #PF inference (e.g. SGXFuzz) rarely pass TBridge's simple checks. However, many SGX apps are open-source, EDL can be used to craft inputs that avoid rejection.
- Data read from untrusted memory is also untrusted. We can instrument (at LLVM IR) to intercept untrusted memory accesses for testing.



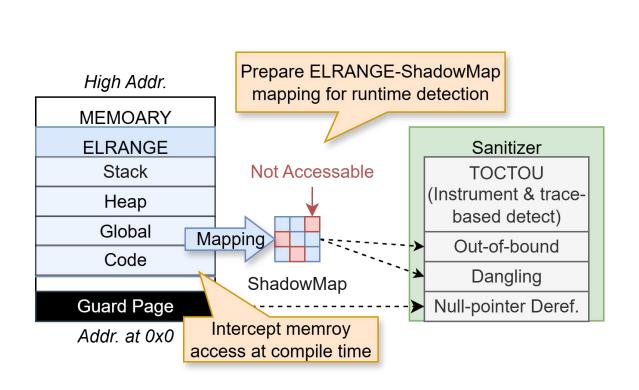
Enclave Name	Encla	ve Cov.		Coverage ¹ ing Cov.	Effect	iveness	Input '	Validity
	SGXFuzz	EnclaveFuzz		EnclaveFuzz	SGXFuzz	EnclaveFuzz	SGXFuzz	EnclaveFuz
intel-sgx-ssl	0.75%	18.04%	0.02%	18.39%	1.66%	99.66%	0%	100%
AE LE	3.85%	11.67%	14.29%	32.08%	1.98%	15.25%	26.89%	100%
AE PCE	4.10%	13.94%	22.53%	45.34%	3.49%	15.30%	17.48%	100%
AE PVE	2.36%	8.63%	10.05%	16.95%	6.32%	22.62%	33.15%	100%
AE QE	2.64%	3.20%	13.23%	6.68%	3.60%	16.13%	5.52%	100%
SGX_SQLite	2.39%	6.78%	1.45%	7.20%	26.64%	99.96%	30.39%	100%
TaLoS	5.86%	9.78%	4.66%	10.00%	36.56%	99.58%	53.50%	100%
mbedtls-SGX	6.54%	30.64%	8.16%	32.64%	53.68%	99.66%	21.23%	100%
wolfssl	3.64%	42.44%	0.38%	45.00%	7.72%	99.78%	38.27%	99.99%
sgx-walle							30.06%	99.99%
sgx-dnet	nclus	ion 1 · (Great	ly imp	rove i	nnut	69.15%	100%
plinius	IICIUS	ion 1.	Jicat	iy iiip	OVC	iipat	68.41%	100%
sgxwalle	:ام:ام،	1 (2)	ر ام مر		/ 1.	۸	20.74%	100%
BiORAM-S	vallai	ty (3x)	ana	covera	ge (4)	().	48.43%	82.95%
bolos-encla		, , ,			•		40.10%	84.09%
ehsm	3.69%	16.91%	3.81%	15.00%	76.97%	81.60%	0%	91.79%
sgx-reencrypt	8.60%	33.31%	14.92%	31.26%	20.26%	28.26%	84.38%	100.00%
SGXCryptoFile	5.85%	17.62%	15.04%	80.56%	4.15%	5.88%	0%	100.00%
trusted-function-frame	2.53%	1.97%	2.13%	1.53%	75.64%	75.22%	0%	100.00%
wasm-micro-runtime	3.95%	1.67%	2.08%	0.94%	32.64%	46.04%	78.04%	100.00%
wasin-inicio-tuntinic				23.54%	19.26%	49.21%	33.29%	97.94%

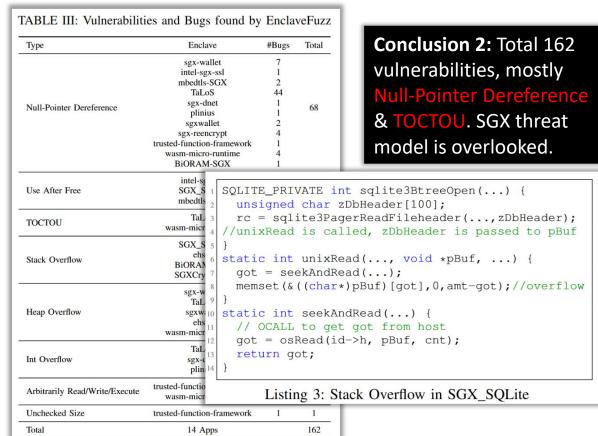


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• Insight:

• Memory corruption can be detected by instrumenting memory accesses (e.g., with AddressSanitizer), but the instrumentation must be adapted to the enclave memory layout (ELRANGE).

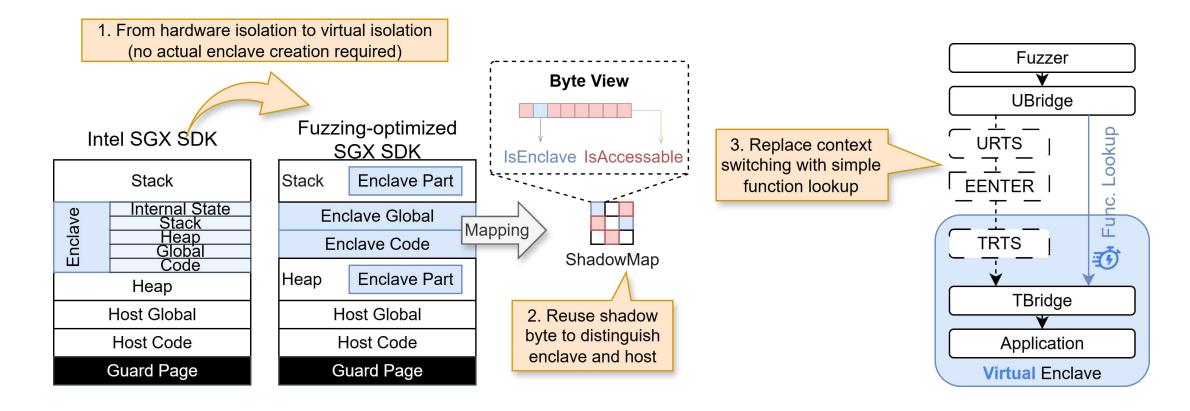






• Insight:

• The enclave creation and context switching are very time-consuming. Aside from TBridge, the remaining can be optimized away for fuzzing.



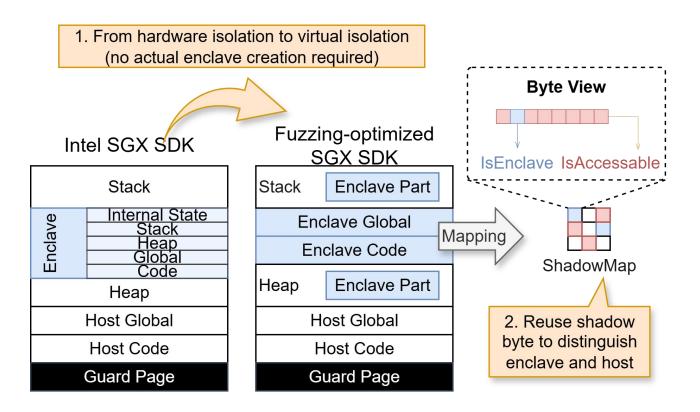


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• Insight:

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remaining can be optimized away for fuzzing.



Enclave Name	Enclave SIM		EnclaveFu HW	ZZ-	EnclaveFuzz (Opt.SDK)
TABLE	VII: Opti	mized S	DK Perfe	ormar	
Item	Opt.SDK	Sim w.	HW w.	Sim.	HW.
1K Create	0.14s	1.89s	28.27s	1.50	s 25.63s
SGX SDK's simu hardware mod	e (1x, t	oaselir	ne). End	clave	9
	e (1x, t	oaselir	ne). End	clave	9
hardware mod creation is 200	e (1x, k × faste	aselir r than	ne). End in hard	clave	e re mode ^{501k}
hardware mod creation is 200 plinius sgxwallet	e (1x, k × faste	oaselir r than	ne). End in hard ^{54k} ^{218k}	clave dwa	re mode 501k 1.9M
hardware mod creation is 200 plinius sgxwallet BiORAM-SGX	e (1x, k × faste 71k 430k 1M	oaselir r than	in hard 18	clave dwa	re mode 501k 1.9M 9M
hardware mod creation is 200 plinius sgxwallet BiORAM-SGX bolos-enclave	e (1x, k × faste 71k 430k 1M 96M	oaselir r than	in hard 54k 218k 26K 30M	clave dwa	re mode 501k 1.9M 9M 505M
hardware mod creation is 200 plinius sgxwallet BiORAM-SGX bolos-enclave ehsm	e (1x, k × faste 71k 430k 1M 96M 227K	oaselir r than	ne). End in hard 54k 218k 26K 30M 163K	clave dwa	re mode 501k 1.9M 9M 505M 212K
hardware mod creation is 200 plinius sgxwallet BiORAM-SGX bolos-enclave ehsm sgx-reencrypt	e (1x, k × faste 71k 430k 1M 96M 227K 14M	oaselir r than	ne). End in hard 54k 218k 26K 30M 163K 10M	clave dwa	re mode 501k 1.9M 9M 505M 212K 15M
hardware mod creation is 200 plinius sgxwallet BiORAM-SGX bolos-enclave ehsm	e (1x, k × faste 71k 430k 1M 96M 227K	oaselir r than	ne). End in hard 54k 218k 26K 30M 163K	clave dwa	re mode 501k 1.9M 9M 505M 212K
plinius sgxwallet BiORAM-SGX bolos-enclave ehsm sgx-reencrypt SGXCryptoFile	e (1x, k × faste 71k 430k 1M 96M 227K 14M 2M	oaselir r than	ne). End in hard 54k 218k 26K 30M 163K 10M 467K	dwa	re mode 501k 1.9M 9M 505M 212K 15M 18M

TEE Security = Architecture × Semantics × Implementation

EPILOGUE

TEEs are designed to enhance application security, but improper use can instead undermine it, leading to privacy breaches, financial losses, regulatory violations, and ultimately, a loss of user trust.

Thanks!

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