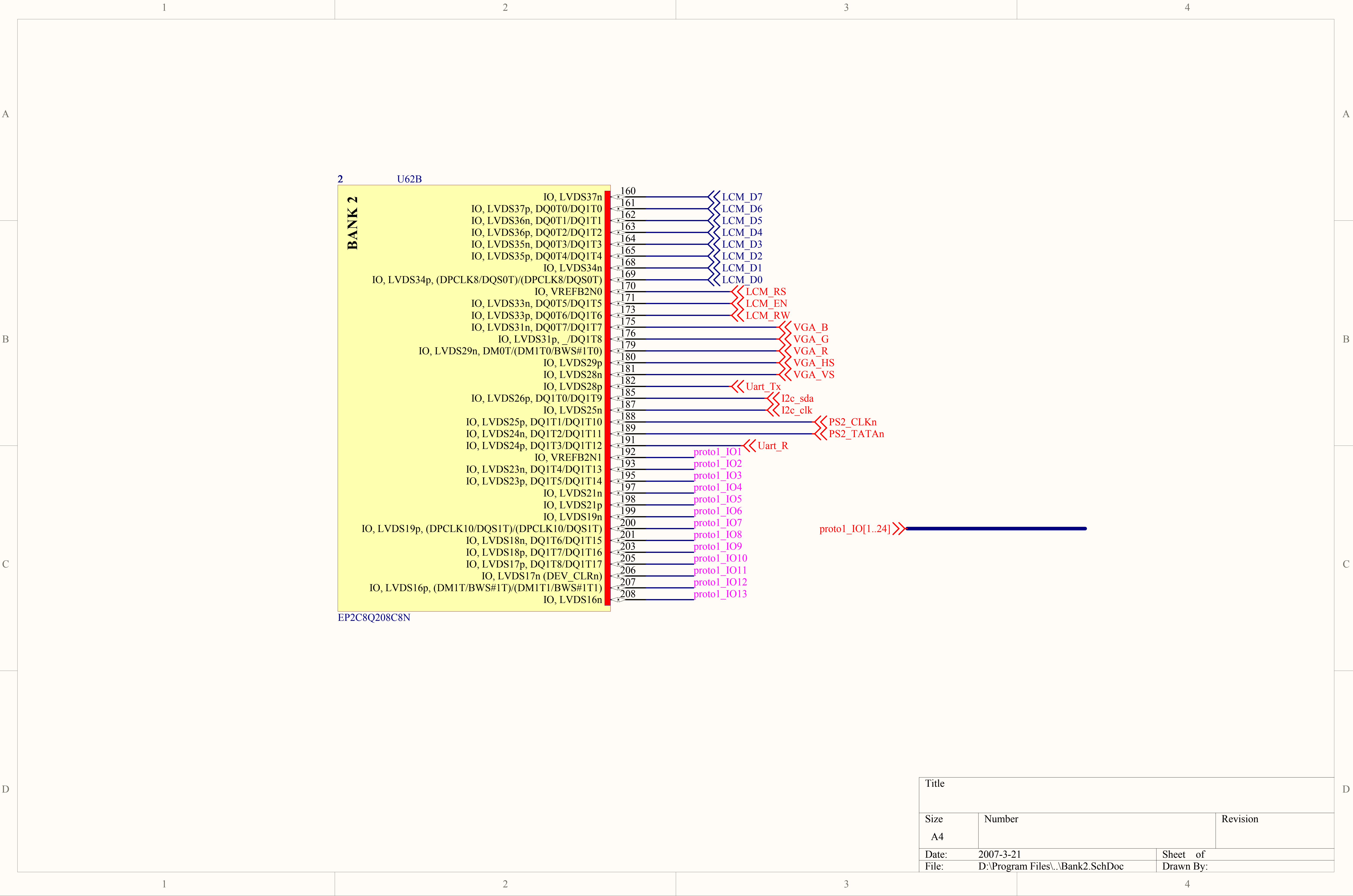
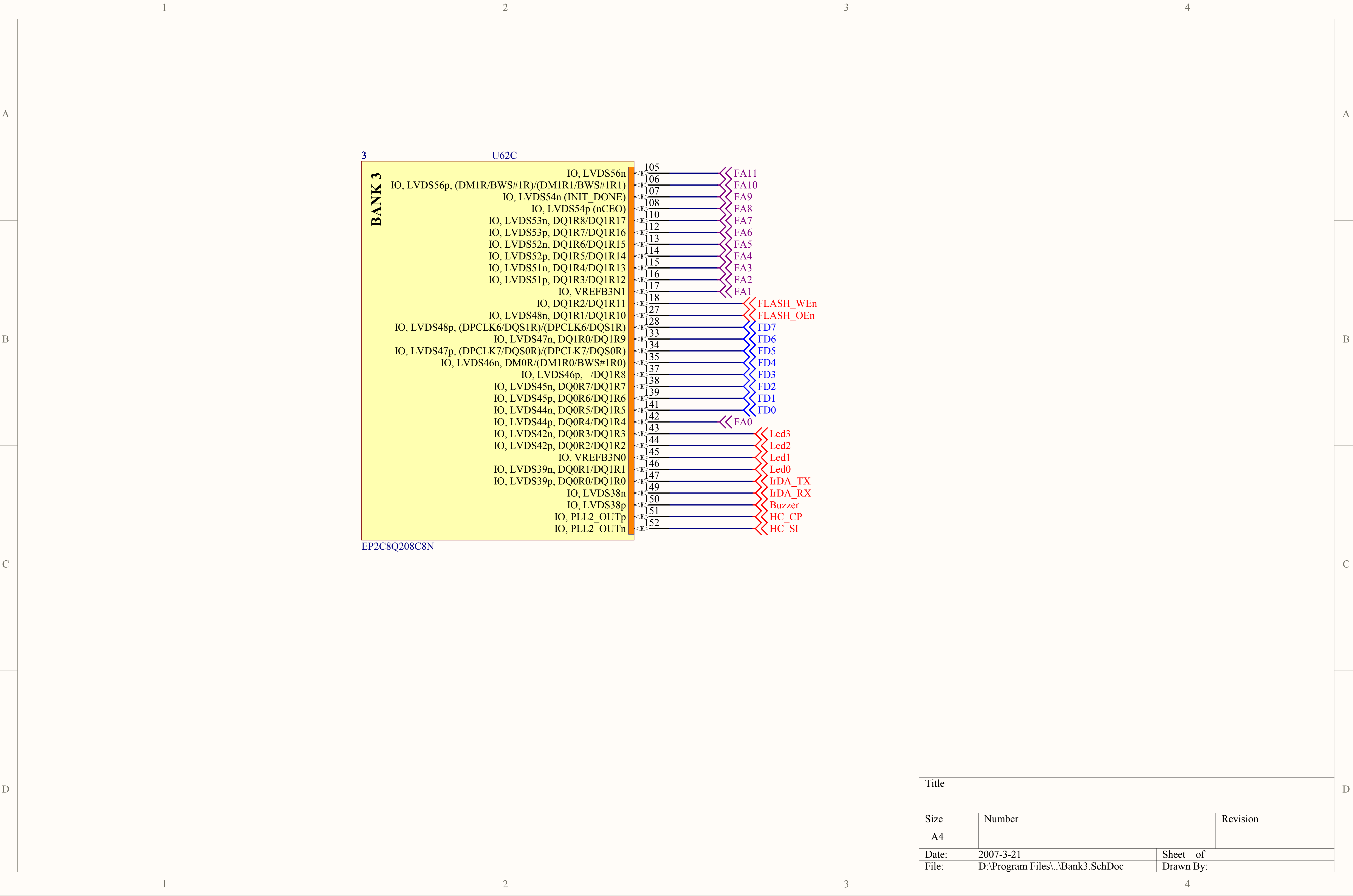
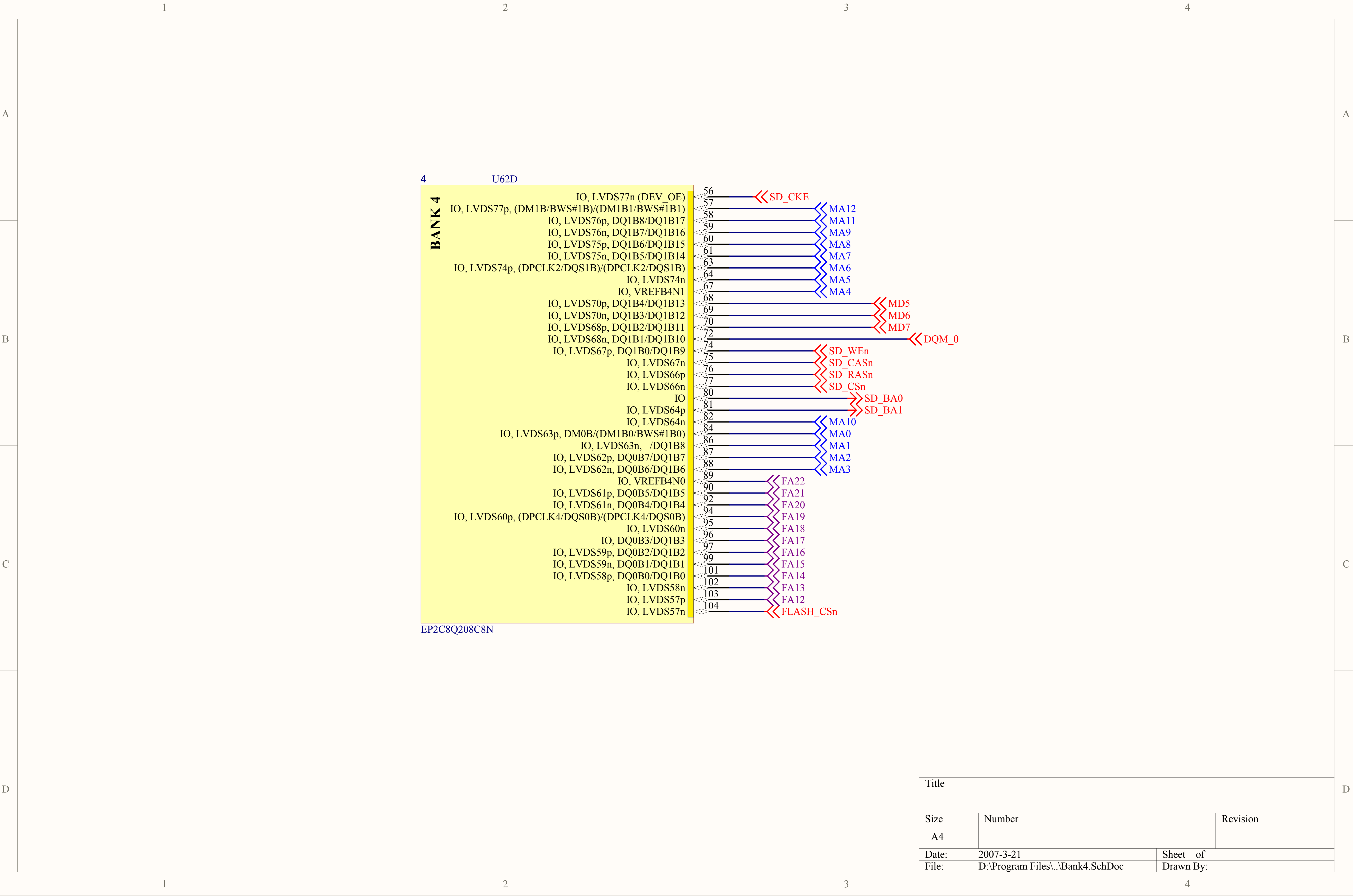


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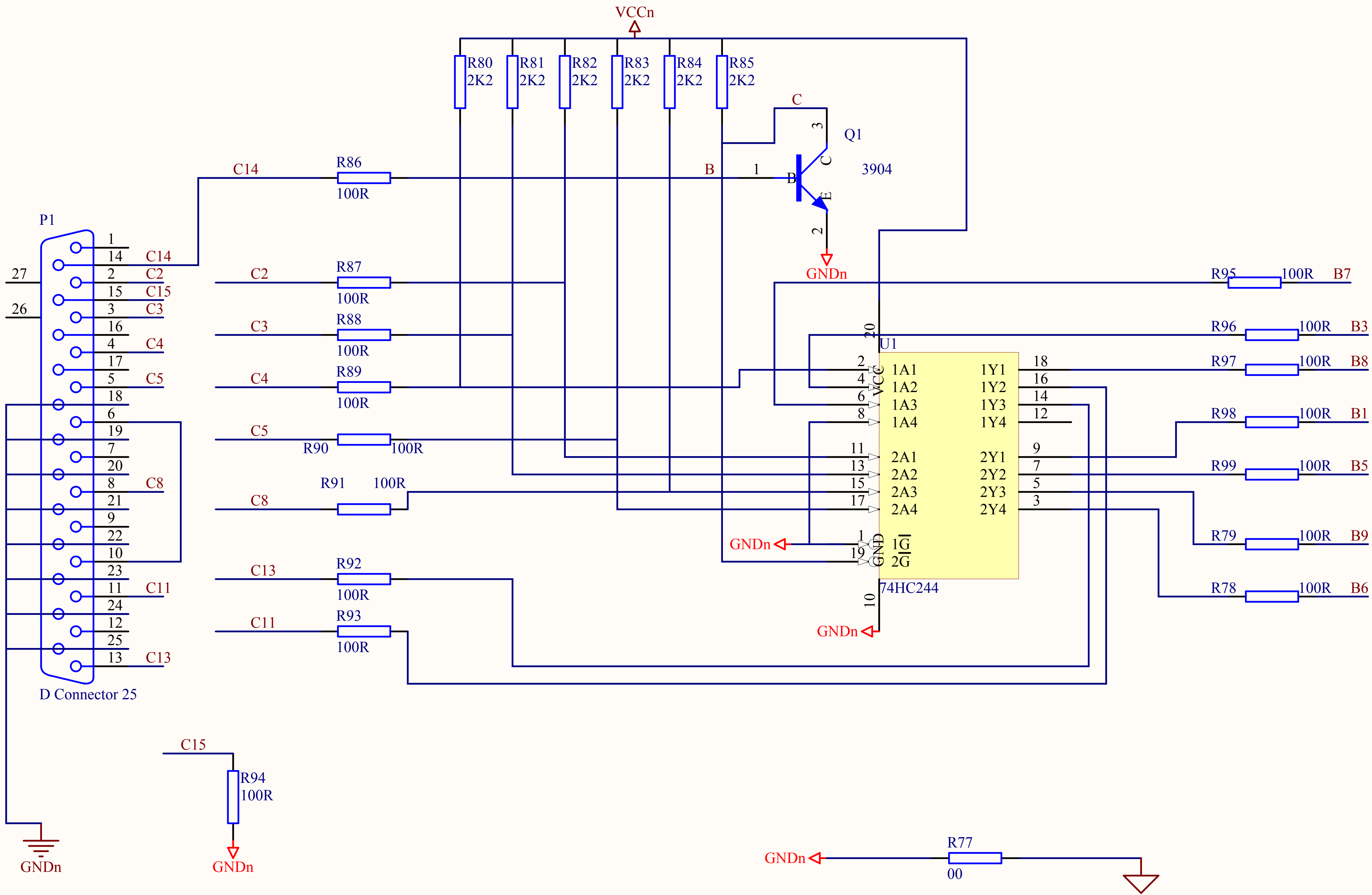
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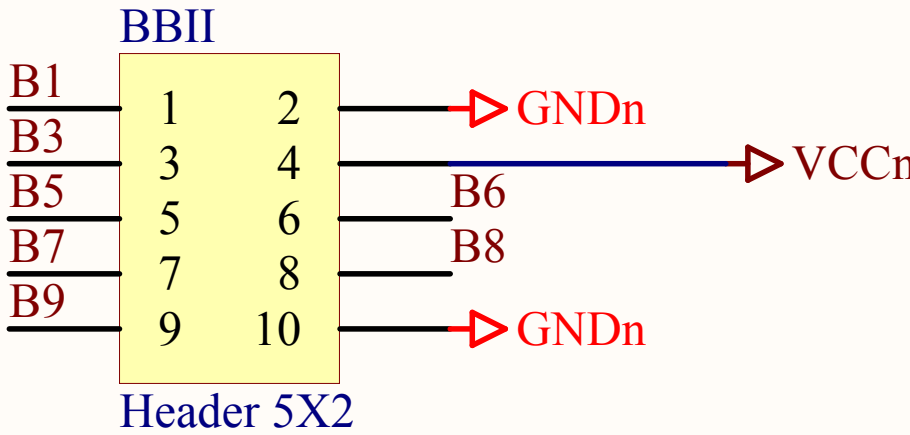
C

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AS_o AS JTAG

male header connections:
pin# jtag
1 DCLK
2 GND
3 CONFIG_DONE
4 Vcc
5 CONFIG_n
6 CE_n
7 DATAO
8 CS_N
9 ASDO
10 GND



Altera BytemasterII Parallel Port Download Cable

1. All series resistors are 0.1k.
2. All pull-up resistors are 2.2k.

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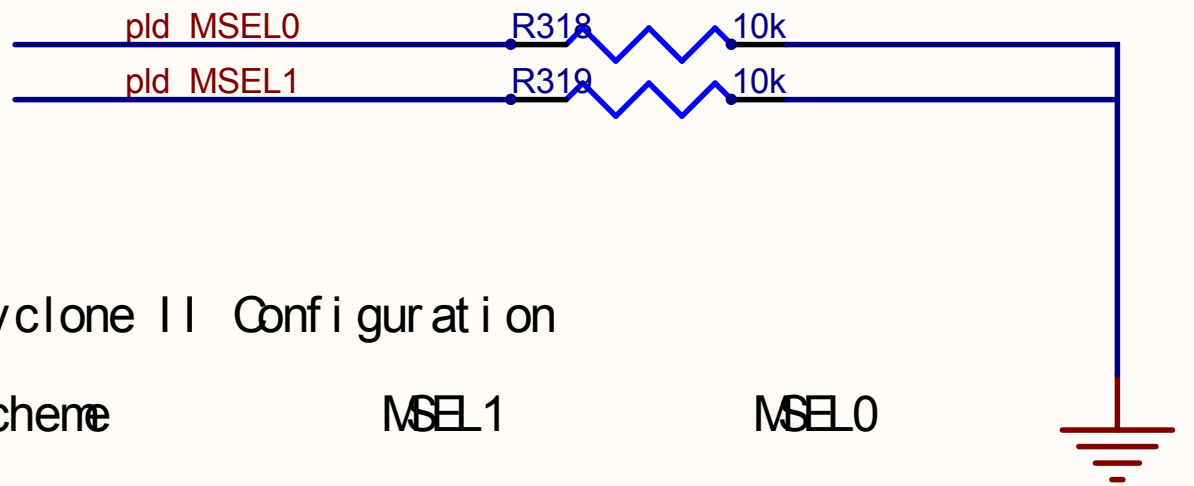
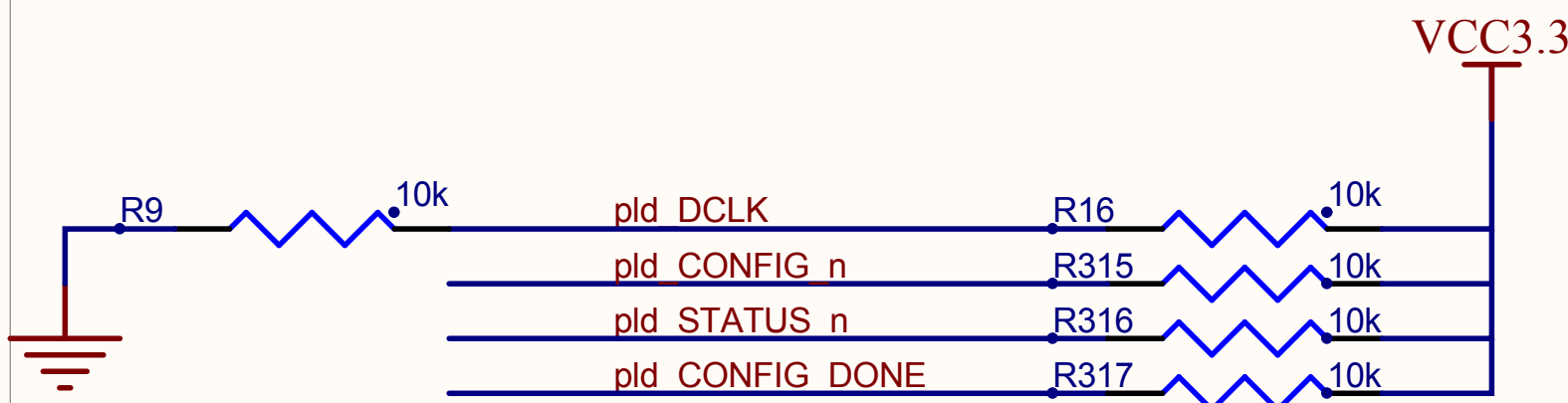
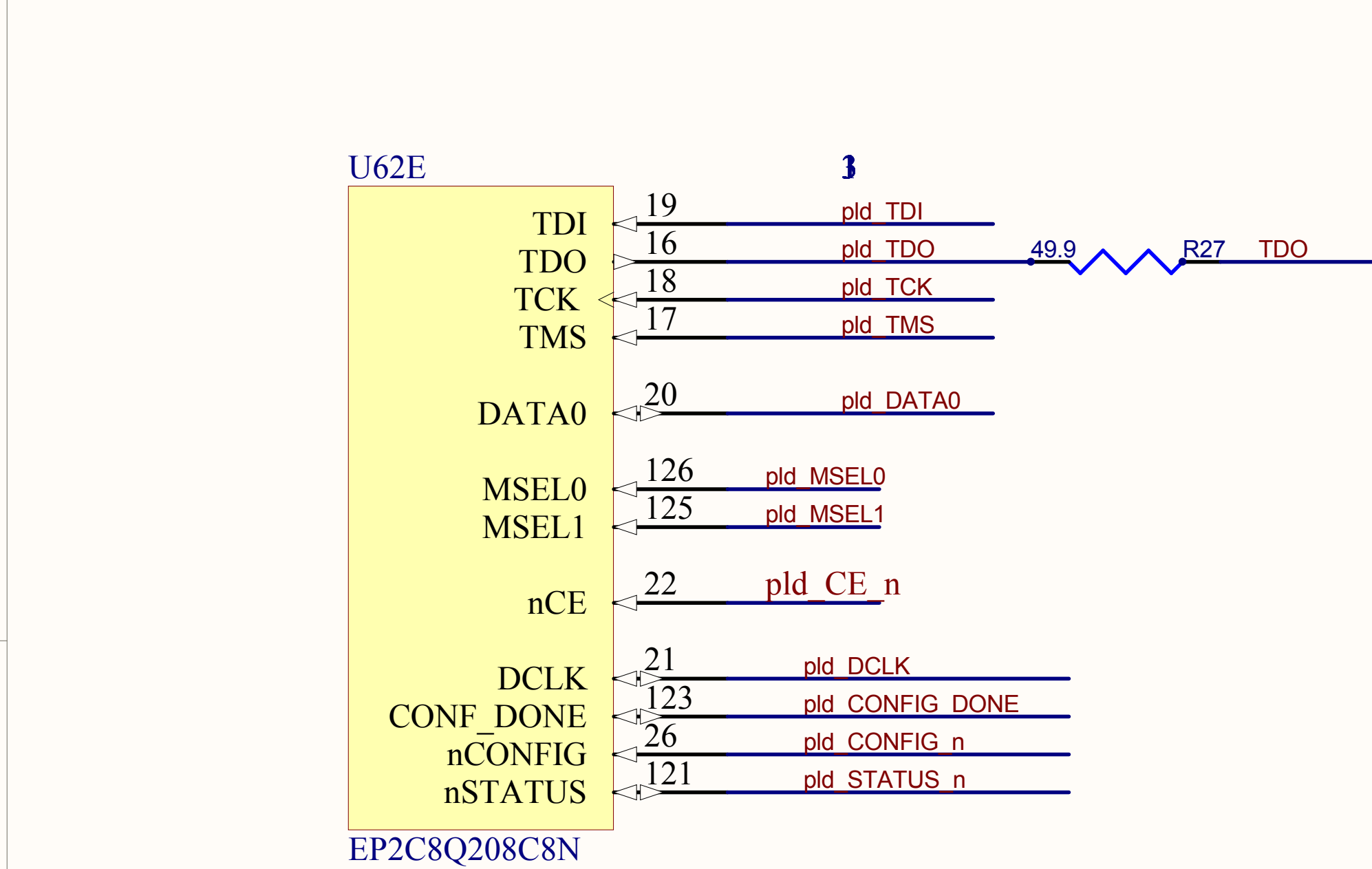
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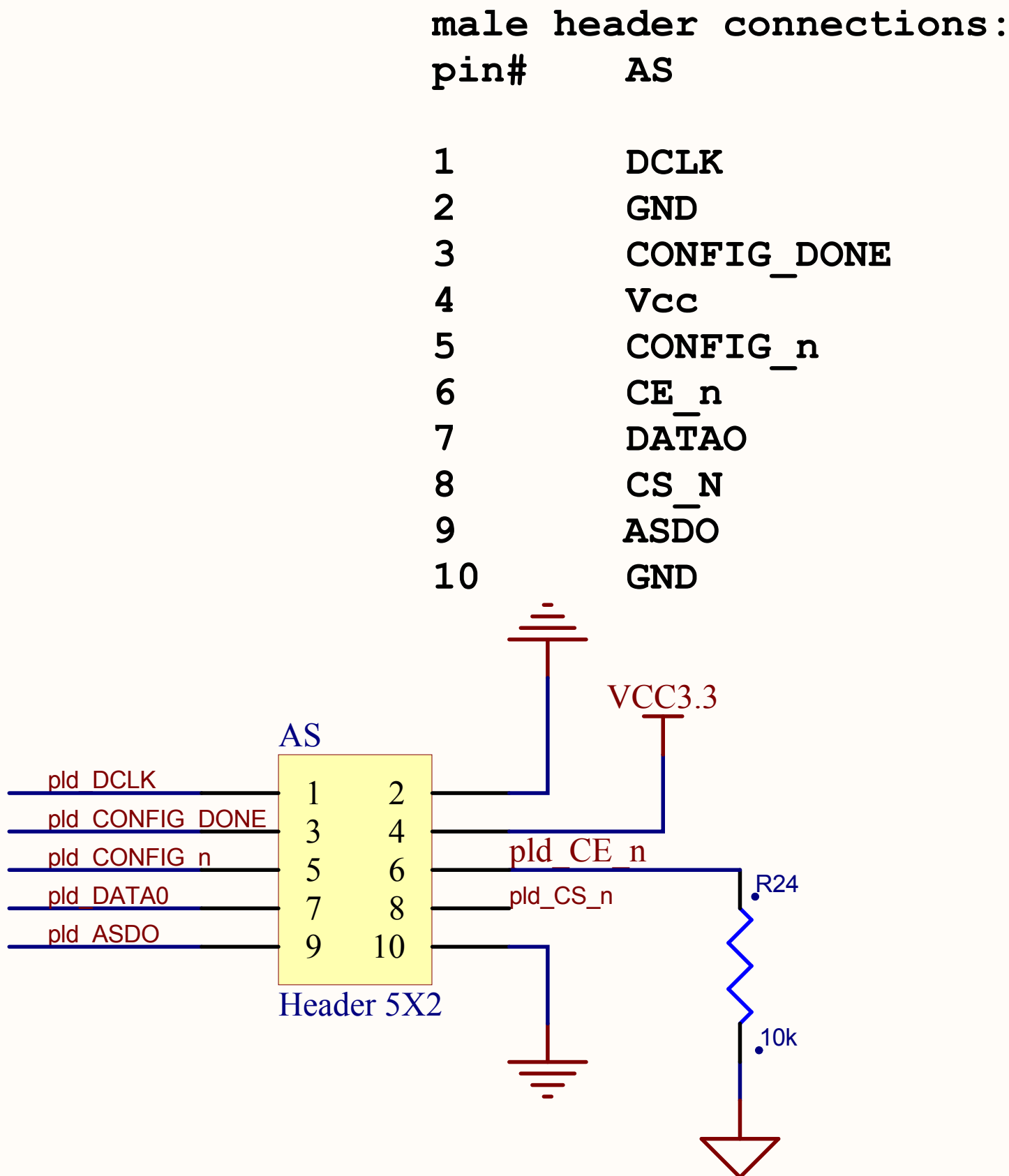
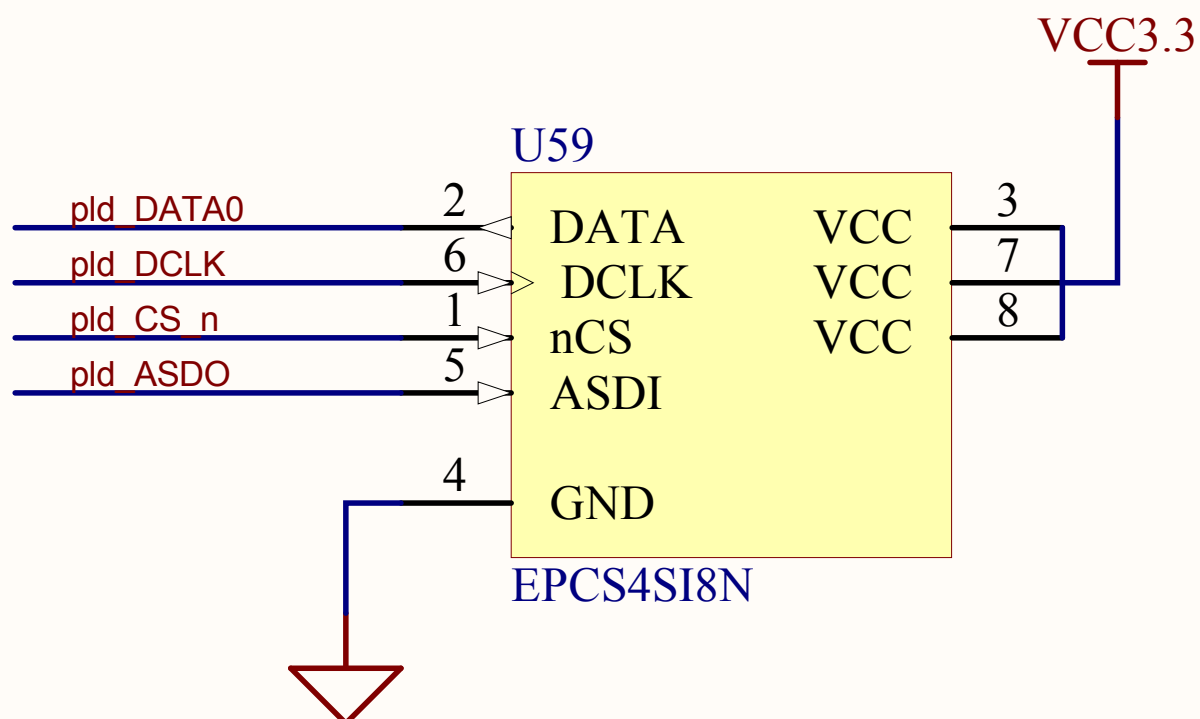
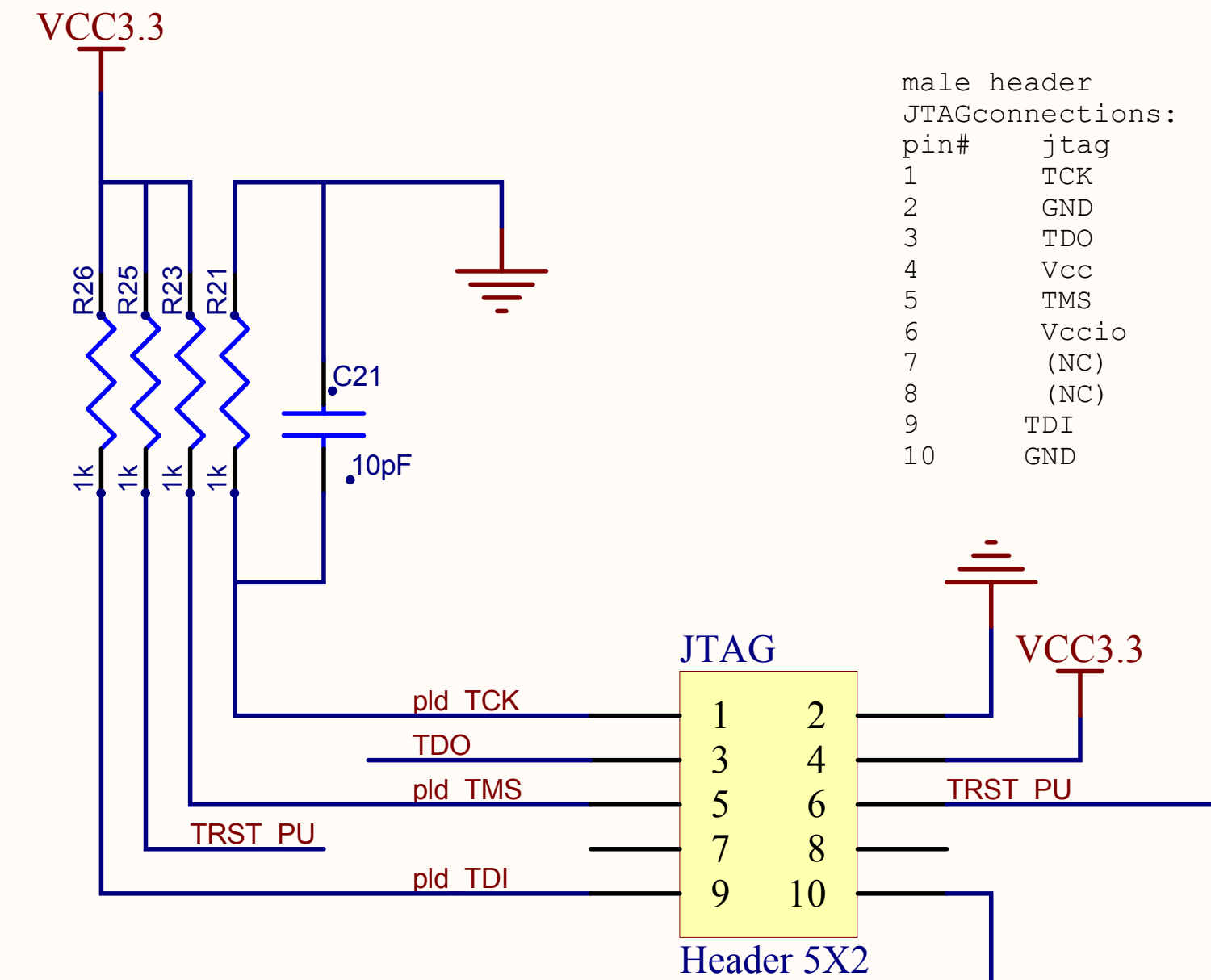
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Cyclone II Configuration

Scheme	MSEL1	MSEL0
AS(20M)	0	0
PS	0	1
Fast AS(40M)	1	0
JTAG	*	*



Title		
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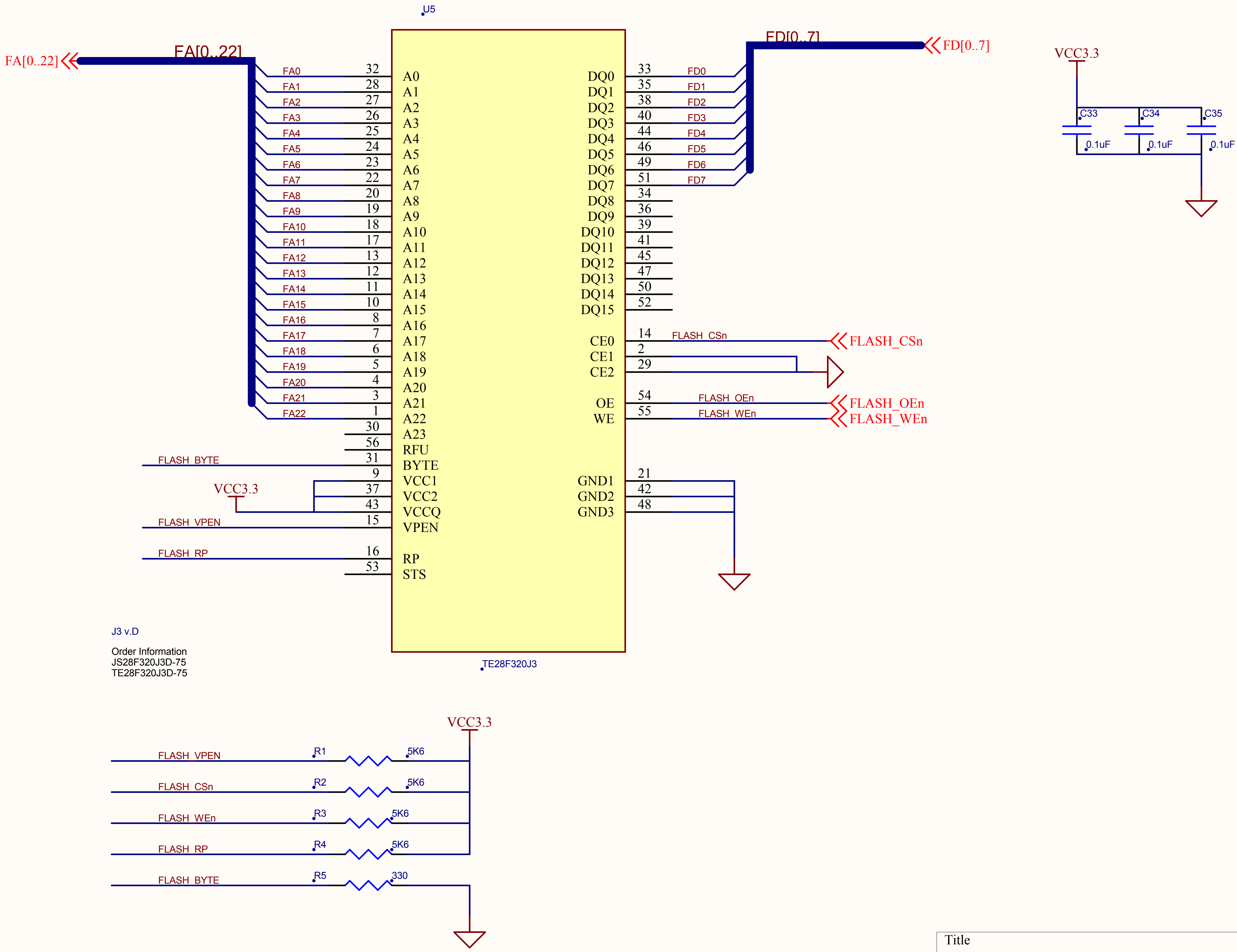
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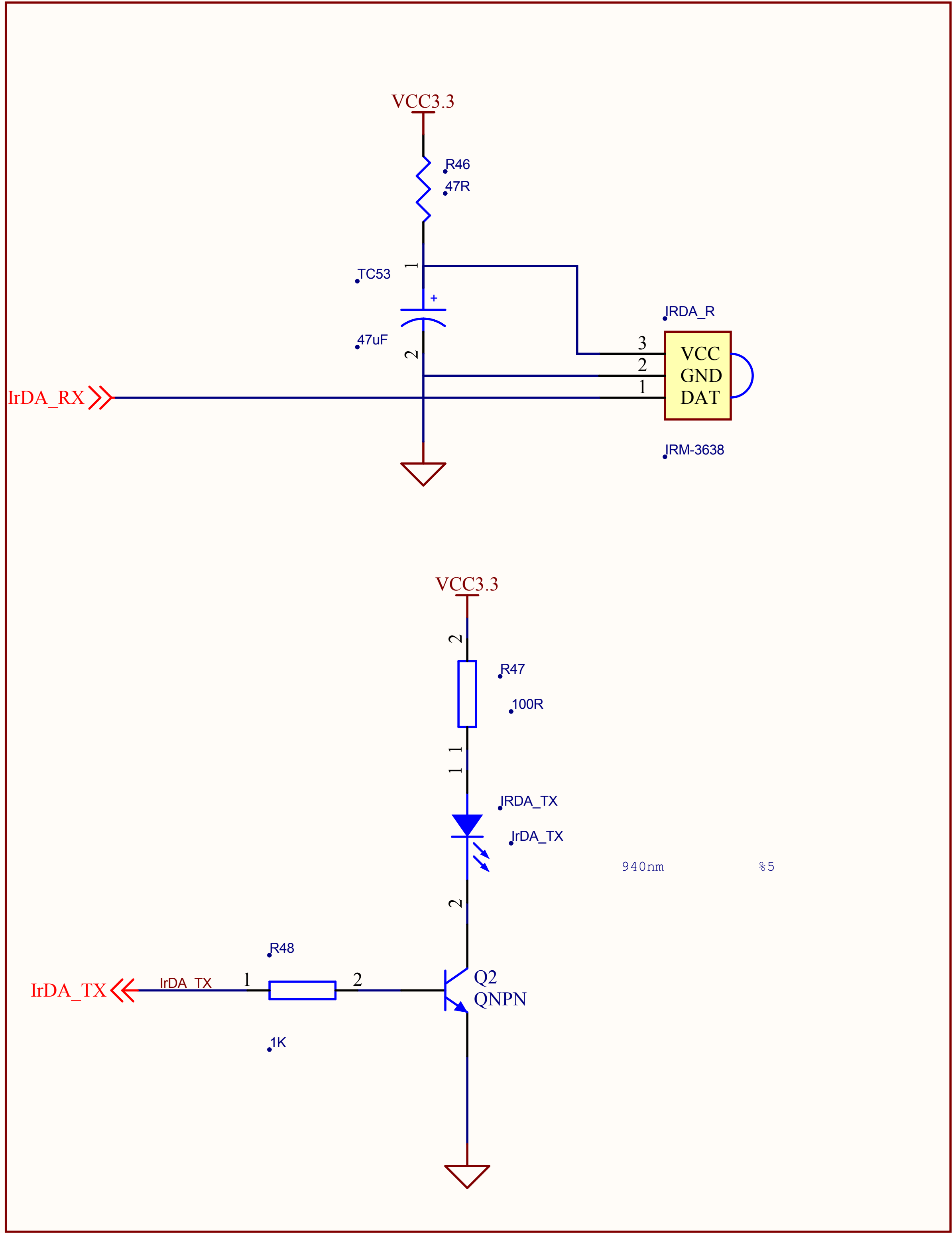
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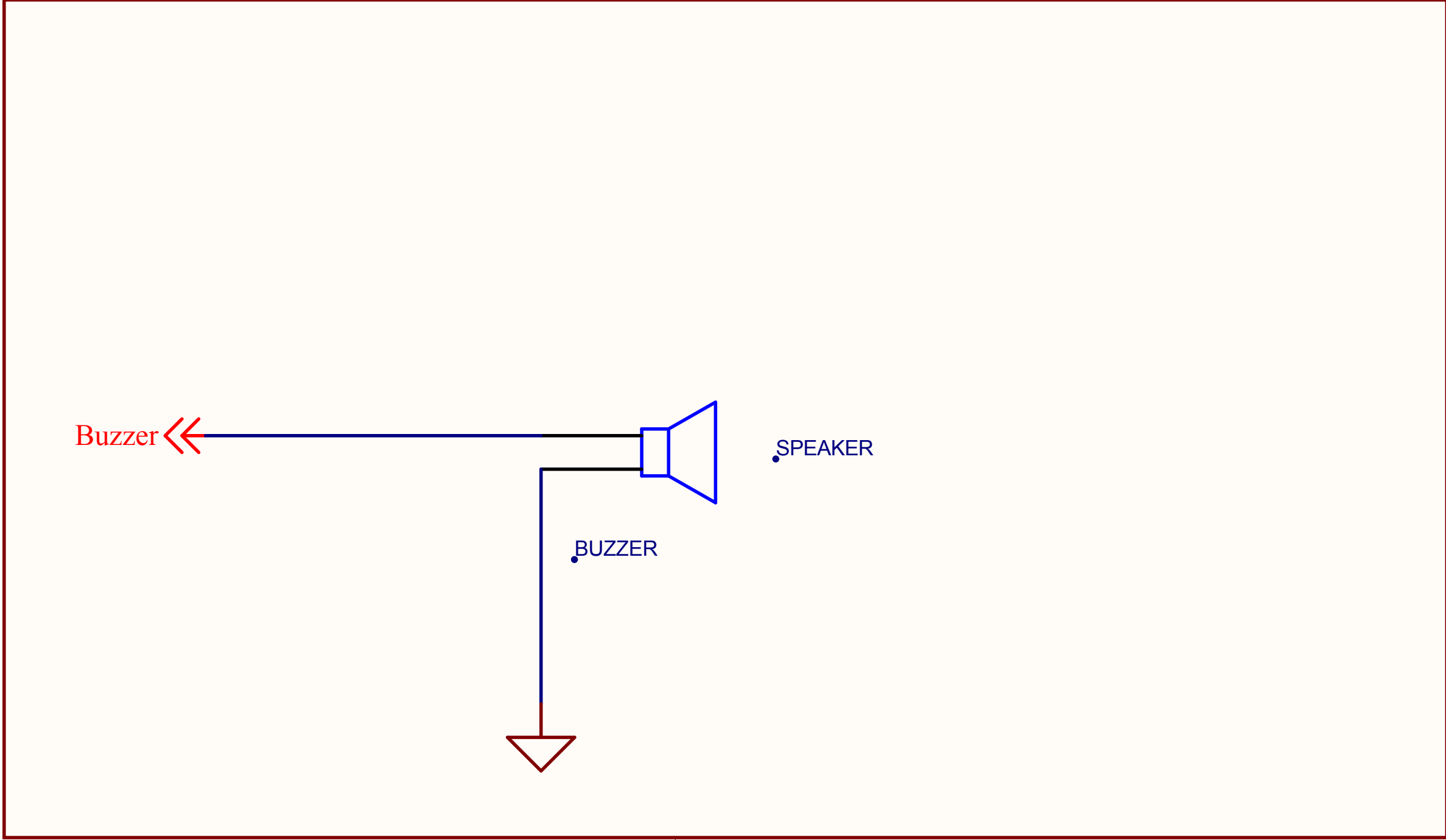
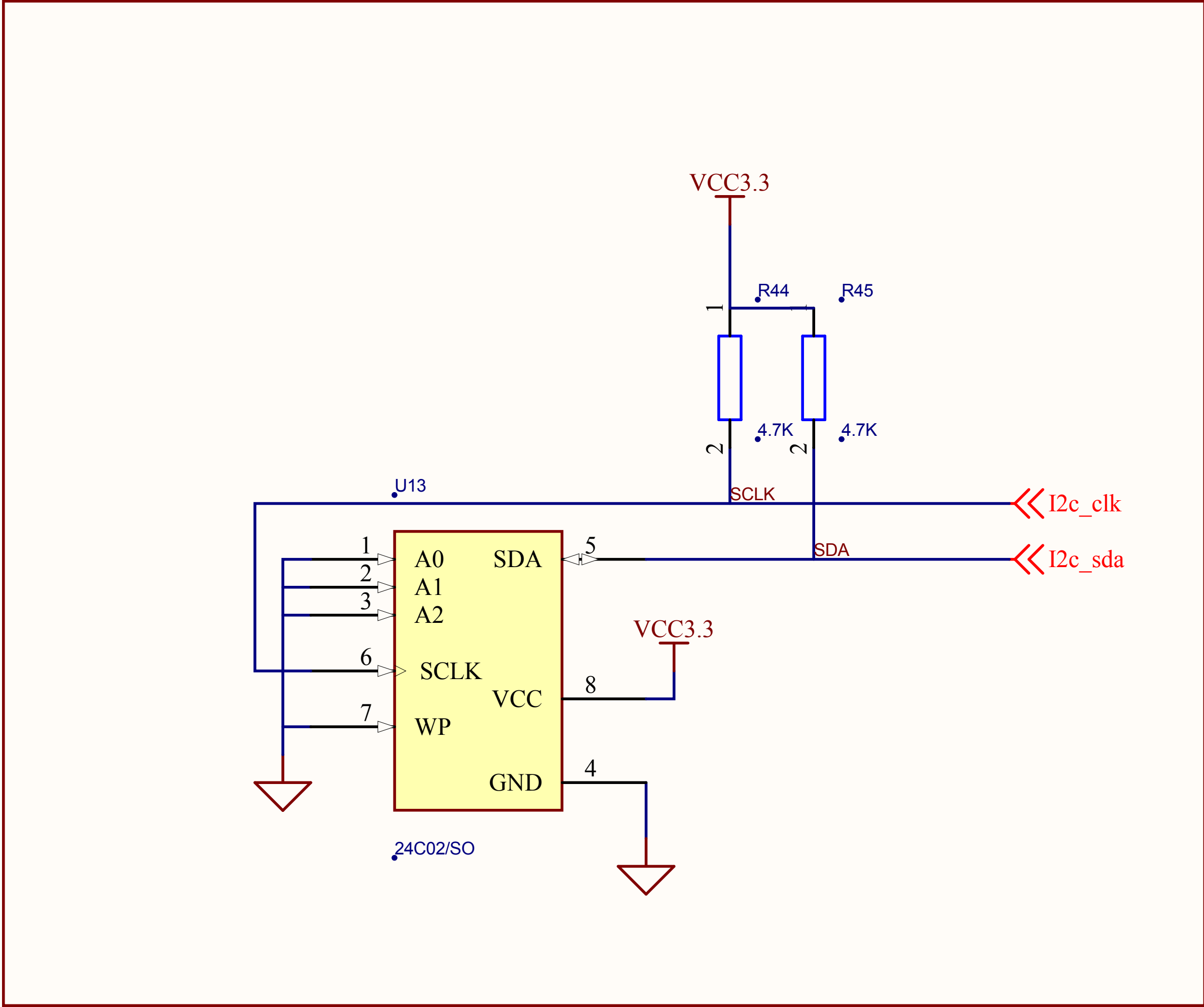


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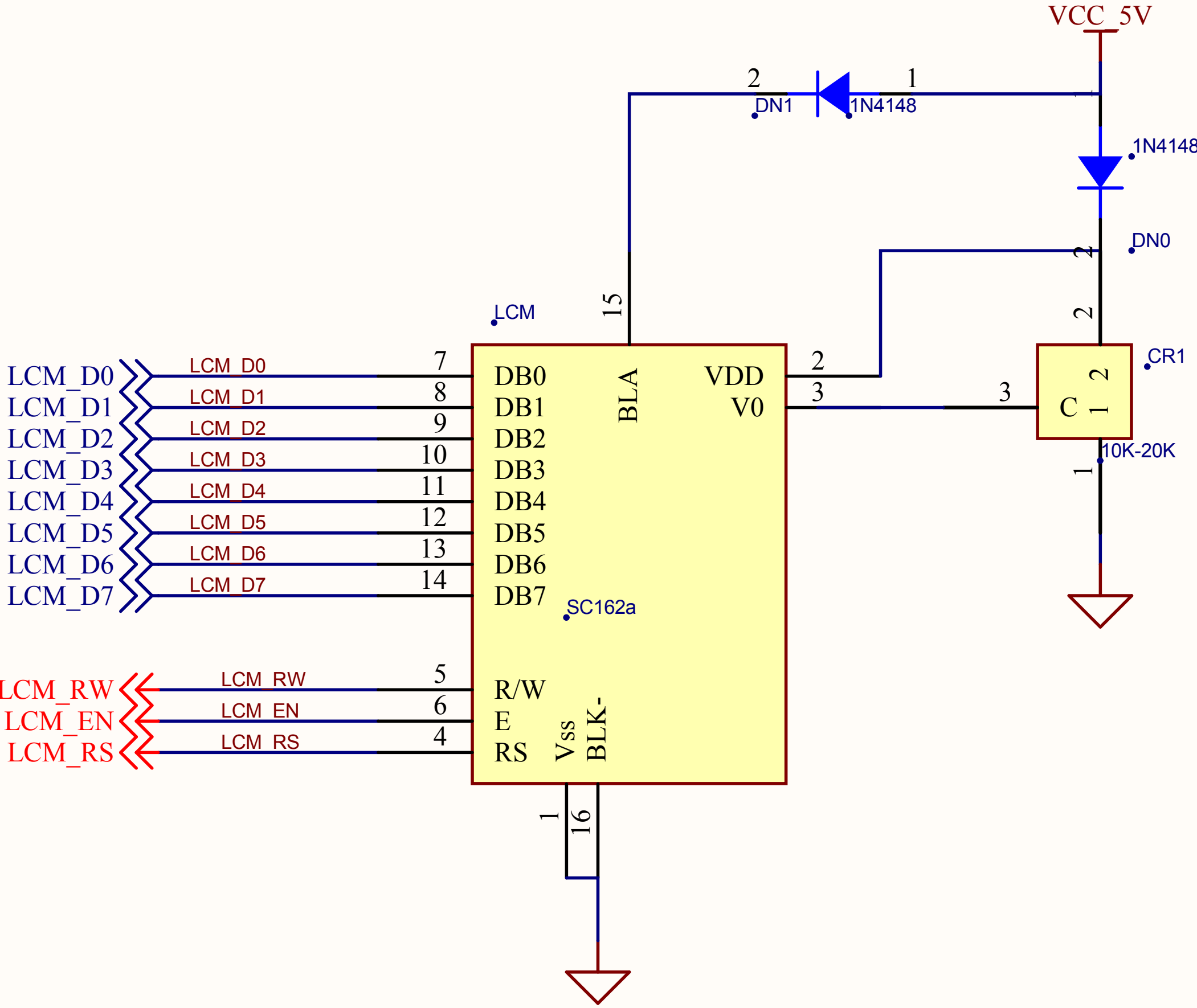
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Size A4	Number	Revision
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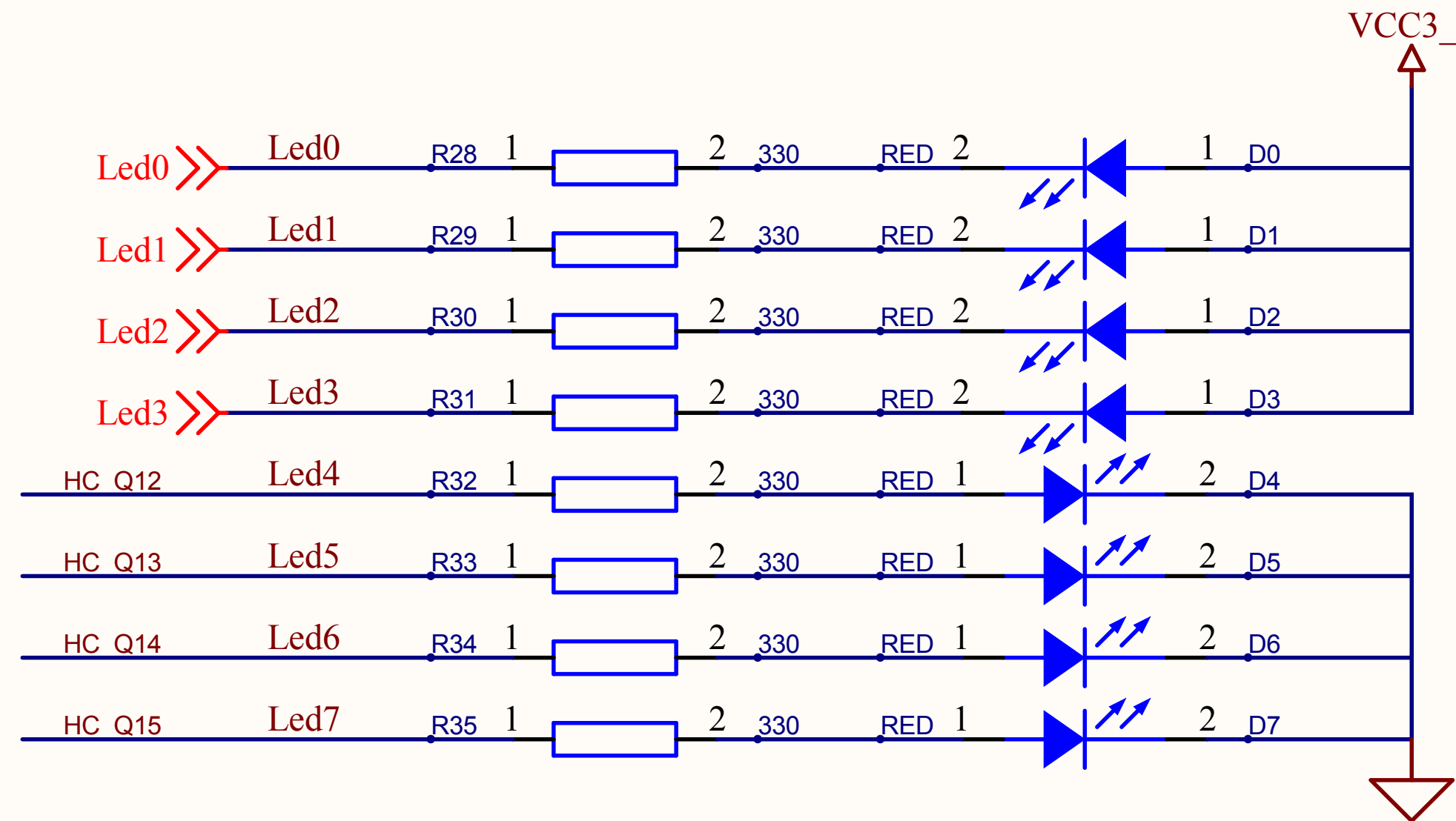
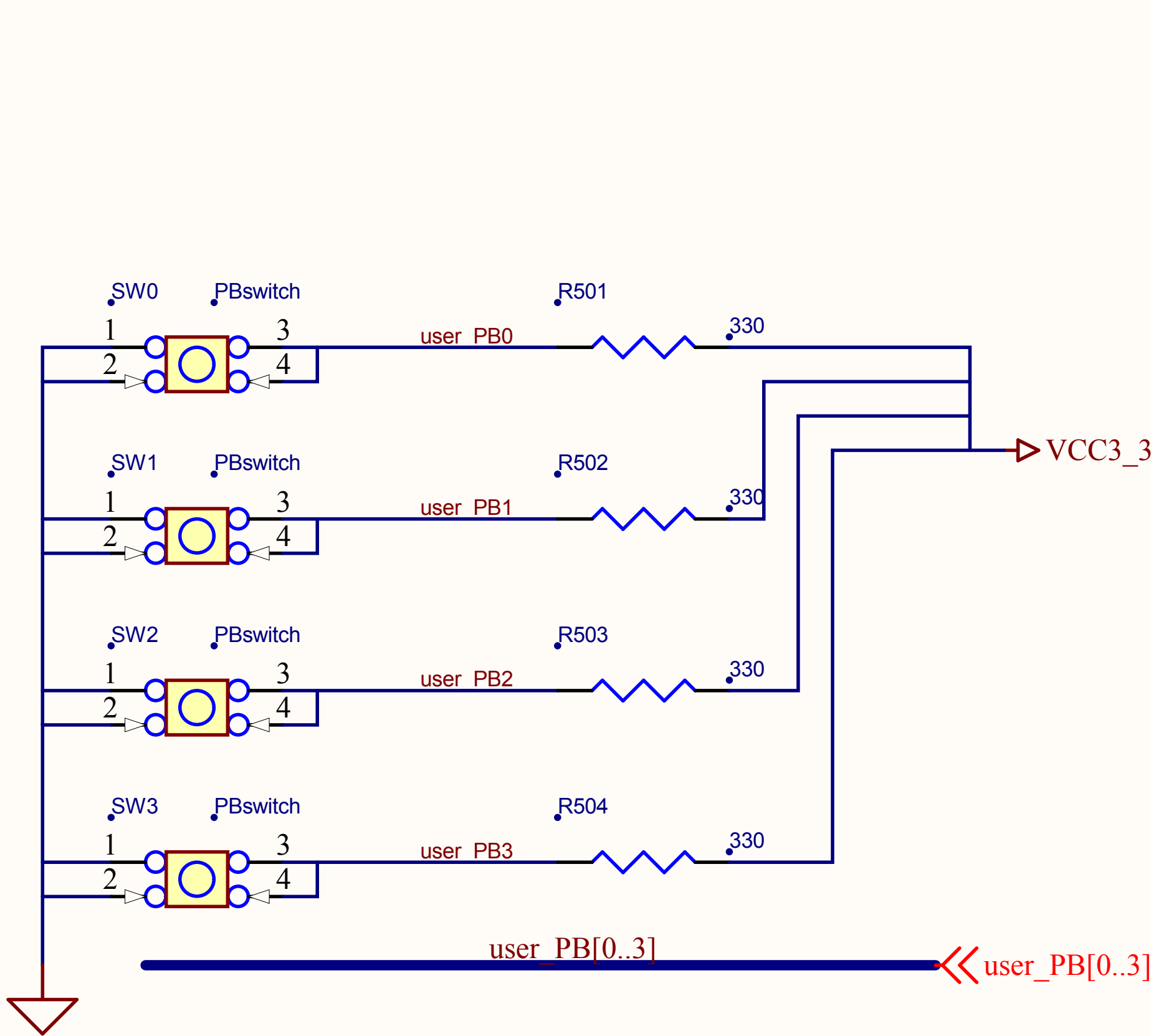
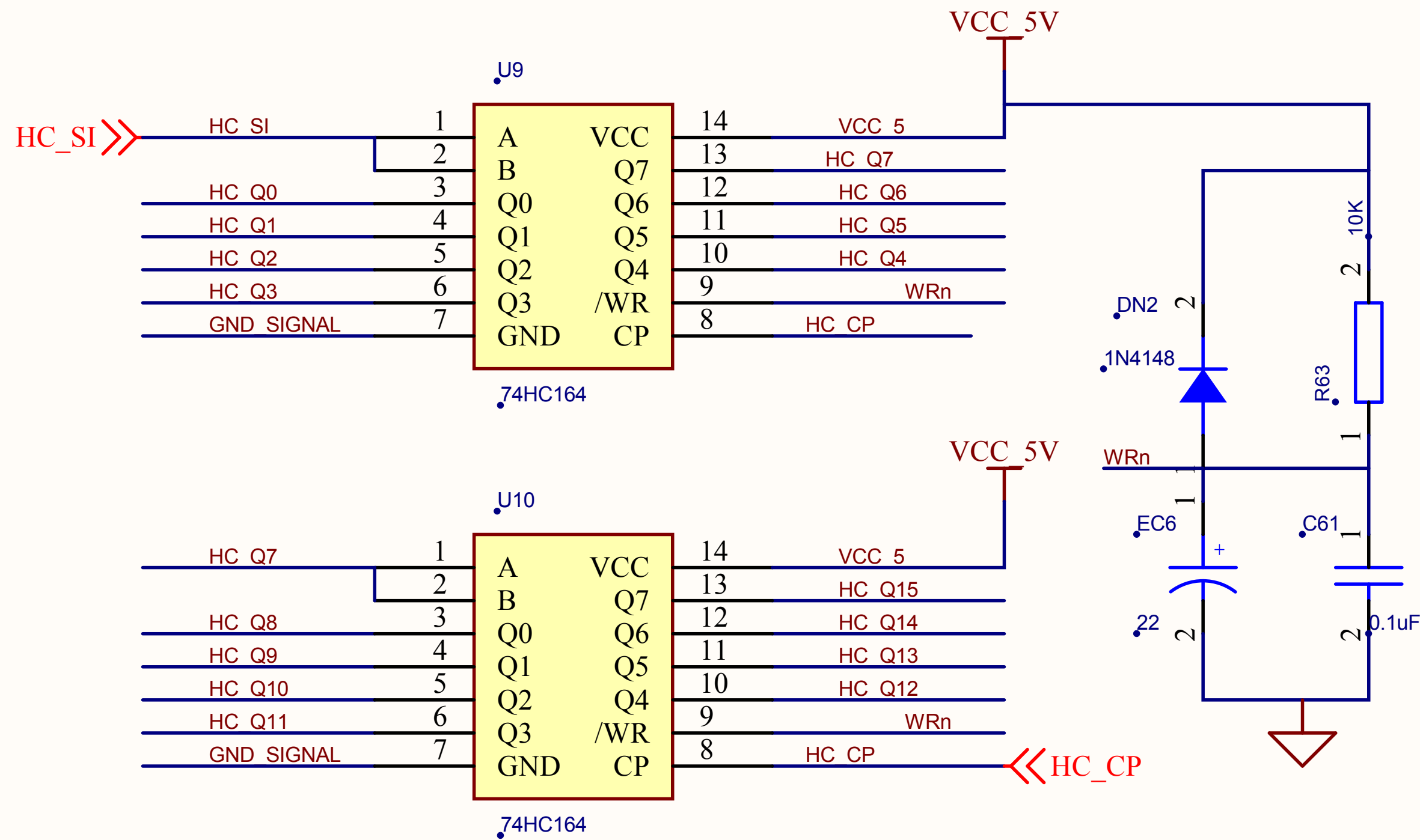
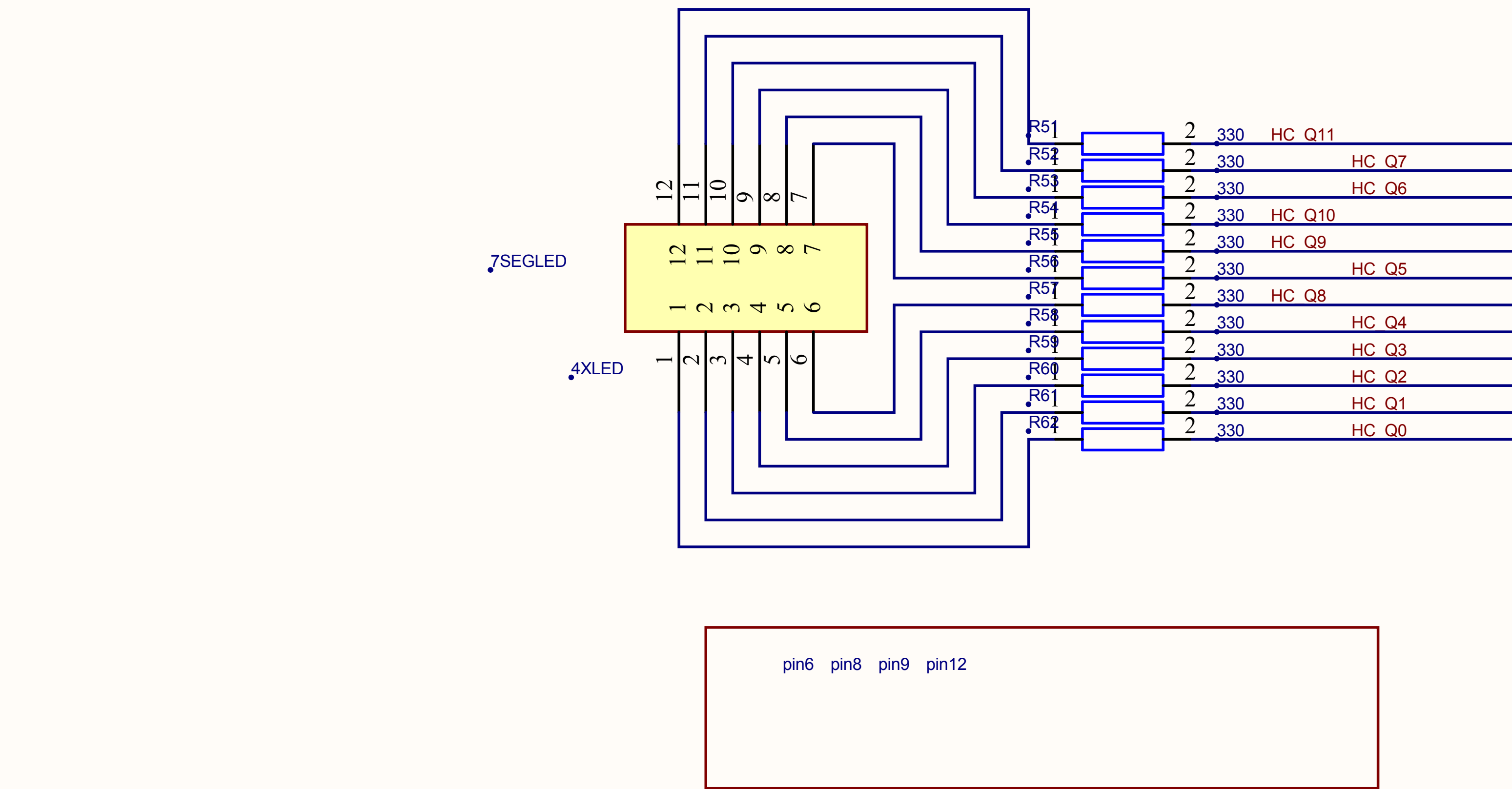
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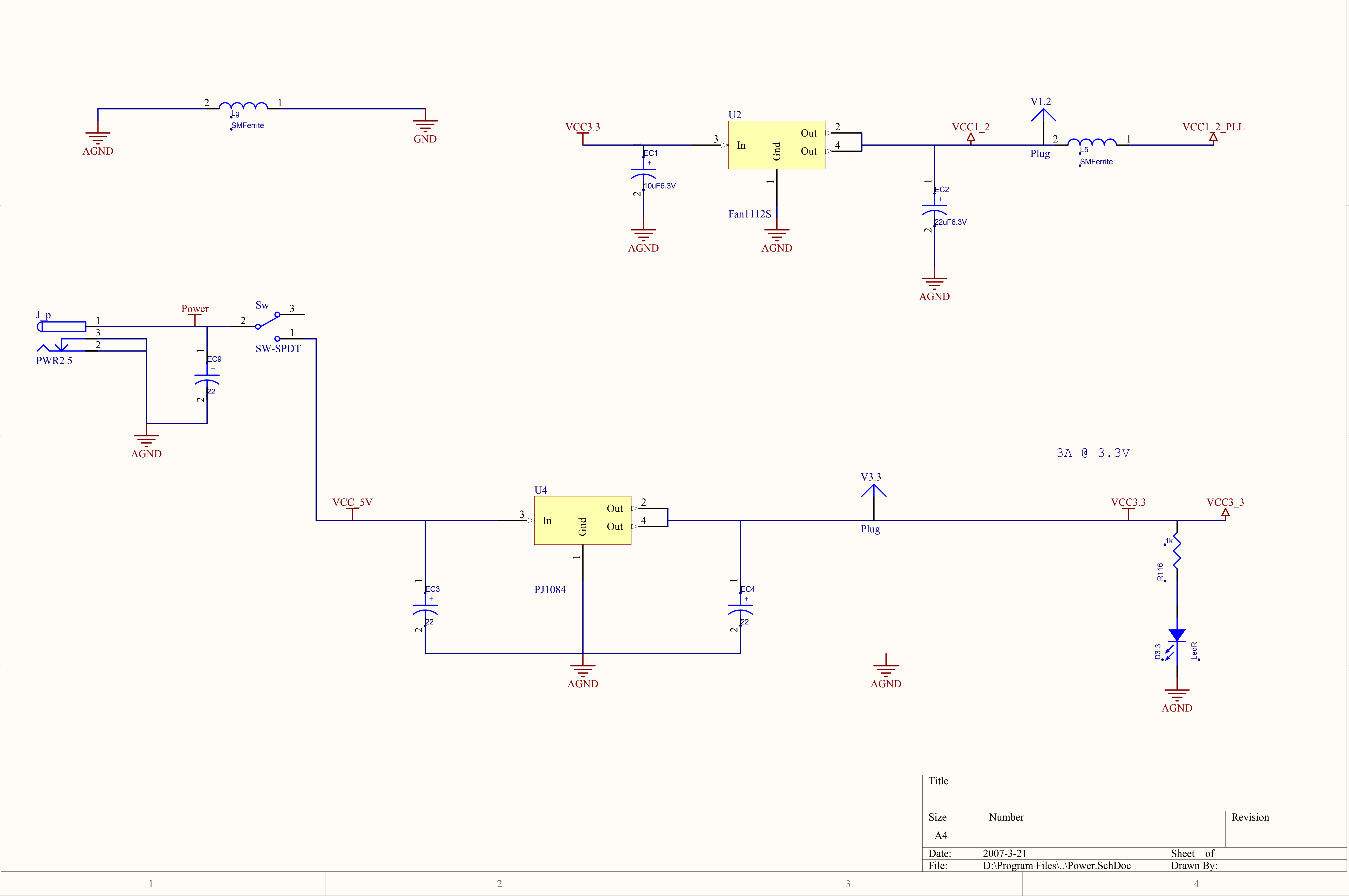
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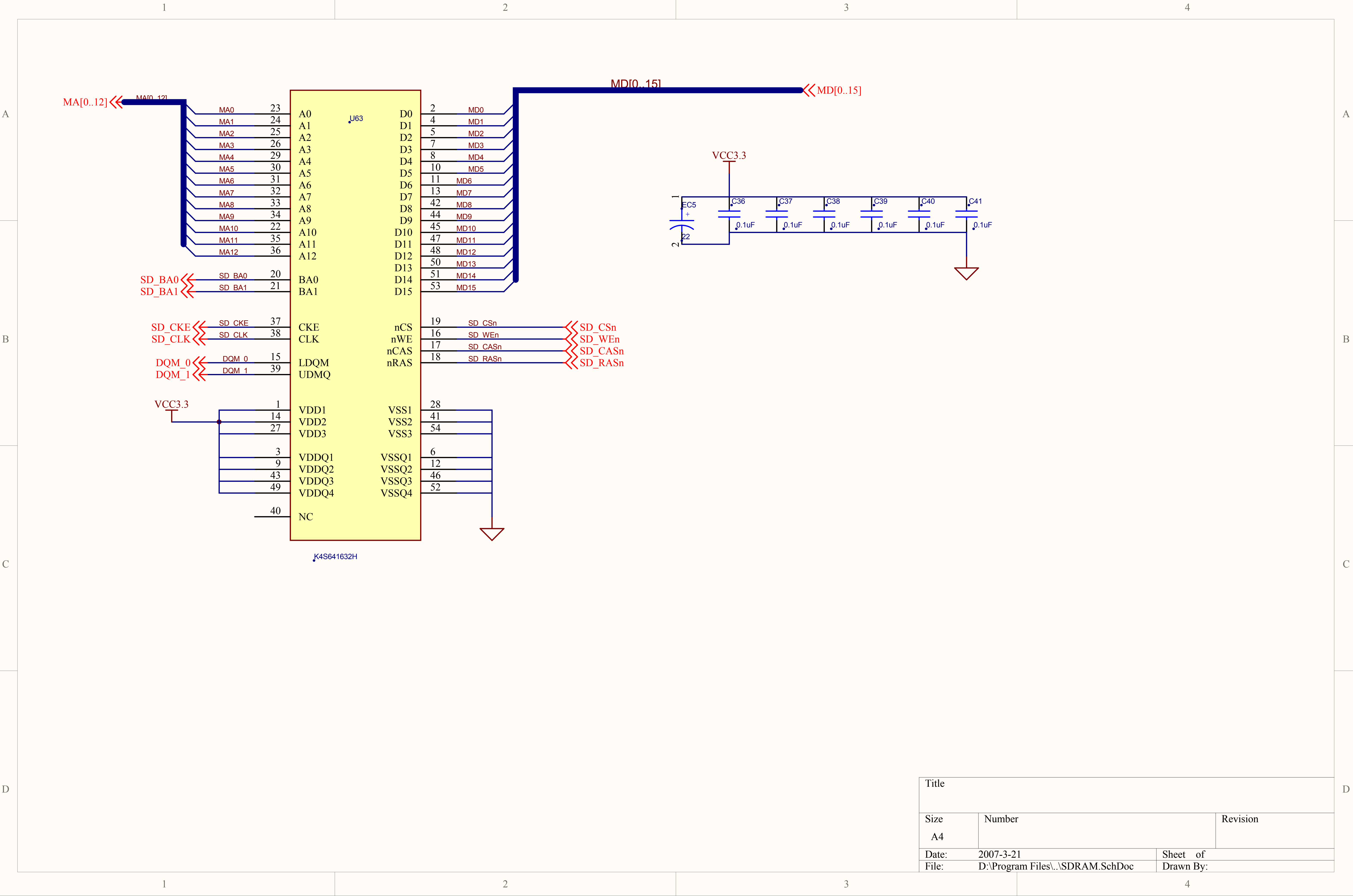
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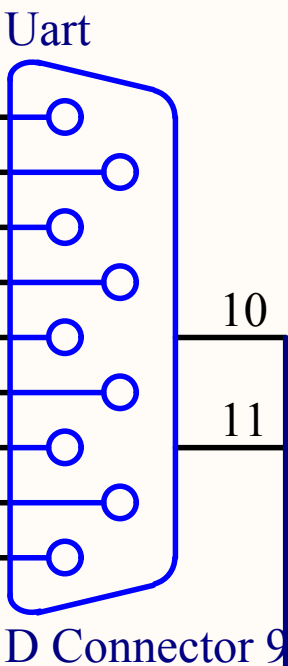
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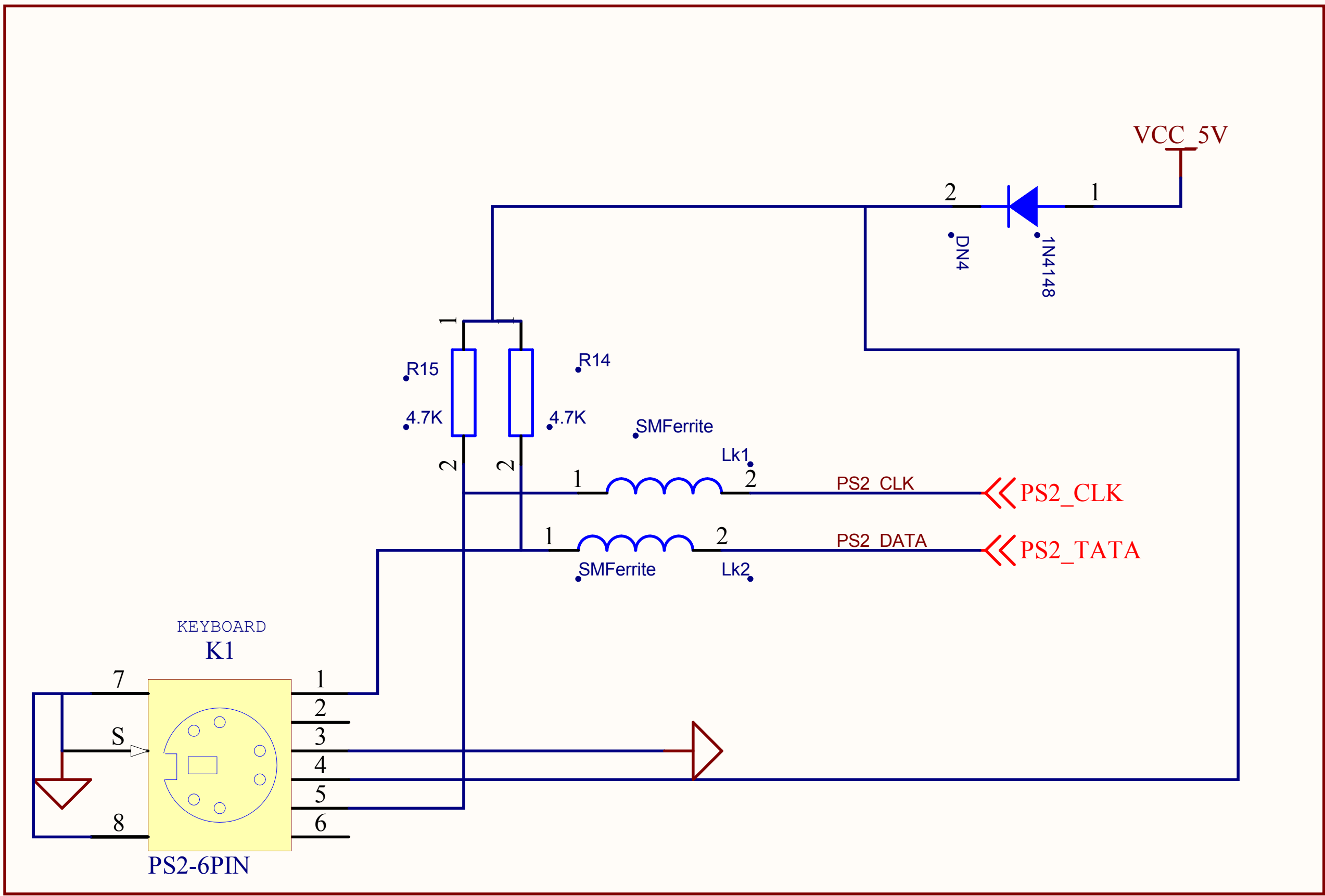
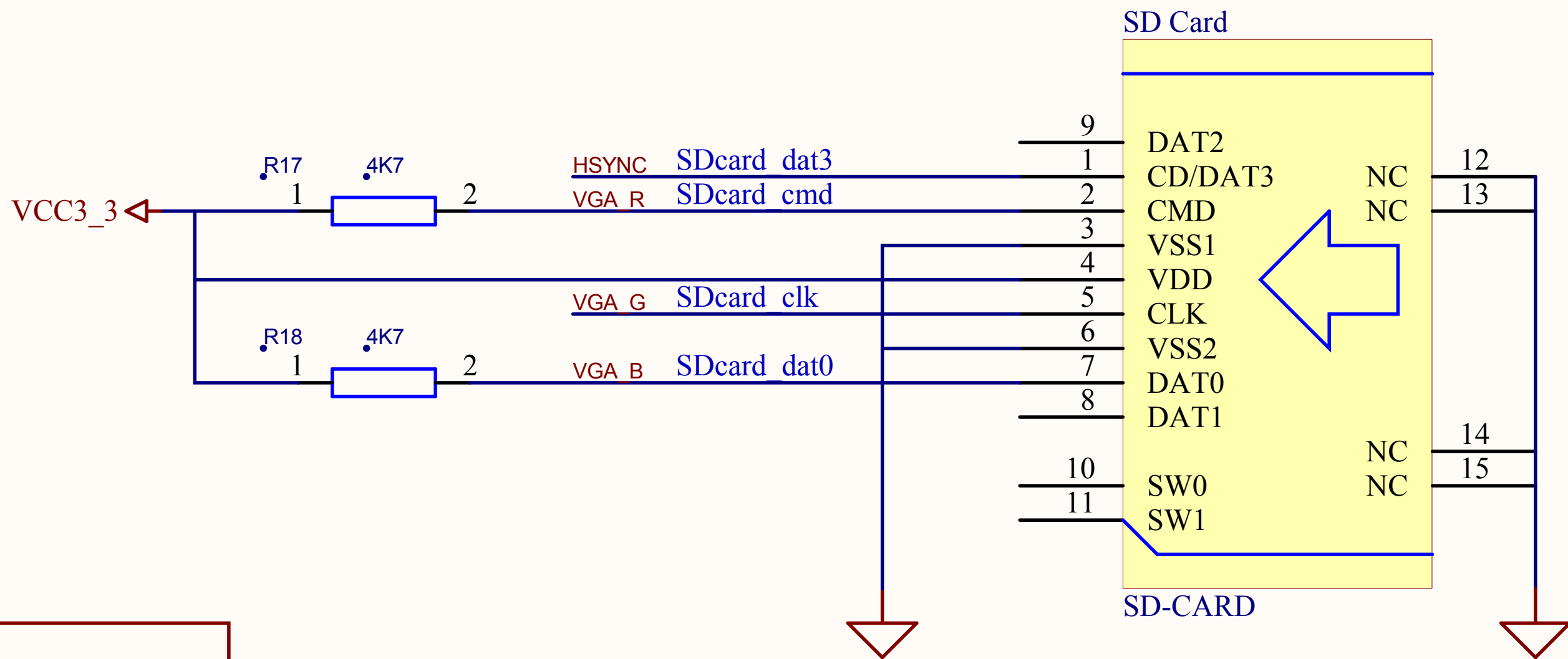
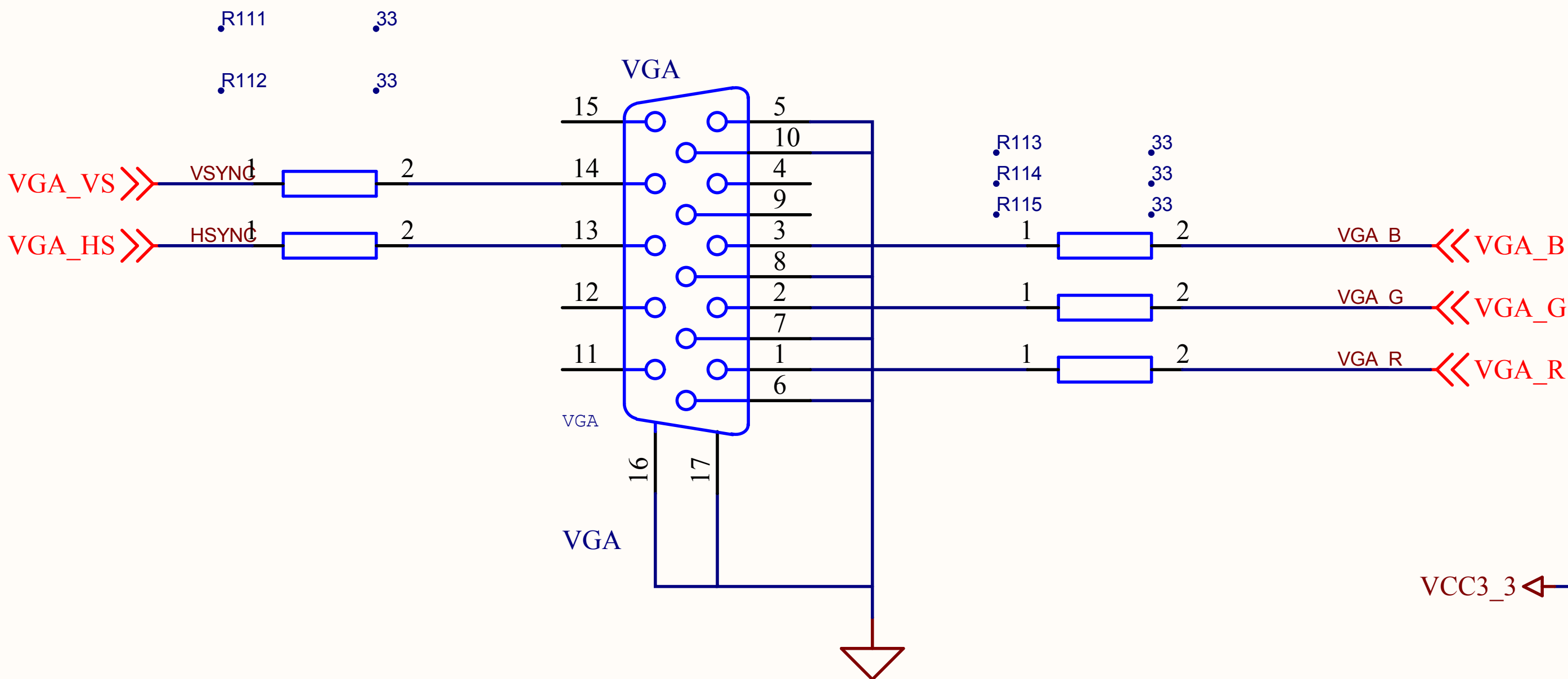
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