CPRI PICS V2.0 (2015-10-09)

Interface Conformance Specification

Protocol Implementation Conformance Statement (PICS);

Interface Conformance Specification

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# Introduction

The Common Public Radio Interface (CPRI) specification enables a large product differentiation in terms of functionality, management, and characteristics.

The PICS is a statement of the capabilities and options of the CPRI specification that have been implemented. The PICS can be used for several purposes such as:

* a checklist for implementation test and verification;
* a detailed indication of the capabilities of the implementation;
* a preliminary check of the interoperability between implementations;

# **Protocol Implementation Conformance Statement (PICS) proforma**

The supplier of a protocol implementation that is claimed to conform to the CPRI specification shall complete the following protocol implementation conformance statement (PICS) proforma to demonstrate compliance to mandatory requirements and list the subset of optional features supported.

## **Instructions for completing the PICS proforma**

The PICS proforma is a fixed-format questionnaire following the CPRI specification subsections plan.

Answers to the questionnaire items have to be provided in the “Support” column, either by simply marking an answer to indicate a restricted choice (usually Yes, No, or Not Applicable), or by entering a value or a set or range of values.

Each item is identified by an item reference in the first column. The second column is the feature description; the third column is the reference to the CPRI specification subsections related to this item. The fourth column contains option values or comments related to the question to be answered. The fifth column shows the status of the support of the items: mandatory, optional or not applicable. The sixth column provides space for the answer.

More than one PICS proforma may be required to specify the capabilities of an implementation that would support different configurations with major capability differences.

## Conformance Lists

Table 2‑1 Protocol

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 1-1 | Supported Radio Access Technologies (RAT) | - | Support of at least one RAT in the option list is required | m |  |
| Support of UTRA FDD | o |  |
| Support of WiMAX | o |  |
| Support of E-UTRA | o |  |
| Support of GSM | o |  |
| Other (specify) | o |  |
| 1-2 | Support of “IQ Data” | 4.1 | User plane information in the form of IQ data Mandatory for base stations with a functional decomposition according to section 2.4 | m |  |
| 1-3 | Support of “Synchronization” | 4.1 4.2.7.5 | Start of hyperframe, HFN, BFN | m |  |
| 1-4 | Support of “L1 Inband Protocol” | 4.1 4.2.7.6 | Protocol version, start-up, L1 (SDI, RAI, Reset, LOS, LOF) Pointer p | m |  |
| 1-5 | Support of “C&M data” | 4.1 4.2.7.7 | Link without C&M is called passive link | o |  |
| 1-6 | Support of “Protocol Extensions” | 4.1 4.2.7.8 | Reserved for future protocol extensions  Reserved bits marked with “r” means that a transmitter shall send 0’s for bits marked with “r”, and the receiver shall not interpret bits marked with “r” (transmit: r = 0, receiver: r = don’t care). | m |  |
| 1-7 | Support of “Vendor Specific Information” | 4.1 4.2.7.9 |  | o |  |
| 1-8 | Support of “Control AxC Data” | 4.2.7.10 |  | o |  |

Table 2‑2 Physical Layer

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 2-1 | Line Bit Rate | 4.2.1 | The support of at least one line bit rate in the option list is required: | m |  |
| Line bit rate option 1 | o |  |
| Line bit rate option 2 | o |  |
| Line bit rate option 3 | o |  |
| Line bit rate option 4 | o |  |
| Line bit rate option 5 | o |  |
| Line bit rate option 6 | o |  |
| Line bit rate option 7 | o |  |
| Line bit rate option 7A | o |  |
| Line bit rate option 8 | o |  |
| Line bit rate option 9 | o |  |
| Line bit rate option 10 | o |  |
| 2-2 | Physical Layer Mode  Electrical Interface Optical Interface | 4.2.2 | The support of at least one of the physical layer modes is required. If supported, provide corresponding physical layer specification | m |  |
| Support of CPRI “Mode” E.6 | o |  |
| Support of CPRI “Mode” E.12 | o |  |
| Support of CPRI “Mode” E.24 | o |  |
| Support of CPRI “Mode” E.30 | o |  |
| Support of CPRI “Mode” E.48 | o |  |
| Support of CPRI “Mode” E.60 | o |  |
| Support of CPRI “Mode” E.79 | o |  |
| Support of CPRI “Mode” E.96 | o |  |
| Support of CPRI “Mode” E.99 | o |  |
| Support of CPRI “Mode” E.119 | o |  |
| Support of CPRI “Mode” E.238 | o |  |
| Support of CPRI “Mode” OS.6 | o |  |
| Support of CPRI “Mode” OS.12 | o |  |
| Support of CPRI “Mode” OS.24 | o |  |
| Support of CPRI “Mode” OS.30 | o |  |
| Support of CPRI “Mode” OS.48 | o |  |
| Support of CPRI “Mode” OS.60 | o |  |
| Support of CPRI “Mode” OS.96 | o |  |
| Support of CPRI “Mode” OS.99 | o |  |
| Support of CPRI “Mode” OS.119 | o |  |
| Support of CPRI “Mode” OS.238 | o |  |
| Support of CPRI “Mode” OL.6 | o |  |
| Support of CPRI “Mode” OL.12 | o |  |
| Support of CPRI “Mode” OL.24 | o |  |
| Support of CPRI “Mode” OL.30 | o |  |
| Support of CPRI “Mode” OL.48 | o |  |
| Support of CPRI “Mode” OL.60 | o |  |
| Support of CPRI “Mode” OL.96 | o |  |
| Support of CPRI “Mode” OL.99 | o |  |
| Support of CPRI “Mode” OL.119 | o |  |
| Support of CPRI “Mode” OL.238 | o |  |
| 2-3 | 8B/10B Line Coding | 4.2.5 | Mandatory for line bit rate option 1 – 7 | m |  |
| N.A. for line bit rate option 7A - 10 | N.A. | N.A. |
| 2-4 | 64B/66B Line Coding | 4.2.5 | Mandatory for line bit rate option 7A - 10 | m |  |
| N.A. for line bit rate option 1 - 7 | N.A. | N.A. |
| 2-5 | Detection of 8B/10B code violation | 4.2.6 | Mandatory if 8B/10B line coding is used | m |  |
| N.A. if 64B/66B line coding is used | N.A. | N.A. |
| 2-6 | Detection of sync header violation | 4.2.6 | Mandatory if 64B/66B line coding is used | m |  |
| N.A. if 8B/10B line coding is used | N.A. | N.A. |

Table 2‑3 Basic Frame Structure

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 3-1 | Support of “Basic Frame Structure” | 4.2.7.1 | Length of basic frame = 1/3.84MHz Length of Control word in Table 3 Transmission Sequence and Scrambling | m |  |
| 3-2 | Side-stream Scrambler | 4.2.7.1.2 6.5 | Optional if 8B/10B line coding is used | o |  |
| N.A. if 64B/66B line coding is used | N.A. | N.A. |
| 3-3 | 64B/66B Scrambler | 4.2.7.1.2 | Mandatory if 64B/66B line coding is used | m |  |
| N.A. if 8B/10B line coding is used | N.A. | N.A. |
| 3-3A | 64B/66B RS-FEC mode | 4.2.7.1.2 | Support of at least one RS-FEC mode ("on" or "off") in the option list is required: | m |  |
| Line bit rate option 7A: RS-FEC mode on | o |  |
| Line bit rate option 7A: RS-FEC mode off | o |  |
| Line bit rate option 8: RS-FEC mode on | o |  |
| Line bit rate option 8: RS-FEC mode off | o |  |
| Line bit rate option 9: RS-FEC mode on | o |  |
| Line bit rate option 9: RS-FEC mode off | o |  |
| Line bit rate option 10: RS-FEC mode on | o |  |
| Line bit rate option 10: RS-FEC mode off | o |  |
| 3-4 | IQ Sample Width | 4.2.7.2.1 | Support of at least one width in the option list is required: | m |  |
| Support of Downlink sample width M = 8 | o |  |
| Support of Downlink sample width M = 9 | o |  |
| Support of Downlink sample width M = 10 | o |  |
| Support of Downlink sample width M = 11 | o |  |
| Support of Downlink sample width M = 12 | o |  |
| Support of Downlink sample width M = 13 | o |  |
| Support of Downlink sample width M = 14 | o |  |
| Support of Downlink sample width M = 15 | o |  |
| Support of Downlink sample width M = 16 | o |  |
| Support of Downlink sample width M = 17 | o |  |
| Support of Downlink sample width M = 18 | o |  |
| Support of Downlink sample width M = 19 | o |  |
| Support of Downlink sample width M = 20 | o |  |
| Support of Uplink sample width M’ = 4 | o |  |
| Support of Uplink sample width M’ = 5 | o |  |
| Support of Uplink sample width M’ = 6 | o |  |
| Support of Uplink sample width M’ = 7 | o |  |
| Support of Uplink sample width M’ = 8 | o |  |
| Support of Uplink sample width M’ = 9 | o |  |
| Support of Uplink sample width M’ = 10 | o |  |
| Support of Uplink sample width M’ = 11 | o |  |
| Support of Uplink sample width M’ = 12 | o |  |
| Support of Uplink sample width M’ = 13 | o |  |
| Support of Uplink sample width M’ = 14 | o |  |
| Support of Uplink sample width M’ = 15 | o |  |
| Support of Uplink sample width M’ = 16 | o |  |
| Support of Uplink sample width M’ = 17 | o |  |
| Support of Uplink sample width M’ = 18 | o |  |
| Support of Uplink sample width M’ = 19 | o |  |
| Support of Uplink sample width M’ = 20 | o |  |
| 3-5 | Support of Mantissa-Exponent Format | 4.2.7.2.1 |  | o |  |
| 3-6 | Oversampling Ratio for UTRA FDD UL and DL | 4.2.7.2.2 | Support of at least one ratio in the option list is required | m |  |
| N.A. for WiMAX, E-UTRA and GSM | N.A. | N.A. |
| Support of UL Oversampling Ratio 2:  (I, Q, I’, Q’) | o |  |
| Support of UL Oversampling Ratio 4:  (I, Q, I’, Q’, I’’, Q’’, I’’’, Q’’’) | o |  |
| Support of DL Oversampling Ratio 1:  (I, Q) | o |  |
| Support of DL Oversampling Ratio 2:  (I, Q, I’, Q’) | o |  |

Table 2‑3 Basic Frame Structure (continued)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 3-7 | Even Size of an AxC Container | 4.2.7.2.2 | Any size but even number of bits | m |  |
| 3-8 | Maximum Number of AxC Containers in a basic frame | - | Specify | m |  |
| 3-9 | Bit Sequence in an AxC Container | 4.2.7.2.2 | The bit sequences for the standard case and the mantissa-exponent case are specified in section 4.2.7.2.2 | m |  |
| 3-10 | Mapping Method of IQ sample(s) in an AxC Container | 4.2.7.2.2 | For UTRA FDD, an AxC Container contains exactly n IQ samples, where n is the oversampling ratio. | m |  |
| 3-11 | Mapping Method of IQ sample(s) in an AxC Container Block | 4.2.7.2.2 | Support of at least one mapping method in the option list is required. | m |  |
| Support of Mapping Method 1 | o |  |
| Support of Mapping Method 2 | o |  |
| Support of Mapping Method 3 | o |  |
| 3-12 | Mapping Method of AxC Container in a Basic Frame | 4.2.7.2.3 | Support of at least one mapping rule in the option list is required | m |  |
| Support of packed position | o |  |
| Support of flexible position | o |  |

Table 2‑4 Hyperframe Structure

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 4-1 | Support of Hyperframe Structure | 4.2.7.3 | 1 hyperframe = 256 basic frame = 66.67us CPRI 10ms frame = 150 hyperframe | m |  |
| 4-2 | Support of Subchannel Structure | 4.2.7.4 | 64 subchannels; 1 subchannel contains 4 control words per hyperframe | m |  |
| 4-3 | Synchronization Control Word for 8B/10B line coding | 4.2.7.5 | Mandatory if 8B/10B line coding is used | m |  |
| N.A. if 64B/66B line coding is used | N.A. | N.A. |
| 4-4 | Synchronization Control Word for 64B/66B line coding | 4.2.7.5 | Mandatory if 64B/66B line coding is used | m |  |
| N.A. if 8B/10B line coding is used | N.A. | N.A. |
| 4-5 | Bit Rate of Slow C&M Channel (HDLC) | 4.2.7.6 4.2.7.7 | Support of at least one bit rate in option list is required | m |  |
| Support of no HDLC configuration | o |  |
| Support of 240 kbits/s HDLC configuration | o |  |
| Support of 480 kbits/s HDLC configuration | o |  |
| Support of 960 kbits/s HDLC configuration  (for line bit rates ≥ 1228.8Mbit/s) | o |  |
| Support of 1920 kbits/s HDLC configuration (for line bit rates ≥ 2457.6Mbit/s) | o |  |
| Support of 2400kbits/s HDLC configuration (for line bit rates ≥ 3072.0Mbit/s) | o |  |
| Support of Highest possible HDLC bit rate (for line bit rates > 3072.0Mbit/s) | o |  |
| 111: HDLC bit rate negotiated on higher layer, see section 4.5.3.4 | o |  |

Table 2‑4 Hyperframe Structure (continued)

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 4-6 | Bit Rate of Fast C&M Channel (Ethernet) | 4.2.7.6 4.2.7.7 | Support of at least one bit rate in option list is required | m |  |
| Support of no Ethernet Channel p =0 | o |  |
| Support of Ethernet pointer p set to 20 | o |  |
| Support of Ethernet pointer p set to 21 | o |  |
| Support of Ethernet pointer p set to 22 | o |  |
| Support of Ethernet pointer p set to 23 | o |  |
| Support of Ethernet pointer p set to 24 | o |  |
| Support of Ethernet pointer p set to 25 | o |  |
| Support of Ethernet pointer p set to 26 | o |  |
| Support of Ethernet pointer p set to 27 | o |  |
| Support of Ethernet pointer p set to 28 | o |  |
| Support of Ethernet pointer p set to 29 | o |  |
| Support of Ethernet pointer p set to 30 | o |  |
| Support of Ethernet pointer p set to 31 | o |  |
| Support of Ethernet pointer p set to 32 | o |  |
| Support of Ethernet pointer p set to 33 | o |  |
| Support of Ethernet pointer p set to 34 | o |  |
| Support of Ethernet pointer p set to 35 | o |  |
| Support of Ethernet pointer p set to 36 | o |  |
| Support of Ethernet pointer p set to 37 | o |  |
| Support of Ethernet pointer p set to 38 | o |  |
| Support of Ethernet pointer p set to 39 | o |  |
| Support of Ethernet pointer p set to 40 | o |  |
| Support of Ethernet pointer p set to 41 | o |  |
| Support of Ethernet pointer p set to 42 | o |  |
| Support of Ethernet pointer p set to 43 | o |  |
| Support of Ethernet pointer p set to 44 | o |  |
| Support of Ethernet pointer p set to 45 | o |  |
| Support of Ethernet pointer p set to 46 | o |  |
| Support of Ethernet pointer p set to 47 | o |  |
| Support of Ethernet pointer p set to 48 | o |  |
| Support of Ethernet pointer p set to 49 | o |  |
| Support of Ethernet pointer p set to 50 | o |  |
| Support of Ethernet pointer p set to 51 | o |  |
| Support of Ethernet pointer p set to 52 | o |  |
| Support of Ethernet pointer p set to 53 | o |  |
| Support of Ethernet pointer p set to 54 | o |  |
| Support of Ethernet pointer p set to 55 | o |  |
| Support of Ethernet pointer p set to 56 | o |  |
| Support of Ethernet pointer p set to 57 | o |  |
| Support of Ethernet pointer p set to 58 | o |  |
| Support of Ethernet pointer p set to 59 | o |  |
| Support of Ethernet pointer p set to 60 | o |  |
| Support of Ethernet pointer p set to 61 | o |  |
| Support of Ethernet pointer p set to 62 | o |  |
| Support of Ethernet pointer p set to 63 | o |  |

Table 2‑5 Synchronization and Timing

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 5-1 | Support of UMTS Frame Timing | 4.2.8.1 | For UTRA FDD and E-UTRA. | m |  |
| N.A. for WiMAX and GSM | N.A. | N.A. |
| 5-2 | Support of WiMAX Frame Timing | 4.2.8.2 | For WiMAX | m |  |
| N.A. for UTRA FDD, E-UTRA and GSM | N.A. | N.A. |
| 5-3 | Support of GSM Frame Timing | 4.2.8.3 | Mandatory for GSM | m |  |
| N.A. for UTRA FDD, WiMAX and E-UTRA | N.A. | N.A. |
| 5-4 | Support of Cable Delay Calibration | 4.2.9 |  | m |  |
| 6.1 |  | o |  |
| 5-5 | Timing Reference at Slave Port | 4.2.9.2 | RE shall use the incoming frame timing at the slave port where SAPs is assigned as synchronization source as the timing reference for any outgoing signals. | m |  |
| 5-6 | Report of “Toffset” | 4.2.9.2 | The fixed offset (Toffset) between input and output signals at RE shall be known to REC (pre-determined or inform) | m |  |
| 5-7 | “Toffset” value range | 4.2.9.2 | Greater than or equal to 0 and less than 256 TC | m |  |
| 5-8 | The accuracy of “Toffset” | 4.2.9.2 | Better than +/-8.138ns (=TC/32) | m |  |
| 5-9 | RE internal delays reporting | 4.2.9.2 | Report of TBdelayDL | m |  |
| Report of TBdelayUL | m |  |
| Report of the value “N” to REC | m |  |
| 5-10 | BFN,HFN handling in UL | 4.2.9.2 | In addition, the downlink BFN and HFN from REC to RE shall be given back in uplink from RE to REC. | m |  |
| 5-11 | The accuracy of TBdelayUL/DL | 4.2.9.3 | The accuracy of TBdelayUL(i) and TBdelayDL(i) which the REC is informed about shall be better than ±8.138ns (=TC/32) | m |  |
| 5-12 | Support of “Link Monitoring” for 8B/10B | 4.2.10.6 | Number of 8B/10B violations shall be presented to higher layers. | o |  |
| 5-13 | Support of “Link Monitoring” for 64B/66B | 4.2.10.6 | Number of sync header violations shall be presented to higher layers. | m |  |
| 5-14 | Support of “Link Monitoring” for 64B/66B with RS-FEC | 4.2.10.6 | Number of corrected and uncorrected RS-FEC codewords and additionally the number of corrected RS-FEC Symbols shall be presented to higher higher layers. | m |  |

Table 2‑6 Link Maintenance

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 6-1 | Support of “LOS” | 4.2.10.2 | Detection/cease/inband bit/actions | m |  |
| 6-2 | Support of “LOF” | 4.2.10.3 | Detection/cease/inband bit/actions | m |  |
| 6-3 | Support of “RAI” | 4.2.10.4 | Detection/cease/inband bit/actions | m |  |
| 6-4 | Support of “SDI” | 4.2.10.5 | Detection/cease/inband bit/actions | m |  |
| 6-5 | Alarm filtering | 4.2.10.1 | Alarm filtering capability | o |  |
| 6-6 | LOS detection by 8B/10B violation | 4.2.10.2 | Mandatory if 8B/10B line coding is used | m |  |
| N.A. if 64B/66B line coding is used | N.A. | N.A. |
| 6-7 | LOS detection by 64B/66B sync header | 4.2.10.2 | Support of at least one method in option list is required if 64B/66B line coding is used | m |  |
| N.A. if 8B/10B line coding is used | N.A. | N.A. |
| When at least 4 64B/66B sync header code violations occur among a whole hyperframe. | o |  |
| When the 10GBASE-R PCS hi\_ber variable is set to “true”. | o |  |
| 6-7A | LOS detection by RS-FEC synchronization failure | 4.2.10.2 | When the RS-FEC codeword synchronization is not achieved: codeword sync is set to “false”. | m |  |
| When at least 10 RS-FEC uncorrected codewords occur among a whole hyperframe. | o |  |
| N.A. if RS-FEC is not used | N.A. | N.A. |
| 6-8 | LOS detection by light power | 4.2.10.2 | Optional if optical mode of CPRI is used | o |  |
| 6-9 | Number of XACQ states | 4.2.10.3 | Specify | m |  |
| 6-10 | Number of XSYNC states | 4.2.10.3 | Specify | m |  |

Table 2‑7 Start-up Sequence

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 7-1 | Support of “Start-up Sequence” | 4.5 |  | m |  |
| 7-2 | Support of “Line Bit Rate Auto-Negotiation” | 4.5.3.2 | If more than one line bit rates is available | m |  |

Table 2‑8 C&M format

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 8-1 | Support of Slow C&M | 4.3 | If link not passive link, support of either slow or fast C&M channel mandatory | o |  |
| 8-2 | Support of Fast C&M | 4.4 | If link not passive link, support of either slow or fast C&M channel mandatory | o |  |
| 8-3 | Slow C&M format | 4.3 | If supported, the Slow C&M format shall follow HDLC standard as described in 4.3 | m |  |
| 8-4 | Fast C&M format | 4.4 | If supported the Fast C&M format shall follow 802.3 as described in 4.4 | m |  |

Table 2‑9 CPRI IP specifications

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Item | Feature | Section | Value/Comment | Status | Support |
| 9-1 | Jitter Contribution on LINK | R18 | Specify value in ppb | o |  |
| 9-2 | Round trip delay of CPRI IP excluding cable delay | R26 | Specify value in us | o |  |
| 9-3 | U-Plane BER | R27 | Specify maximum BER of U-Plane. | o |  |
| 9-4 | C&M BER | R28 | Specify maximum BER of C&M | o |  |
| 9-5 | Test conditions |  | Specify the test conditions and assumptions associated with items 9-1 to 9-4 (may be provided in a separate document) | o |  |

# **List of Abbreviations**

PICS Protocol implementation conformance statement

m Mandatory function

N.A. Not Applicable

o Optional function

# References

1. CPRI Specification V7.0

# History

|  |  |  |
| --- | --- | --- |
| Version | Date | Description |
| V 1.0 | 2014-04-01 | First PICS for CPRI specification |
| V 2.0 | 2015-10-09 | Updated version of PICS for CPRI Specification V7.0. |