1.

a)

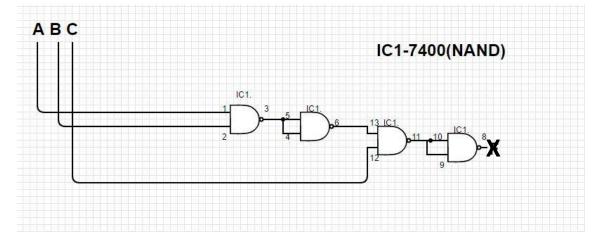
sinais de entrada- A,B,C

sinais de saída -X

b)

Α	В	С	$\overline{A.B}$	A.B	$\overline{A.B.C}$	X=ABC
0	0	0	1	0	1	0
0	0	1	1	0	1	0
0	1	0	1	0	1	0
0	1	1	1	0	1	0
1	0	0	1	0	1	0
1	0	1	1	0	1	0
1	1	0	0	1	1	0
1	1	1	0	1	0	1

c)



2.

a)

b)

$$\overline{B.\overline{A.A}.\overline{C.C}} = \overline{B.\overline{A.A}.\overline{C.C}}$$

15portas NAND

2 CI'S- 7400

c)	Α	В	С	F
	0	0	0	1
	0	0	1	0
	0	1	0	0
	0	1	1	0
	1	0	0	1
	1	0	1	0
	1	1	0	1
	1	1	1	0

