

Unit 5 Test

Your Score: **90%**
18 Correct out of **20**
Question 1 of 20

_____ is the time needed to position the read/write head over the correct track.

- ☒ Transfer time

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Question 2 of 20

_____ computing enables researchers to easily and transparently access computer facilities without regard for their location.

- ☒ Grid

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A(n) _____ is like a special-purpose computer whose responsibility is to handle the details of input/output and to compensate for any speed differences between I/O devices and other parts of the computer.

- ☒ I/O controller

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Which of the following is not a component that makes up the Von Neumann architecture?

- ☒ ACU

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Question 5 of 20

The best measure of speed for computers that execute scientific programs that do an enormous amount of floating-point arithmetic is _____.

- ☒ GFLOPS

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The branch of computer science that studies computers in terms of their major functional units is _____.

- ☒ computer organization

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Which of the following phases is not part of the Von Neumann cycle?

- ☒ encode

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_____ computers are built using the principle called superposition, in which a single bit of data, now called a qubit, can be either a 0 or a 1 or both a 0 and a 1 simultaneously.

- ☒ Quantum

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_____ is the functional unit of a computer that stores and retrieves instructions and data.

- ☒ Memory

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When memory locations are stored in row major order, each memory ____ is connected to two selection lines, the row selection line and the column selection line.

- ☒ cell

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The ____ code field is a unique unsigned integer code assigned to each machine language operation recognized by the hardware.

- ☒ operation

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A disk stores information in units called ____, each of which contains an address and a data block with a fixed number of bytes.

- ☒ sectors

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In ____ parallelism, a computer system has multiple, independent processors each with its own primary memory unit, and every processor is capable of executing its own separate program in its own private memory at its own rate.

- ☒ MIMD

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The instructions that can be decoded and executed by the control unit of a computer are represented in ____.

- ☒ machine language

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In parallel processing, each of the processors tackles a small part of the overall problem and then communicates its result to the other processors via the ____ network.

- ☒ interconnection

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Processors today have clock rates of about ____.

- ☒ 3-5 GHz

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The real key to using ____ parallel processors is to design solution methods that effectively utilize the large number of available processors.

- ☒ massively

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The ____ is the subsystem that performs such mathematical and logical operations as addition, subtraction, and comparison for equality.

- ☒ ALU
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For the control unit to fetch and execute instructions, it relies on two special registers called the ____.

- ☒ PC and IR

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The multiple processors within a ____ cluster do not have to be identical or to belong to a single administrative organization.

- ☒ MIMD