

实验五 线下验收表

序号	指令 PC=0	RV32 汇编语句	控制信号赋值, 未列出的控制信号值为 0	输出信号值 (32 位, 16 进制数)
1	008000ef		ExtOp=4, RegWr=1, ALUBSrc=1, Branch=1	x1:
2	ffdf06f		ExtOp=4, RegWr=1, ALUBSrc=1, Branch=1	x0:
3	00100013		RegWr=1, ALUBSrc=2	x0:
4	deadc2b7		ExtOp=1, RegWr=1, ALUBSrc=2, ALUCtr=15	x5:
5	eef28293		RegWr=1, ALUBSrc=2	x5:
6	0052d333		RegWr=1, ALUCtr=5	x6:
7	00135313		RegWr=1, ALUBSrc=2, ALUCtr=5	x6:
8	4052d3b3		RegWr=1, ALUCtr=13	x7:
9	00529e33		RegWr=1, ALUCtr=1	x28:
10	001e1e13		RegWr=1, ALUBSrc=2, ALUCtr=1	x28:
11	006e0eb3		RegWr=1	x29:
12	01000513		RegWr=1, ALUBSrc=2	x10:
13	005500a3		ExtOp=2, ALUBSrc=2, MemWr=1, MemOp=5	M[0x10]:
14	00551223		ExtOp=2, ALUBSrc=2, MemWr=1, MemOp=6	M[0x14]:
15	00551323		ExtOp=2, ALUBSrc=2, MemWr=1, MemOp=6	M[0x14]:
16	00552423		ExtOp=2, ALUBSrc=2, MemWr=1	M[0x18]:
17	00150483		RegWr=1 , ALUBSrc=2 , MemOp=5 , MemToReg=1	x9:
18	00154903		RegWr=1 , ALUBSrc=2 , MemOp=1 , MemToReg=1	x18:
19	00451983		RegWr=1 , ALUBSrc=2 , MemOp=6 , MemToReg=1	x19:
20	00655a03		RegWr=1 , ALUBSrc=2 , MemOp=2 , MemToReg=1	x20:
21	00452a83		RegWr=1, ALUBSrc=2, MemToReg=1	x21:
22	00a55b03		RegWr=1 , ALUBSrc=2 , MemOp=2 , MemToReg=1	x22:
23	ffff0bb7		ExtOp=1, RegWr=1, ALUBSrc=2, ALUCtr=15	x23:
24	017afc33		RegWr=1, ALUCtr=7	x24:
25	016c6cb3		RegWr=1, ALUCtr=6	x25:
26	fb9e92e3		ExtOp=3, ALUCtr=2, Branch=5	PC:
27	00000073			PC: