



设计文档

6 测试数据和运行结果

测试程序以及指令执行过程中，每条指令执行完毕之后寄存器和存储器的内容见于下表：

地址	指令	功能	机器代码	寄存器内容				内存单元				
				R0	R1	R2	R3	40H	41H	81H	82H	83H
F0H	JR3	指令跳至R3	11001100	×	×	40H	00H	6AH	A3H	×	×	×
00H	LDA R0, [R2]	[R2]→R0	10111000	6AH	×	40H	00H	6AH	A3H	×	×	×
01H	MOV R3, R2	R2→R3	00001011	6AH	×	40H	40H	6AH	A3H	×	×	×
02H	INC R2	R2+=1	01100010	6AH	×	41H	40H	6AH	A3H	×	×	×
03H	LDA R1, [R2]	[R2]→R1	10111001	6AH	A3H	41H	40H	6AH	A3H	×	×	×
04H	ADD R2, R3	R2+R3→R2	00011110	6AH	A3H	81H	40H	6AH	A3H	×	×	×
05H	MOV R3, R0	R0→R3	00000011	6AH	A3H	81H	6AH	6AH	A3H	×	×	×
06H	ADD R0, R1	R0+R1→R0	00010100	0DH	A3H	81H	6AH	6AH	A3H	×	×	×
07H	STO R0, [R2]	R0→[R2]	10101000	0DH	A3H	81H	6AH	6AH	A3H	0DH	×	×
08H	INC R2	R2+=1	01100010	0DH	A3H	82H	6AH	6AH	A3H	0DH	×	×
09H	MOV R0, R3	R3→R0	00001100	6AH	A3H	82H	6AH	6AH	A3H	0DH	×	×
0AH	SUB R0, R1	R0-R1→R0	00100100	C7H	A3H	82H	6AH	6AH	A3H	0DH	×	×
0BH	STO R0, [R2]	R0→[R2]	10101000	C7H	A3H	82H	6AH	6AH	A3H	0DH	C7H	×
0CH	INC R2	R2+=1	01100010	C7H	A3H	83H	6AH	6AH	A3H	0DH	C7H	×
0DH	MOV R0, R3	R3→R0	00001100	6AH	A3H	83H	6AH	6AH	A3H	0DH	C7H	×
0EH	AND R0, R1	R0&R1→R0	00110100	22H	A3H	83H	6AH	6AH	A3H	0DH	C7H	×
0FH	STO R0, [R2]	R0→[R2]	10101000	22H	A3H	83H	6AH	6AH	A3H	0DH	C7H	22H
10H	LDA R3, [R2]	[R2]→R3	10111011	22H	A3H	83H	22H	6AH	A3H	0DH	C7H	22H
11H	OUT R3	输出R3	11111100	22H	A3H	83H	22H	6AH	A3H	0DH	C7H	22H
12H	DEC R2	R2-=1	01110010	22H	A3H	82H	22H	6AH	A3H	0DH	C7H	22H
13H	LDA R3, [R2]	[R2]→R3	10111011	22H	A3H	82H	C7H	6AH	A3H	0DH	C7H	22H
14H	OUT R3	输出R3	11111100	22H	A3H	82H	C7H	6AH	A3H	0DH	C7H	22H
15H	DEC R2	R2-=1	01110010	22H	A3H	81H	C7H	6AH	A3H	0DH	C7H	22H
16H	LDA R3, [R2]	[R2]→R3	10111011	22H	A3H	81H	0DH	6AH	A3H	0DH	C7H	22H
17H	OUT R3	输出R3	11111100	22H	A3H	81H	0DH	6AH	A3H	0DH	C7H	22H
18H	STP	中止	11100000	22H	A3H	81H	0DH	6AH	A3H	0DH	C7H	22H

运算结果：寄存器 R0—22 R1—A3 R2—81 R3—0D

内存单元 40H中存6AH

41H中存A3H

81H中存0DH

82H中存C7H

83H中存22H

其实就跟之前的[《实验结果》](#)之《验收程序》内容一样。

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