# Yueting Li

## li-yueting.github.io | in yuetingli | | in

Research Interest: I'm very interested in developing efficient VLSI circuits design automation tools as well as the circuit itself. I hope I could contribute to a more plastic, simple, reusable integrated circuit design flow and attract more people to this field. Optimization algorithms and machine learning are also my interest, especially those could be used in optimizing IC model/layout/verification processes.

#### Education —

♦ Stanford University, Department of Electrical Engineering Master student in Electrical Engineering

2021 - 2023

♦ Huazhong University of Sci & Tech (HUST), Department of Electrical Engineering B.E. in Electrical Engineering and Automation

2016 - 2020

### Research Experience -

♦ Research Assistant, Murmann Mixed-Signal Group, Stanford University Advisor: Boris Murmann

02/2022 – present

- Open-source analog layout automation using Skywater 130 technology and tools [Code].
- Tape-out of the above layout [Code].
- ♦ Research Assistant, Computational Neuroimage Science Lab, Stanford University Advisor: Kilian Pohl

09/2021 - 03/2022

- Brain structural and functional image signal co-analysis using GCN
- ♦ Research Assistant, Systems Engineering, Chinese University of Hong Kong Advisor: Prof. Anthony Man-Cho So

11/2020 - 09/2021

- Solution of the Pari-mutuel derivative call auction model.
- Large-scale telecommunication sparse signal recovery. [Code].
- ♦ Research Assistant, Silicon Nano Device Lab, National University of Singapore Advisor: Prof. Aaron Thean

10/2019 - 11/2019

– Developing Bluetooth low energy biomedical wearable sensor monitoring stress, glucose et al.

#### Teaching Experience -

TA of EE382, Stanford University	01/2022 - 03/2022
TA of EE134, Stanford University	09/2021 - 12/2021
Peer mentor of summer undergraduate research at Chinese University of Hong Kong	06/2021 - 08/2021
TA of undergraduate summer AI course at the National University of Singapore	08/2019
Volunteer Teaching in the rural senior high at Enshi, Hubei, China	08/2018

#### **Publication** -

[Paper][Code][Video] "Joint Graph Convolution for Analyzing Brain Structural and Functional Connectome" **Yueting Li**, Qingyue Wei, Eshan Adeli, Kilian Pohl, Qingyu Zhao Medical Image Computing and Computer Assisted Intervention (MICCAI), 2022.

[Paper] "Deconvolutional Networks on Graph Data" Jia Li, Jiajin Li, Yang Liu, Jianwei Yu, **Yueting Li**, Hong Cheng Neural Information Processing Systems (NeurIPS), 2021.

## Honors & Awards —

MICCAI 2022 Travel Award (didn't accept due to travel inconvenience)	08/2022
Outstanding Undergraduate Award at HUST	06/2020
Ranked $1^{st}$ in the AI Final Project, National University of Singapore	01/2019
Scholarship for Academic Progress, school of Electrical Engineering, HUST	09/2018
Arts and Sports Scholarship, HUST	11/2017, 5/2017
Vice President, The Student Union of School of Electrical Engineering, HUST	09/2016 - 12/2018
Volunteer Service, Students' International Communication Association, HUST	09/2017 - 12/2017

## Interests

painting, playing Ping Pong, the learning process, programming, boba milk tea