University of Illinois at Urbana-Champaign Dept. of Electrical and Computer Engineering

ECE 120: Introduction to Computing

Extending Keyless Entry

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Combinational Logic Design Allows Use of Abstraction

Recall combinational logic design.

- One can always design from gates (or even transistors),
- but it's often easier to build with components such as adders, comparators, muxes, and decoders.

We use common functionality

- addition, comparison, selection, and identifying encoded values (respectively)
- to abstract away details of low-level logic.

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FSM Designs Also Allow Use of Abstraction

The same holds for FSMs:

- one can always design every state,
- but often we want to organize an FSM hierarchically,
- and analyze states in groups rather than individually.

We can use both combinational logic components and sequential logic components such as registers and shift registers to simplify the design task.

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Let's Extend Our Keyless Entry FSM

As you may recall, our FSM design only reacted to user input (the **ULP** buttons).

For example,

- ∘ if a user pushes the panic button **P**,
- and then does nothing more,
- the FSM stays in the **ALARM** state,
- and the alarm sounds forever (until the car battery dies).

Let's modify the design to make the FSM turn the alarm off after some time.

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A Quick Review of I/O for Keyless Entry

Outputs are as follows:

D driver door; 1 means unlocked

R remaining doors; 1 means unlocked

A alarm; 1 means alarm is sounding

And inputs are as follows:

U unlock button; 1 means it's been pressed

L lock button; 1 means it's been pressed

P panic button; 1 means it's been pressed

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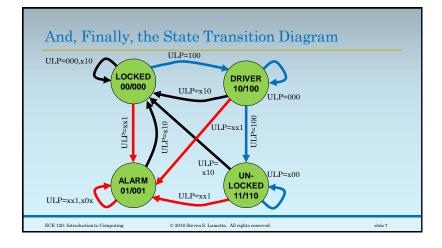
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Also Review the State Table

The state table below gives the state IDs and the outputs for each state.

meaning	state	S_1S_0	D	R	A
vehicle locked	LOCKED	00	0	0	0
driver door unlocked	DRIVER	10	1	0	0
all doors unlocked	UNLOCKED	11	1	1	0
alarm sounding	ALARM	01	0	0	1
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Start by Extending the Abstract Model

So what exactly do we want to change?

After a user turns on the alarm, the FSM should start measuring time.

Once a certain amount of time has passed, the FSM should turn off the alarm.

In what unit can an FSM measure time?

In clock cycles.

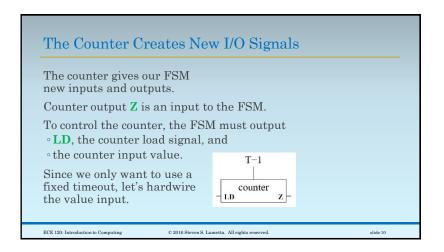
What component can we use? A counter.

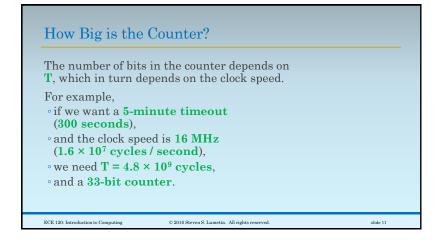
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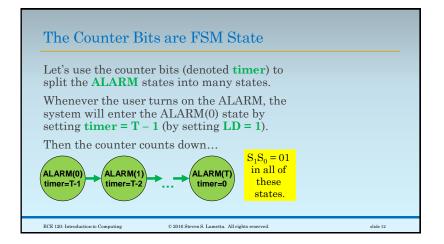
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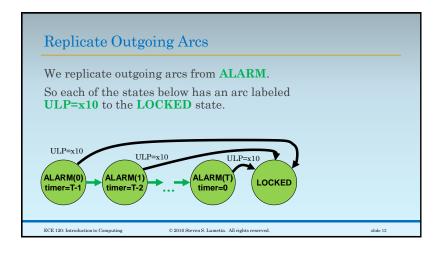
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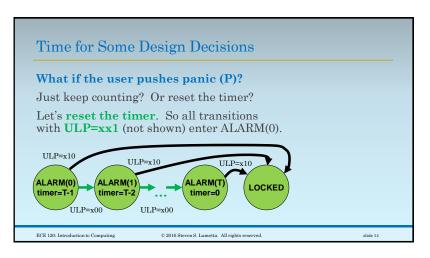
Use a Binary Counter to Measure Time Let's use a binary down new value to load counter with a load input **LD**. • When LD = 1, the counter counter loads a new value (from the LD above in the figure). • The counter bits represent an unsigned value. • The count (stored value) goes down towards 0. • When the count reaches 0. the counter outputs Z = 1. You know how to build one. ECE 120: Introduction to Computing © 2016 Stavan S. Lumatta. All rights recorved

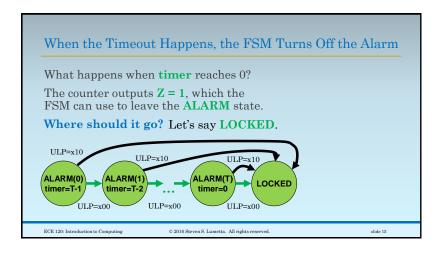












Treat the Other Three States as Single States The timer bits are part of the FSM state. What about the other three states in the original design (LOCKED, DRIVER, and UNLOCKED)? These states are also split into many new states! But their behavior is independent of the timer bits. So we continue to treat them as single states.

Two Issues Need to be Addressed by the Implementation

Now let's think about implementation.

Can we reuse the old design? Yes!

We have two issues to address:

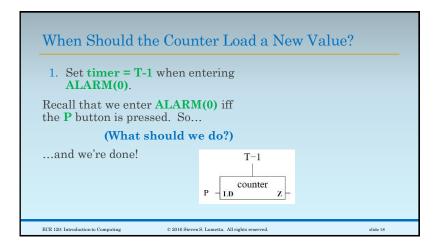
- 1. Set timer = T-1 when entering ALARM(0).
- 2. Move from **ALARM** to **LOCKED** when **Z** = 1.

The counter handles the transitions from ALARM(t) to ALARM(t-1).

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Simplify the Implementation with a Mux

2. Move from **ALARM** to **LOCKED** when **Z** = **1**.

ALARM is $S_1S_0 = 01$.

LOCKED is $S_1S_0 = 00$.

So we only need to change S_0^+ . How?

Let's use a mux:

- the 0 input comes from the original S_0^+ logic.
- the 1 input is 0 (to reach $S_1S_0 = 00$).

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Calculating the Mux Select is the Hardest Part

What controls the mux select?

We want to force the **ALARM** to **LOCKED** transition when ...

- The system is in ALARM ($S_1S_0 = 01$),
- \circ AND **ULP** = **x00**,
- \circ AND $\mathbf{Z} = \mathbf{1}$.

So the mux select signal is $\mathbf{S_1'S_0L'P'Z}$.

But if we press **L**, we also move to **LOCKED**.

So we can simplify to S_1 ' S_0 P'Z.

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