University of Illinois at Urbana-Champaign Dept. of Electrical and Computer Engineering

ECE 120: Introduction to Computing

Vending Machine Implementation

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Use Abstraction to Design a Vending Machine FSM

Let's build a more realistic vending machine.

We'll use several components:

- registers,
- · adders.
- omuxes, and
- · decoders.

We'll also develop a new component, **priority encoders**.

And one module specific to this FSM design.

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Let's Assume that Our Machine Sells Three Items

How many items should our vending machine sell?

Each item has

- o a price,
- o an input to identify it (such as a button), and
- o an output to release it.

Three items makes the problem

- · large enough to be interesting, but
- ${}^{\circ}\operatorname{small}$ enough to allow detailed illustration.

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General Protocol for a Vending Machine

- 1. A user sees an item that they want to buy.
- 2. The user puts money into the machine.
- 3. The machine (FSM) keeps track of how much money has been inserted.
- 4. When the user has inserted enough money for the item, the user pushes a button.
- 5. The machine releases the item and deducts the price from the stored money.
- 6. The machine returns change. [Ours won't.]

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Components Needed for the General Protocol

What makes up the state of our vending machine?

Simplest answer: money stored.

Let's **use a register** to record the amount of money.

When money is inserted, use an adder.

When a purchase is made, **use a subtractor** (that is, an adder).

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What is the Unit of Money Stored?

How much do products cost? \$1 to \$2

How much money can the machine store?

Enough for a product, so \$2 to \$4.

Should we accept coins or bills or both?

Realistic answer: both.

Our answer: coins...but no pennies (\$0.01)!

Let's count money in nickels (\$0.05).

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How Big is the Register for Storing Money Inserted?

State is a register N, the number of nickels.

How many bits do we need for N?

The machine should store \$2 to \$4.

The value in N is in units of \$0.05.

So N should hold at most around 40 to 80.

Use a 6-bit register as an unsigned value.

The maximum is then **63**, or **\$3.15**.

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What about Item Prices?

Prices should be easy to change.

Instead of using fixed values, let's use more 6-bit registers: P_1 , P_2 , and P_3 .

Machine owner can set the prices.

Prices are also state, but we abstract them away.

Design the FSM assuming that

- prices are constant, but
- not known in advance (must read registers).

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Initial s	state is	always STATE	<n></n>		
		fina	l state		
input event	cond.	state	accept coin	release product	
none	always	STATE <n></n>	x	none	
quarter inserted	N < 59	STATE <n+5></n+5>	yes	none	
quarter inserted	N ≥ 59	STATE <n></n>	no	none	

IIIIwai s		always STATE< final	state		
input event	cond.	state	accept coin	release product	
item 1 selected	$N \ge P_1$	STATE <n -="" p<sub="">1></n>	x	1	
item 1 selected	$N < P_1$	STATE <n></n>	×	none	

Bits of Input and Output

Inputs include:

- coin inserted: a 3-bit value $C = C_2C_1C_0$ (assume representation provided to us)
- $^{\circ}$ product selection buttons: one for each product: $B_1,\,B_2,$ and B_3

Outputs include:

- ∘ coin accept A (1 means accept, 0 reject)
- \circ item release signals: \mathbf{R}_1 , \mathbf{R}_2 , \mathbf{R}_3

The Input Representation is Provided for Us

coin type	value	# of nickels	$C_2C_1C_0$		
(none)	N/A	N/A	110		
nickel	\$0.05	1	010		
dime	\$0.10	2	000		
quarter	\$0.25	5	011		
half dollar	\$0.50	10	001		
dollar	\$1.00	20	111		
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Outputs Correspond to Inputs in the Previous Cycle

In our class,

- FSM outputs do not depend on input, so
- the FSM cannot respond in the same cycle.

Instead, the FSM's outputs

- · are calculated based on state and inputs,
- then stored for a cycle in flip-flops.

The coin mechanism designer must know that the accept signal comes in the next cycle.

These stored outputs are also state!

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Our Abstract Model and I/O are Specified

We have an abstract model.

We have I/O in bits.

What's next?

Complete the specification!

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Let's Calculate the Size of Our FSM

How many bits of state do we have?

Ignoring prices, we have

- oa 6-bit register, and
- four bits of stored output, so
- a total of 10 bits, or 1024 states.

How many input bits do we have?

Three bits of coin, three buttons, so 6 bits.

1024 states, each with 64 arcs. Good luck!

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Ignore Output "State" and Unused Input Combinations

Obviously, we need to simplify.

First,

- four stored output bits do not affect our transitions, so we can ignore them.
- Each **STATE<N>** thus represents 16 equivalent states.

Second, two bit patterns are unused in the C (coin) representation, so we need only 48 (8 × 6) arcs.

But 48 arcs × 64 states is still too much.

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Choose a Strategy to Handle Multiple Inputs

How can we simplify further?

The abstract model has **nine input events**:

- ono input,
- five types of coins, and
- three types of purchases.

Where do the other 39 arcs come from?

Multiple inputs!

Let's choose a strategy to handle them.

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Ignore Output "State" and Unused Input Combinations

Let's prioritize input events strictly,

meaning that we ignore lower-priority events.

Our strategy is as follows:

- purchases have highest priority:
- item 3, then item 2, then item 1;
- coin type inputs are distinct, so they can't occur at the same time.

Now we can write a complete next state table (for a given set of prices).

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Let's Look at STATE50 with $P_3 = 60$, $P_2 = 10$, $P_1 = 35$

\mathbf{B}_3	\mathbf{B}_2	\mathbf{B}_1	$C_2C_1C_0$	final state	A	\mathbf{R}_3	\mathbf{R}_2	\mathbf{R}_1
1	x	x	xxx	STATE50	0	0	0	0
0	1	x	xxx	STATE40	0	0	1	0
0	0	1	xxx	STATE15	0	0	0	1
0	0	0	010	STATE51	1	0	0	0
0	0	0	000	STATE52	1	0	0	0
0	0	0	011	STATE55	1	0	0	0
0	0	0	001	STATE60	1	0	0	0
0	0	0	111	STATE50	0	0	0	0
0	0	0	110	STATE50	0	0	0	0

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Use a Priority Encoder to Resolve Conflicting Purchases

Purchases have priority, so start with those.

Item 3 has priority, then item 2.

We'll use a **priority encoder**.

Given four input lines, a 4-input priority encoder produces

- a signal **P** indicating that at least one input is active (1), and
- a 2-bit signal **S** encoding the highest priority active input.

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