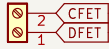
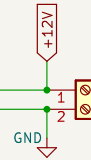


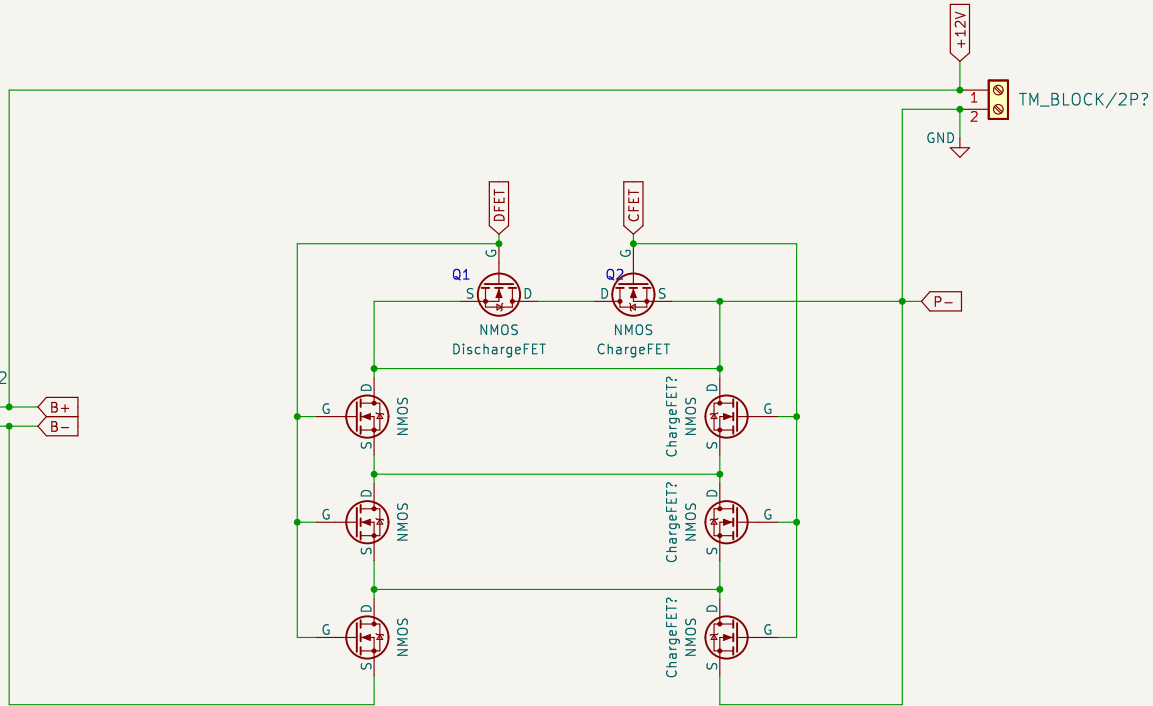
TM_BLOCK/2P1



TM_BLOCK/2P2



TM_BLOCK/2P?



Discharge loop, Q1 is ON: P- -> Q2
internal parasitic diode-> Q1 channel->
B- ->B+ -> P+

Charge loop, Q2 is ON: P+ -> B+ -> B- ->
Q1 internal parasitic diode -> Q2 channel ->
P-