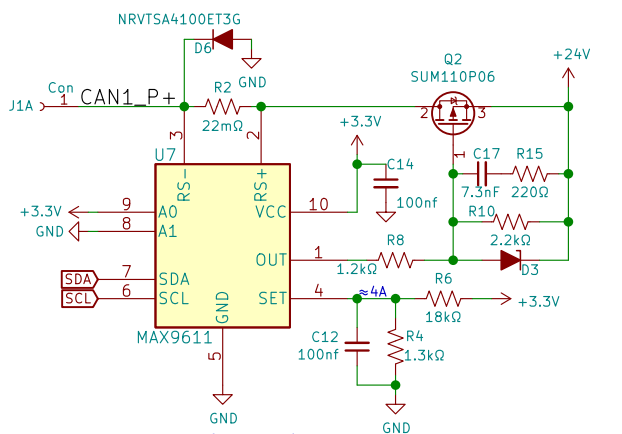


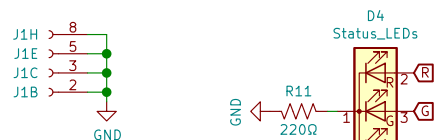
Power module (CAN0)

Addr:0x70



Power module (CAN1)

Addr:0x73

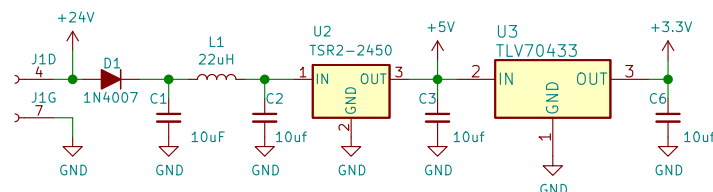


- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole

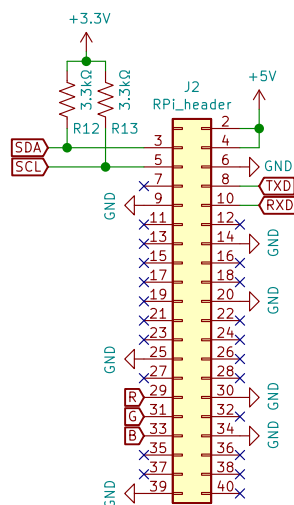


Morfeas Project's Logo

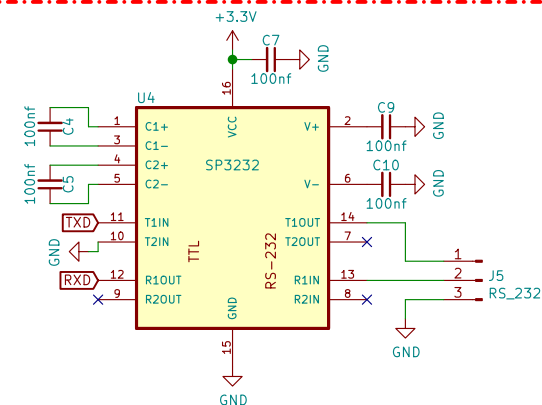
Power Supply Unit



RPI Header

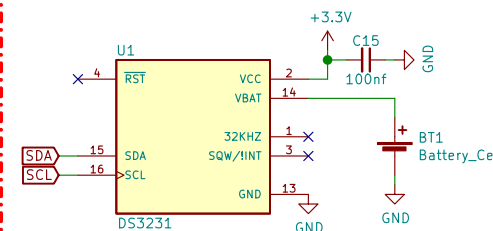


Level Translator (UART <--> RS-232)

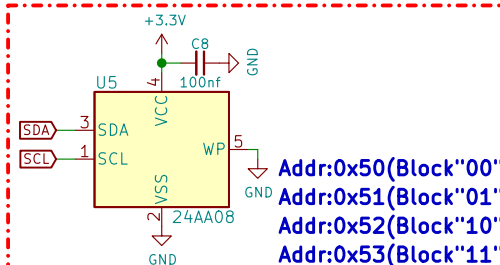


Real Time Clock

Addr:0x68



Configuration EEPROM



Author: Sam Harry Tzavaras

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<https://www.tapr.org/ohl.html>



Sheet: /
 File: Morfeas_RPi_Hat.sch

Title: Morfeas RPi Hat

Size: A4 Date: 2019-12-27

KiCad E.D.A. kicad 5.1.5+dfsg1-2

Rev: V1.0

Id: 1/1