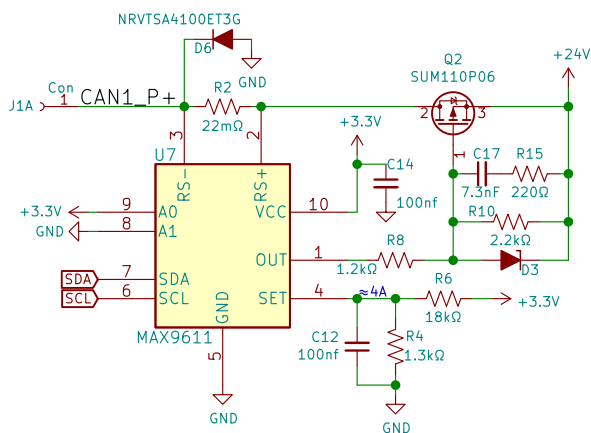


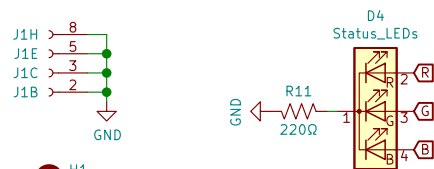
Power module (CAN0)

Addr:0x70



Power module (CAN1)

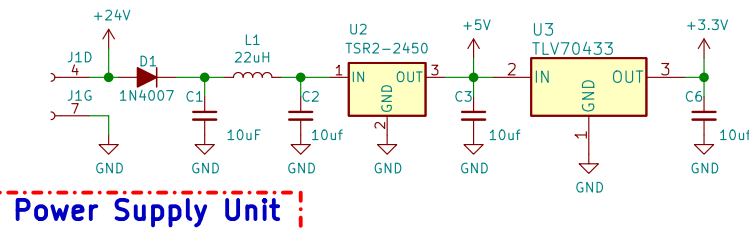
Addr:0x73



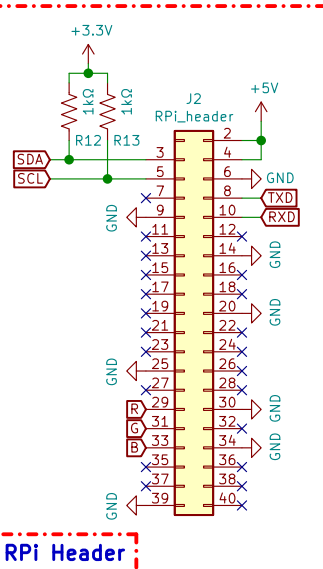
- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole



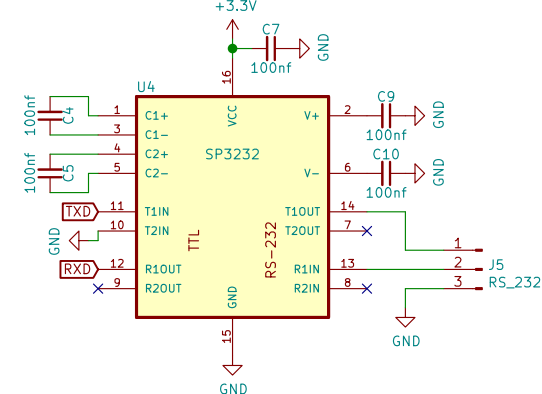
Morfeas Project's Logo



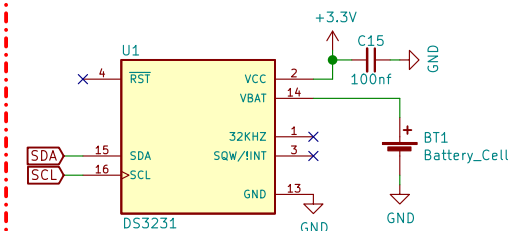
Power Supply Unit



RPi Header

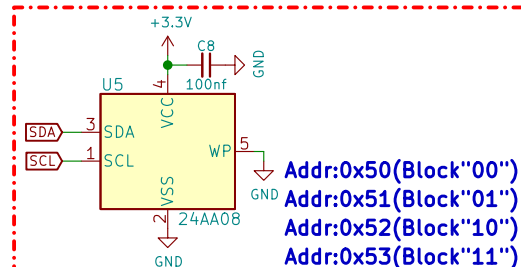


Level Translator (UART <=> RS-232)



Real Time Clock

Addr:0x68



Configuration EEPROM

Addr:0x50(Block"00")
Addr:0x51(Block"01")
Addr:0x52(Block"10")
Addr:0x53(Block"11")

Author: Sam Harry Tzavaras

Licence: TAPR Open Hardware License
<https://www.tapr.org/ohl.html>



Sheet: /
File: Morfeas_RPi_Hat.sch

Title: Morfeas RPi Hat

Size: A4 Date: 2019-12-27

KiCad E.D.A. kicad (5.1.5)-3

Rev: V1.0

Id: 1/1