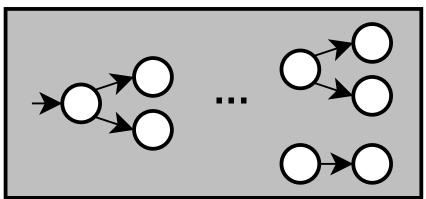


(Pipeline, caches, buses, memory controller, branch predictor, etc.)

Synchronised product and reachability analysis





Models all possible execution traces

State space exploration

