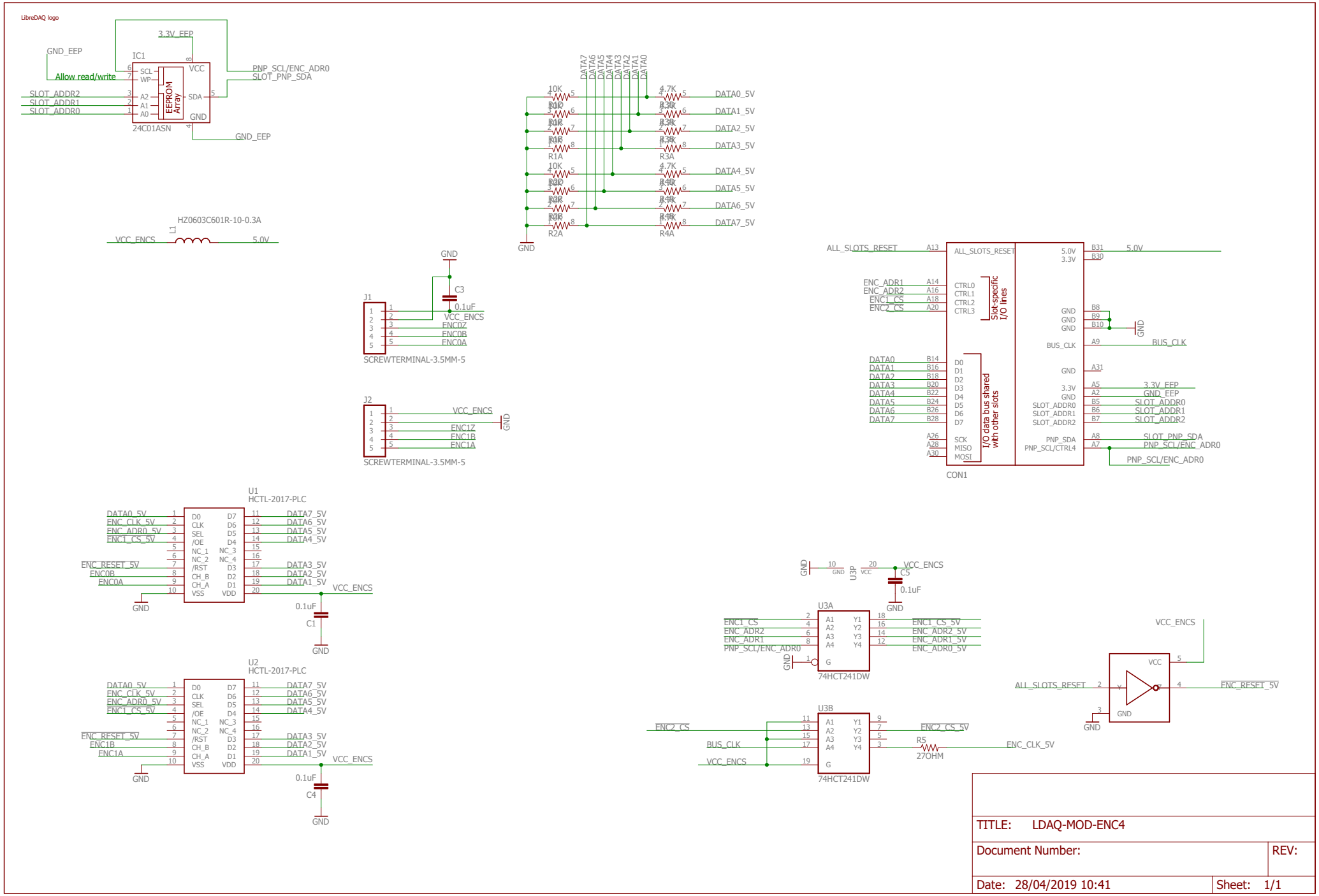
LibreDAQ logo
3.3V_EEP
GND_EEP
IC1
Allow read/write
SCL
WP
VCC
PNP_SCL/ENC_ADR0
SLOT_PNP_SDA
SLOT_ADDR2
SLOT_ADDR1
SLOT_ADDR0
A2
A1
A0
SDA
GND
EEPROM Array
24C01ASN
GND_EEP
HZ0603C601R-10-0.3A
L1
VCC_ENCS
5.0V
DATA7
DATA6
DATA5
DATA4
DATA3
DATA2
DATA1
DATA0
10K
4.7K
R1A
R2A
R3A
R4A
DATA0_5V
DATA1_5V
DATA2_5V
DATA3_5V
DATA4_5V
DATA5_5V
DATA6_5V
DATA7_5V
GND
J1
C3
0.1uF
VCC_ENCS
ENC0Z
ENC0B
ENC0A
SCREWTERMINAL-3.5MM-5
J2
ENC1Z
ENC1B
ENC1A
ALL_SLOTS_RESET
ENC_ADR1
ENC_ADR2
ENC1_CS
ENC2_CS
Slot-specific I/O lines
BUS_CLK
I/O data bus shared with other slots
SCK
MISO
MOSI
3.3V_EEP
GND_EEP
SLOT_ADDR0
SLOT_ADDR1
SLOT_ADDR2
PNP_SDA
PNP_SCL/CTRL4
SLOT_PNP_SDA
PNP_SCL/ENC_ADR0
CON1
U1
HCTL-2017-PLC
ENC_CLK_5V
ENC_ADR0_5V
ENC1_CS_5V
ENC_RESET_5V
C1
U2
C4
U3A
U3B
U3P
C5
74HCT241DW
ENC1_CS_5V
ENC_ADR2_5V
ENC_ADR1_5V
ENC_ADR0_5V
ENC2_CS_5V
ENC_CLK_5V
R5
27OHM
ALL_SLOTS_RESET
ENC_RESET_5V
TITLE: LDAQ-MOD-ENC4
Document Number:
REV:
Date: 28/04/2019 10:41
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