Yexin Wu

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Research Interessts: Natural Language Processing, Large Language Models

EDUCATION

- **University of Illinois, Urbana-Champaign (UIUC)** — *Aug. 2024 - Est. Jun. 2026*
  The Master of Science (M.S.) in computer science (MSCS) — Illinois, United States
  ◦ GPA: 3.77/4.00

- **Shanghai Jiao Tong University (SJTU)** — *Sept. 2020 - Jun. 2024*
  Honors Bachelor of Engineering (B.Eng Hons) in Computer Science — Shanghai, China
  ◦ Members of ACM Honors Class, Zhiyuan College
  ◦ GPA: 3.92/4.30

EXPERIENCE

- **ULab-UIUC** — *Aug. 2024 - Present*
  Research Assistant — Illinois, United States
  ◦ **Advisor:** Prof. Jiaxuan You.
  ◦ Research Topic: Graph-based LLM; Academic benchmark.

- **BCMI Lab** — *Aug. 2022 - Aug. 2023*
  Research Assistant — Shanghai, China
  ◦ **Advisor:** Prof. Hai Zhao; Prof. Zhuosheng Zhang.
  ◦ Research Topic: Reasoning ability and Chain-of-Thought of Language Models

PUBLICATIONS

- **Graph World Model** — *Aug. 2024 - Jan. 2025*
  Authors: Tao F., Yexin W., Guanyu L., Jiaxuan Y.
  ◦ Preprint, under review.
  ◦ We propose GWM, which merges both unstructured and graph-based multi-modal data into a cohesive framework, boosting the performance of various tasks.

- **Mitigating Misleading Chain-of-Thought Reasoning with Selective Filtering** — *Aug. 2022 - Jan. 2023*
  Authors: Yexin W., Zhuosheng Z., Hai Z. [⌂]
  ◦ COLING 2024.
  ◦ We incorporate a CoT filter that can identify and exclude invalid CoTs to form SelFReasoner, thereby preventing invalid CoTs from negatively impacting the final answer prediction.

- **MorphGrower: A Synchronized Layer-by-layer Growing Approach for Plausible and Diverse Neuronal Morphology Generation** — *Month Year*
  Authors: Nianzu Y., Kaipeng Z., Haotian L., Yexin W., Zexin Y., Shengdian J., Jiaxiang W., Yimin W., Junchi Y. [⌂]
  ◦ ICML 2024.
  ◦ Use global information encoded by TGNN and path information encoded by VAE encoder to enhance the VAE model on neuron morphologies generation task.

SKILLS

- **Programming Languages:** C, C++, Java, Python, Verilog
- **Language Skills:** Chinese (Native), English (Fluent), Japanese (Beginner)

TEACHING EXPERIENCE

- **CS1954 Programming, CS1951 Data Structure, CS1952 Programming Practice** — *Fall 2021 - Summer 2022*
  Teaching Assistant at SJTU