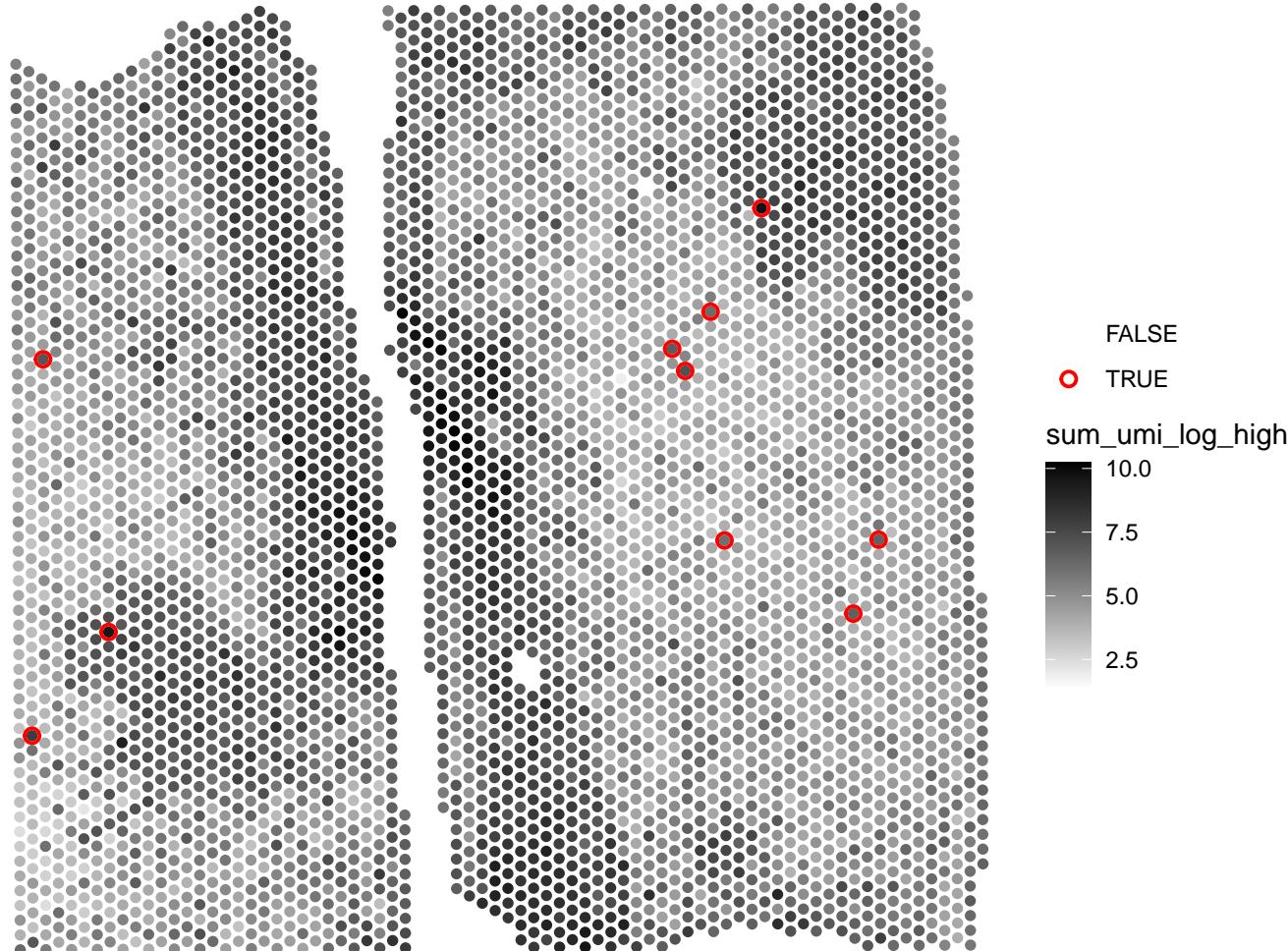
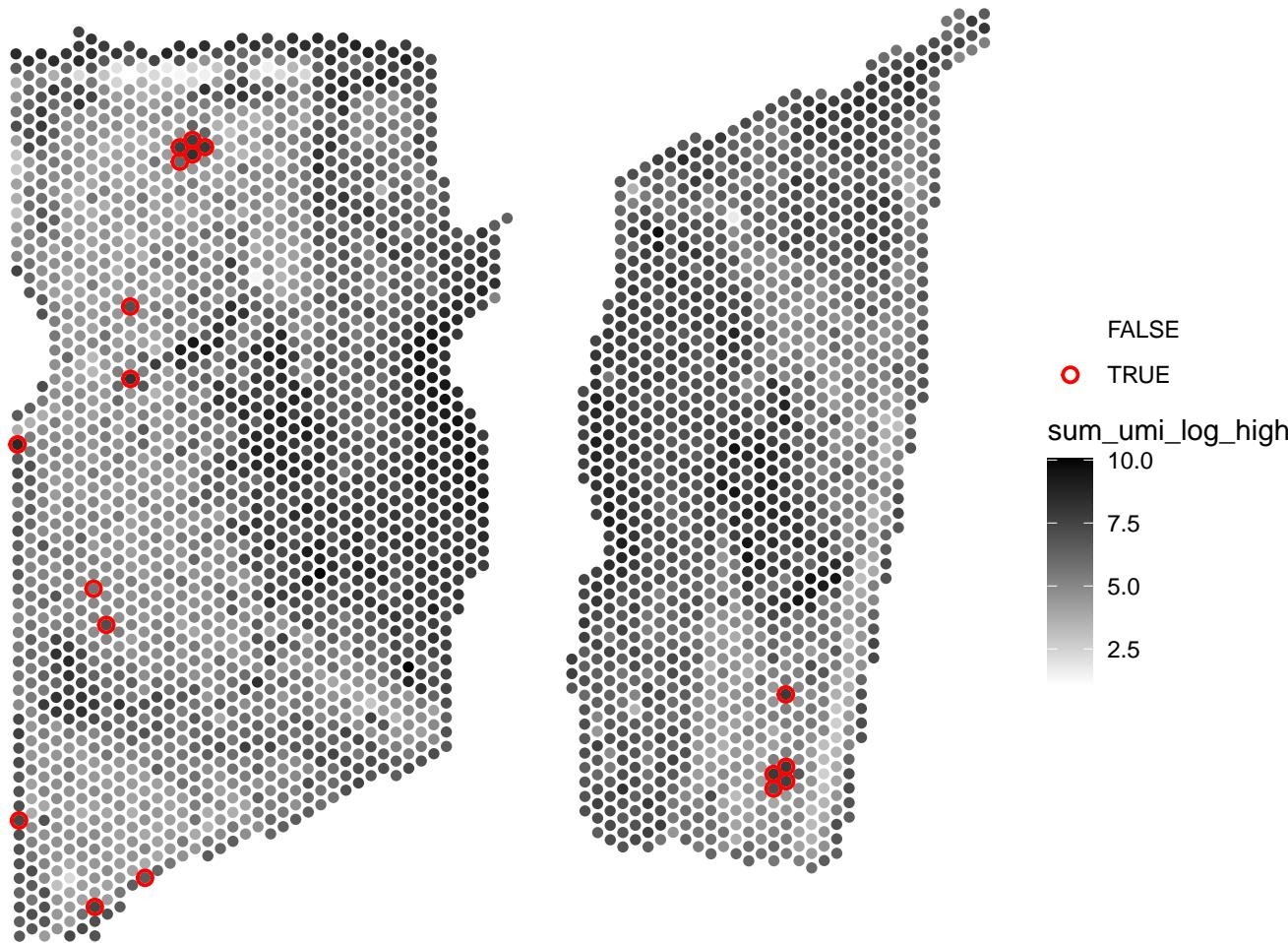


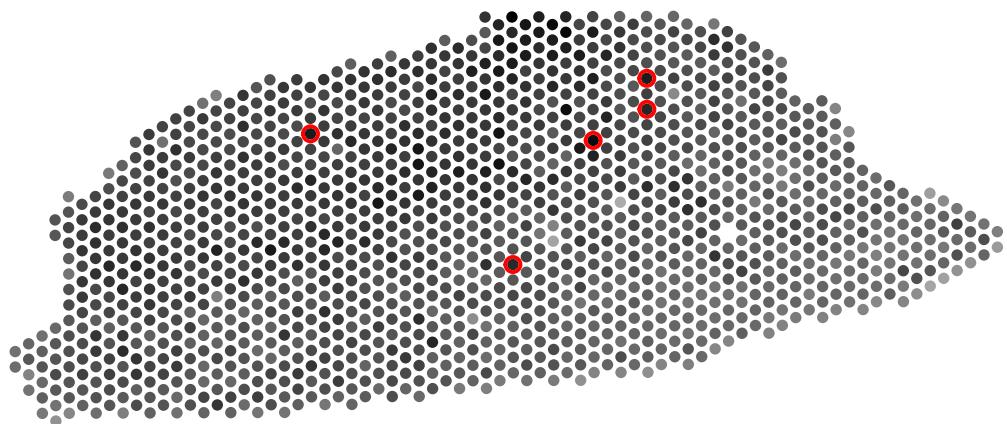
Sample: V13M06–331_A1



Sample: V13M06–331_B1



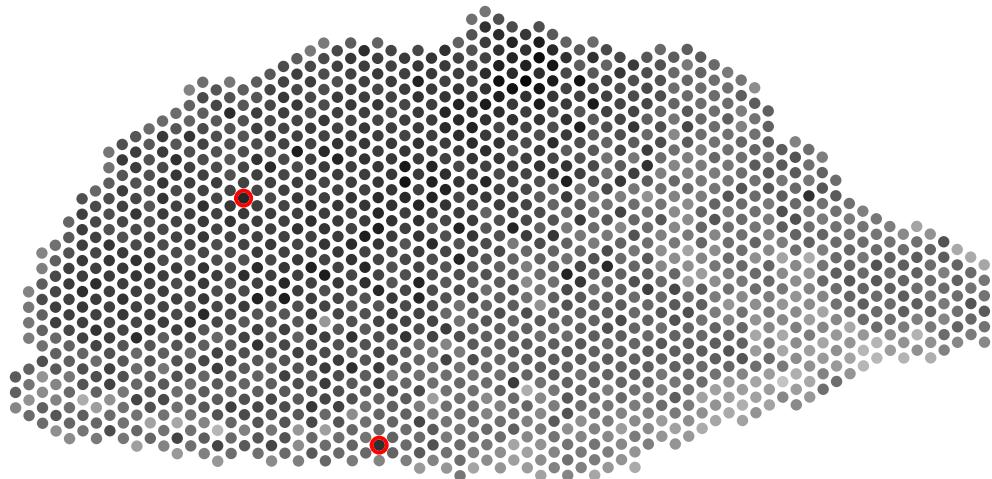
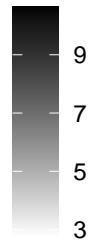
Sample: V13M06–331_C1



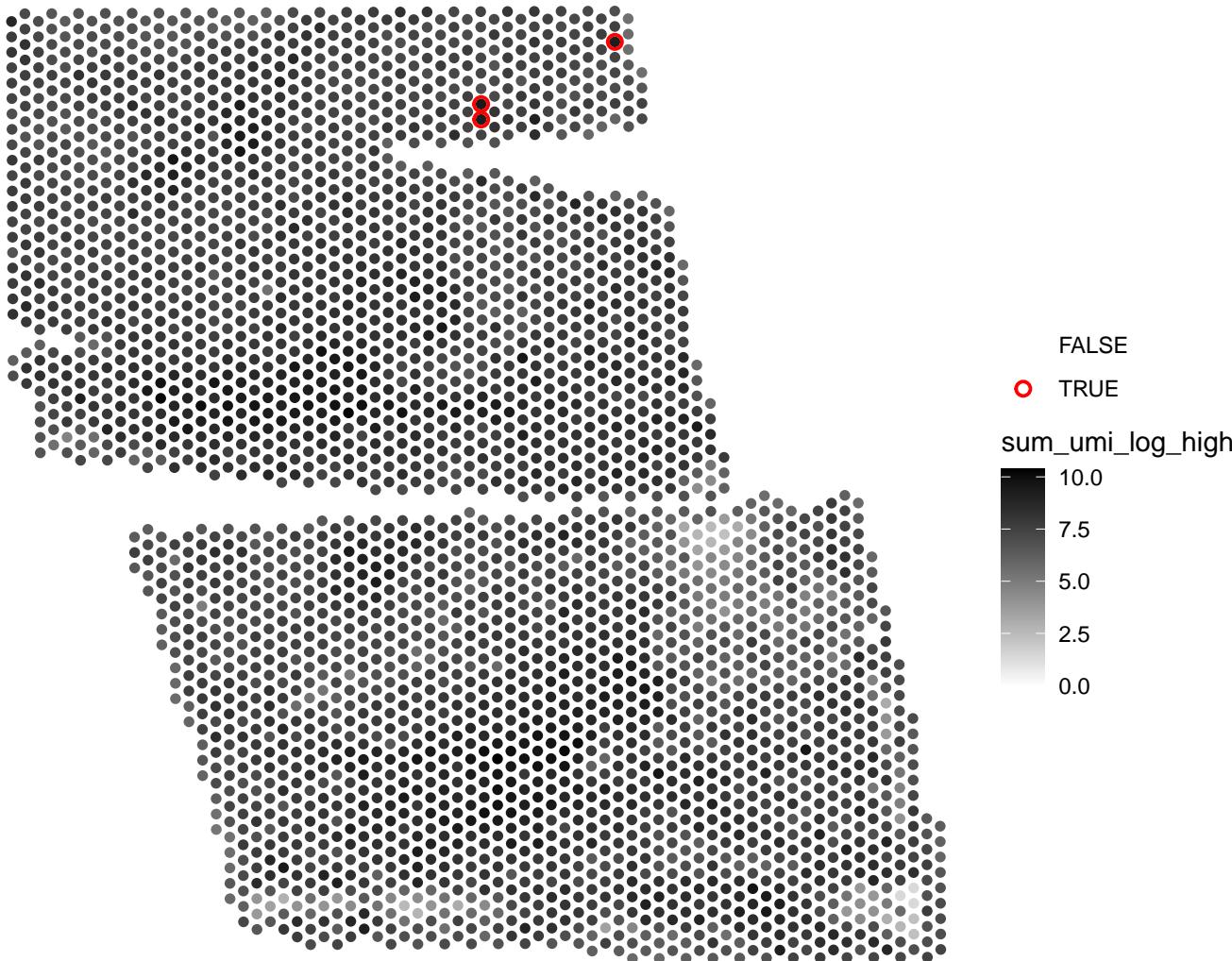
FALSE

TRUE

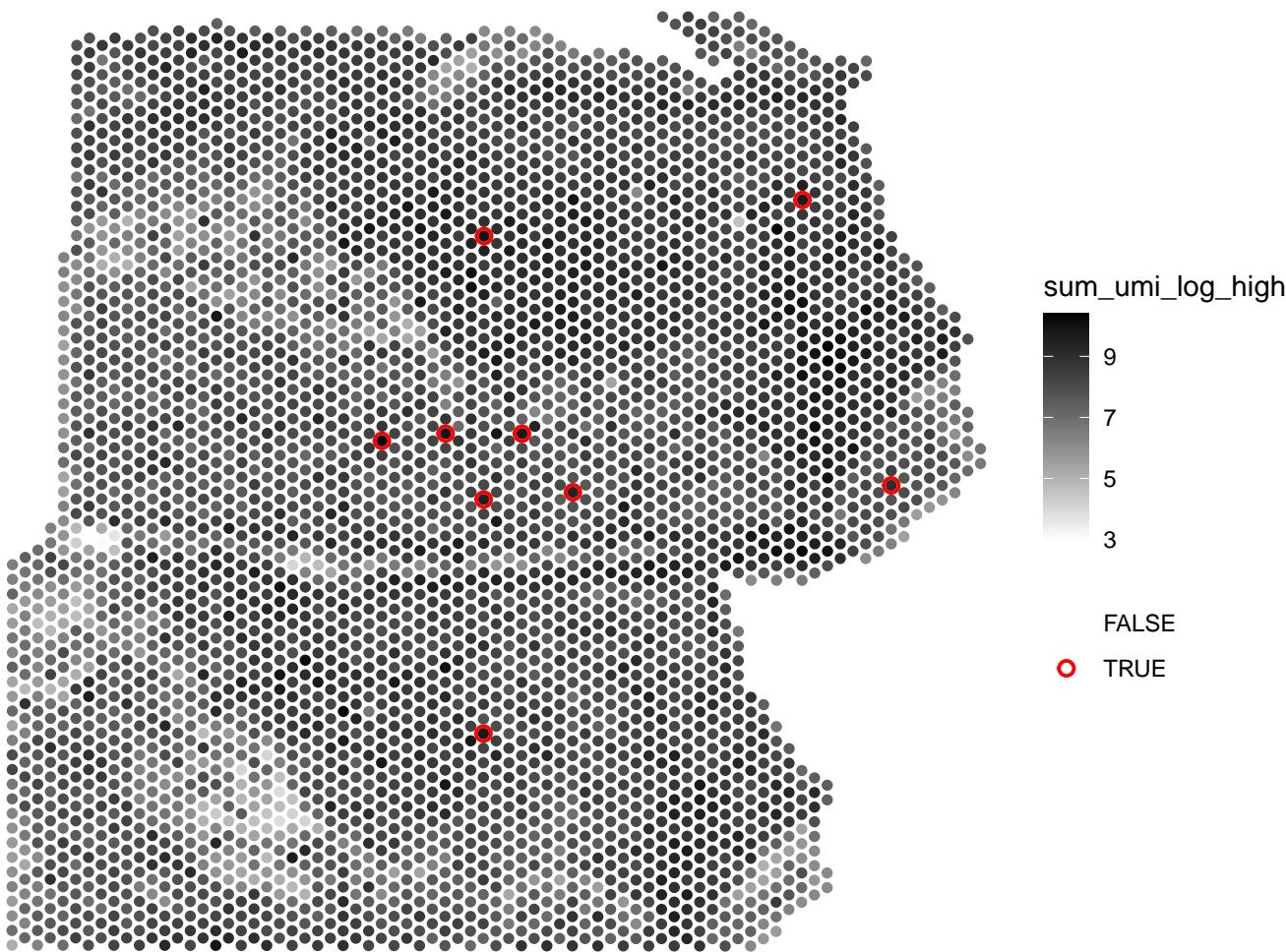
sum_umi_log_high



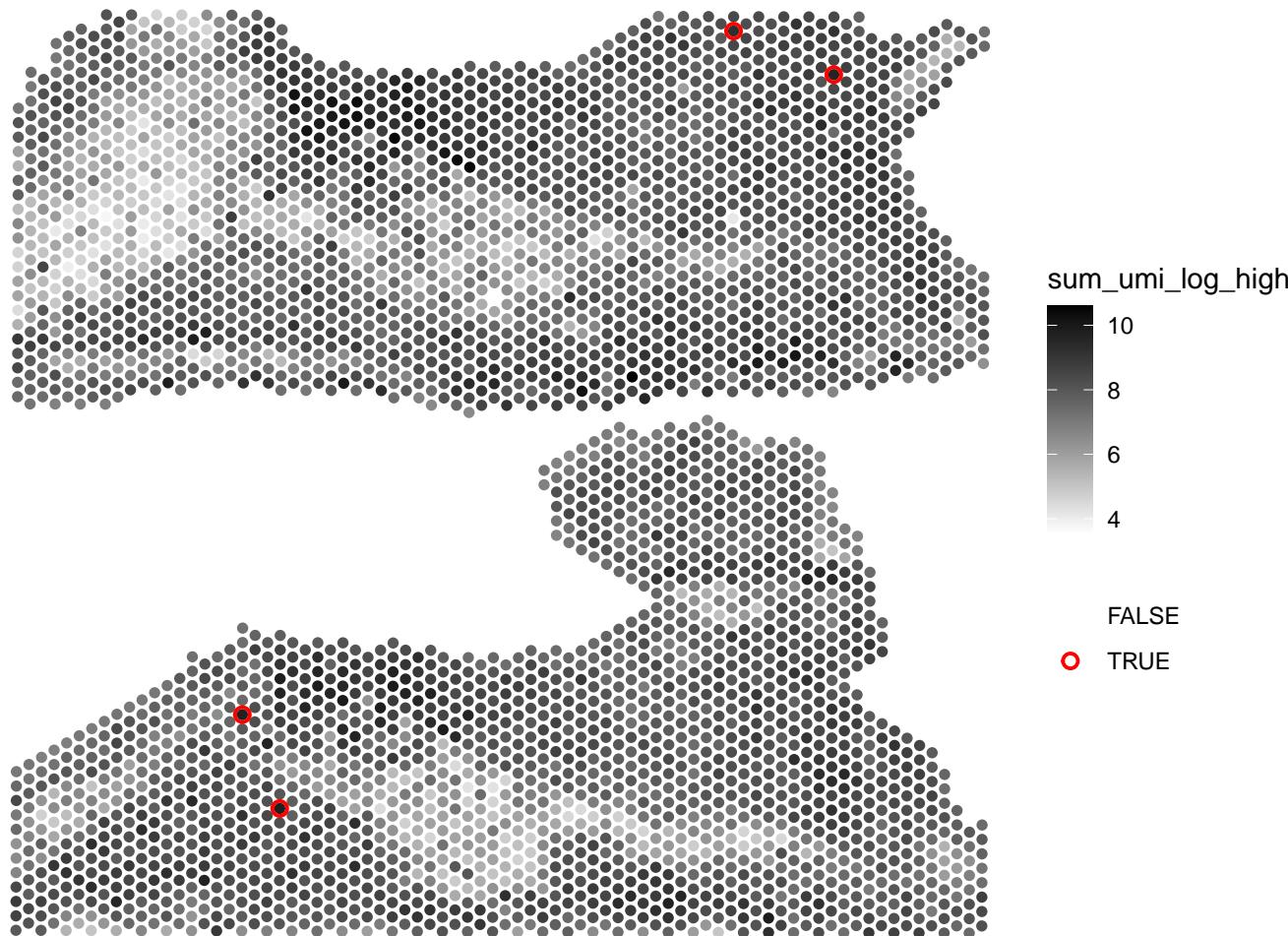
Sample: V13M06–331_D1



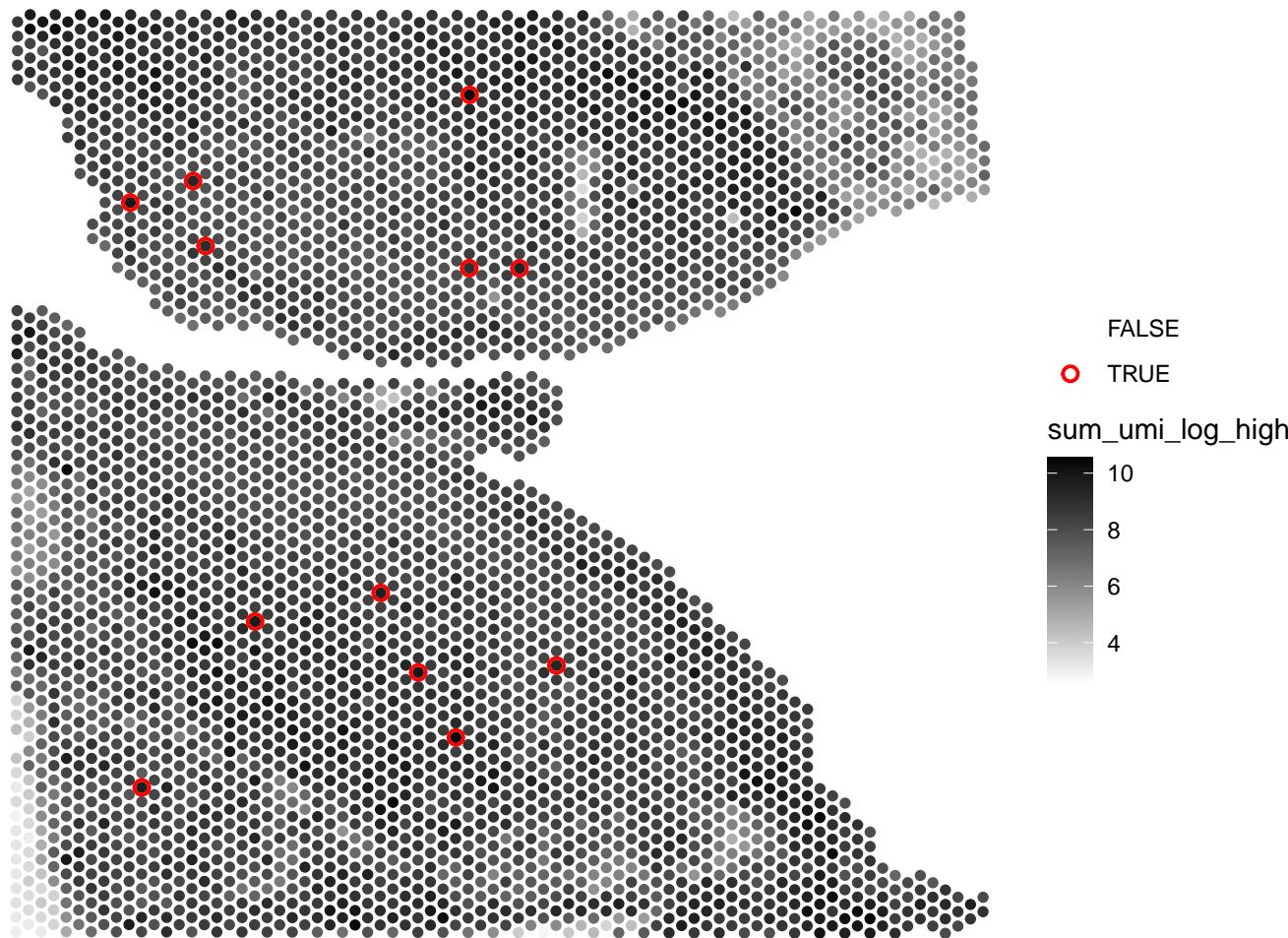
Sample: V13M06–333_A1



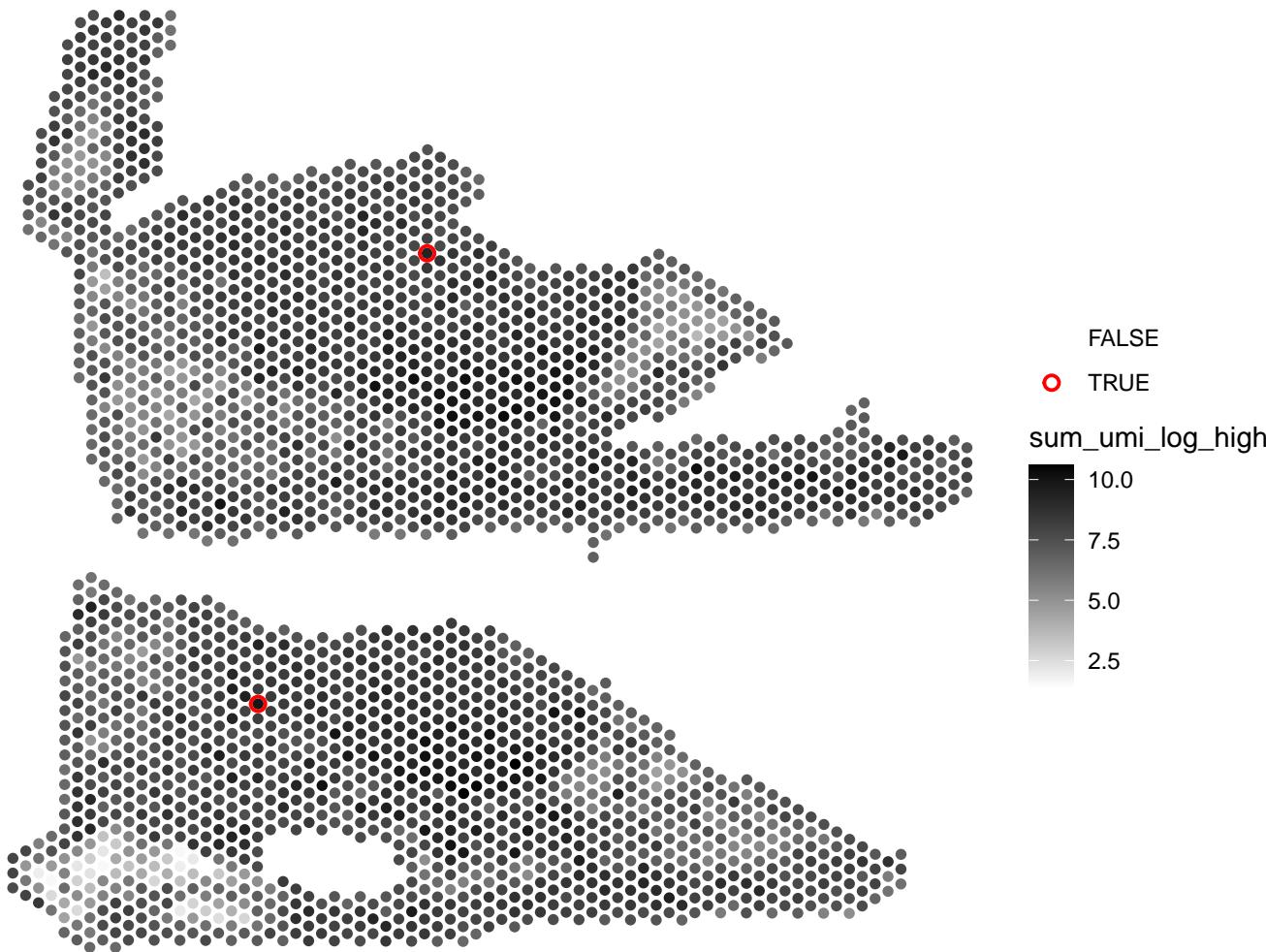
Sample: V13M06–333_B1



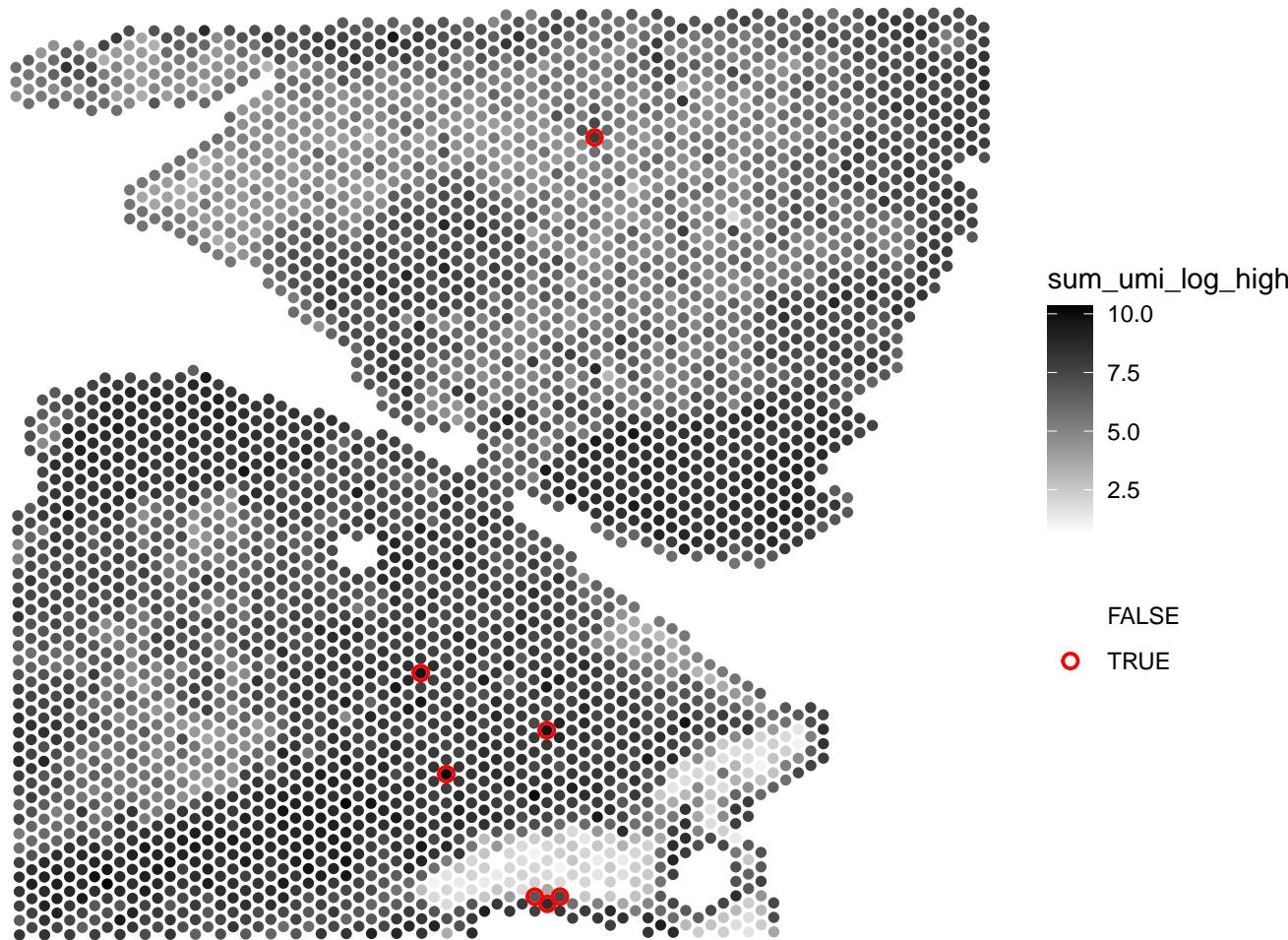
Sample: V13M06–333_C1



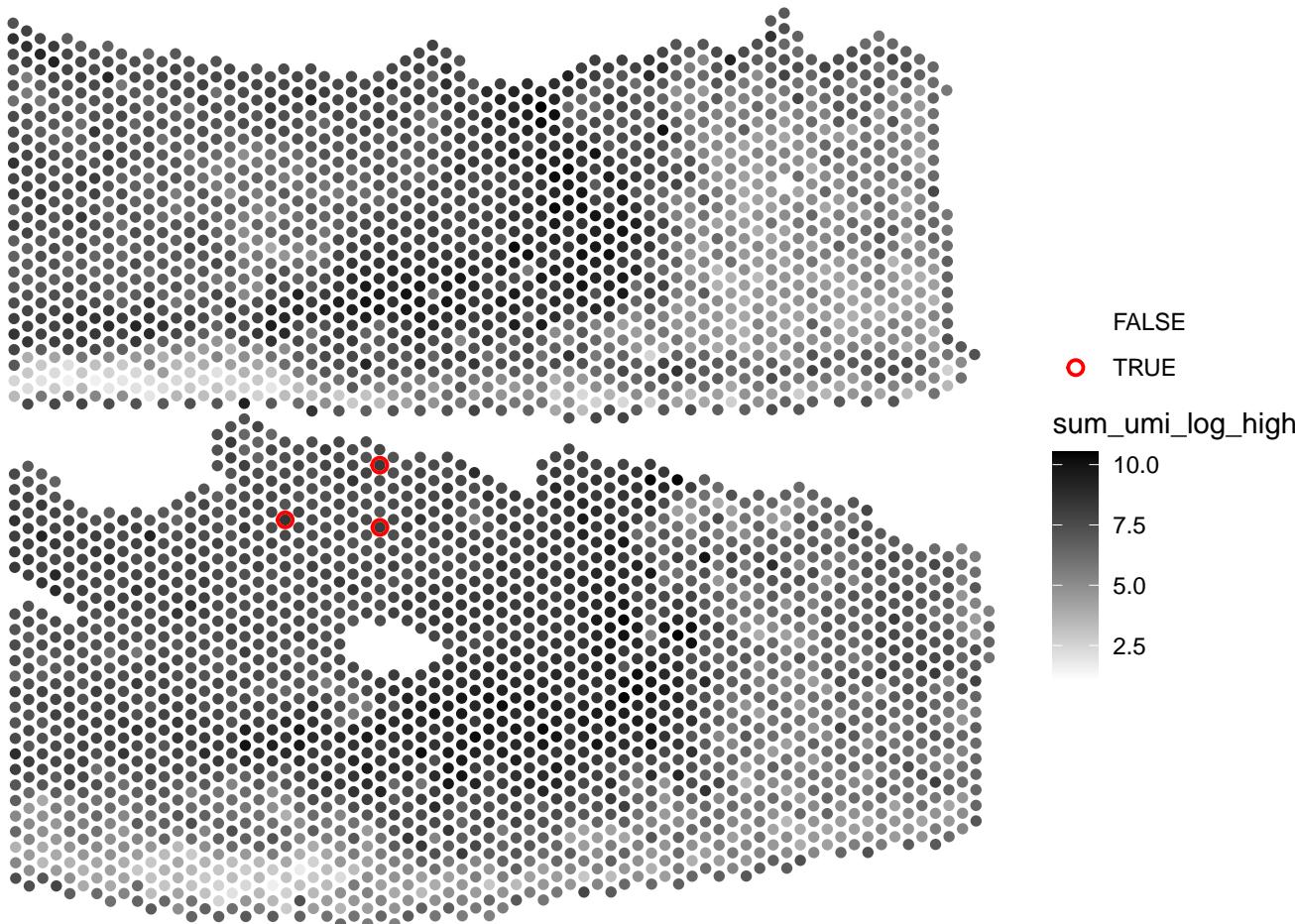
Sample: V13M06–333_D1



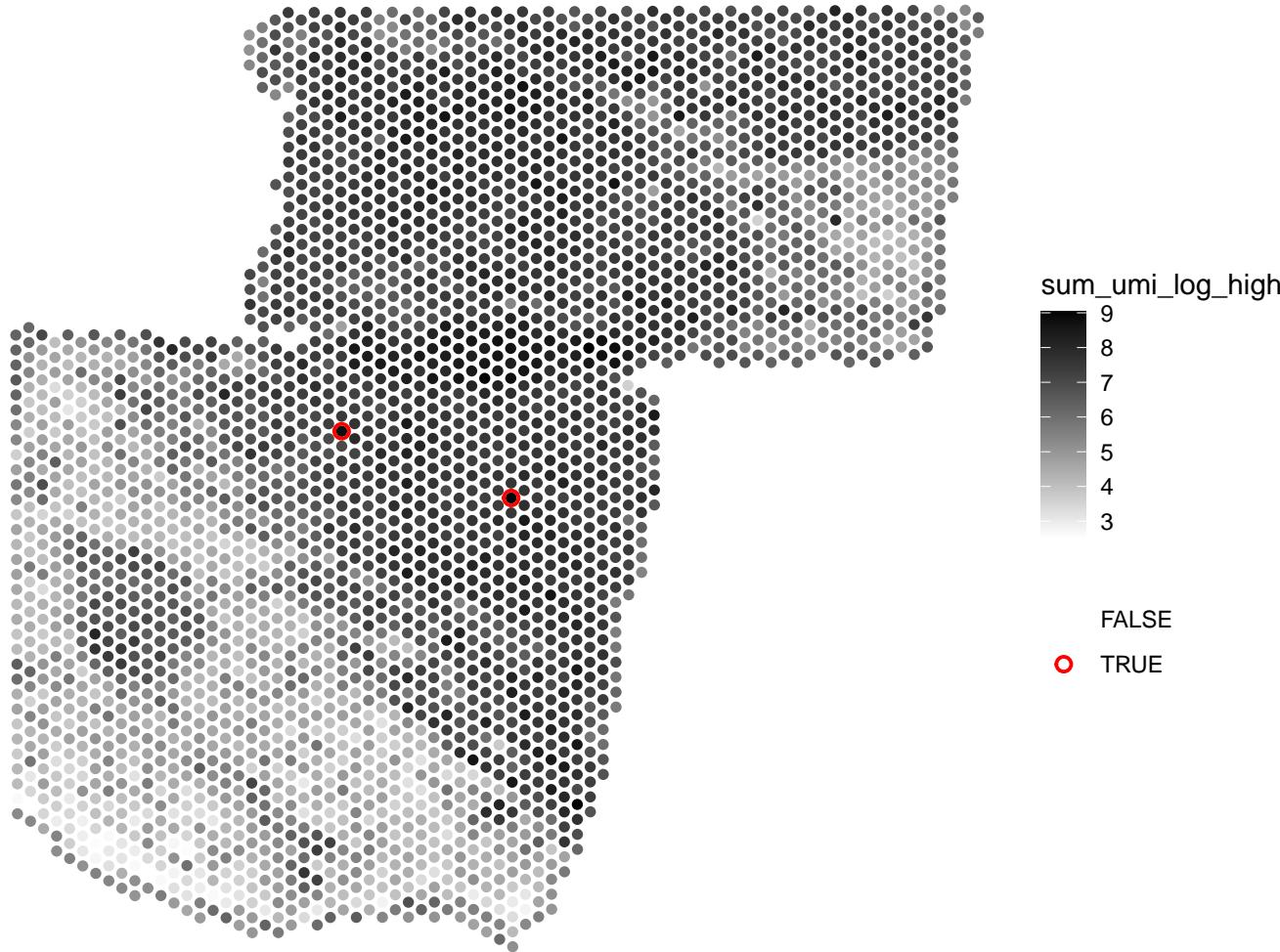
Sample: V13M06–332_A1



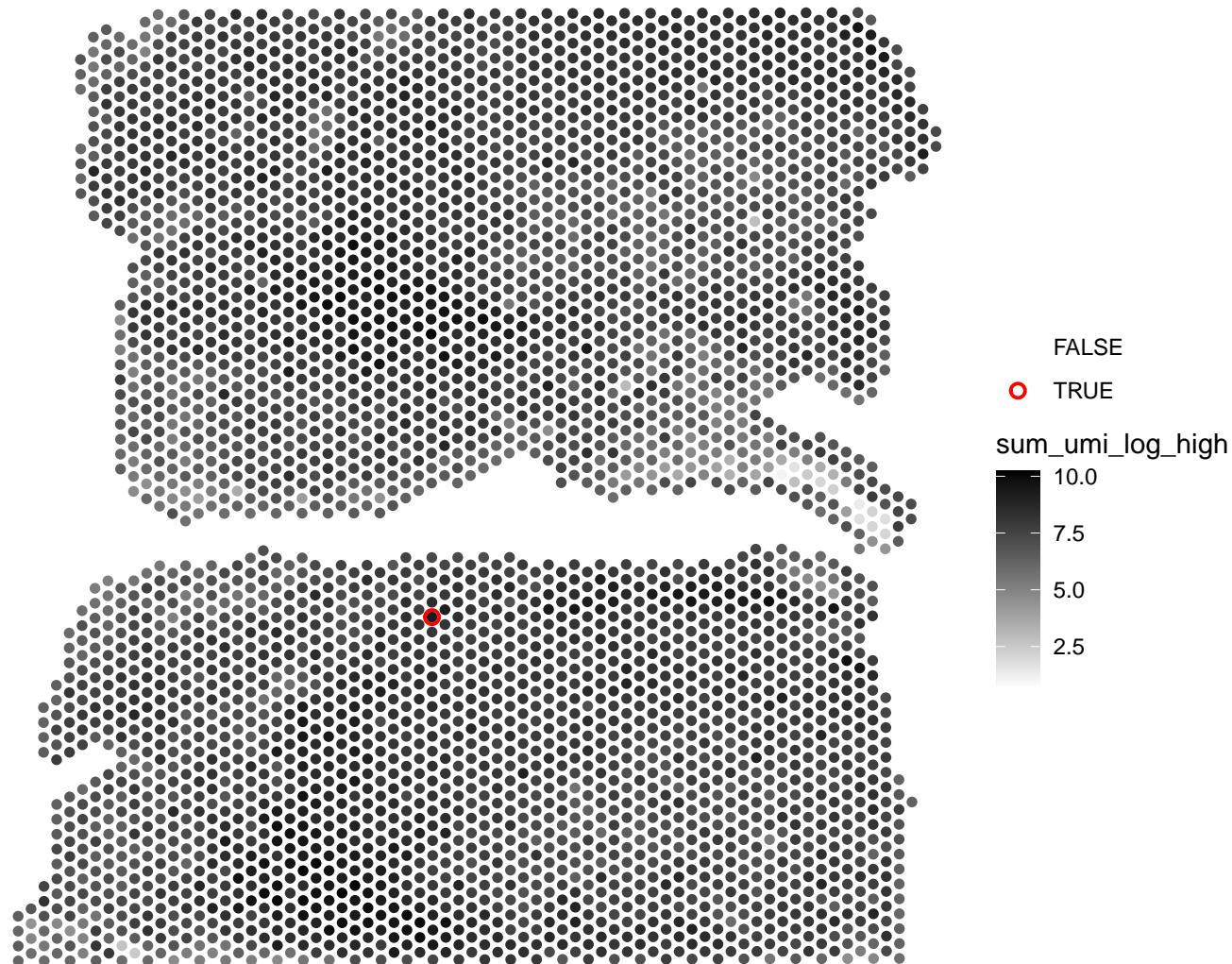
Sample: V13M06–332_B1



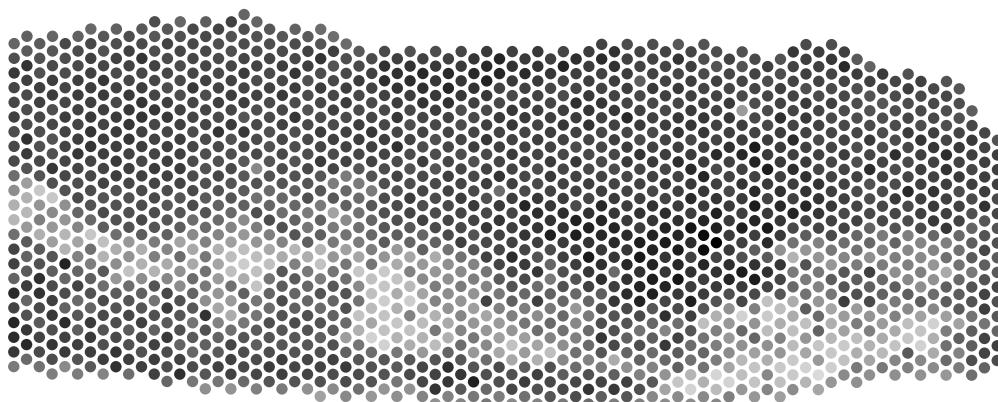
Sample: V13M06–332_C1



Sample: V13M06-332_D1



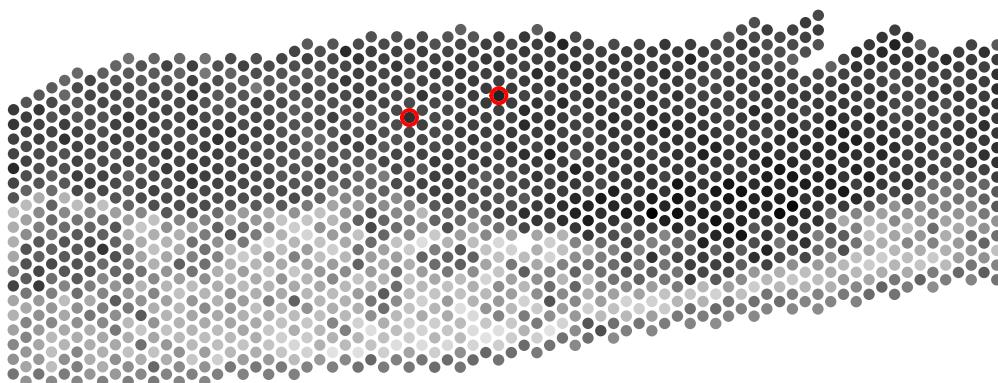
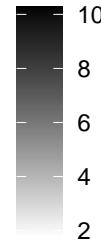
Sample: V13M06–404_A1



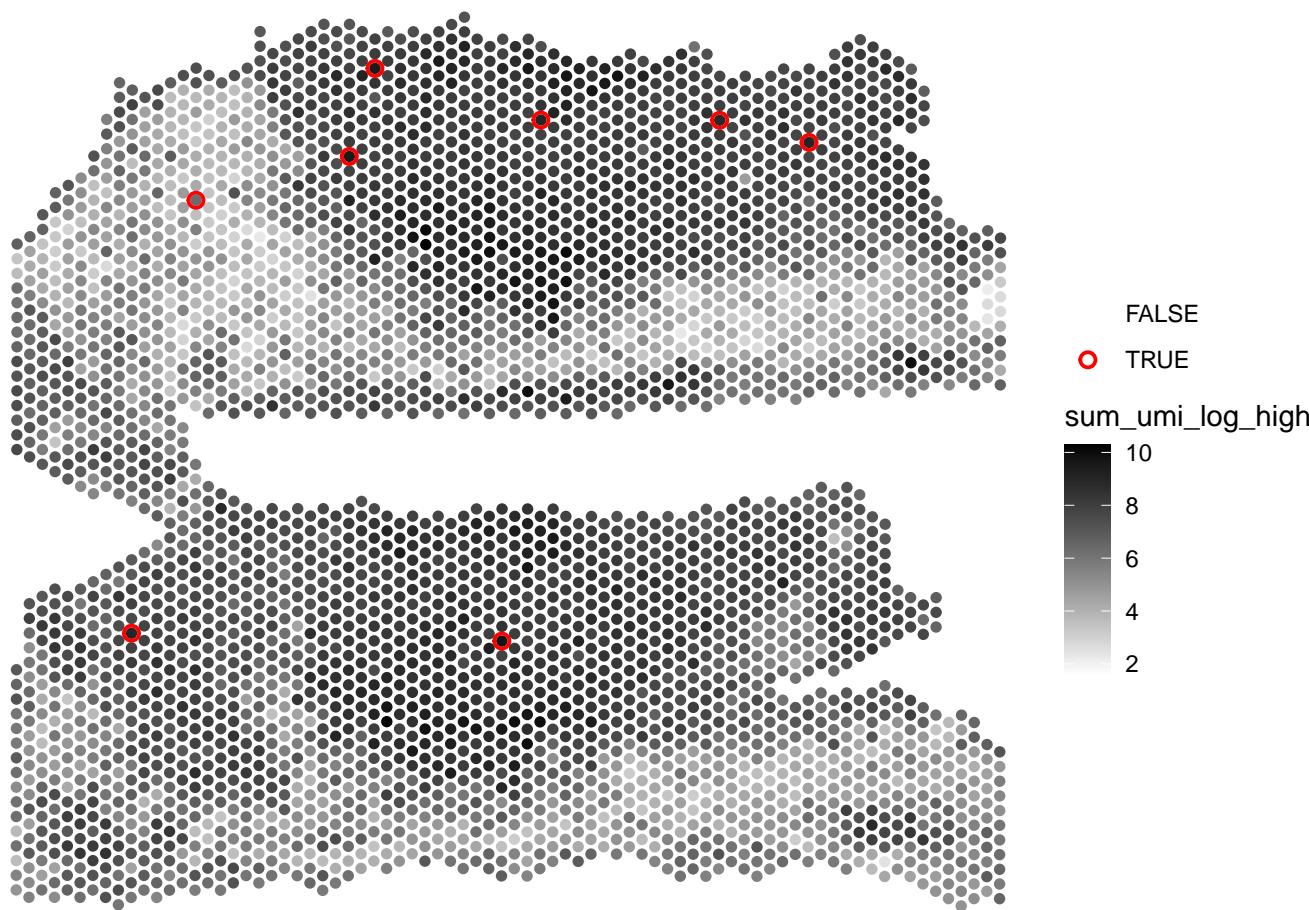
FALSE

TRUE

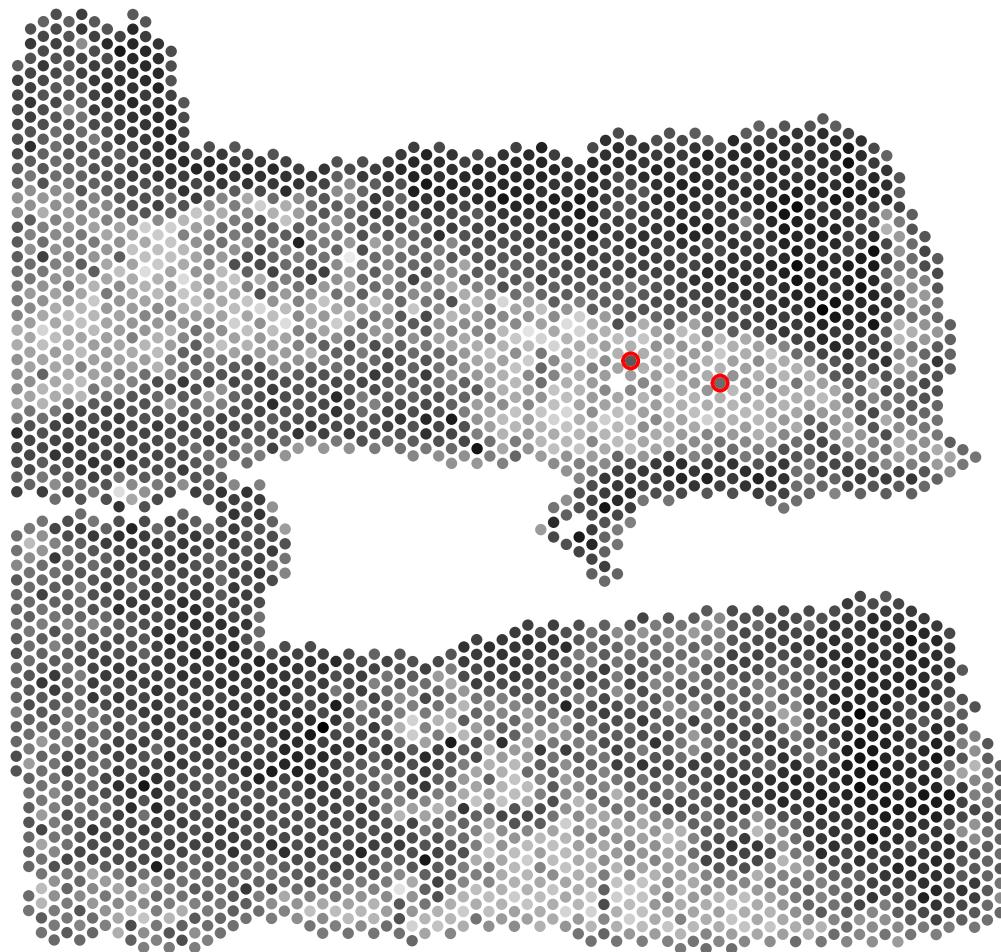
sum_umi_log_high



Sample: V13M06–404_B1



Sample: V13M06–404_C1



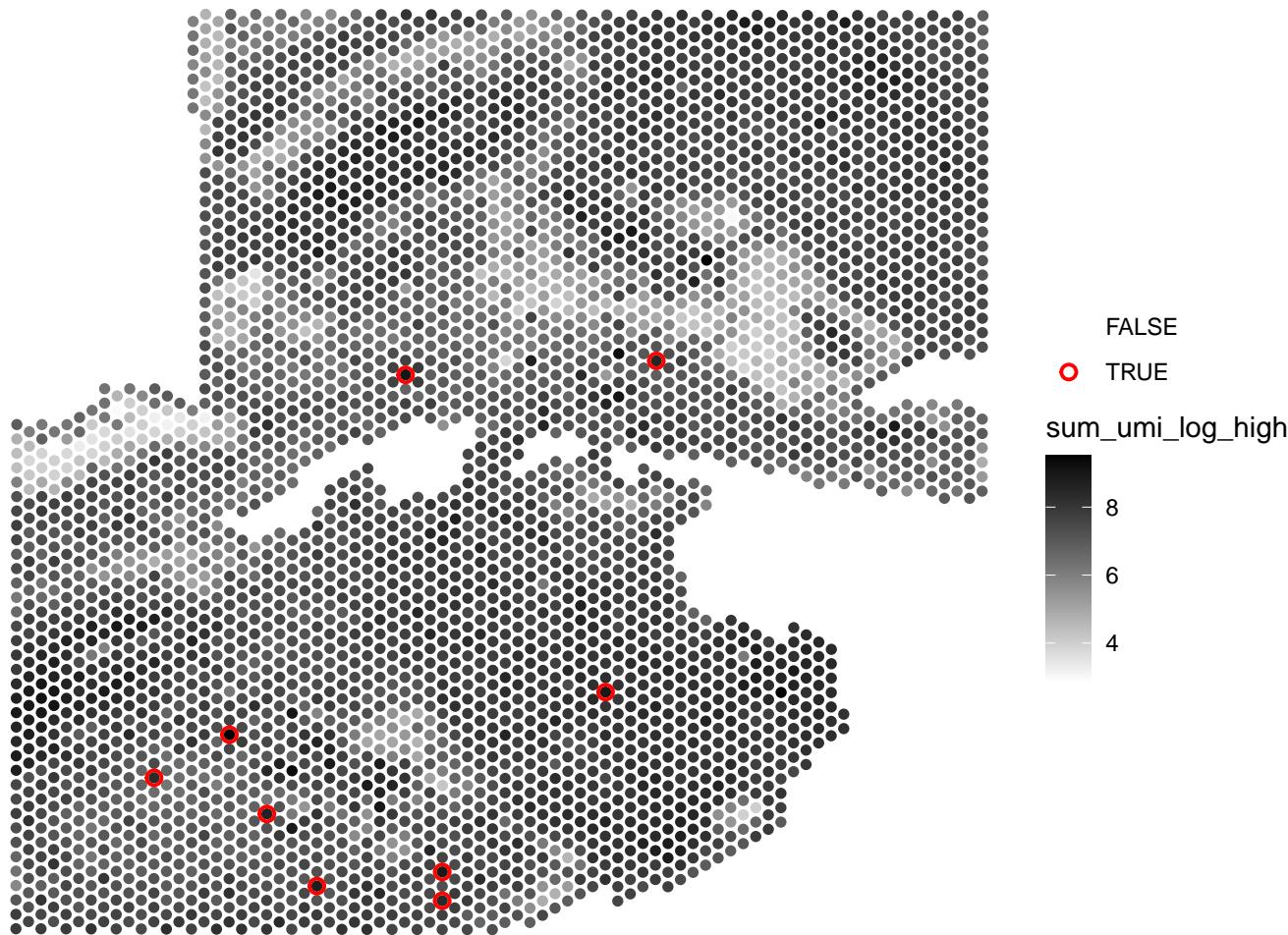
sum_umi_log_high



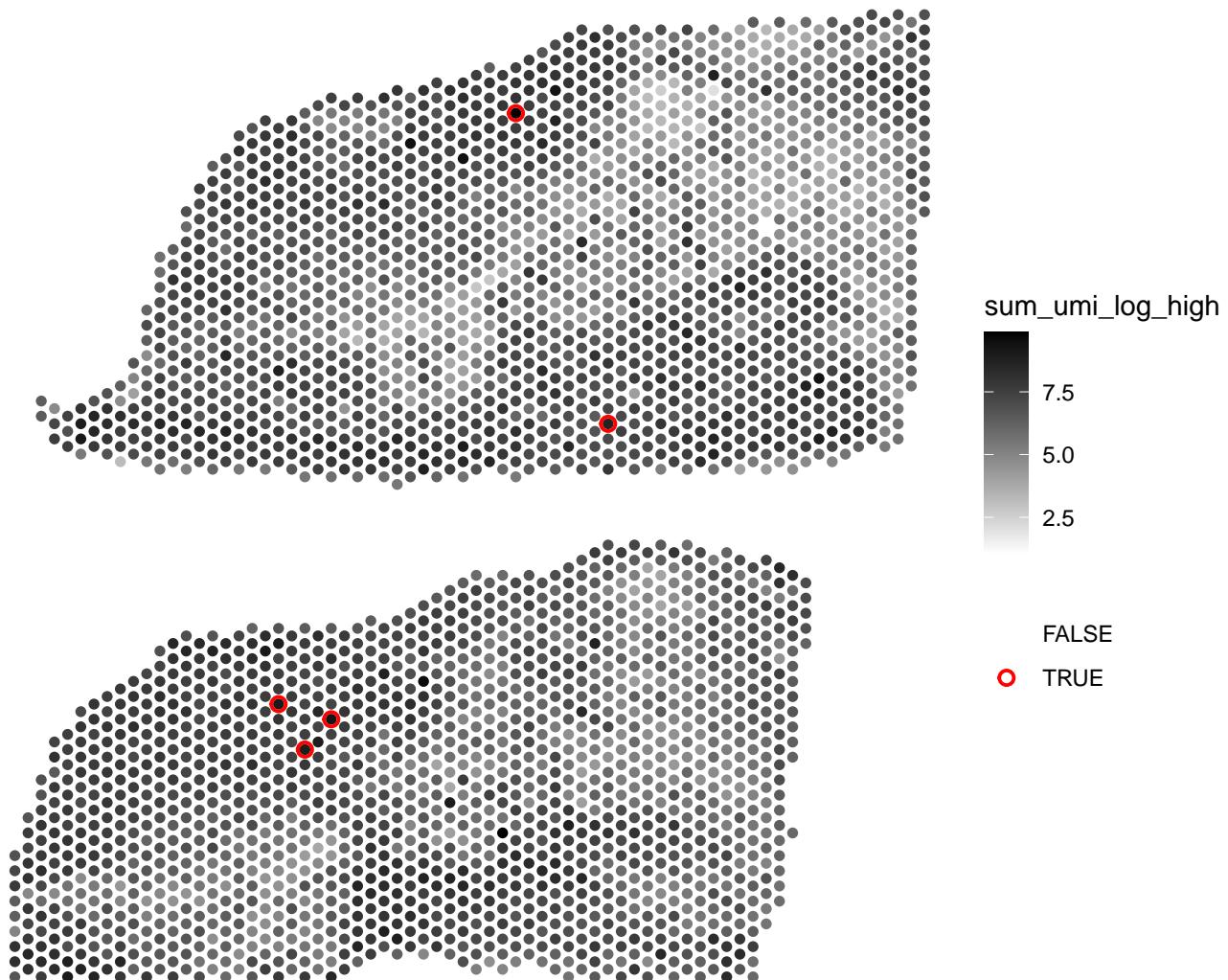
FALSE

TRUE

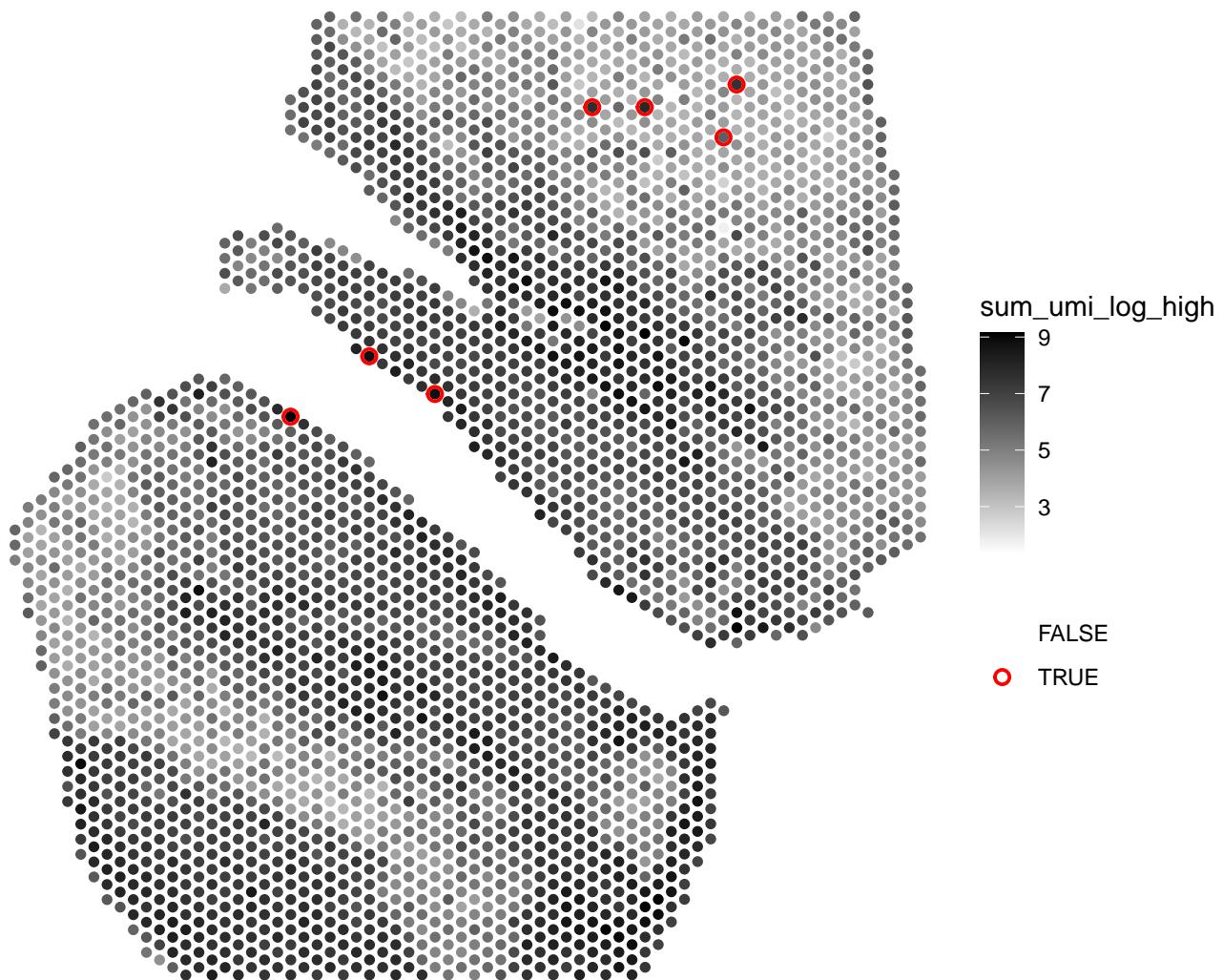
Sample: V13M06–404_D1



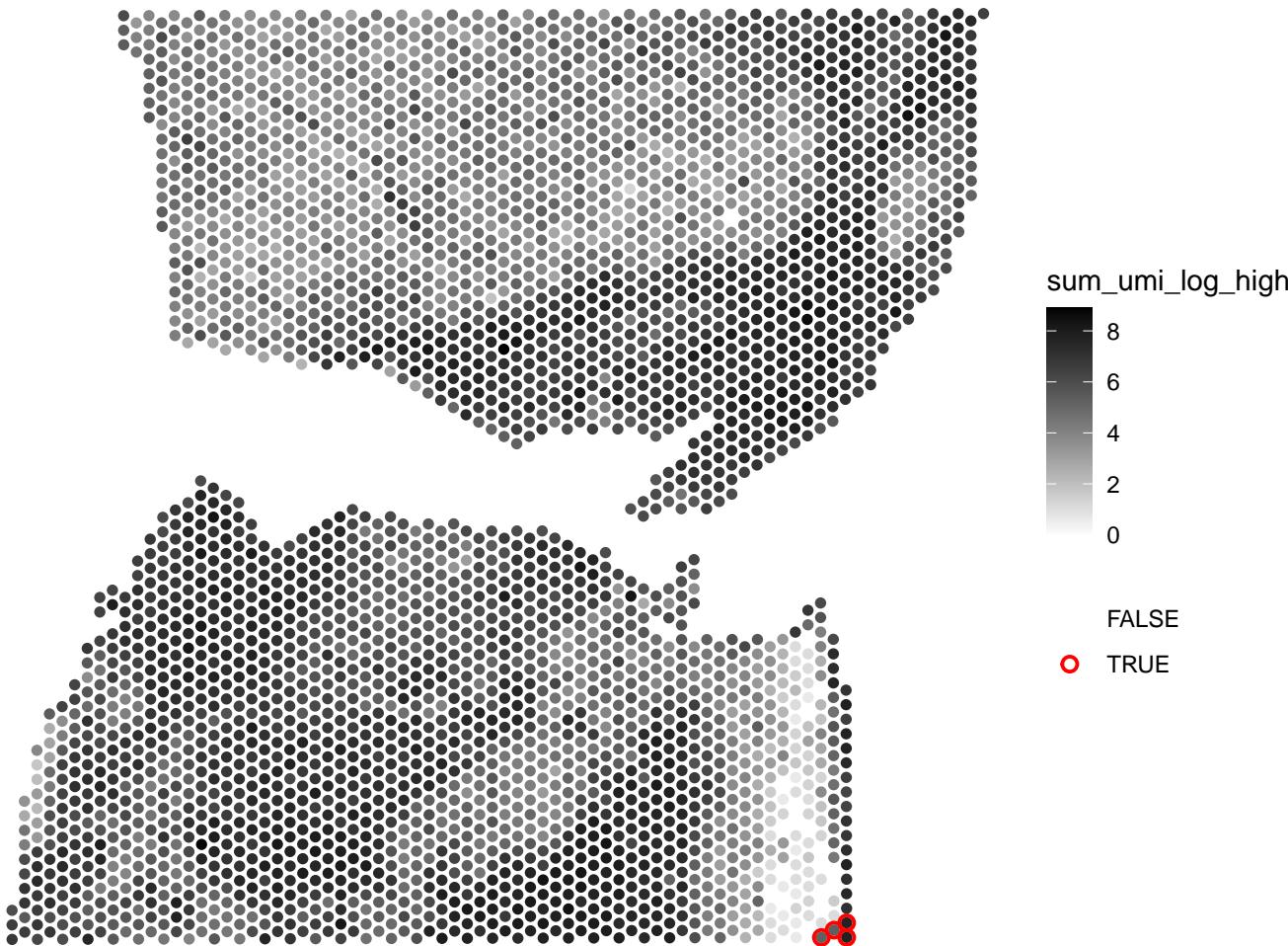
Sample: V13M06–403_A1



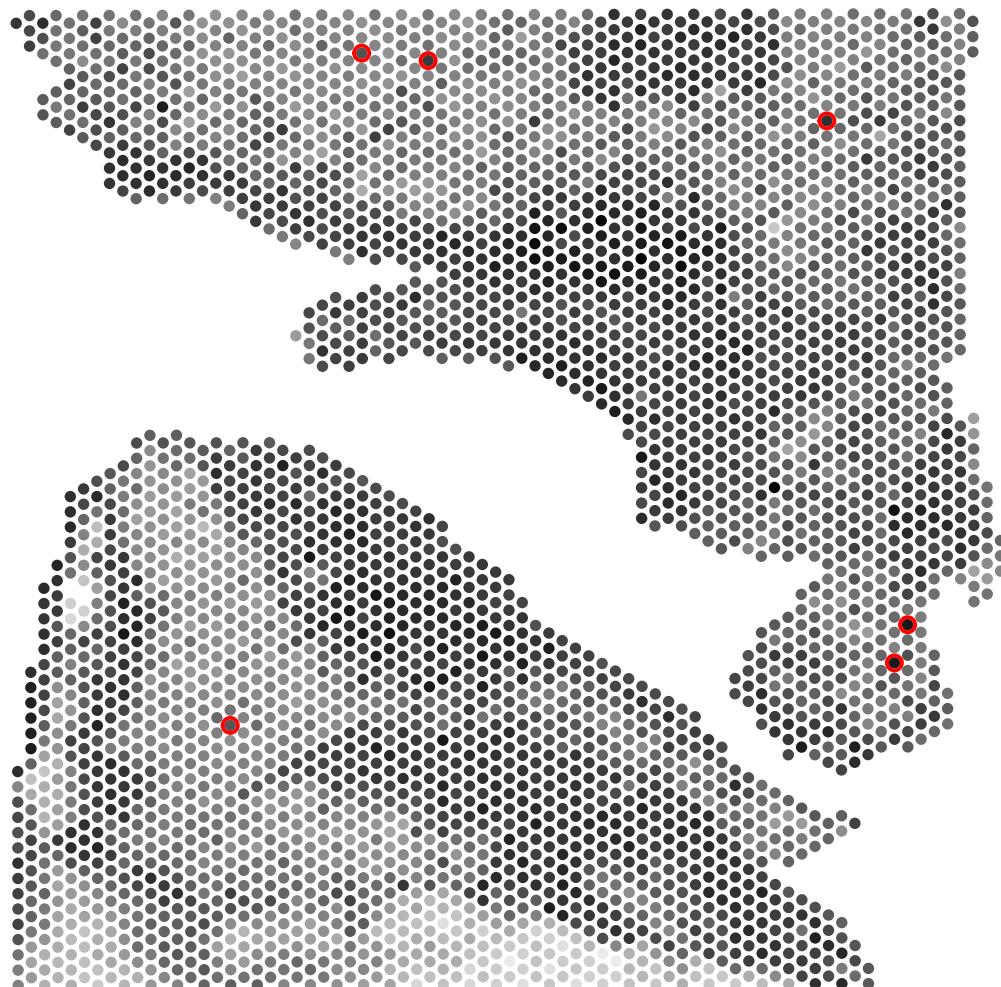
Sample: V13M06–403_B1



Sample: V13M06-403_C1



Sample: V13M06–403_D1



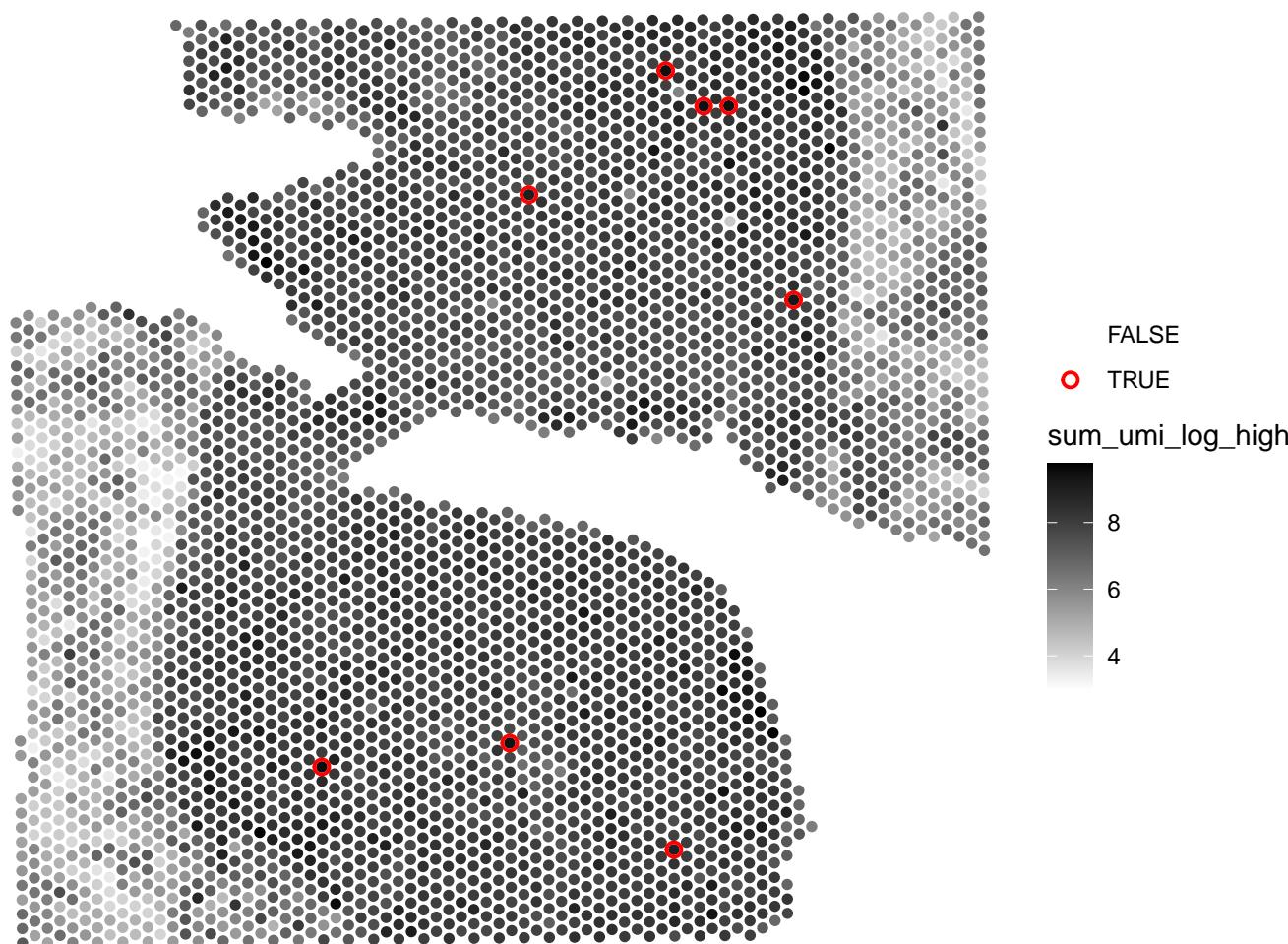
FALSE

TRUE

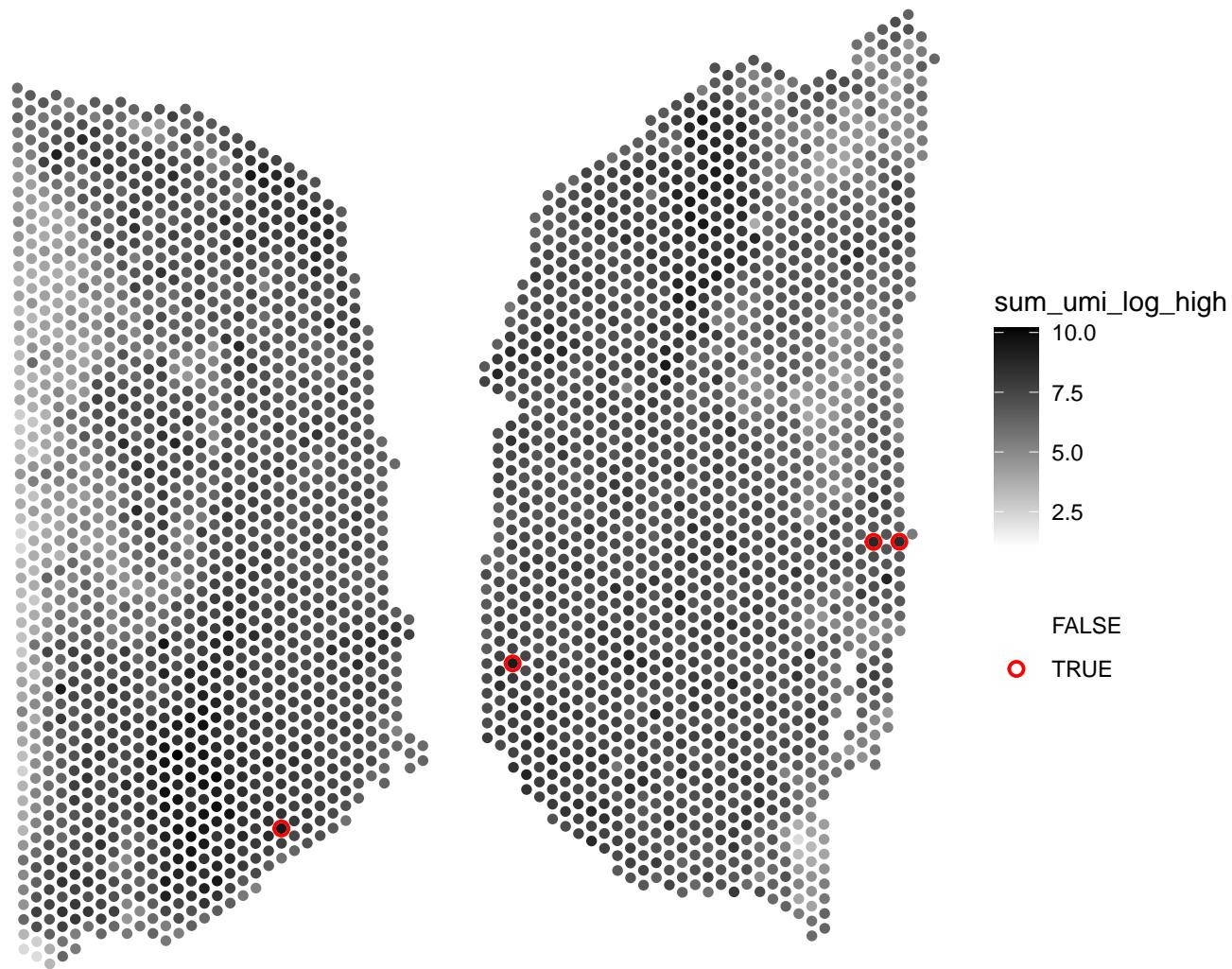
sum_umi_log_high



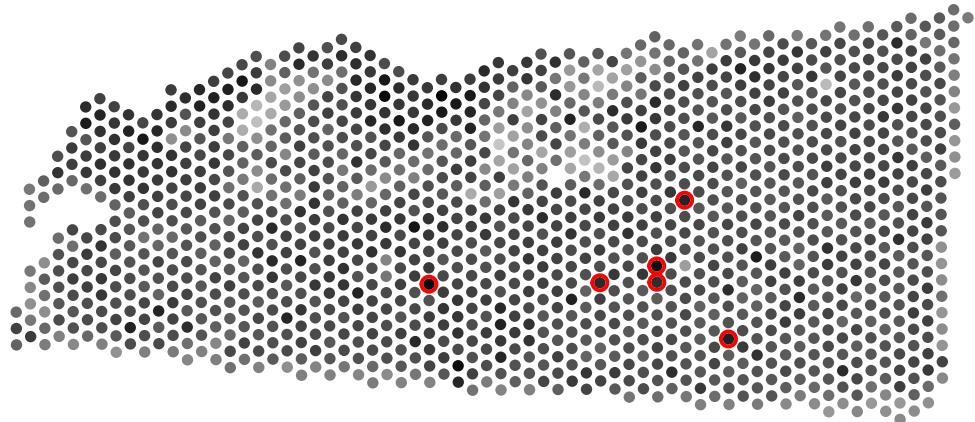
Sample: V13M06–402_A1



Sample: V13M06-402_B1



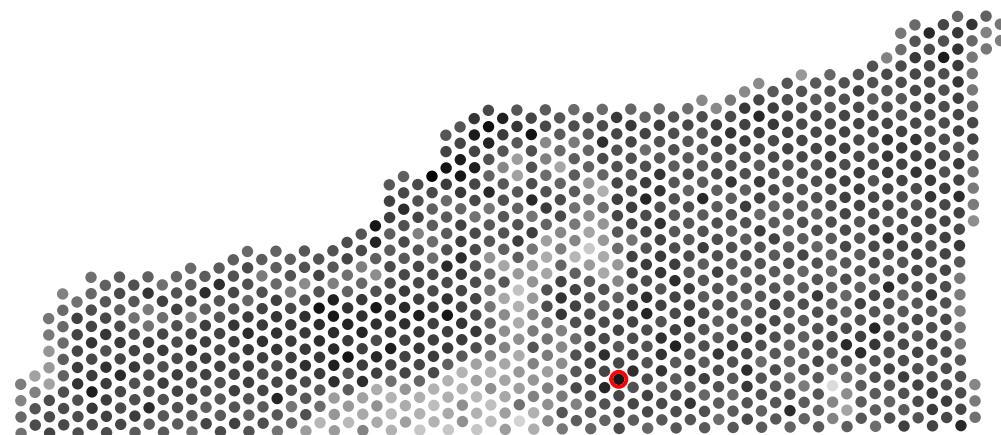
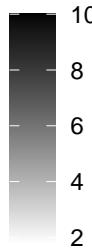
Sample: V13M06–402_C1



FALSE

TRUE

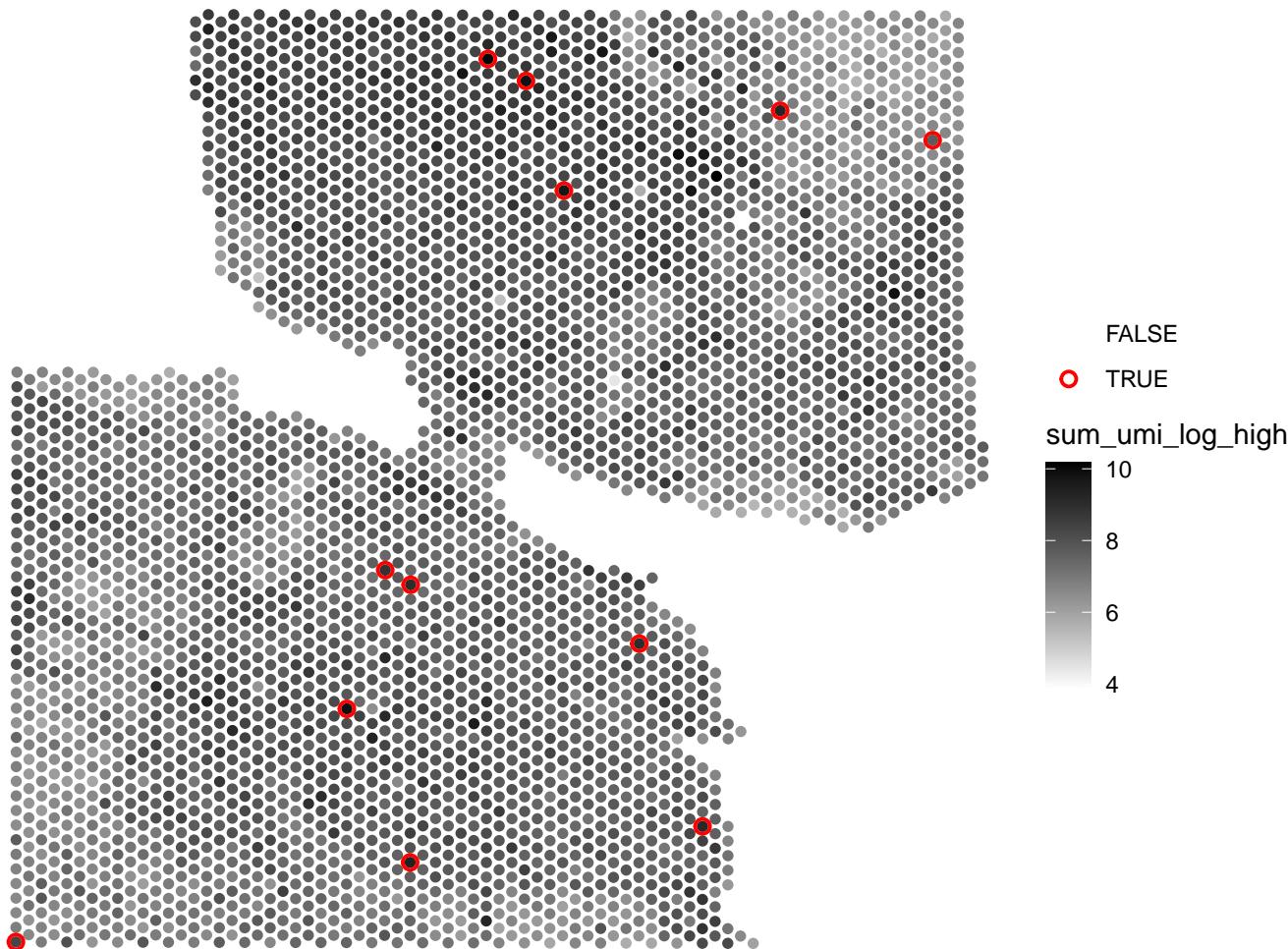
sum_umi_log_high



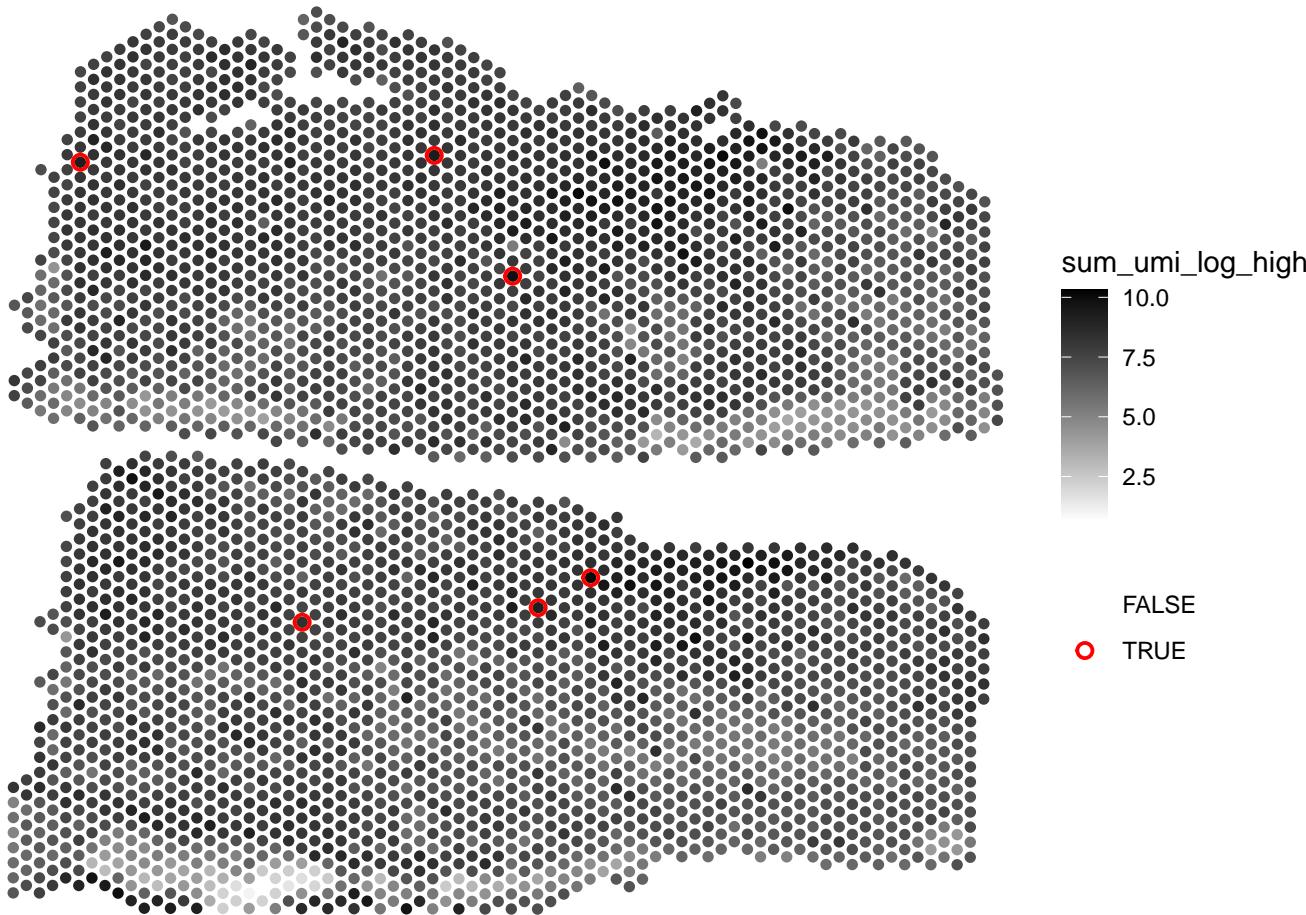
Sample: V13M06–402_D1



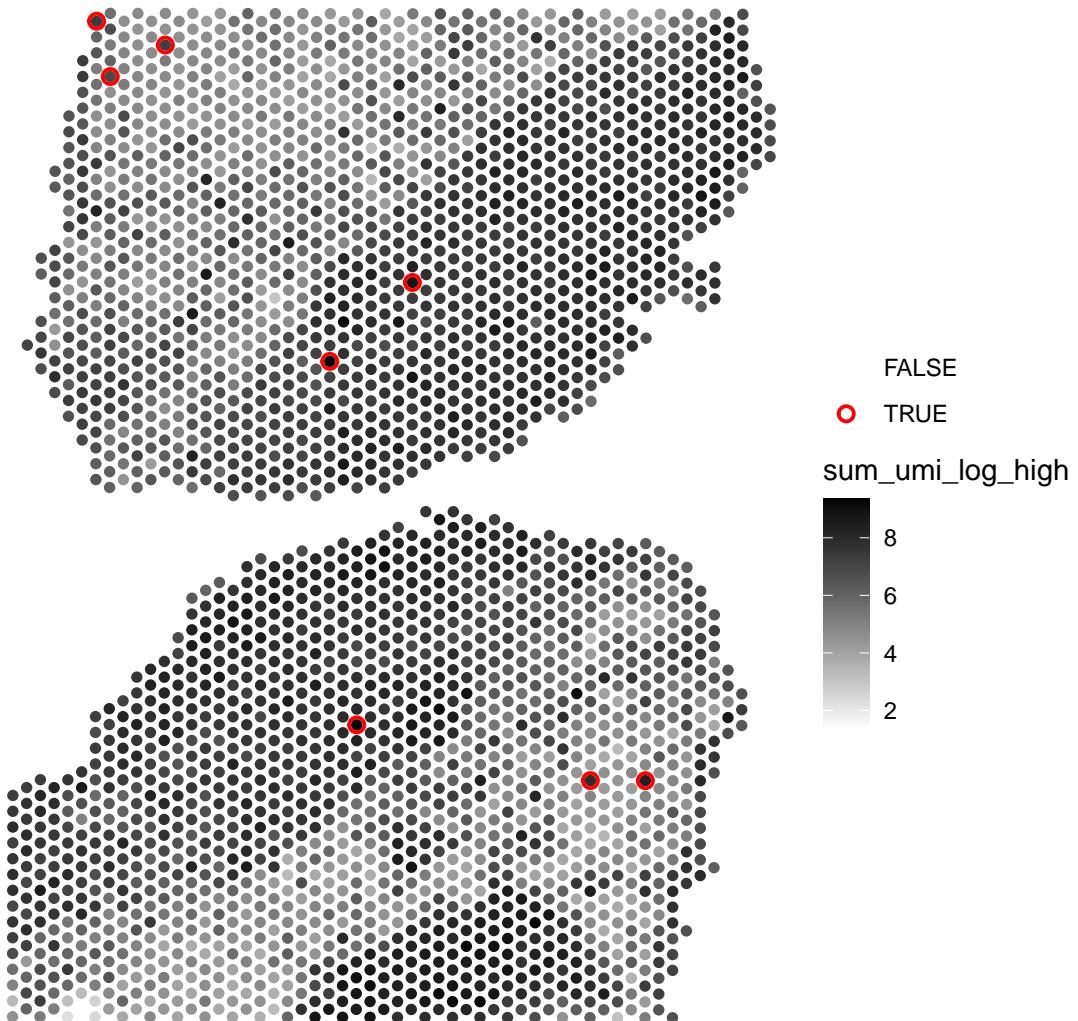
Sample: V13M06–401_A1



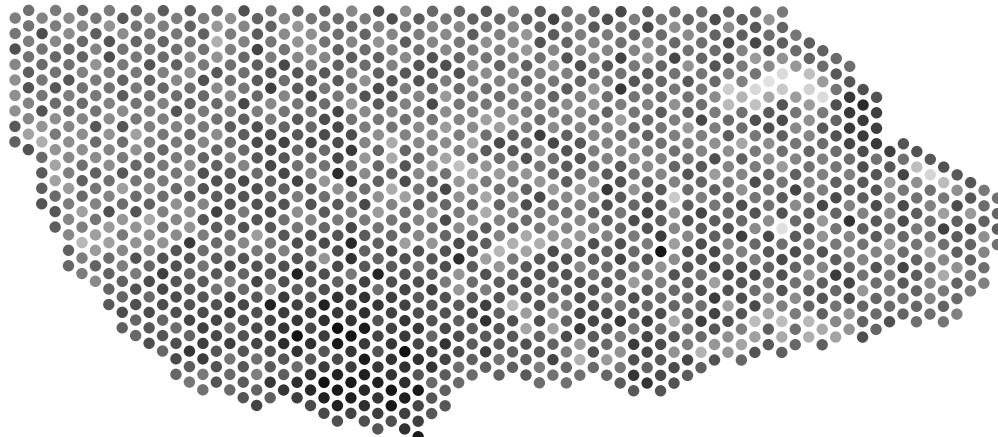
Sample: V13M06–401_B1



Sample: V13M06–401_C1

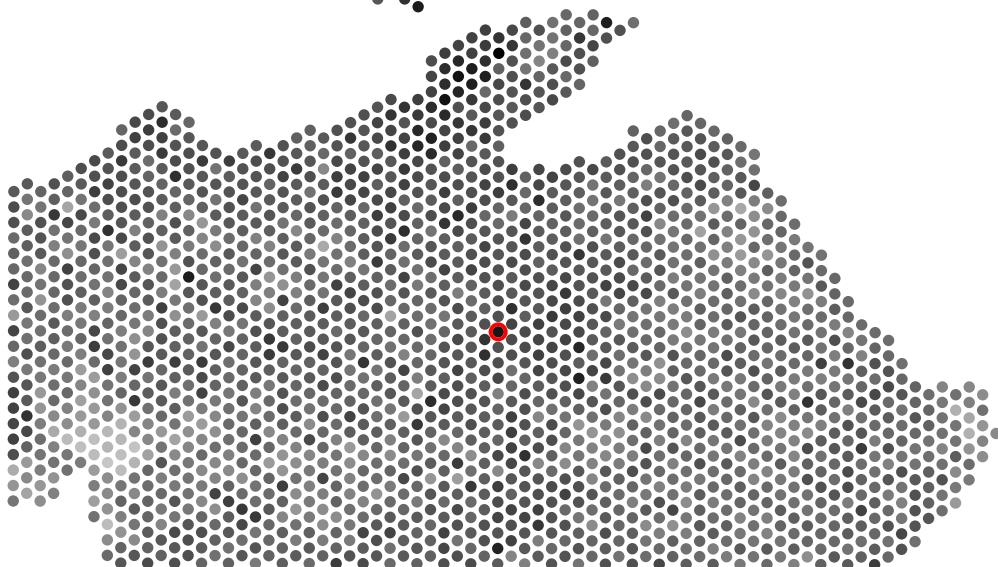


Sample: V13M06-401_D1



FALSE

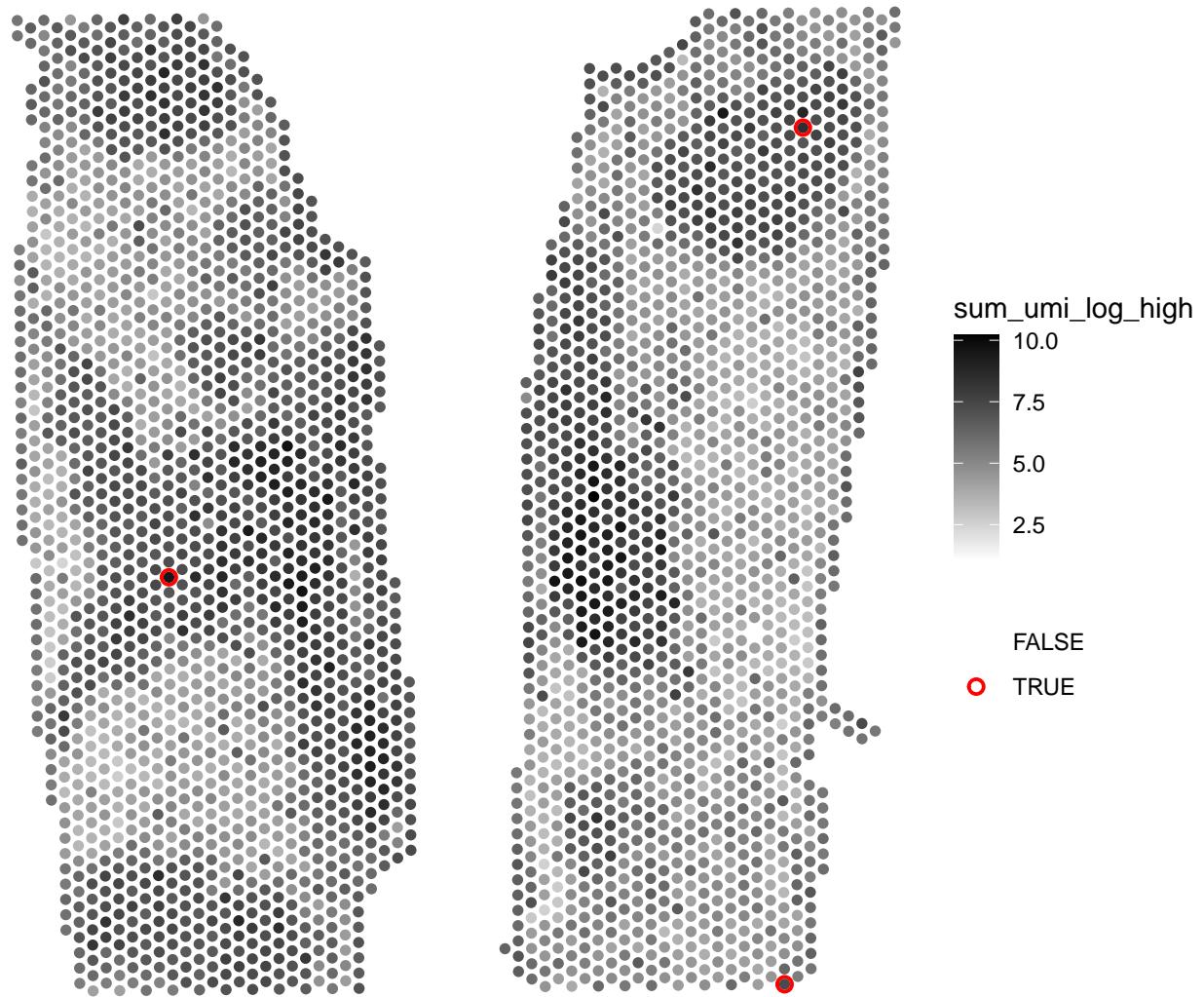
TRUE



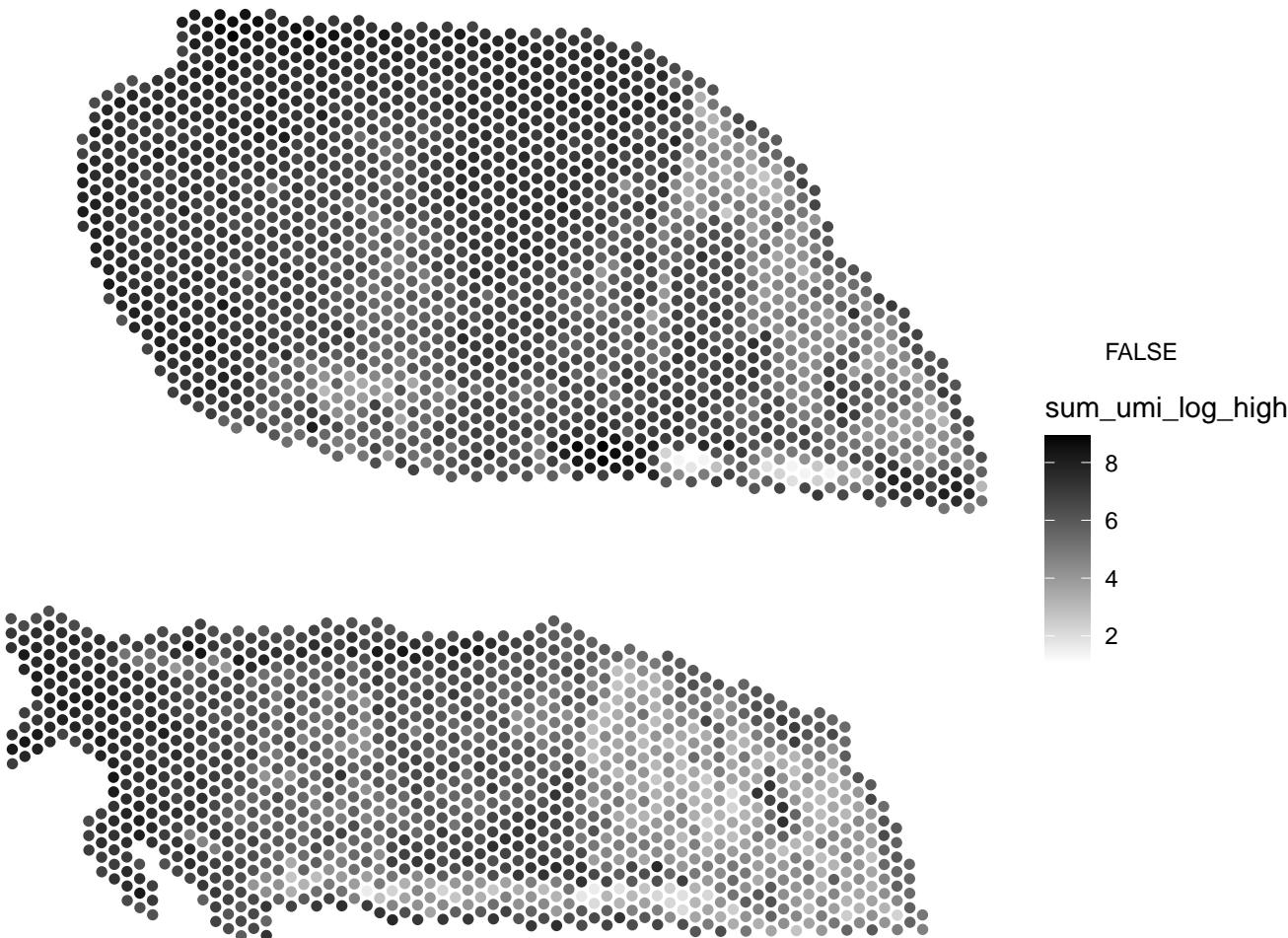
sum_umi_log_high



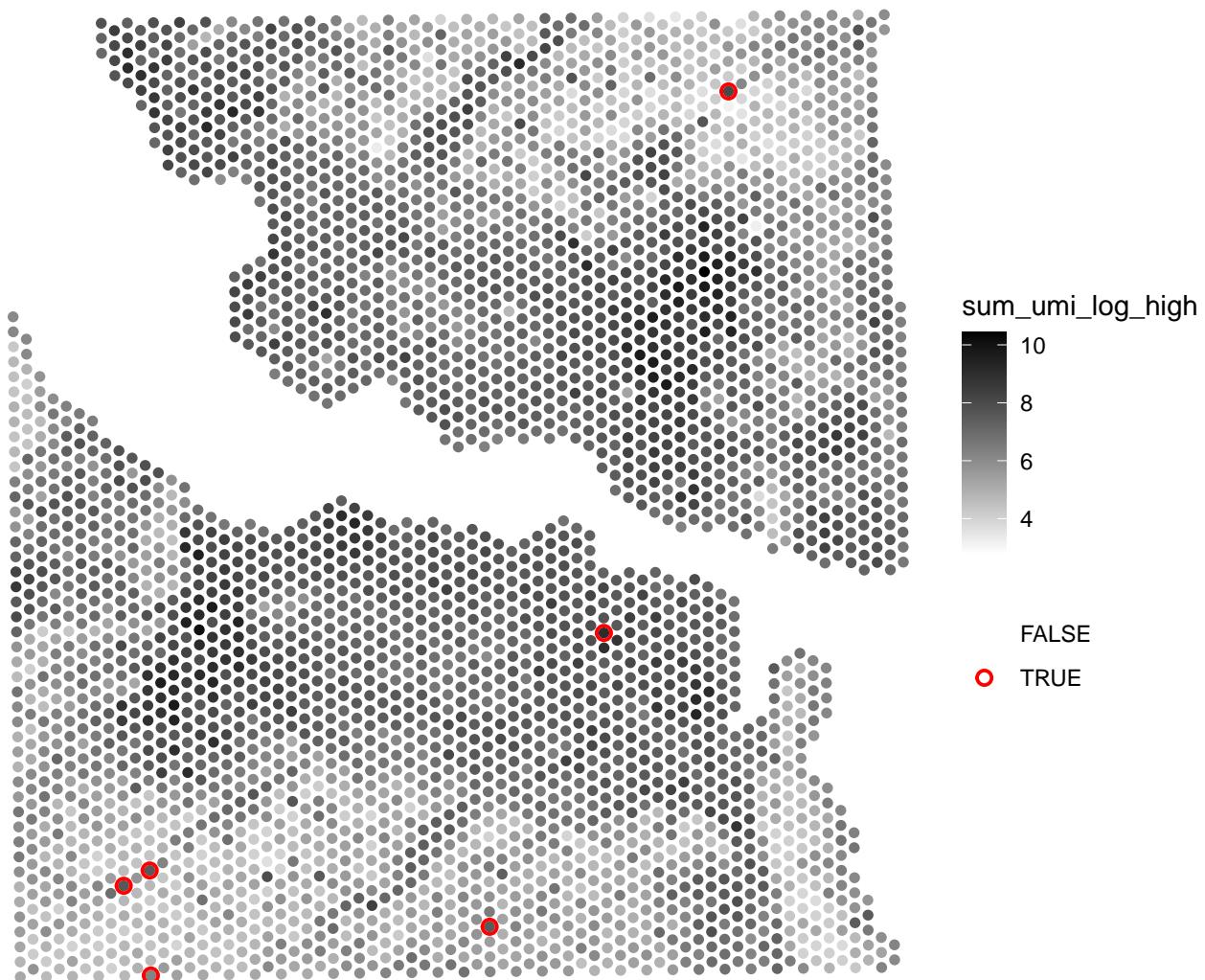
Sample: V13M06-386_A1



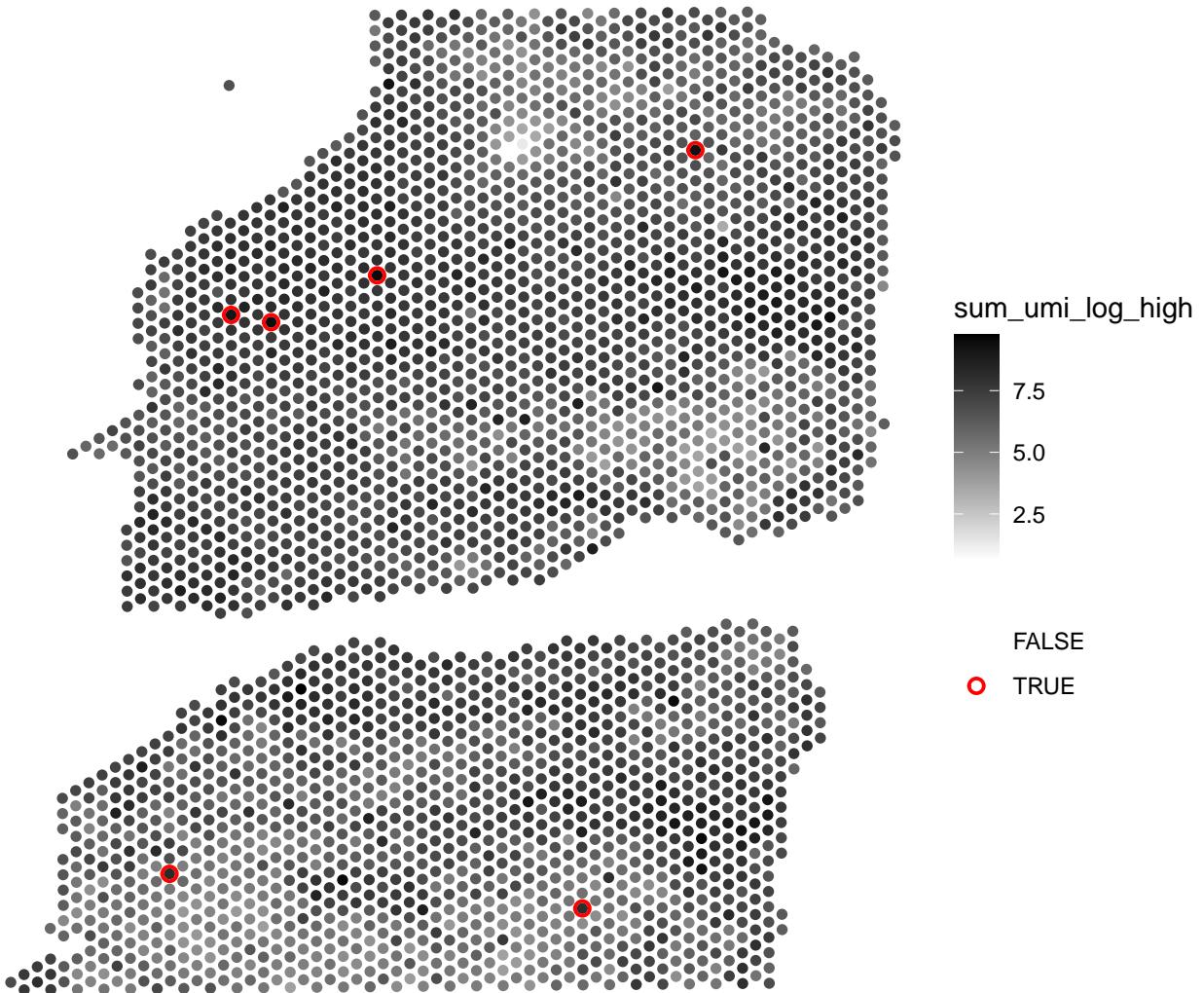
Sample: V13M06–386_B1



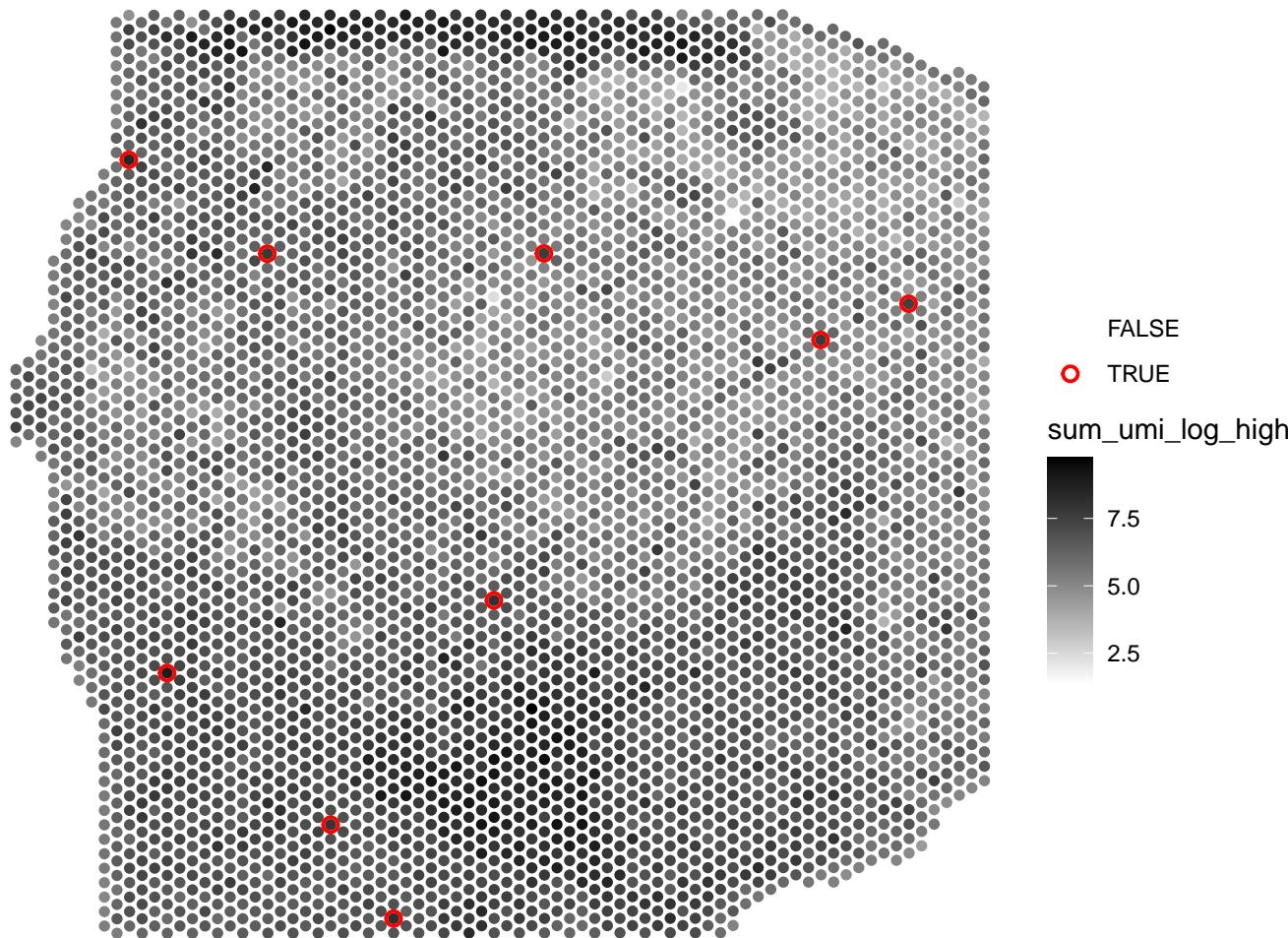
Sample: V13M06-386_C1



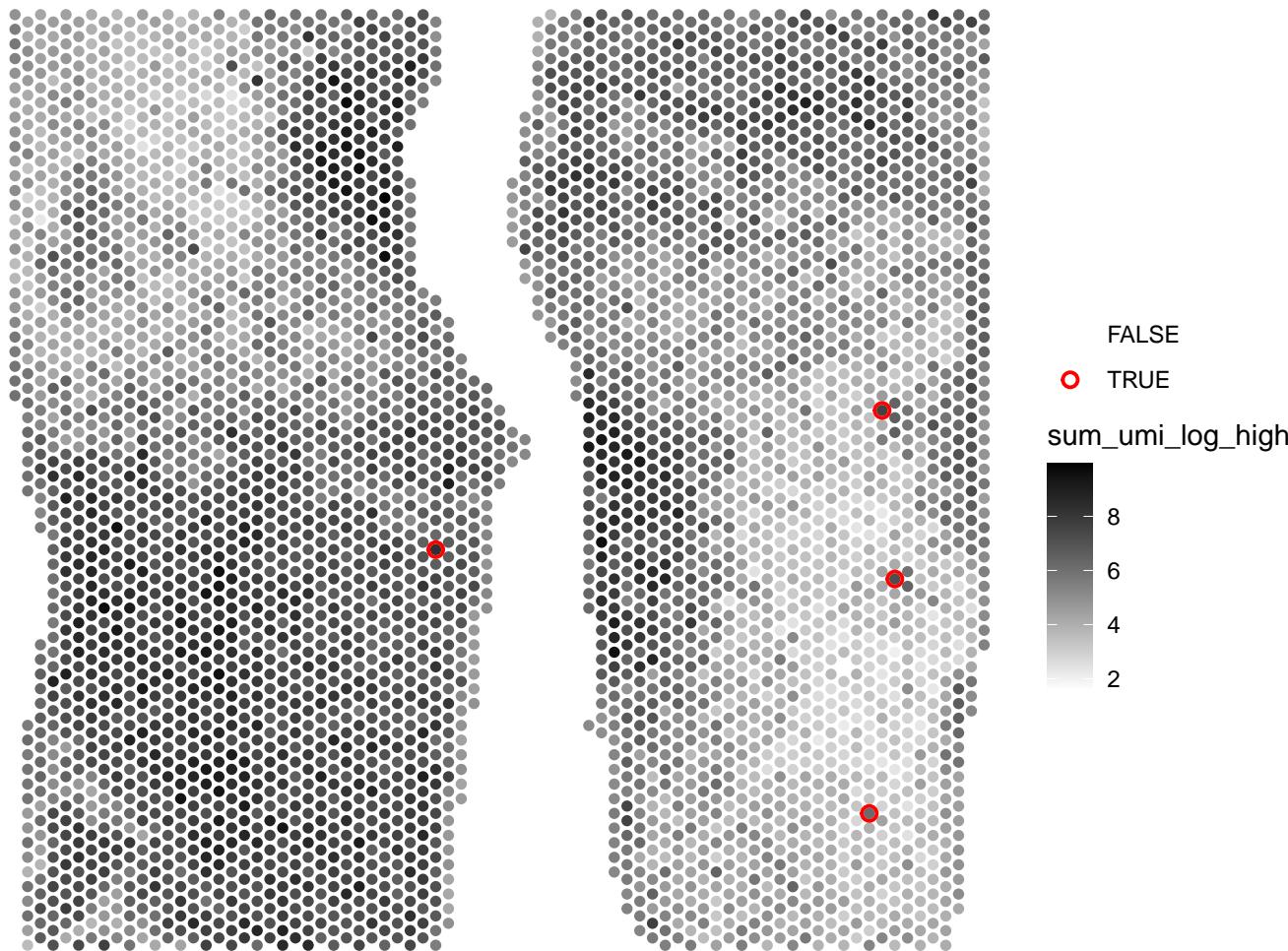
Sample: V13M06-386_D1



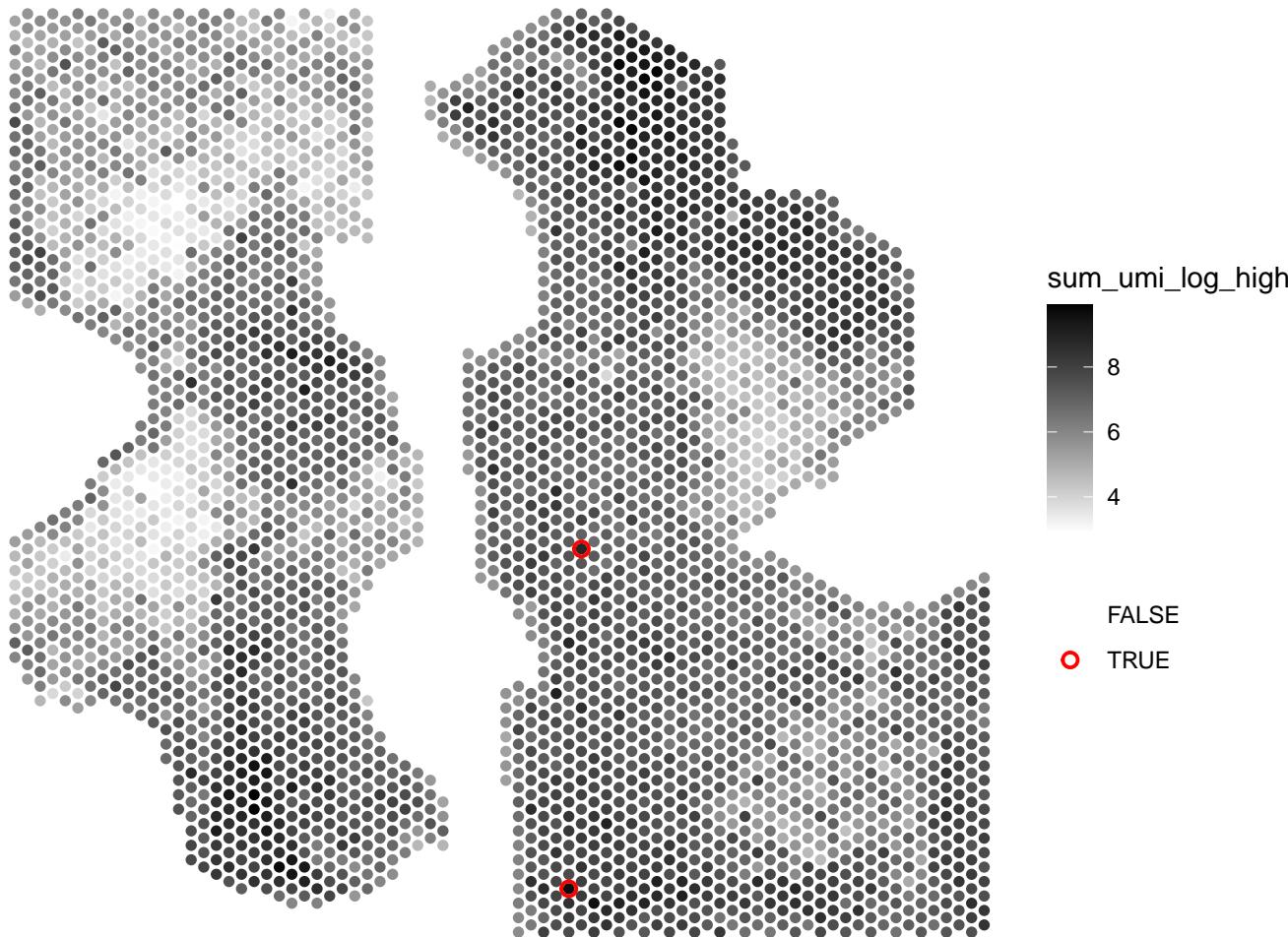
Sample: V13B23–366_A1



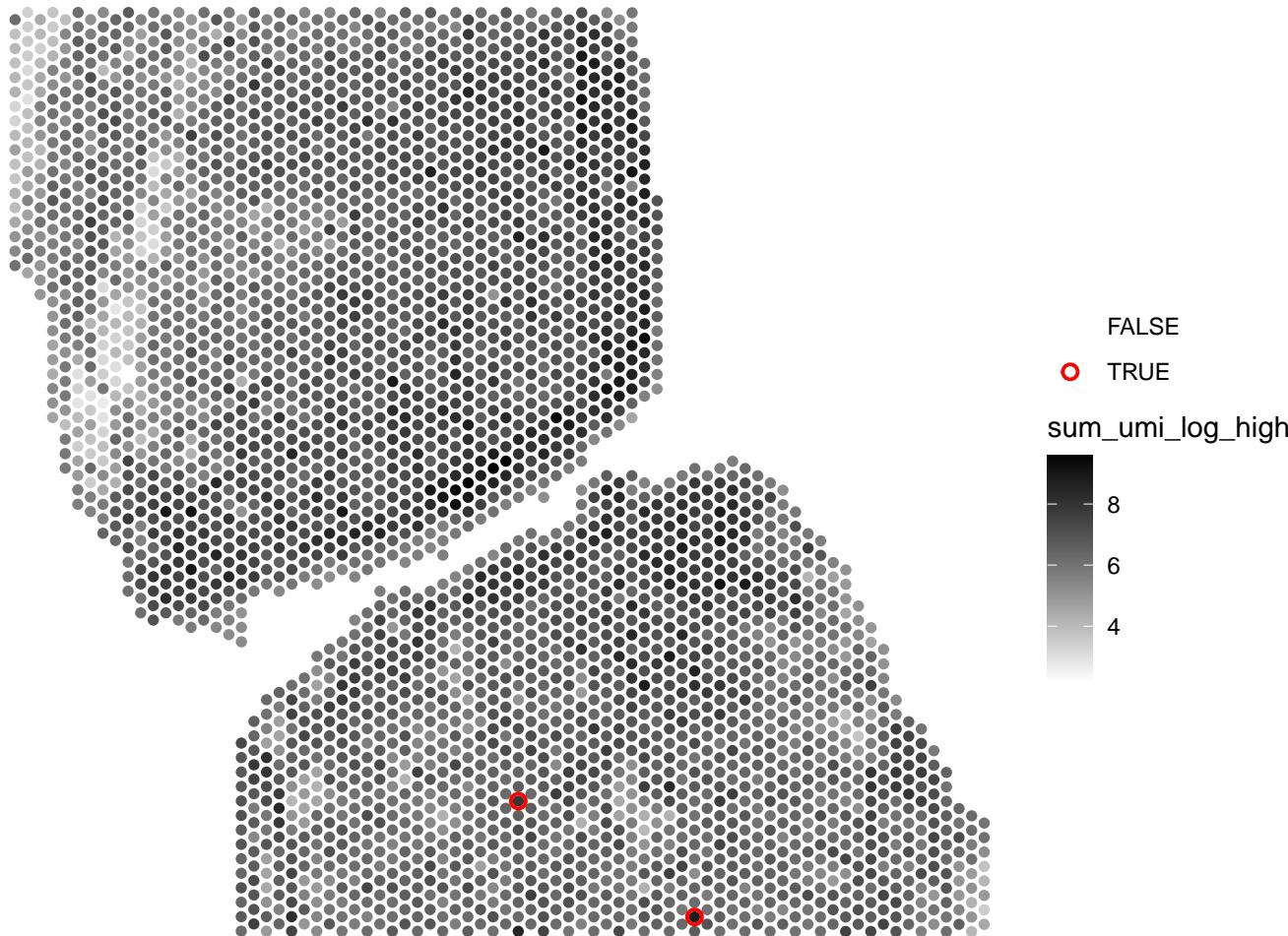
Sample: V13B23–284_A1



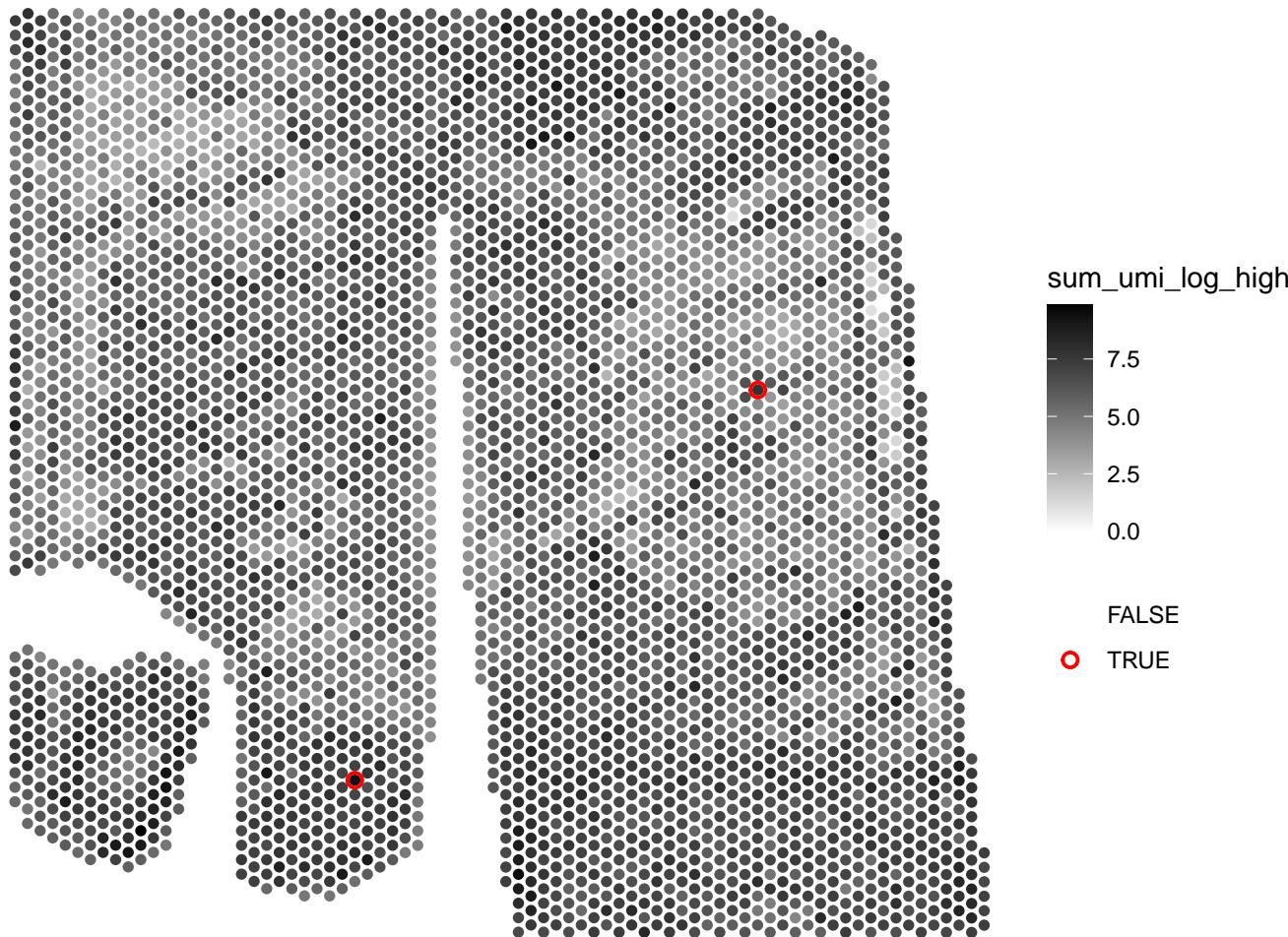
Sample: V13B23–284_B1



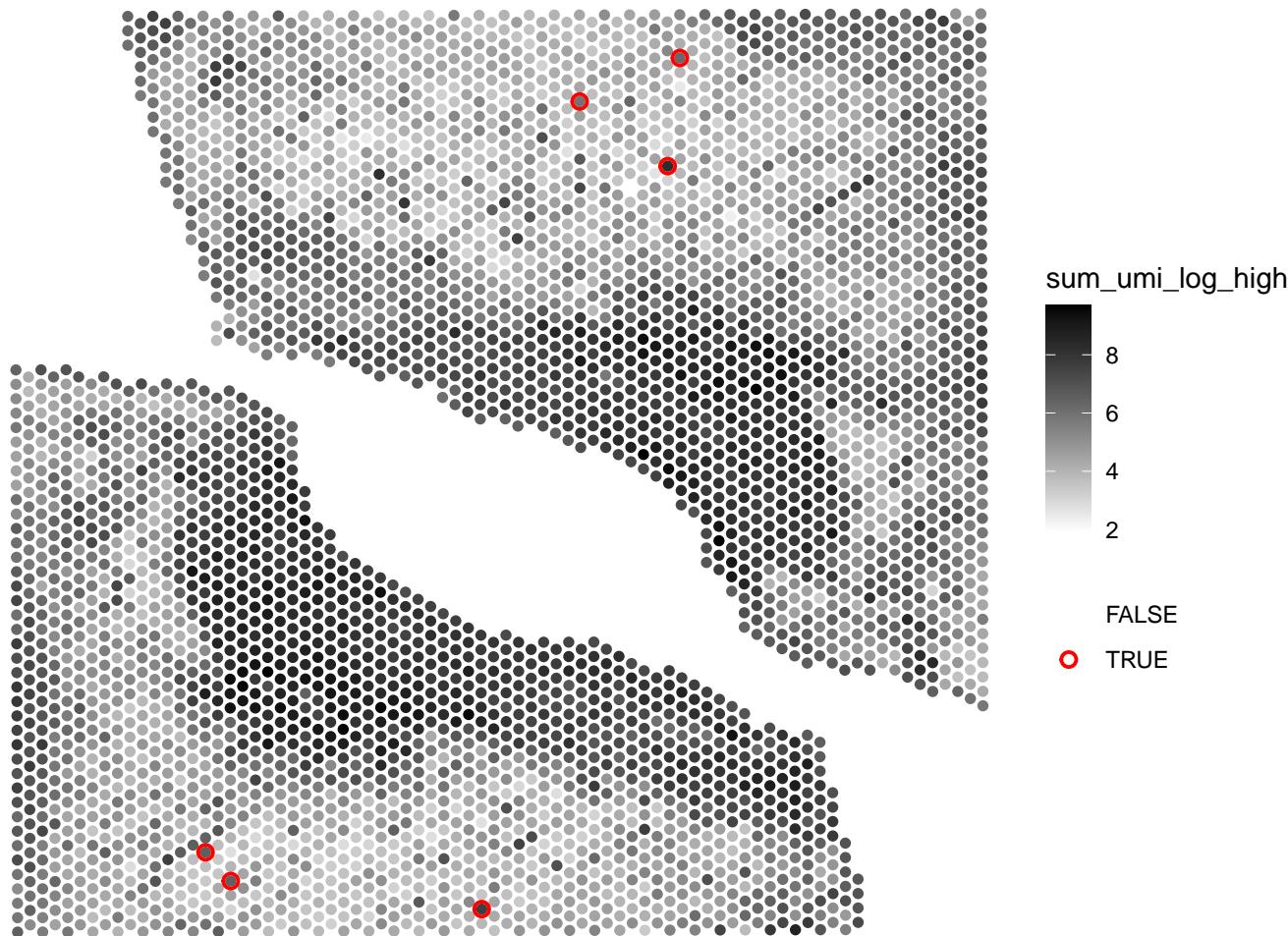
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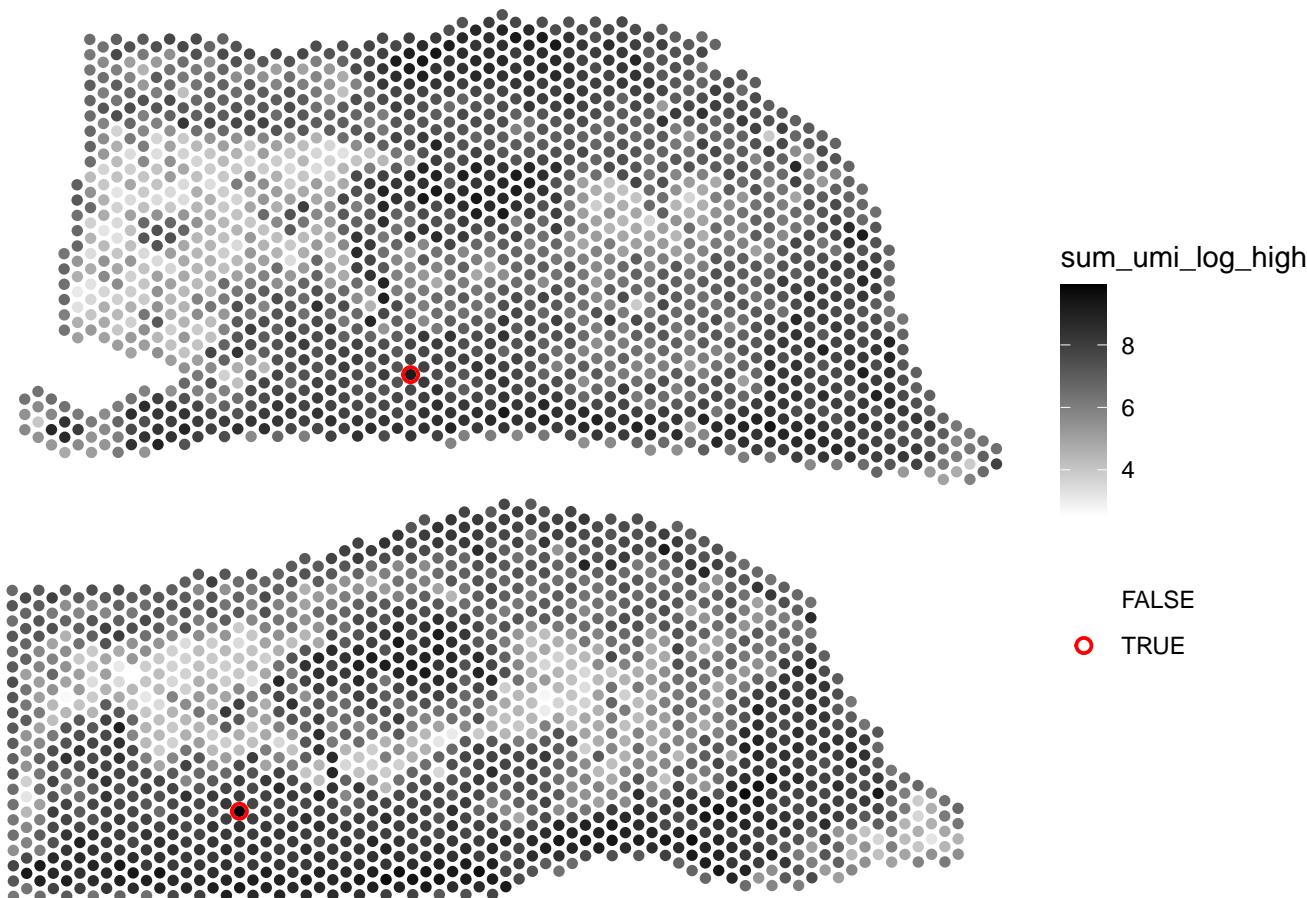
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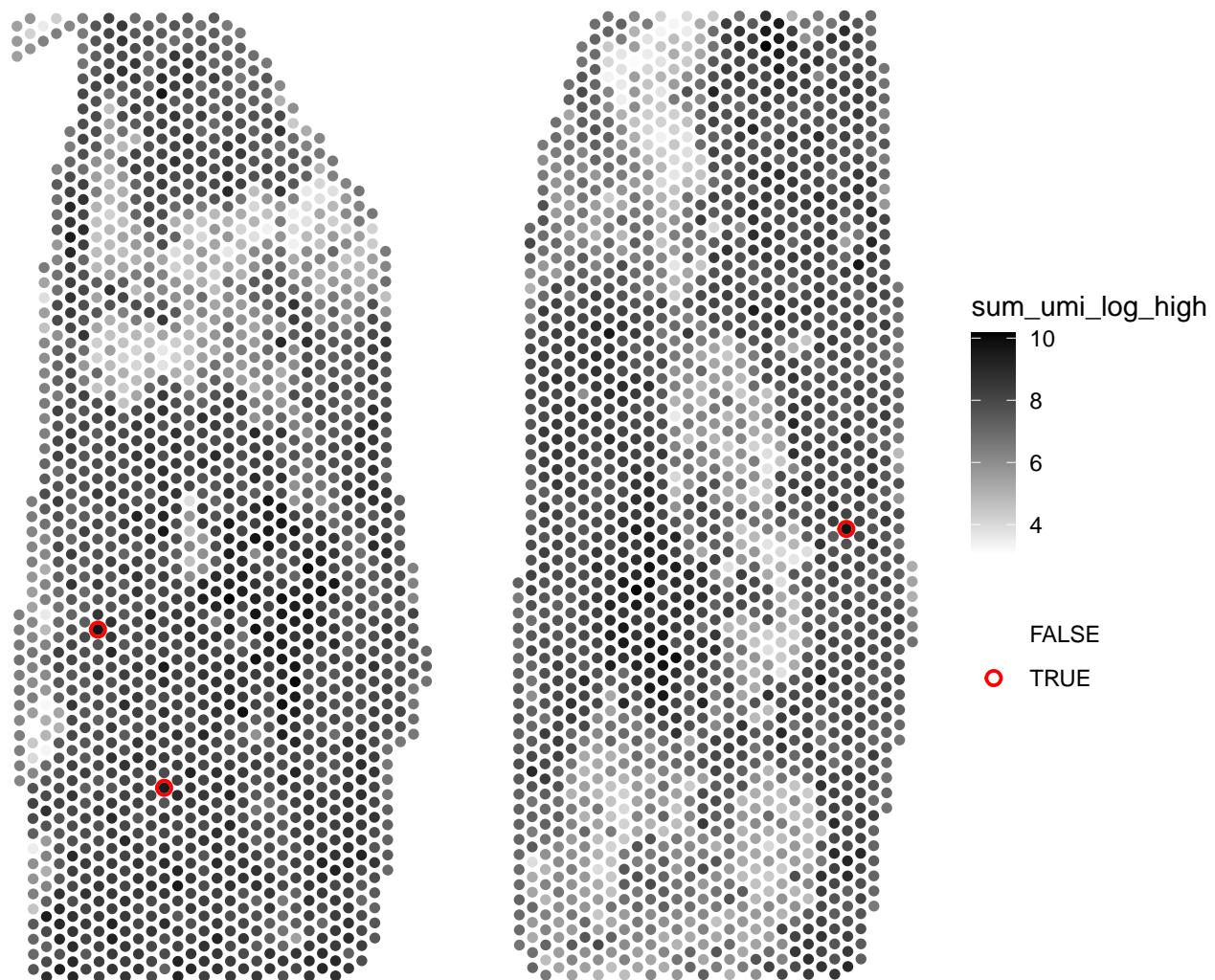
Sample: V13F27–339_A1



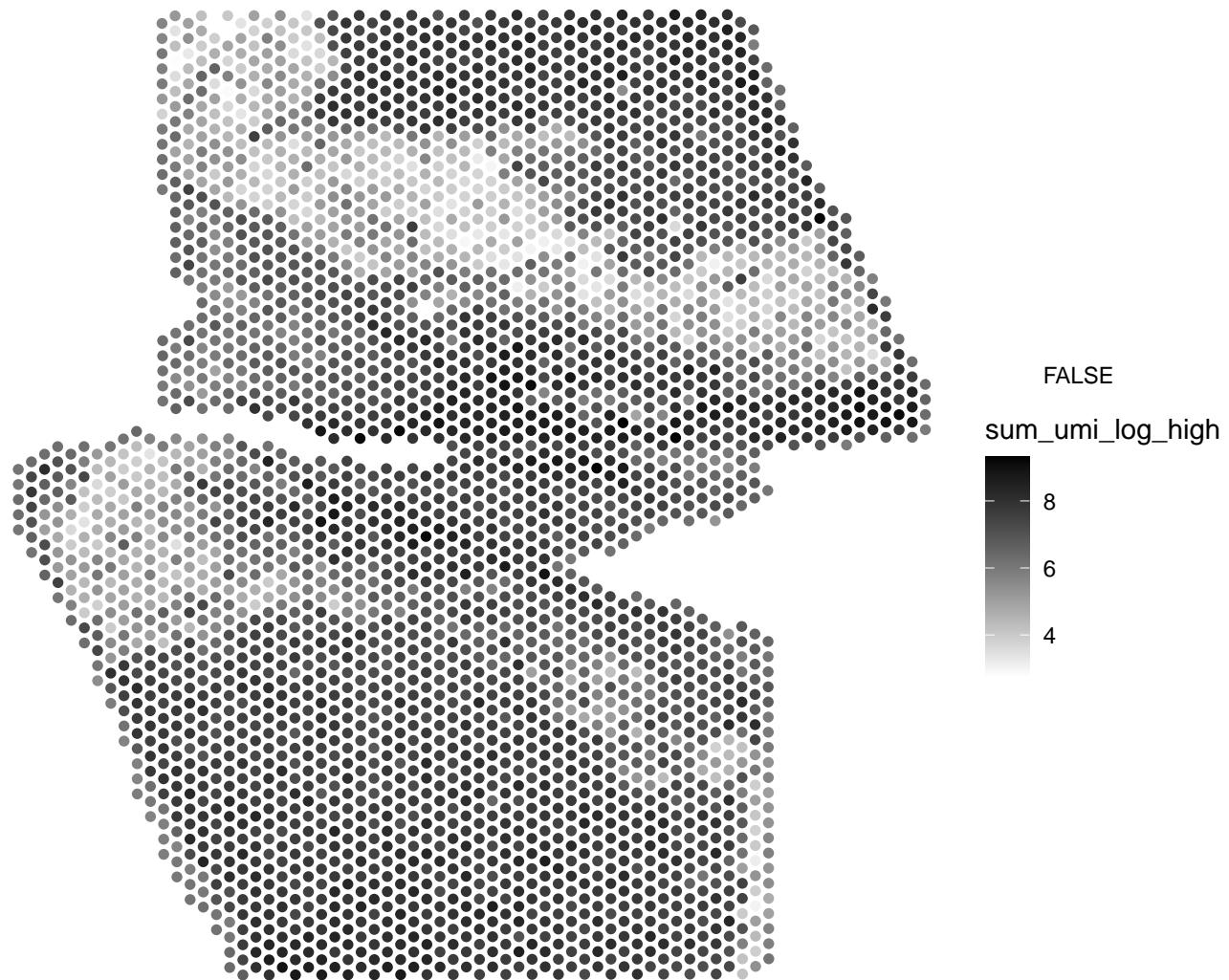
Sample: V13F27–339_B1



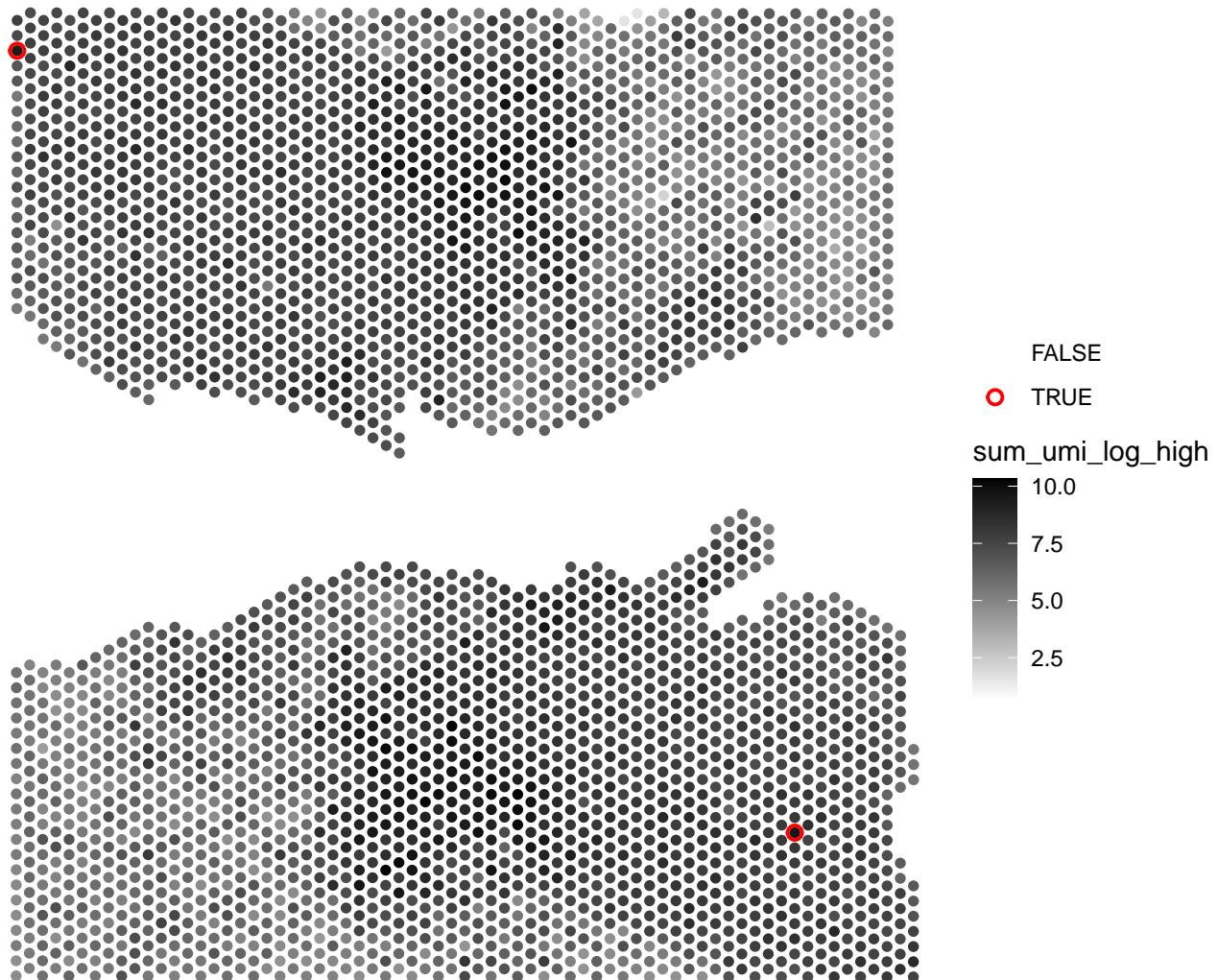
Sample: V13F27-339_C1



Sample: V13F27-339_D1



Sample: V13B23-286_A1



Sample: V13B23–286_B1

