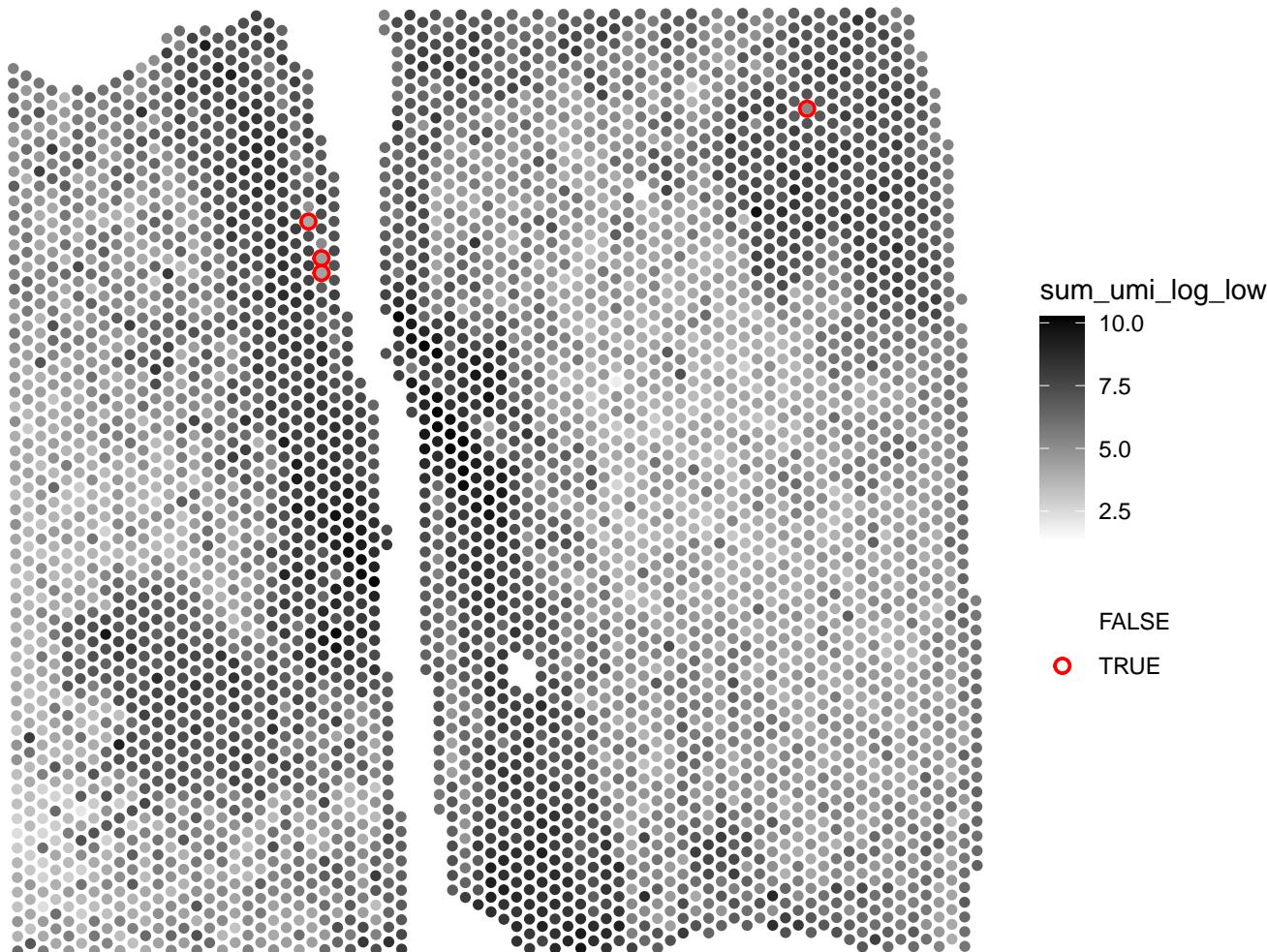
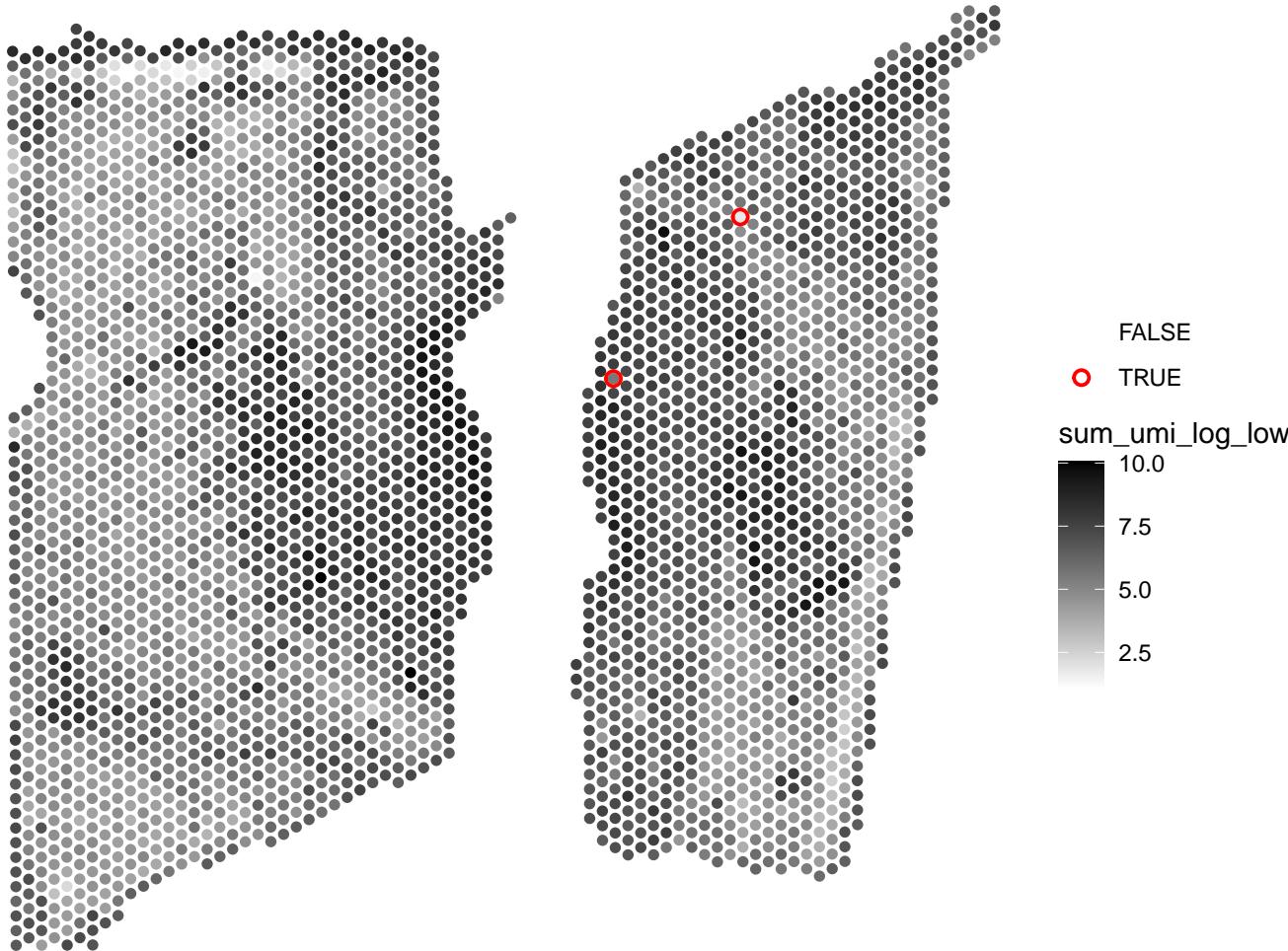


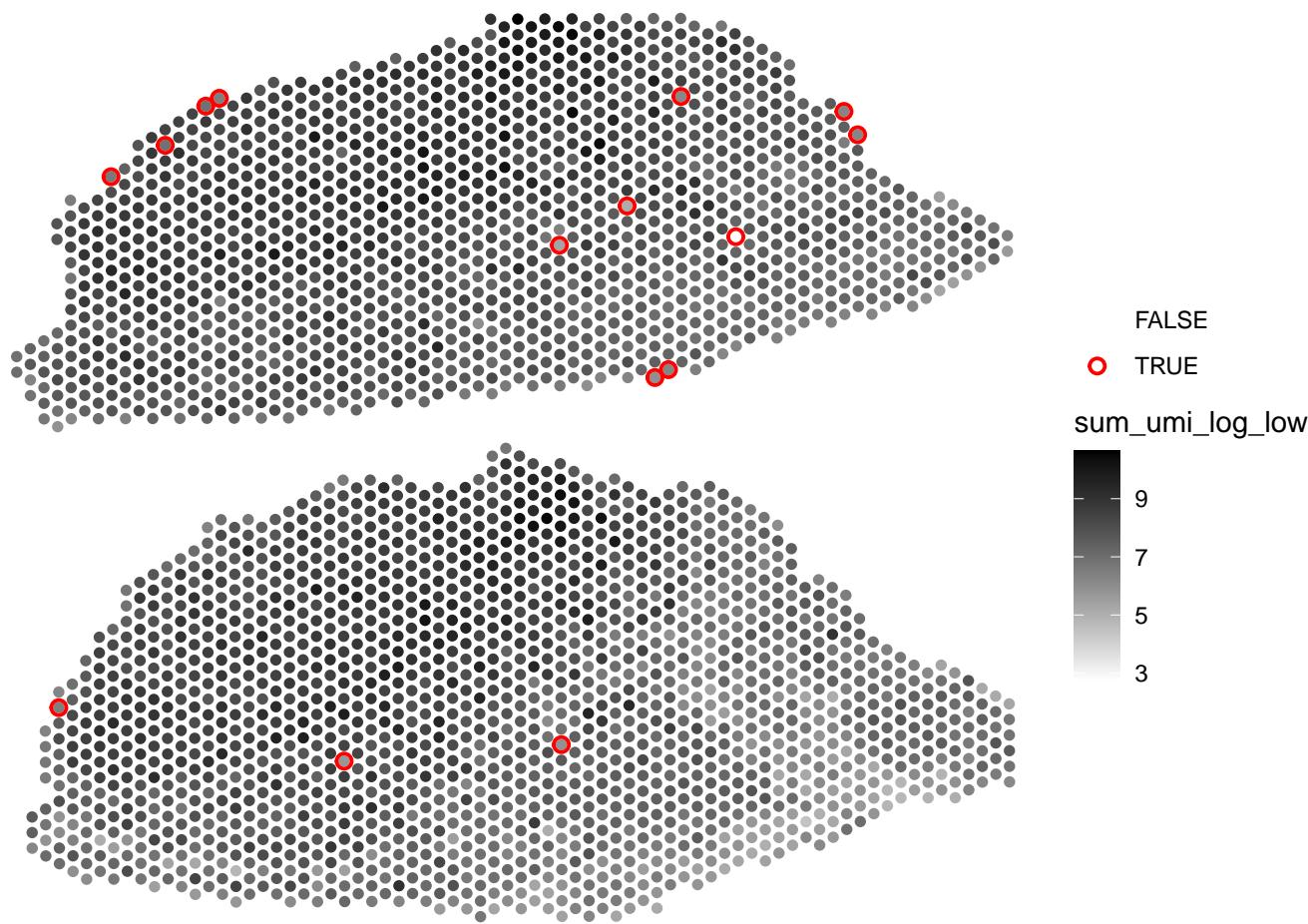
Sample: V13M06–331\_A1



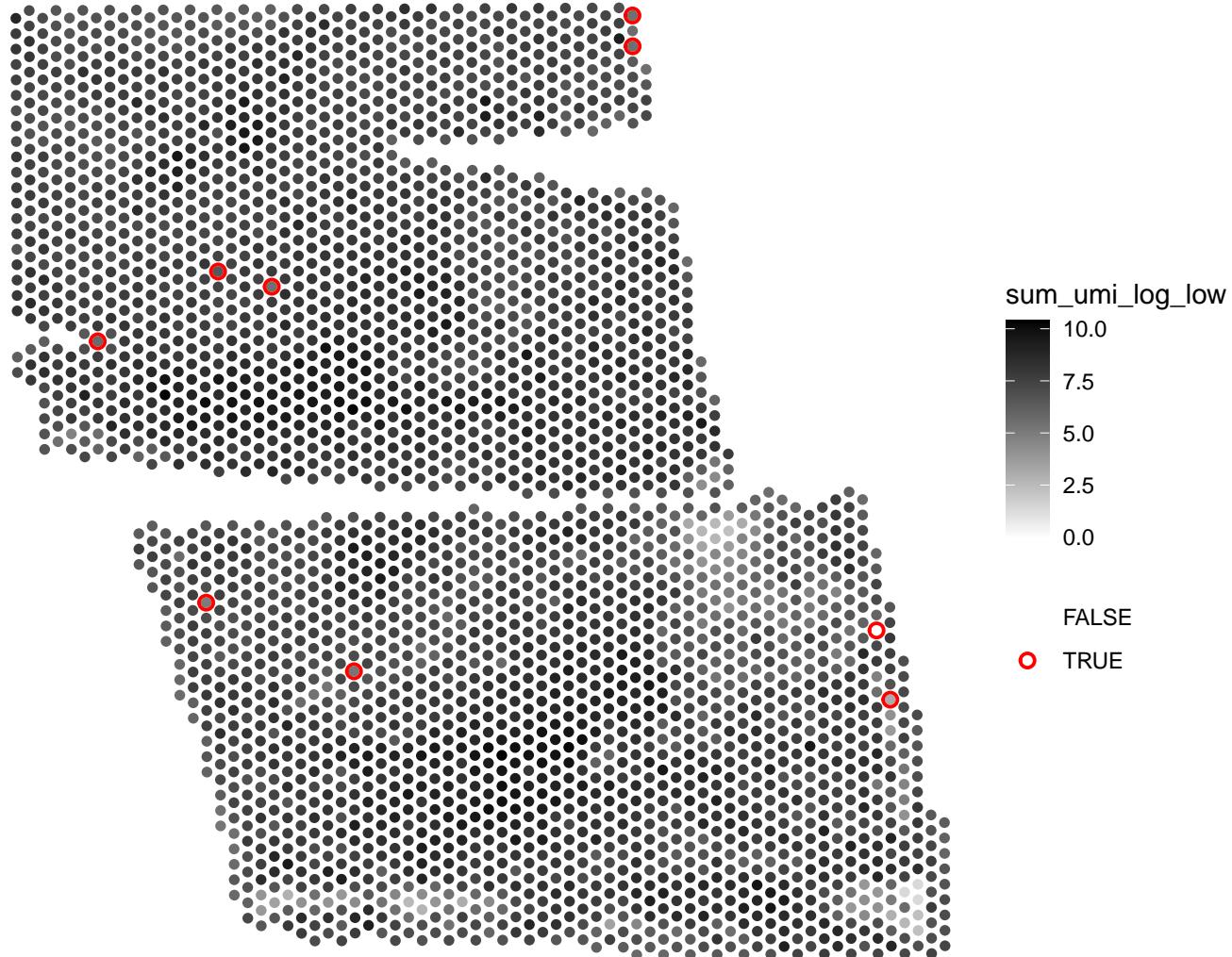
Sample: V13M06–331\_B1



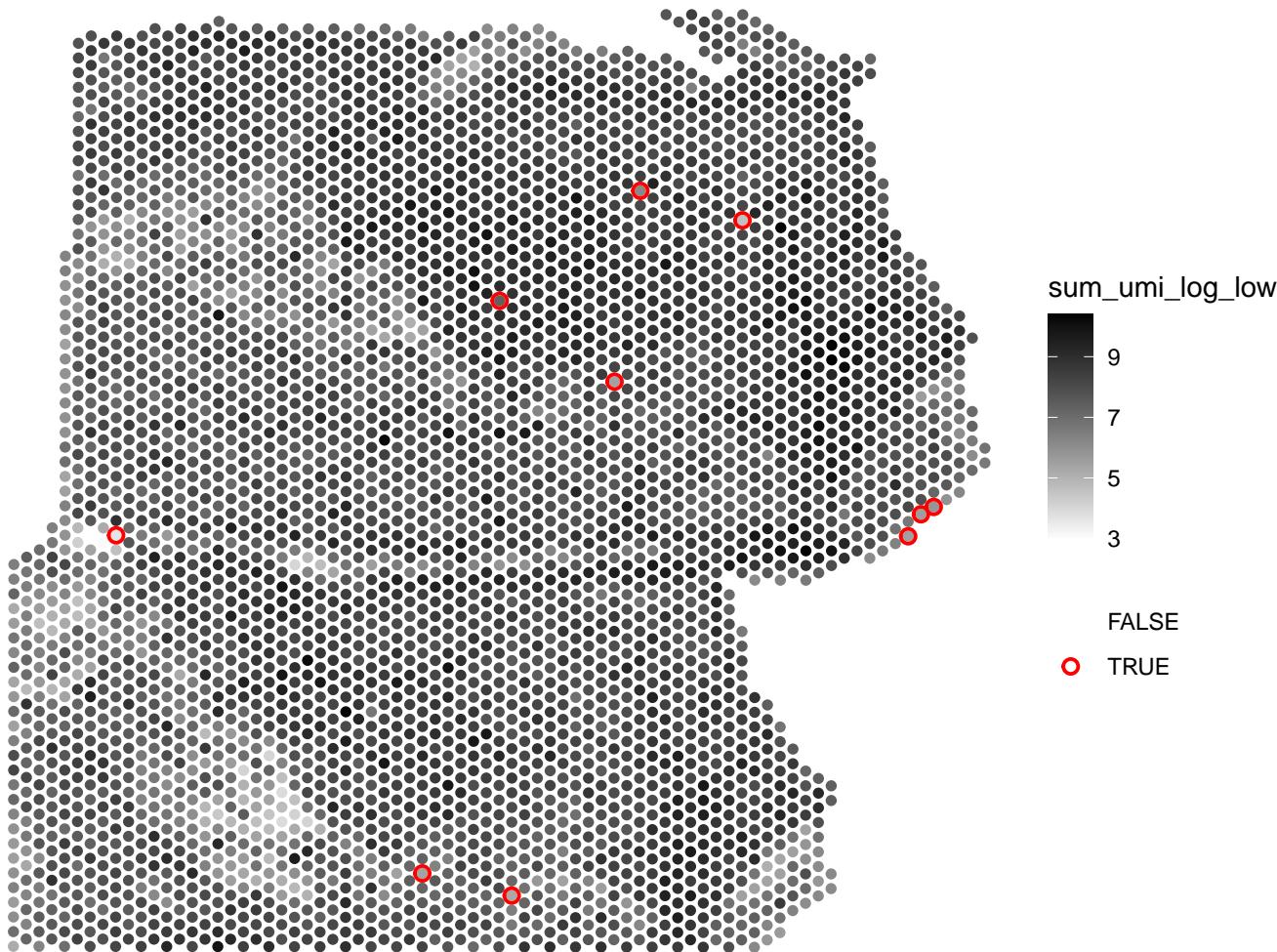
Sample: V13M06–331\_C1



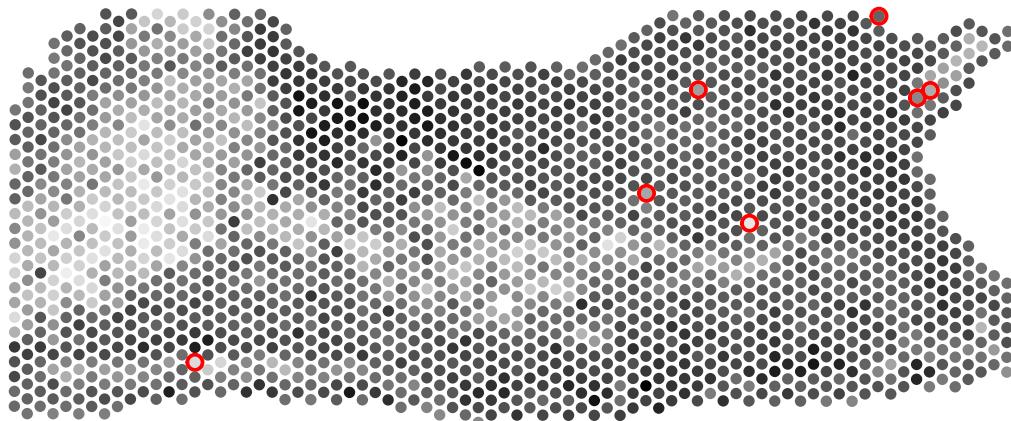
Sample: V13M06-331\_D1



Sample: V13M06–333\_A1



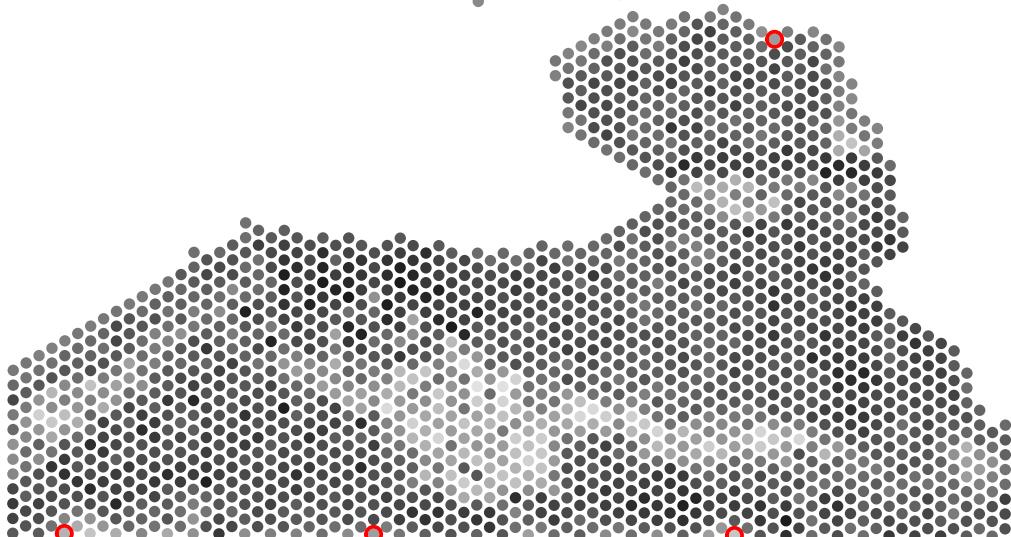
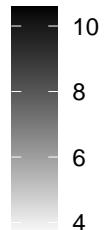
Sample: V13M06–333\_B1



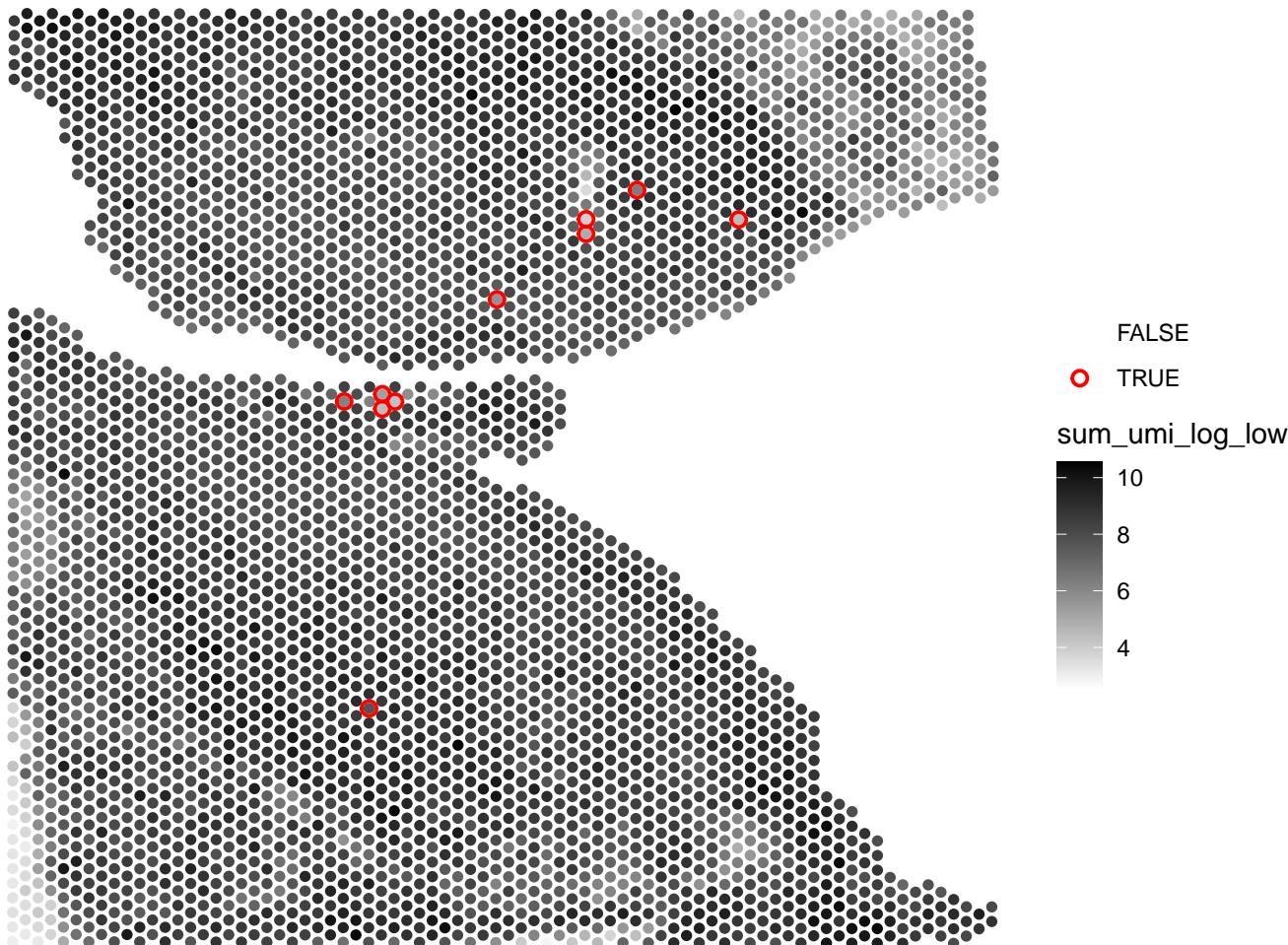
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TRUE

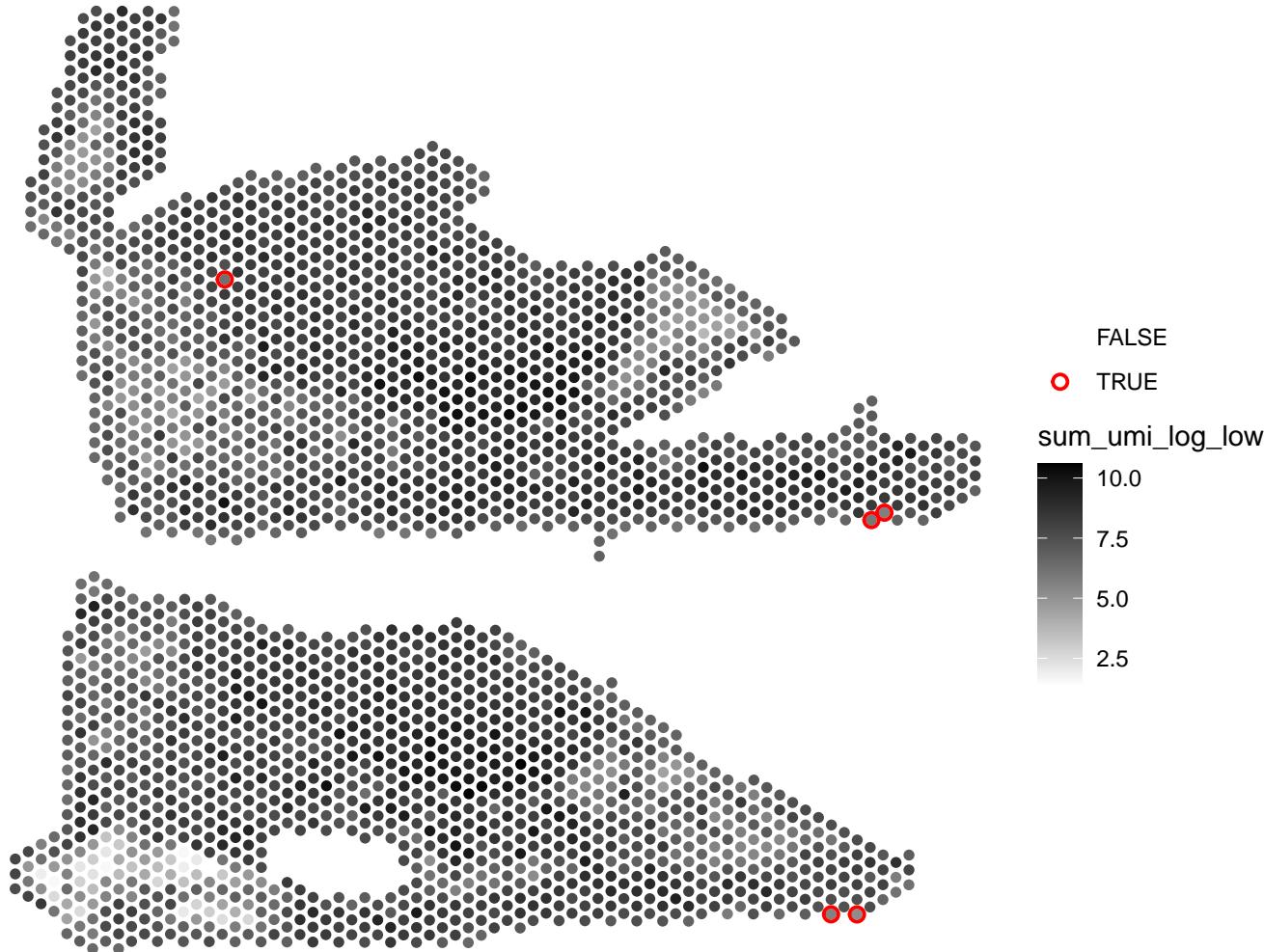
`sum_umi_log_low`



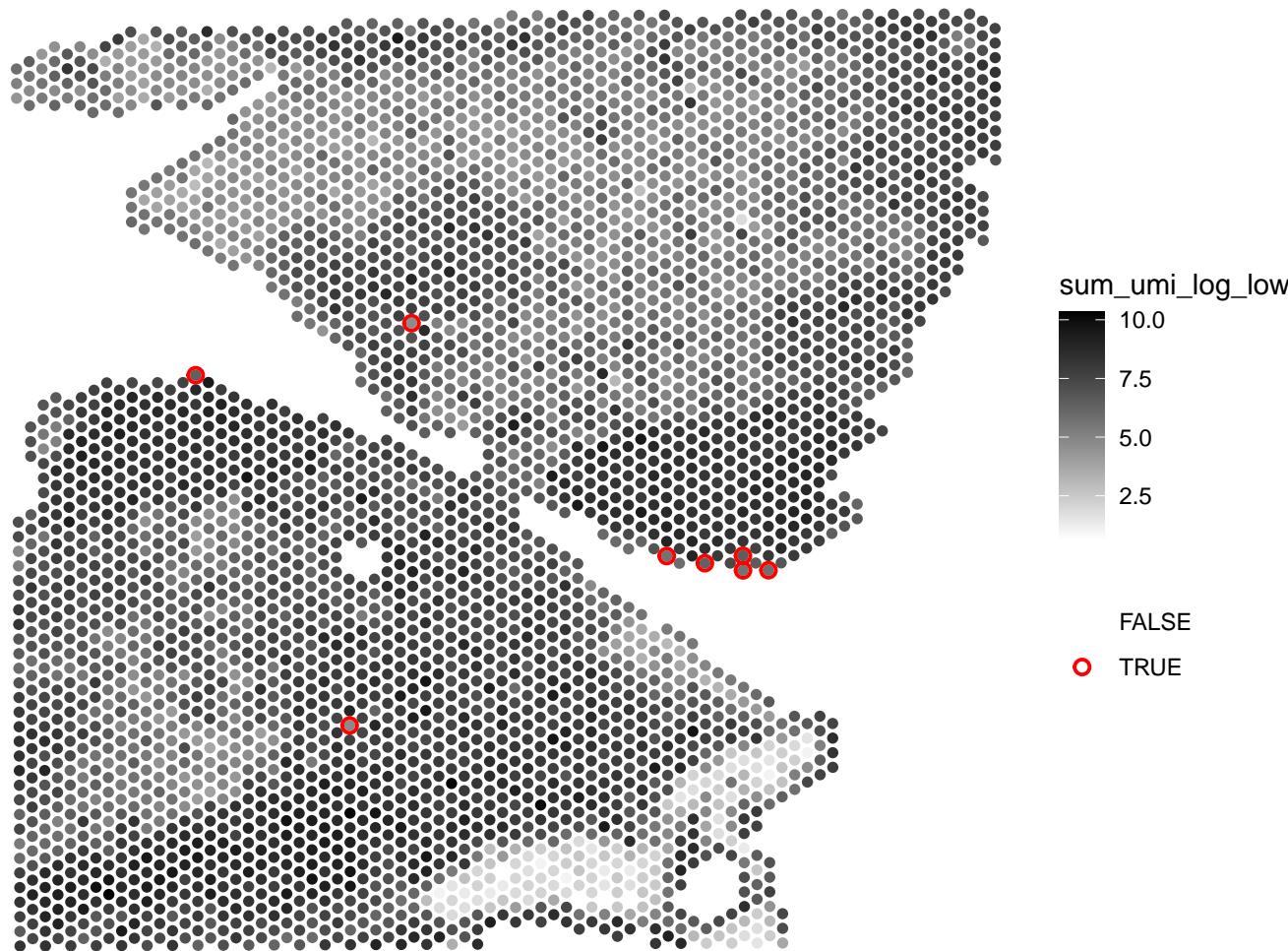
Sample: V13M06–333\_C1



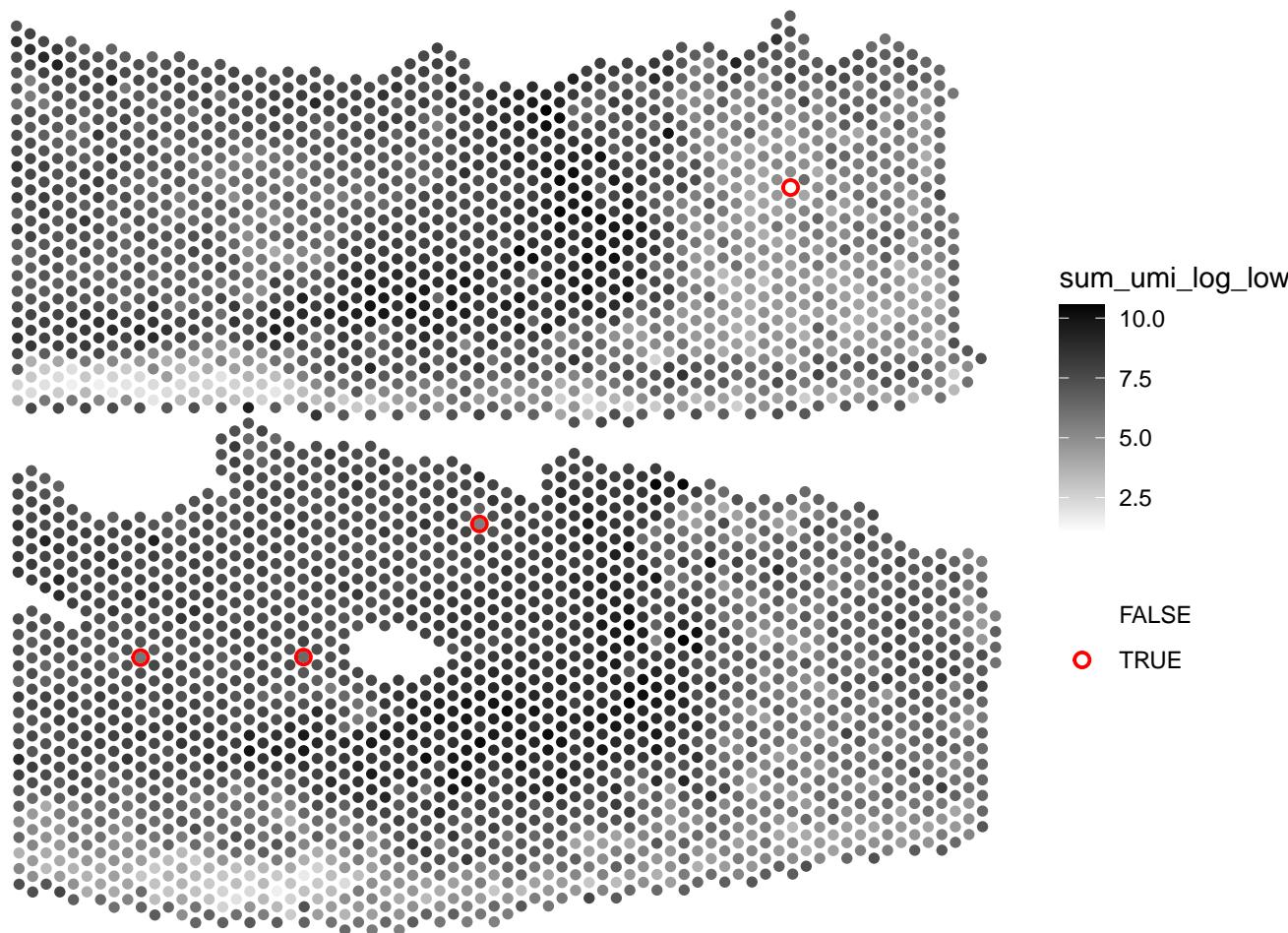
Sample: V13M06–333\_D1



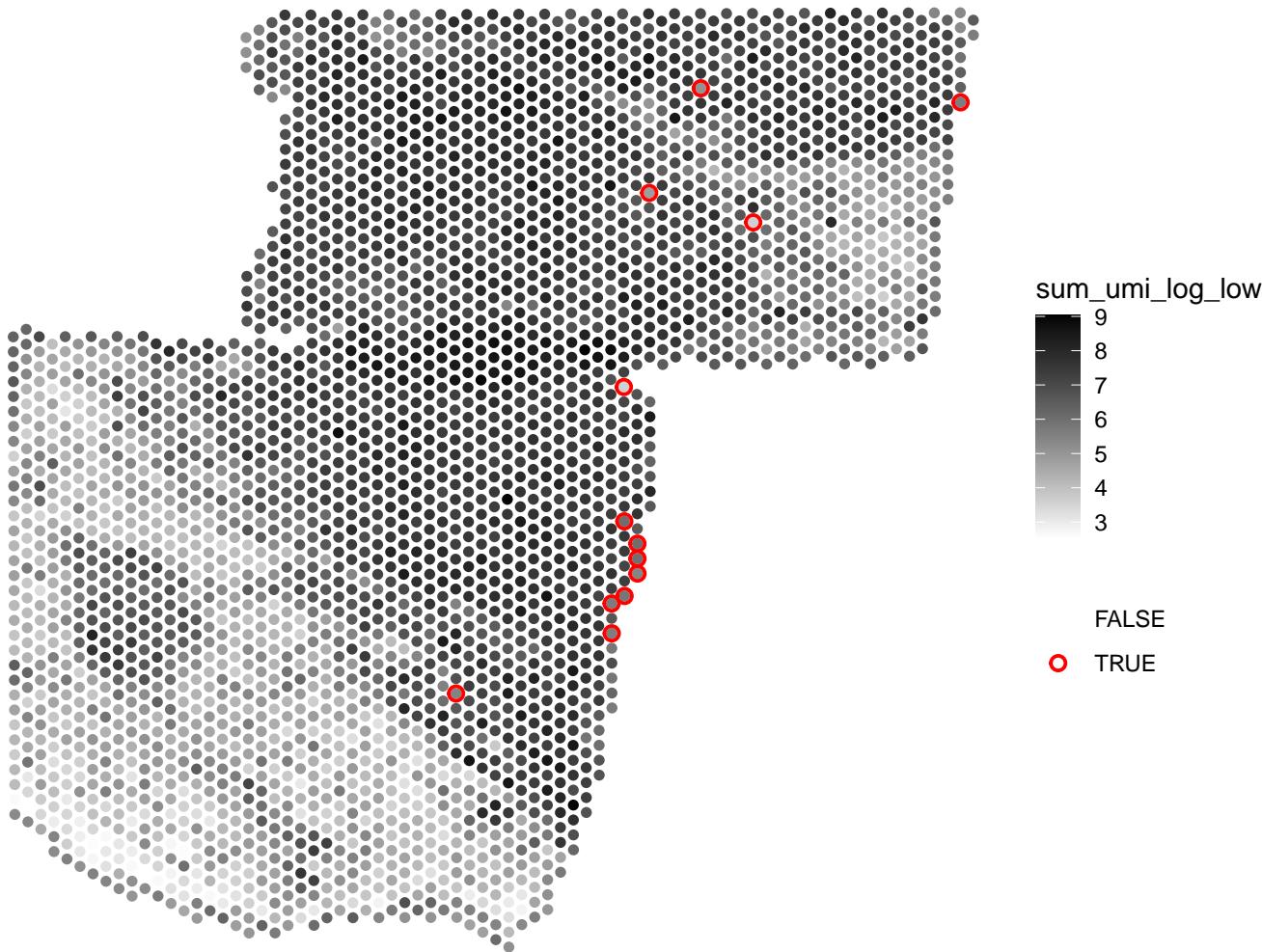
Sample: V13M06–332\_A1



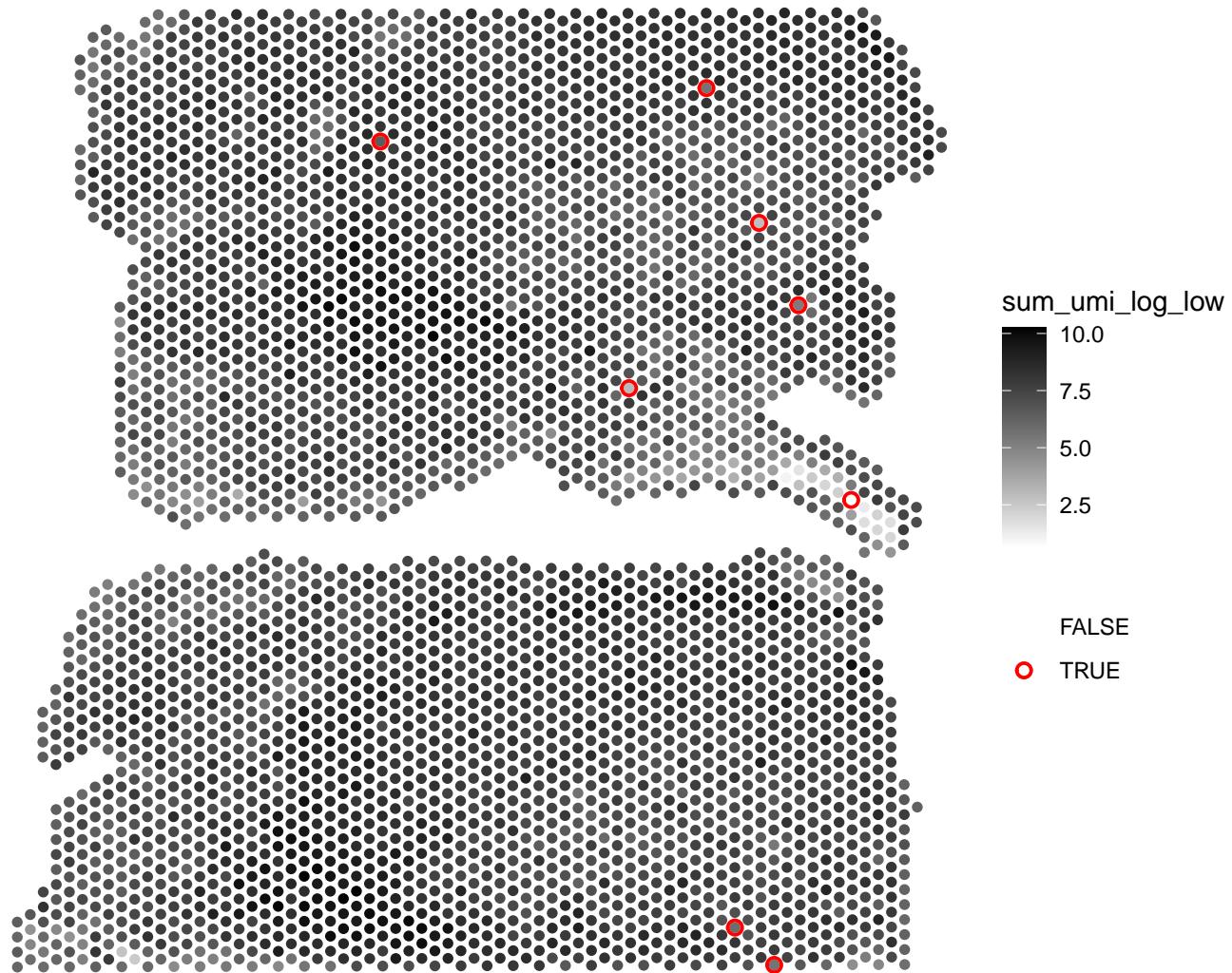
Sample: V13M06–332\_B1



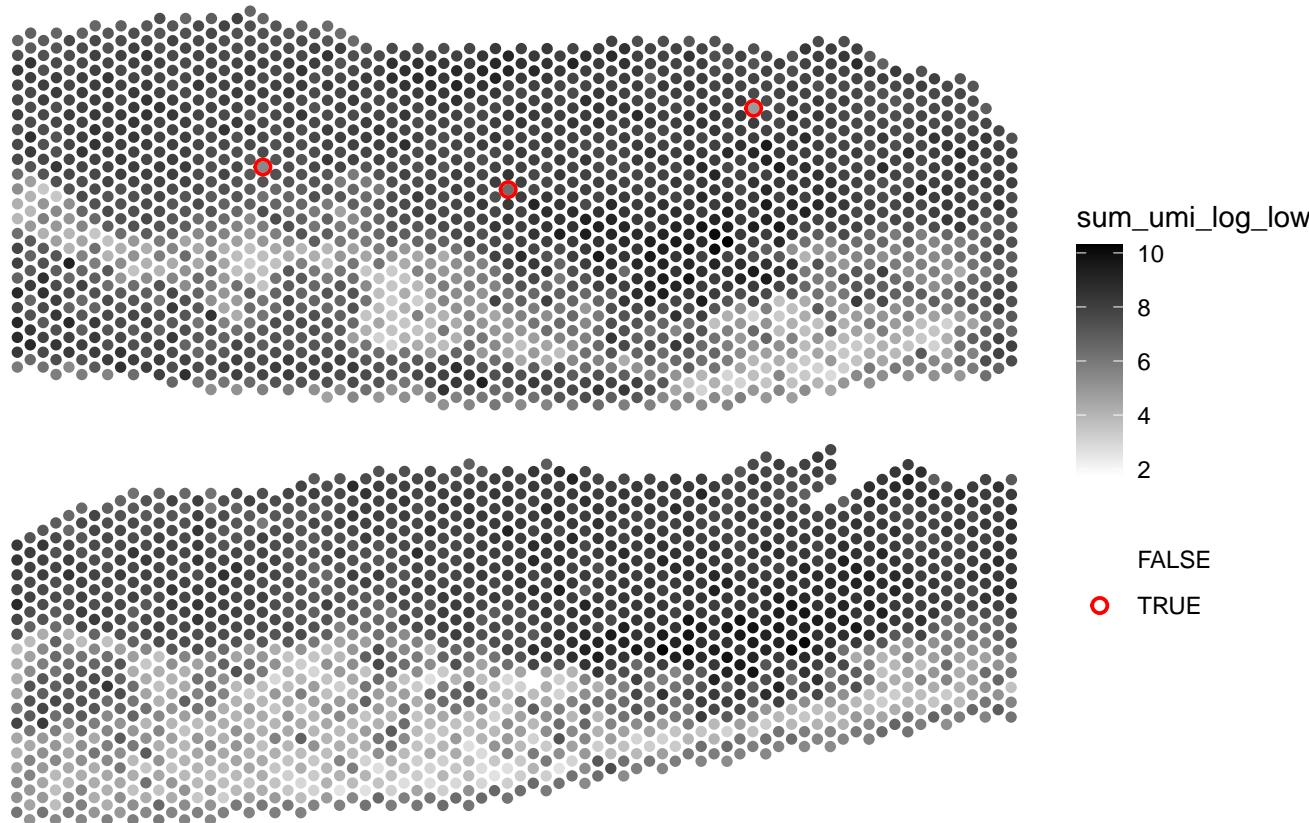
Sample: V13M06–332\_C1



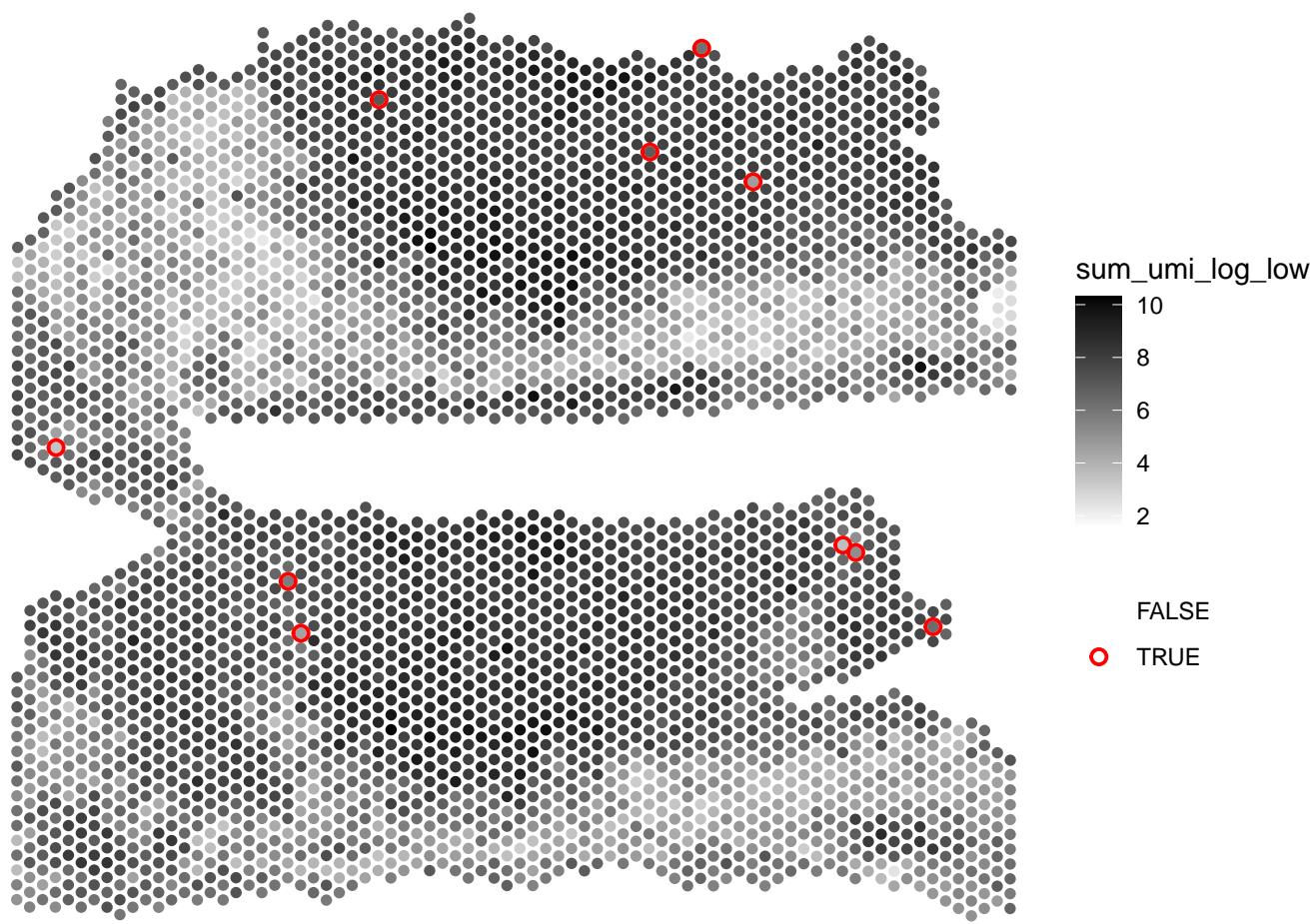
Sample: V13M06-332\_D1



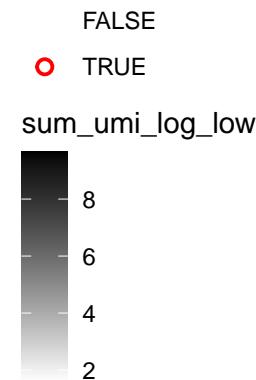
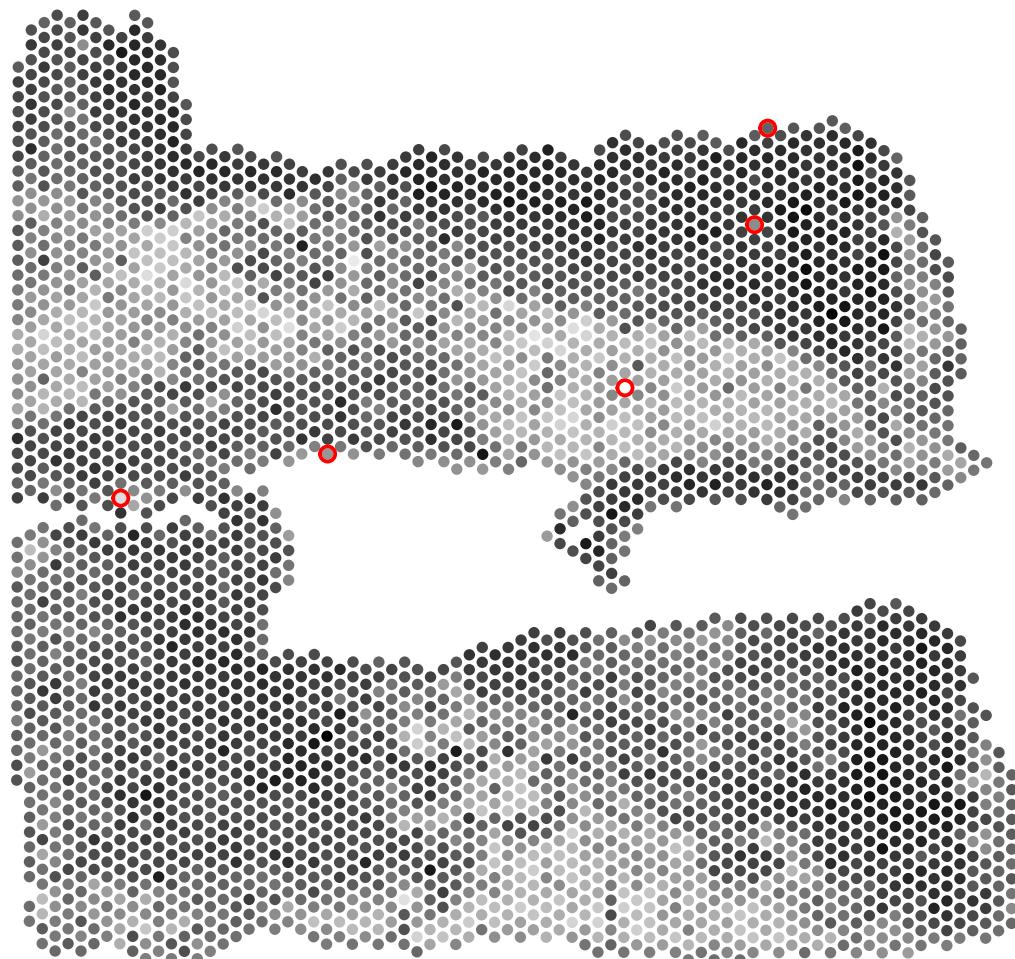
Sample: V13M06-404\_A1



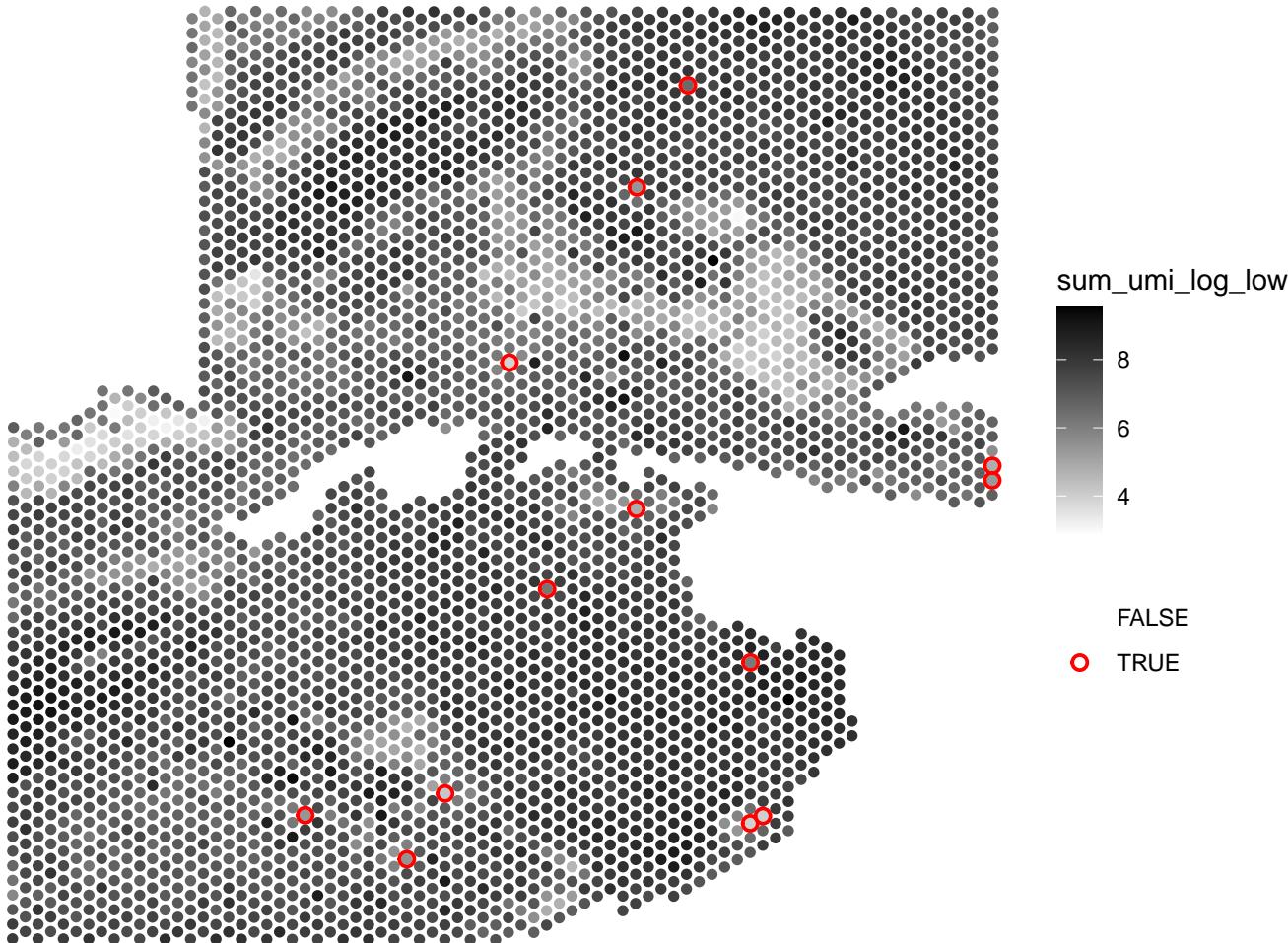
Sample: V13M06–404\_B1



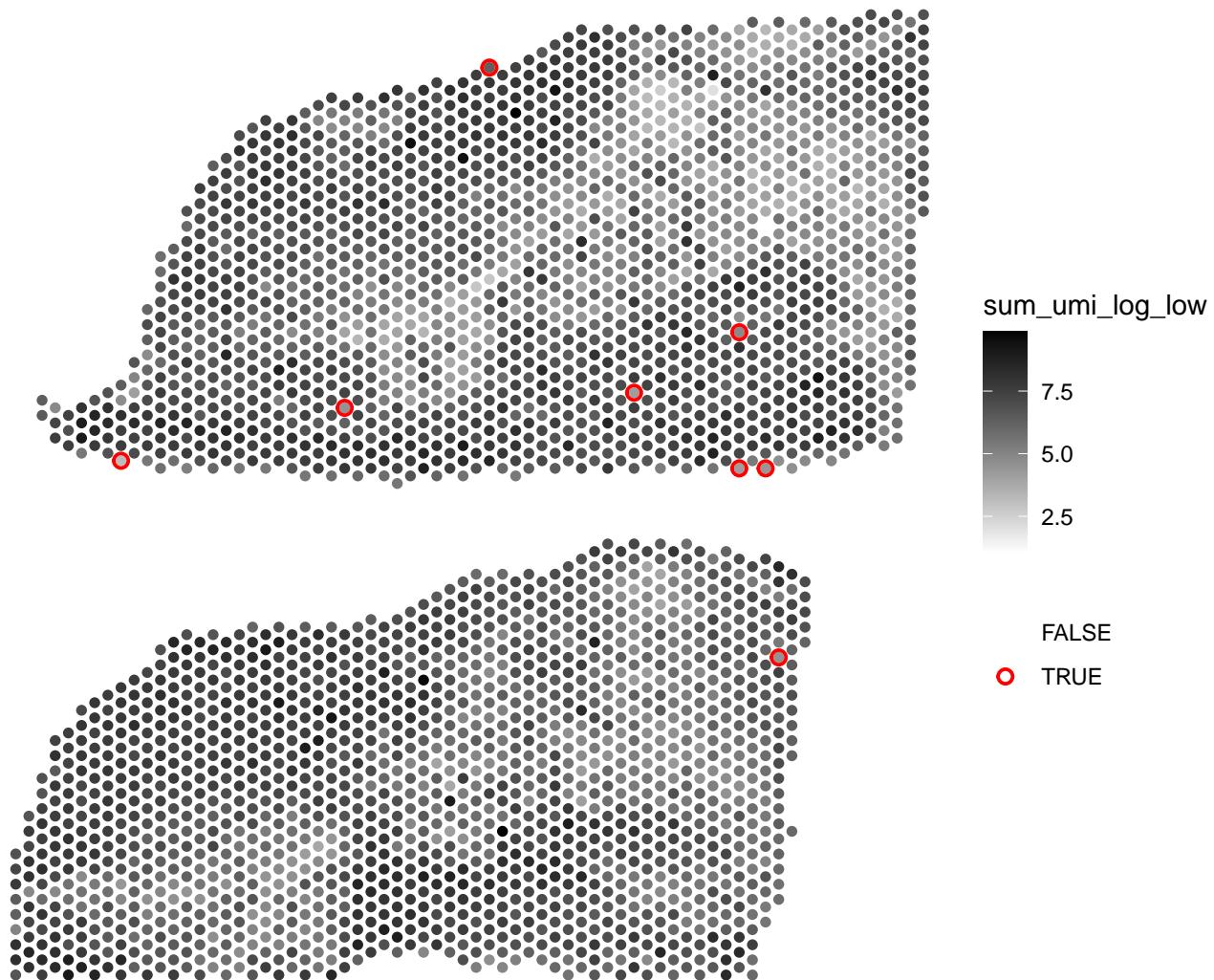
Sample: V13M06–404\_C1



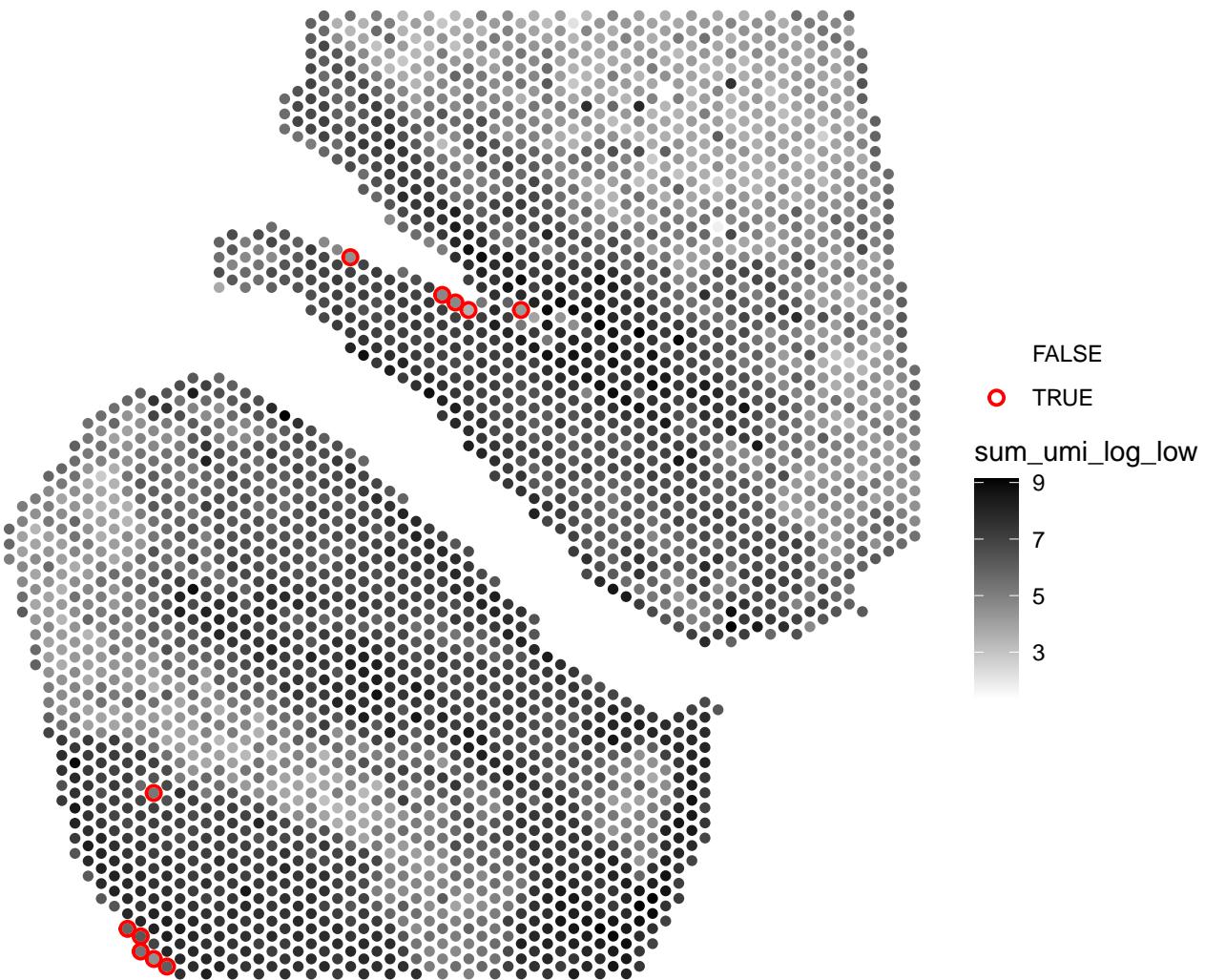
Sample: V13M06–404\_D1



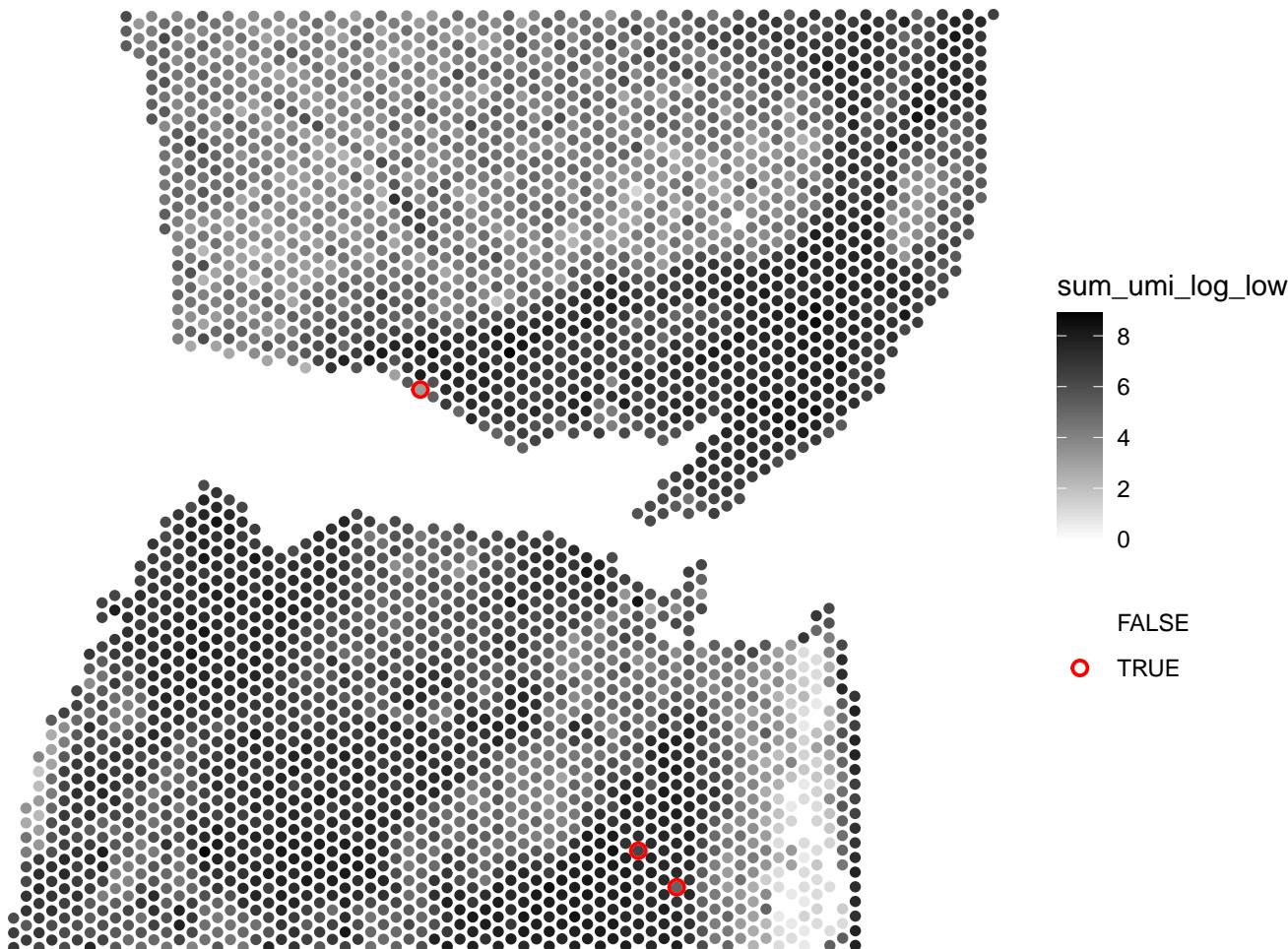
Sample: V13M06–403\_A1



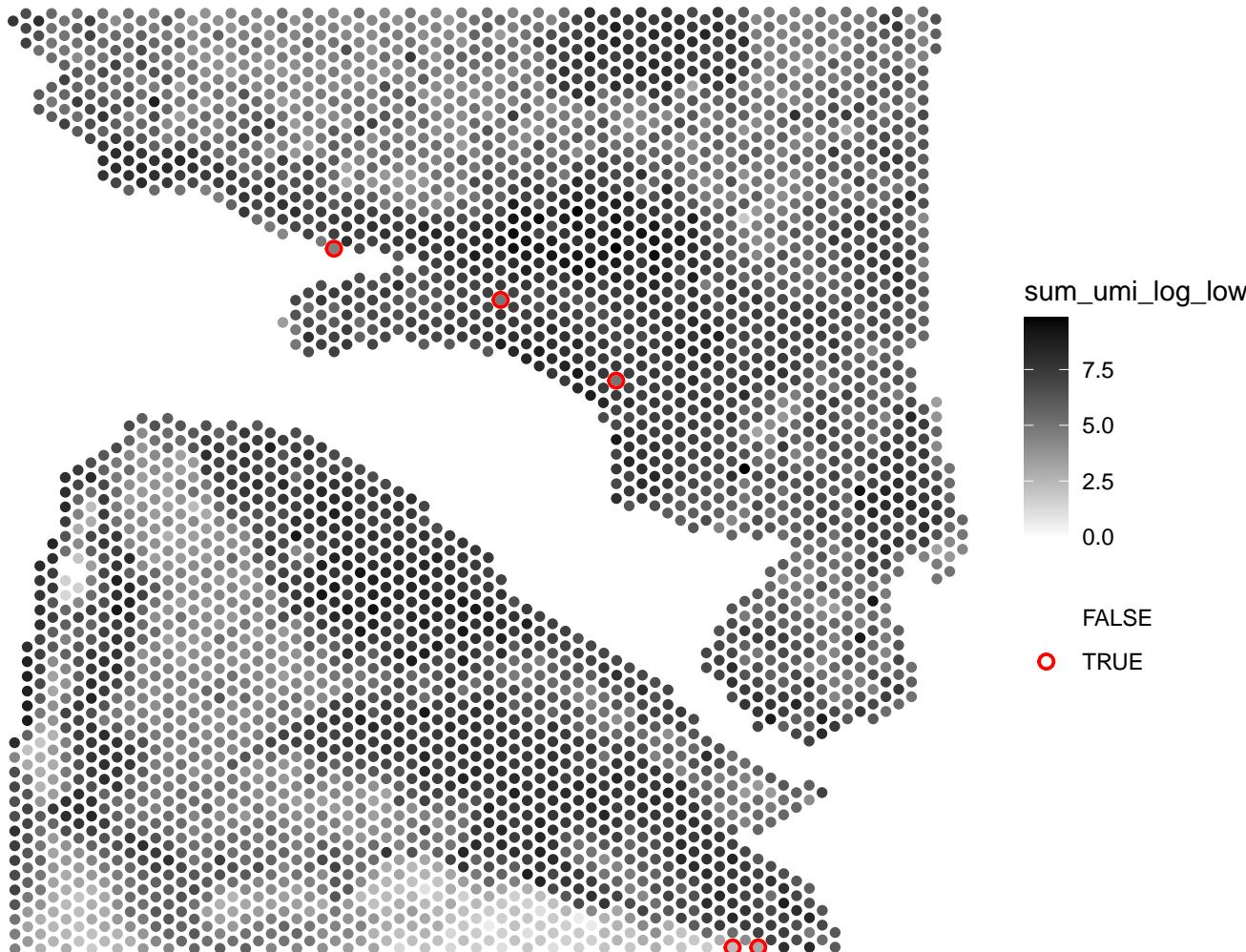
Sample: V13M06–403\_B1



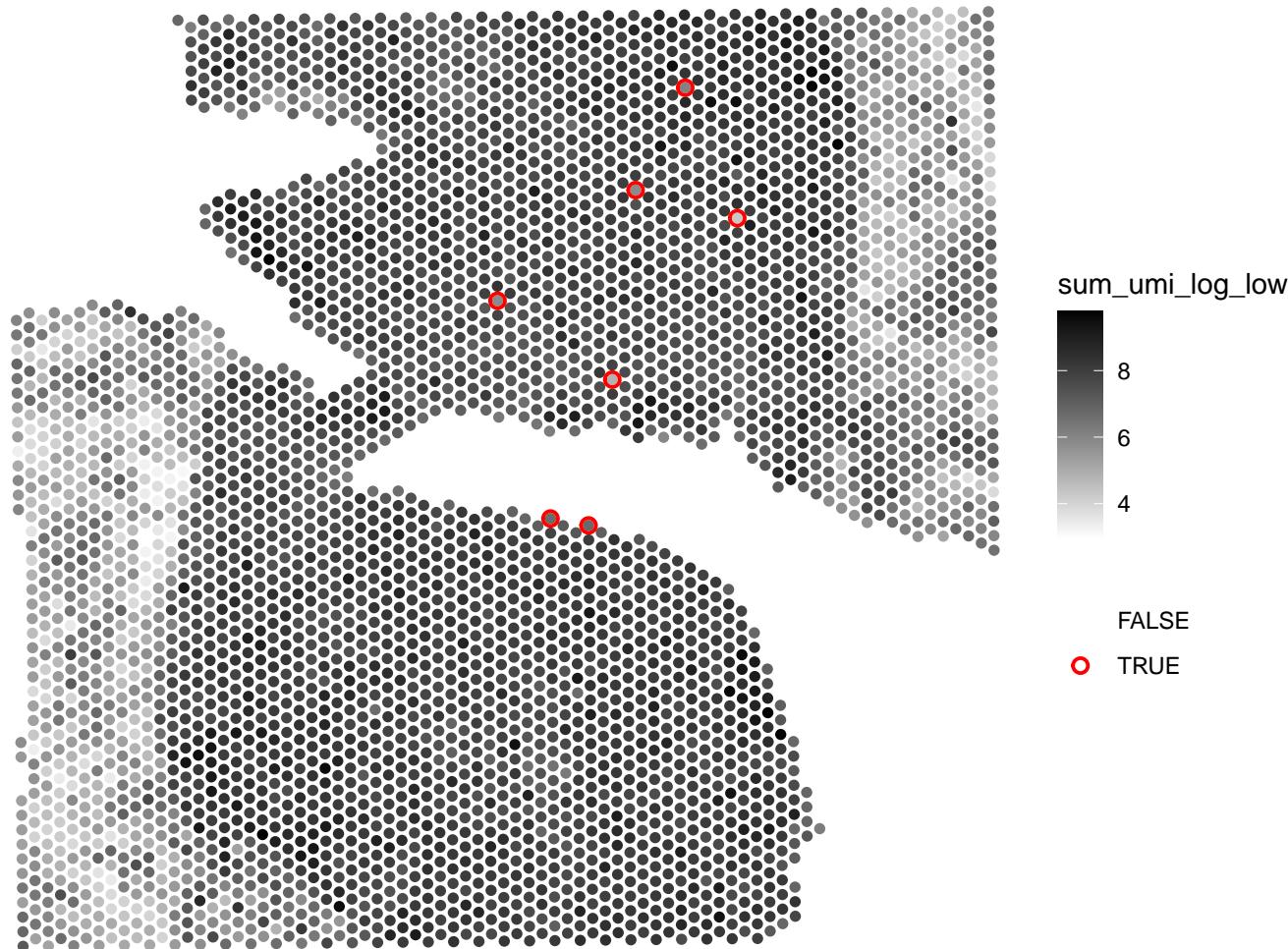
Sample: V13M06–403\_C1



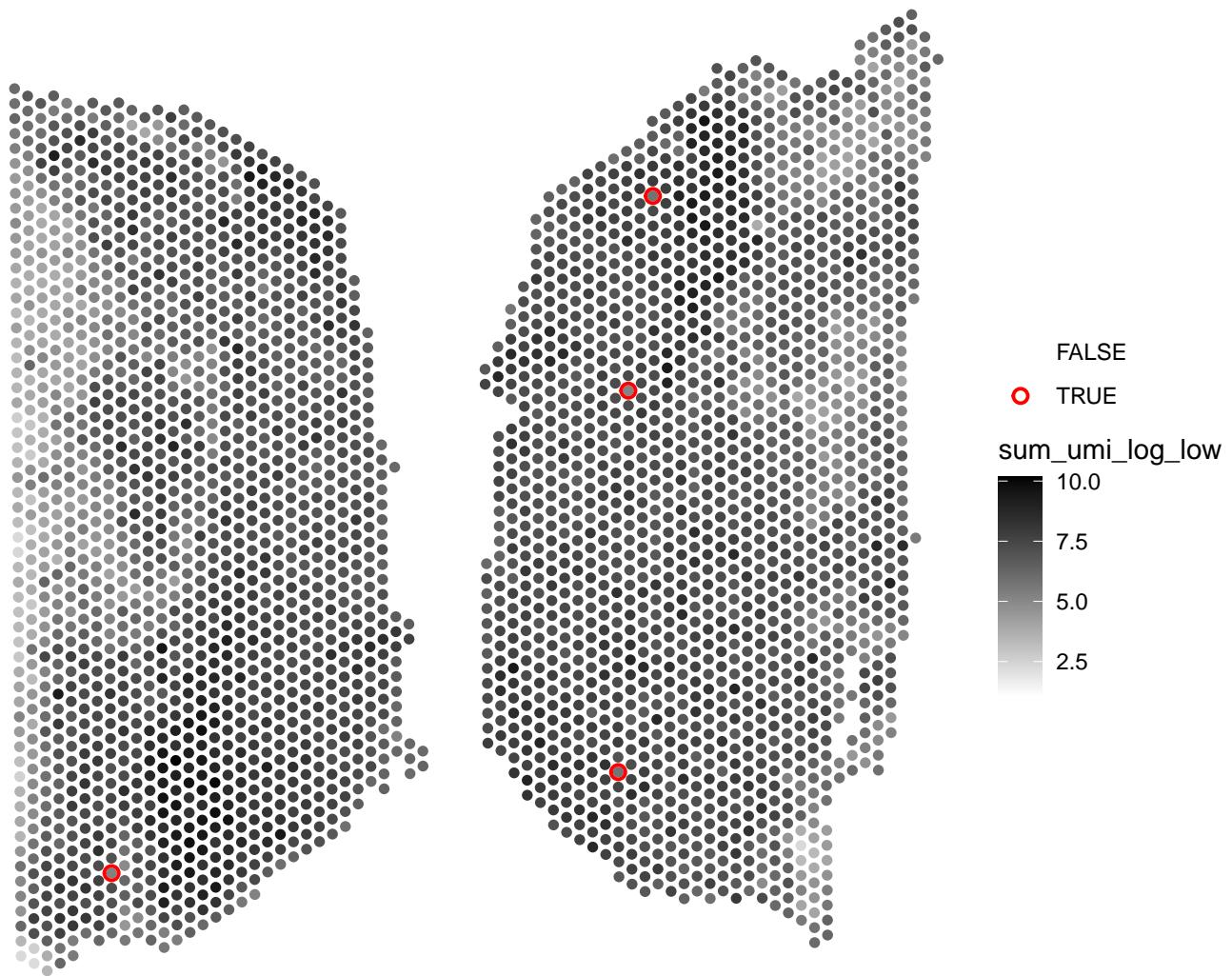
Sample: V13M06-403\_D1



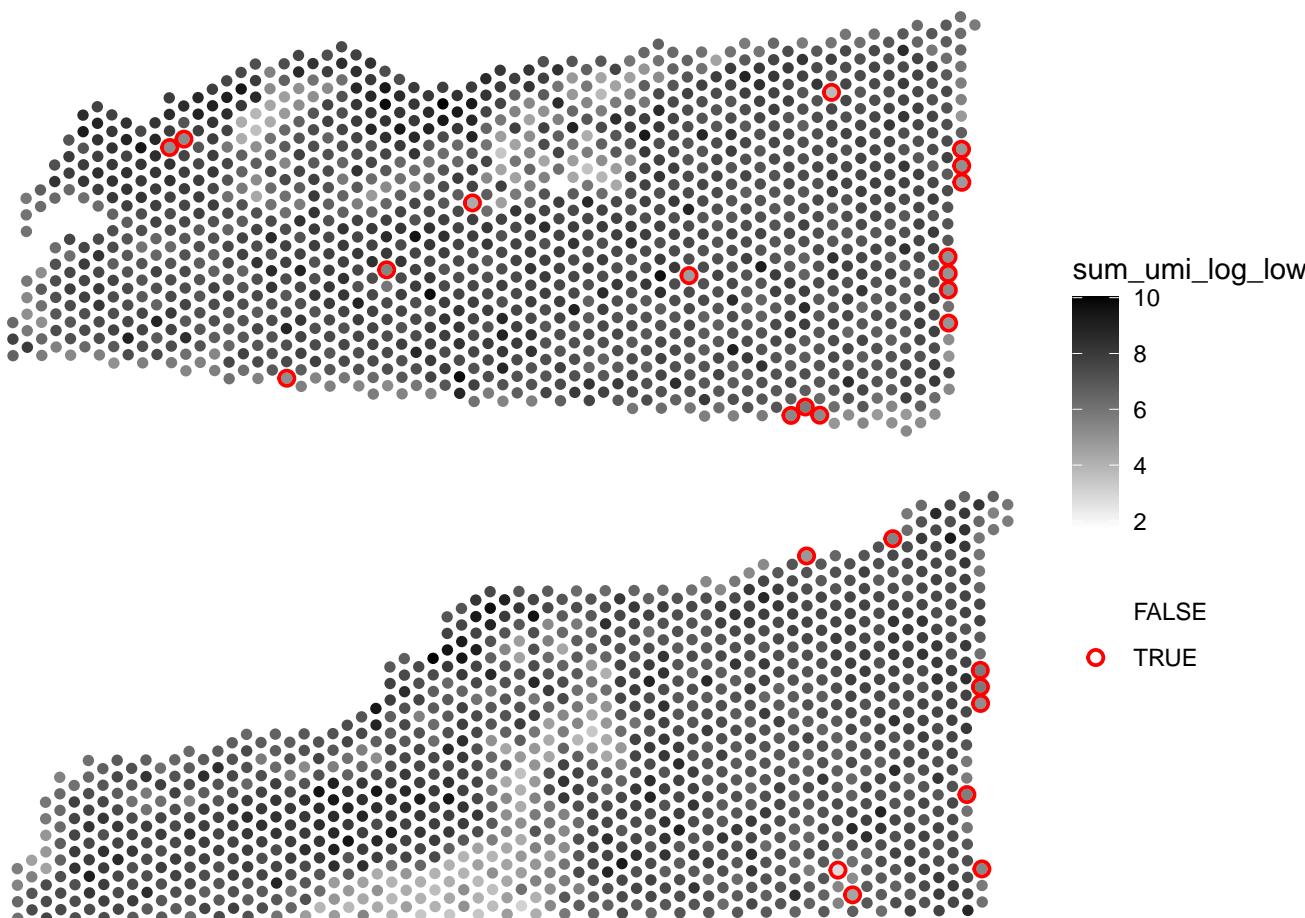
Sample: V13M06–402\_A1



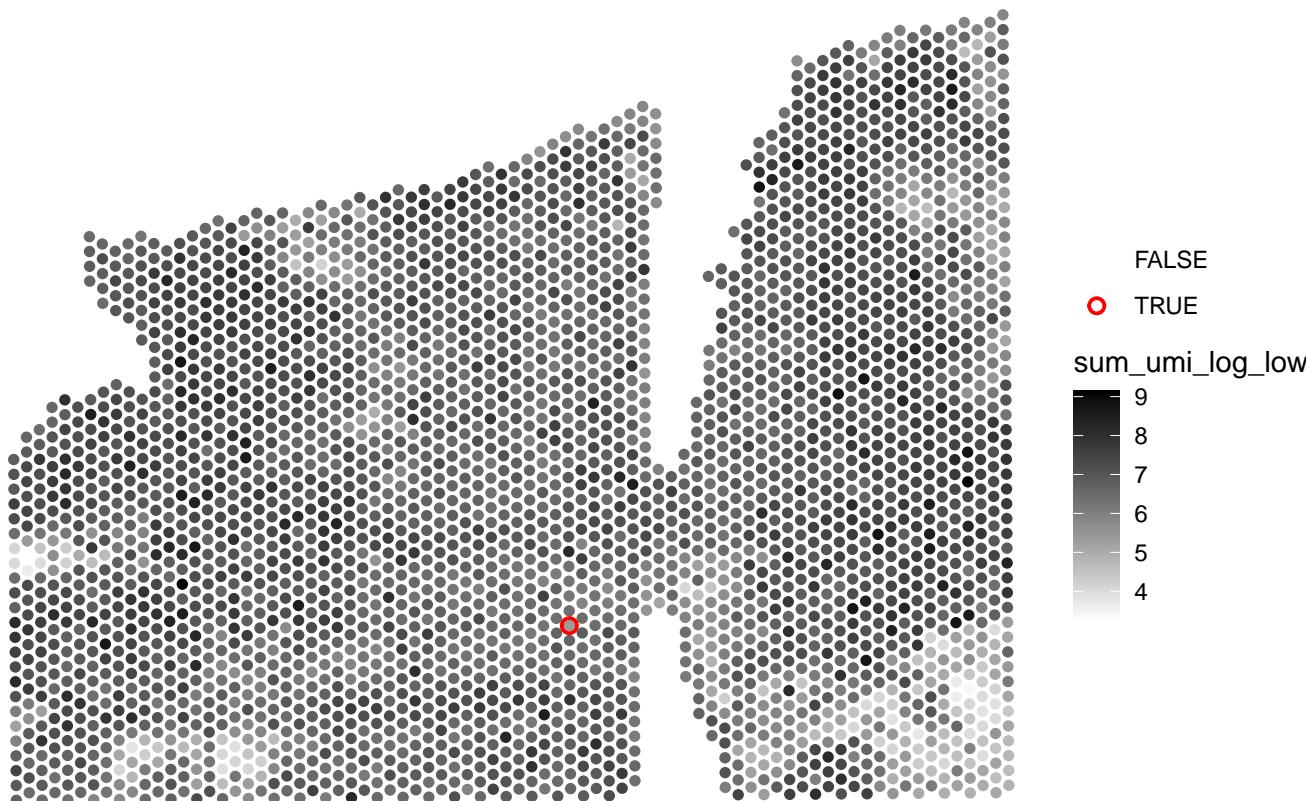
Sample: V13M06–402\_B1



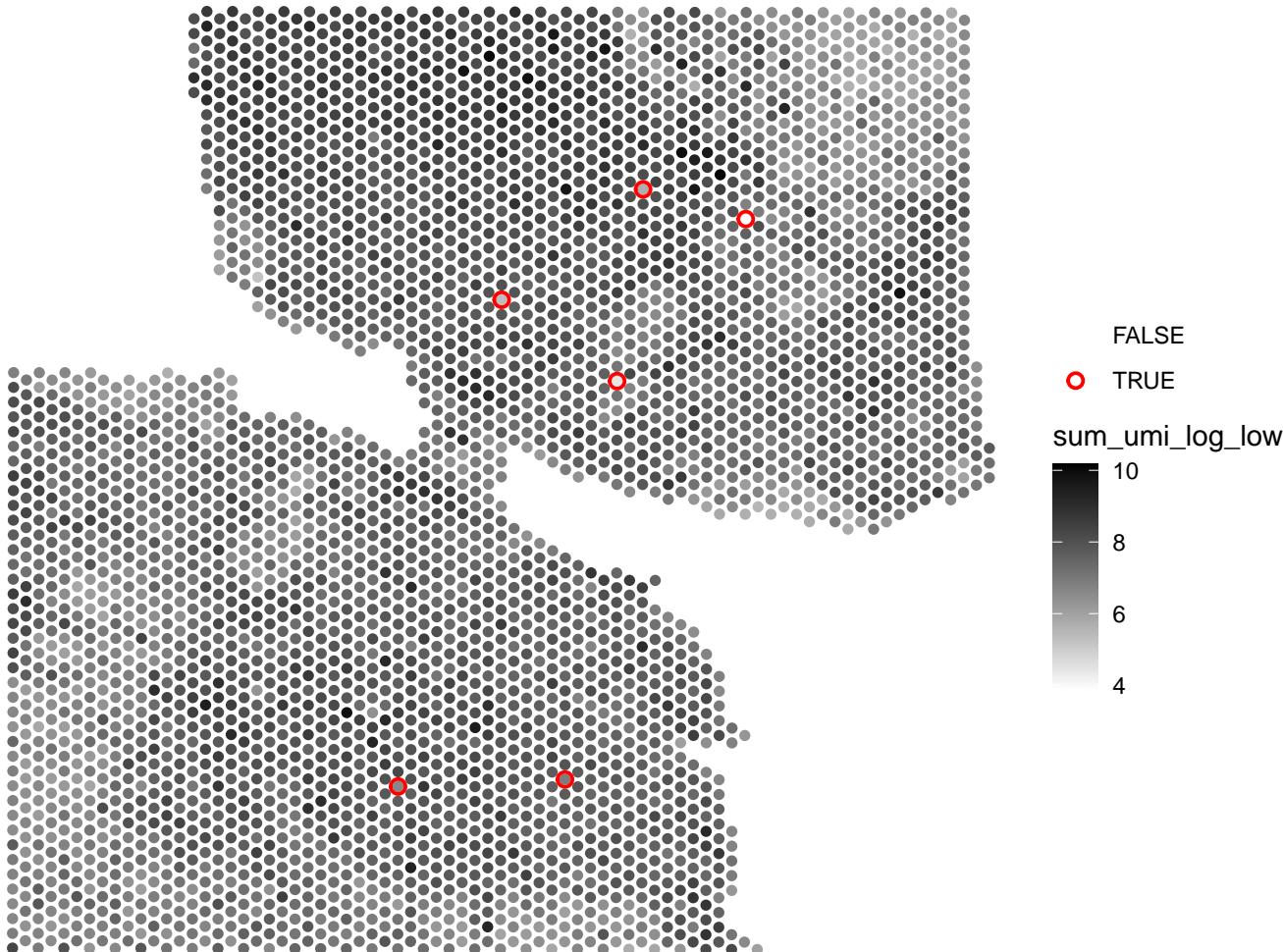
Sample: V13M06–402\_C1



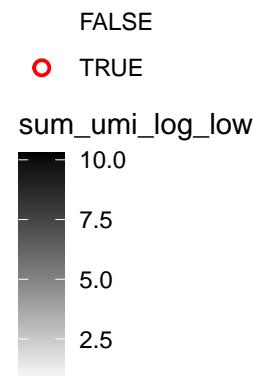
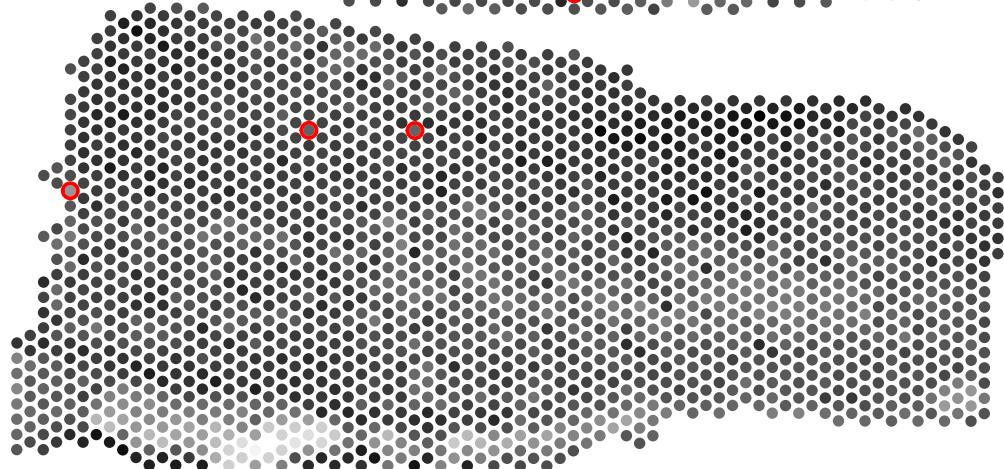
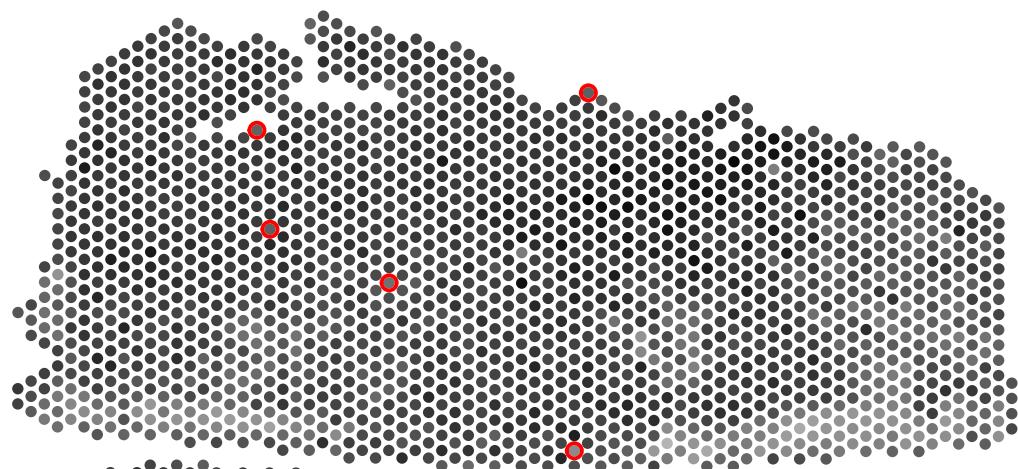
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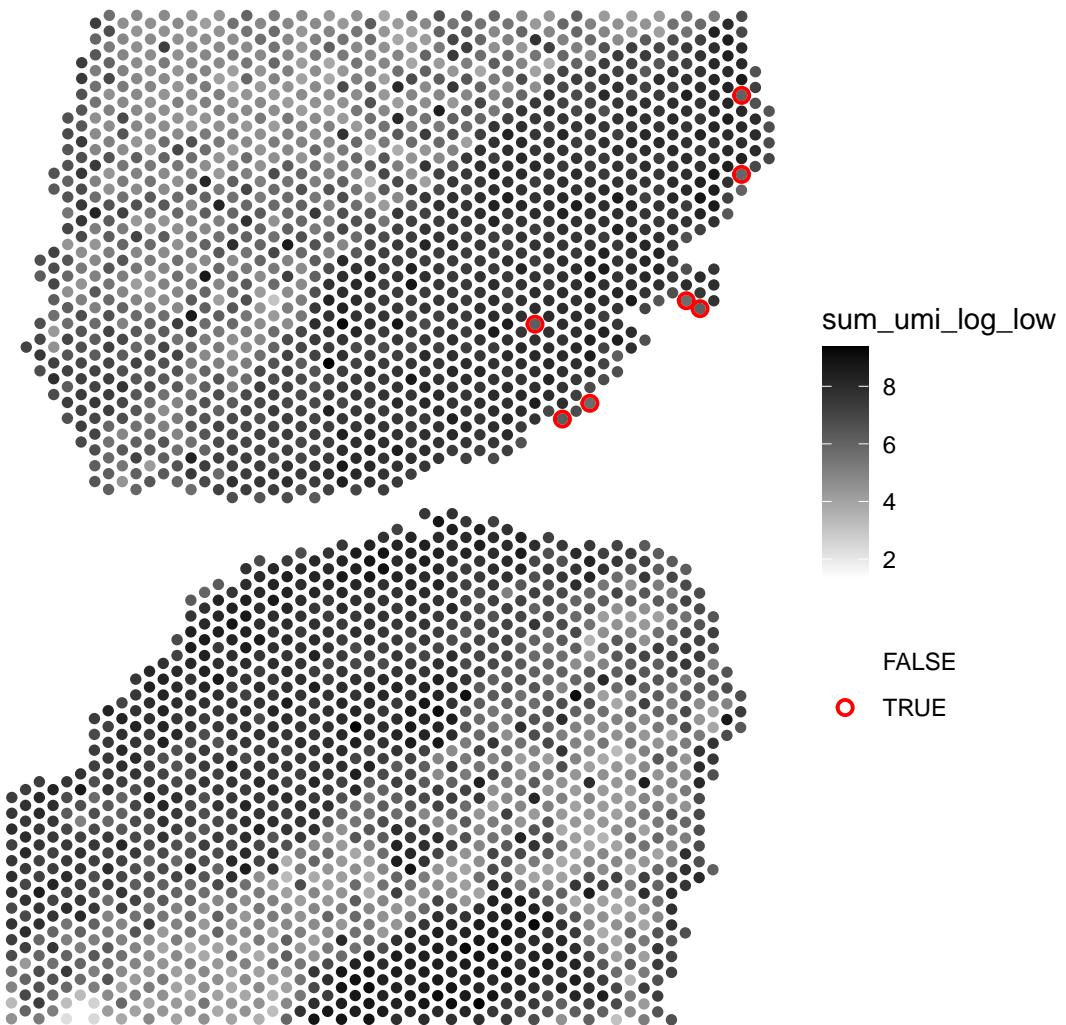
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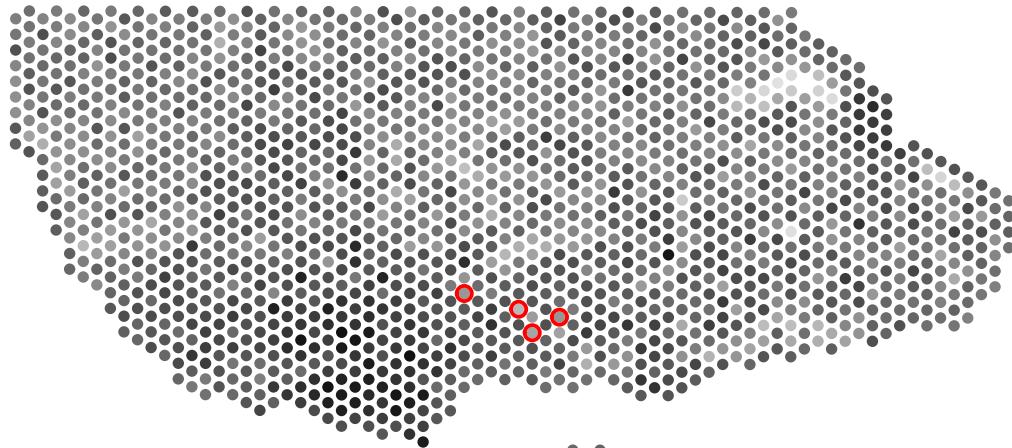
Sample: V13M06–401\_B1



Sample: V13M06–401\_C1



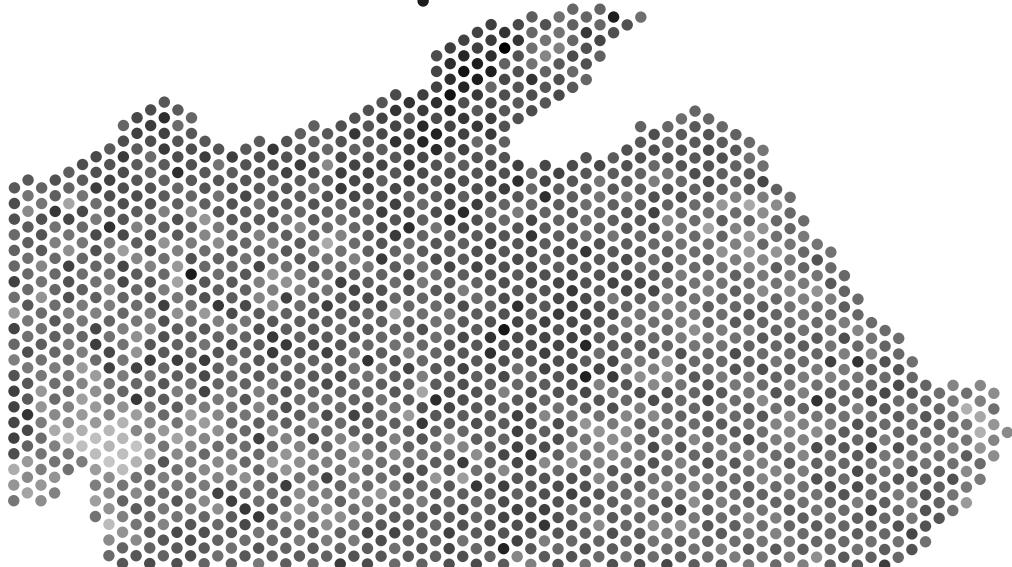
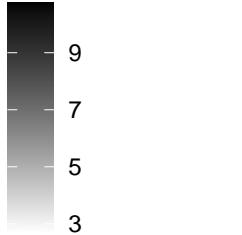
Sample: V13M06-401\_D1



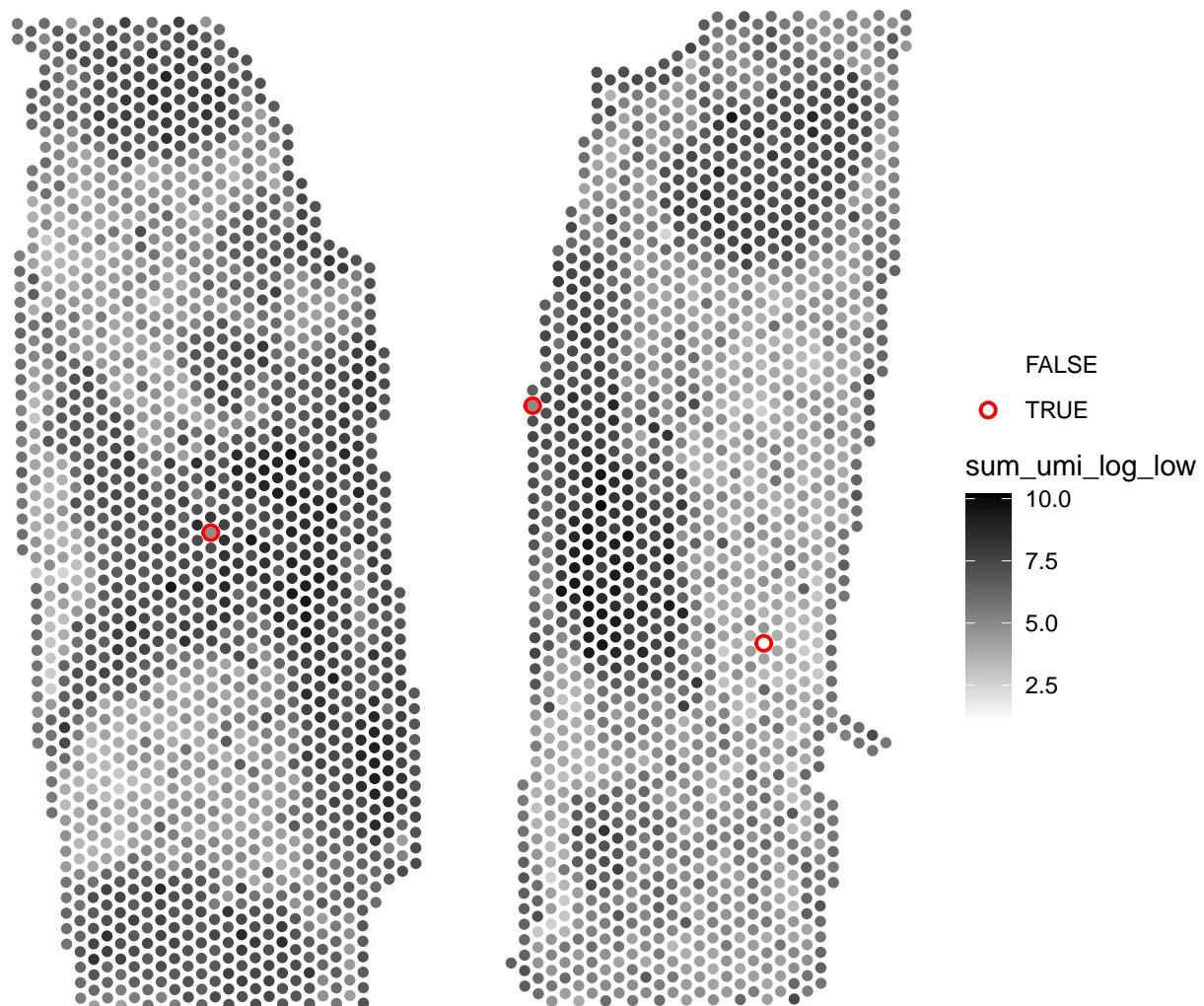
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TRUE

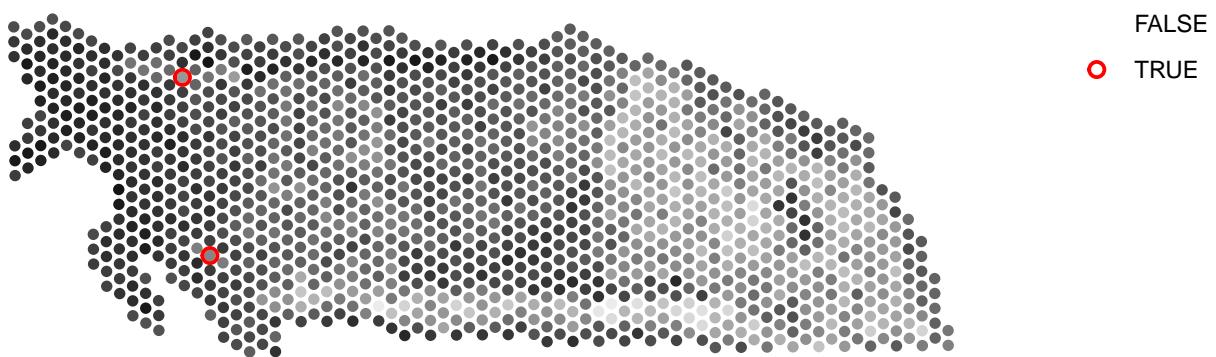
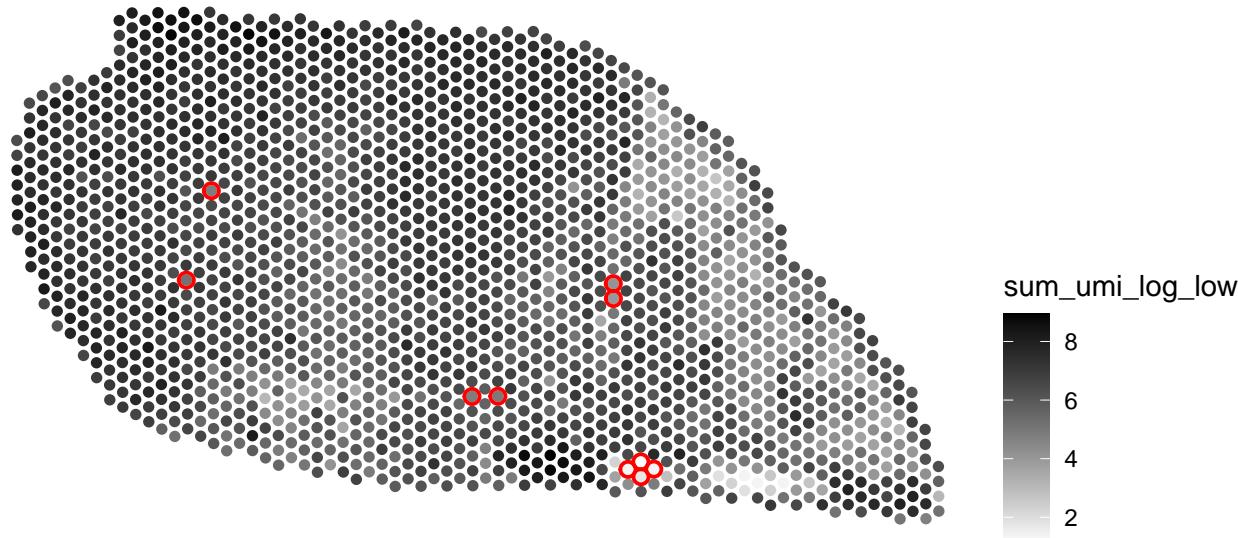
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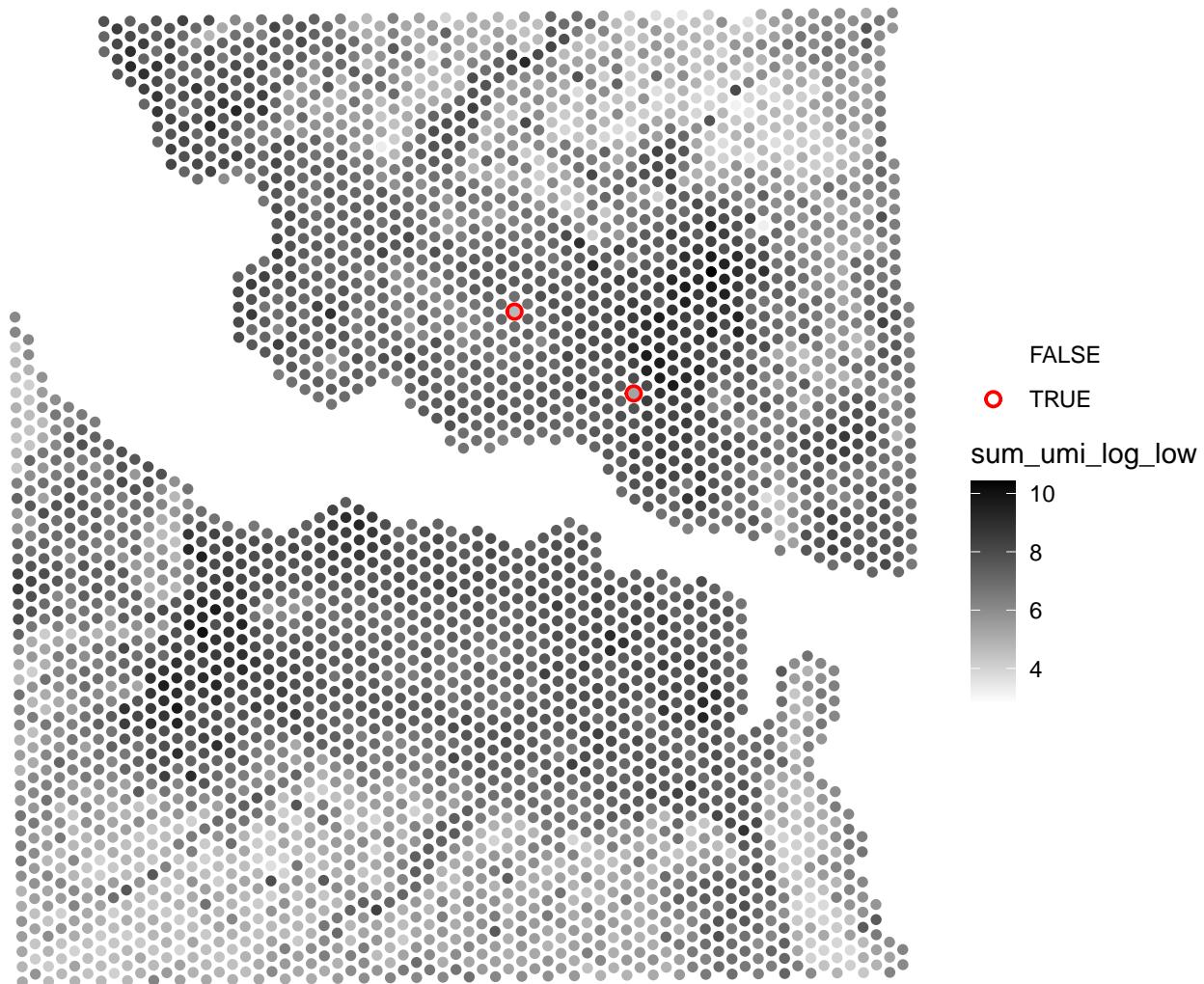
Sample: V13M06–386\_A1



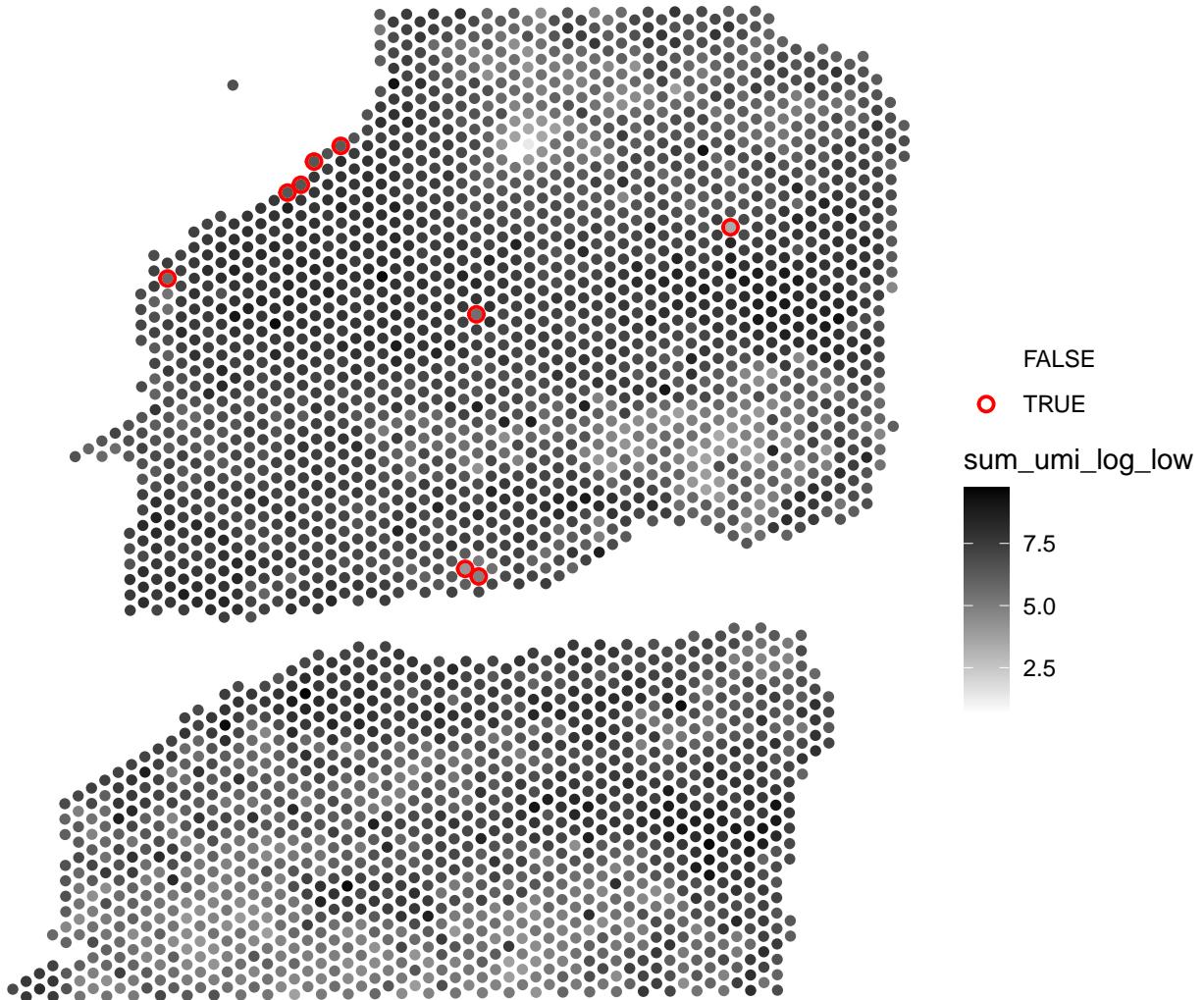
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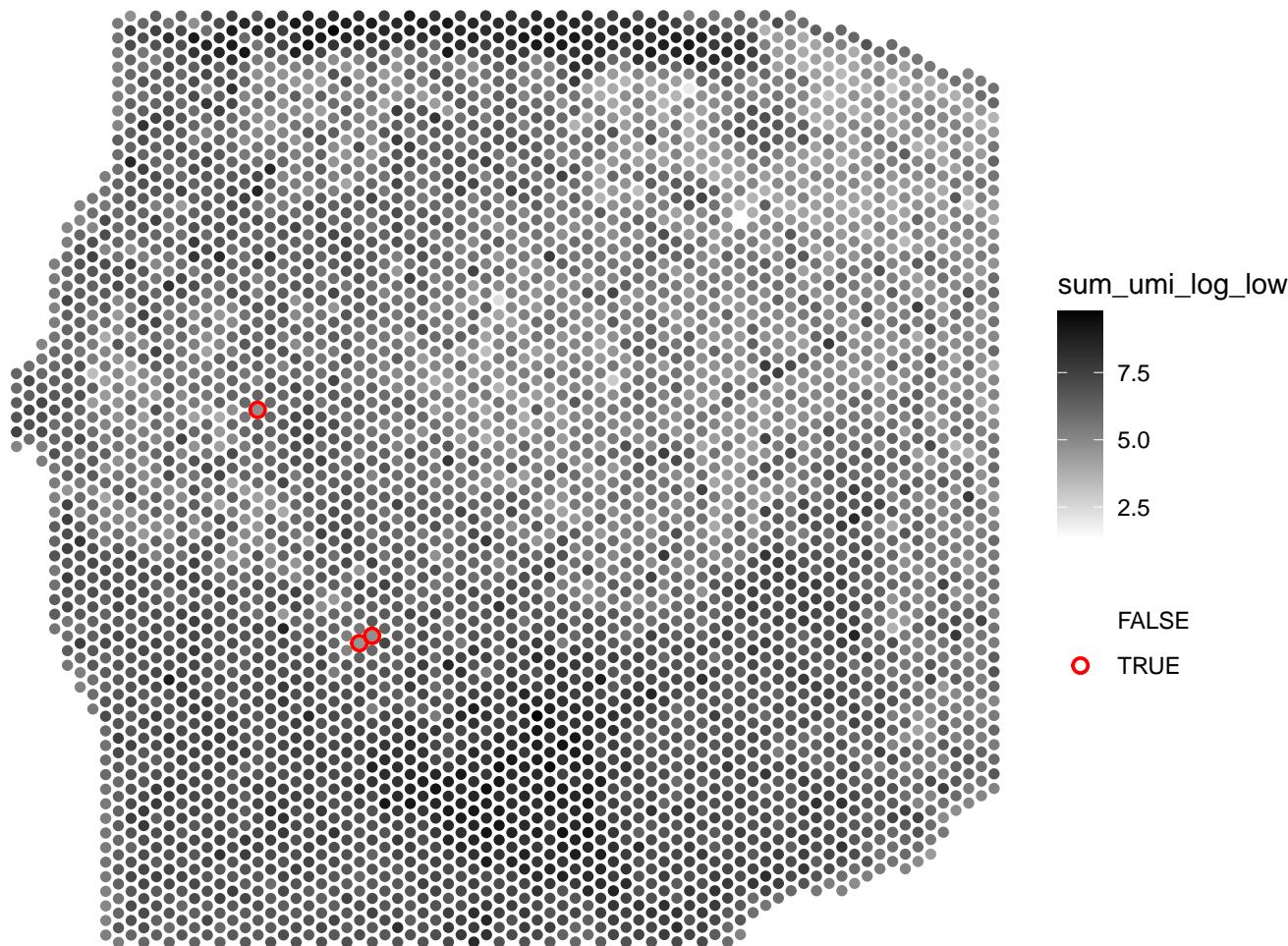
Sample: V13M06-386\_C1



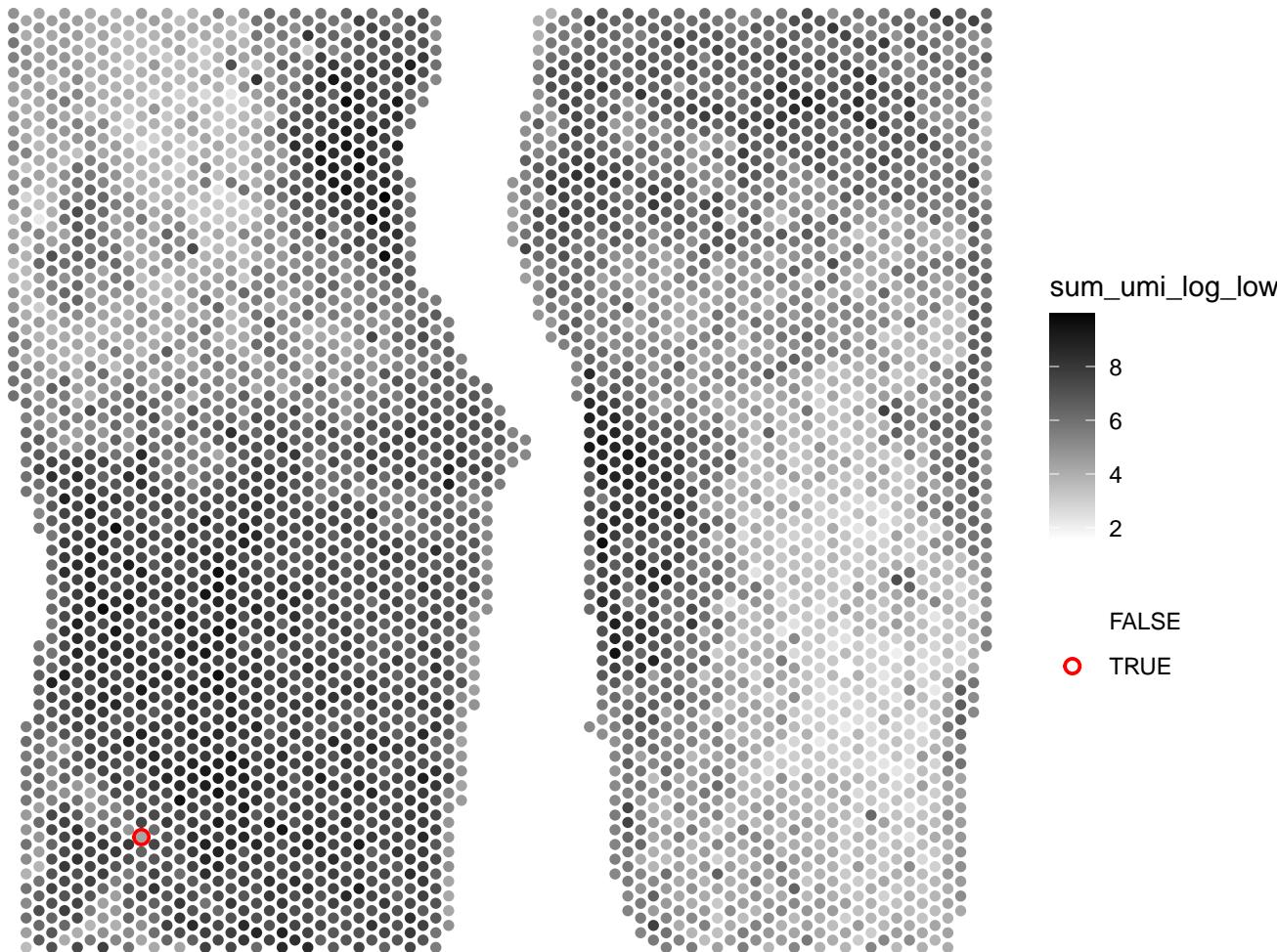
Sample: V13M06-386\_D1



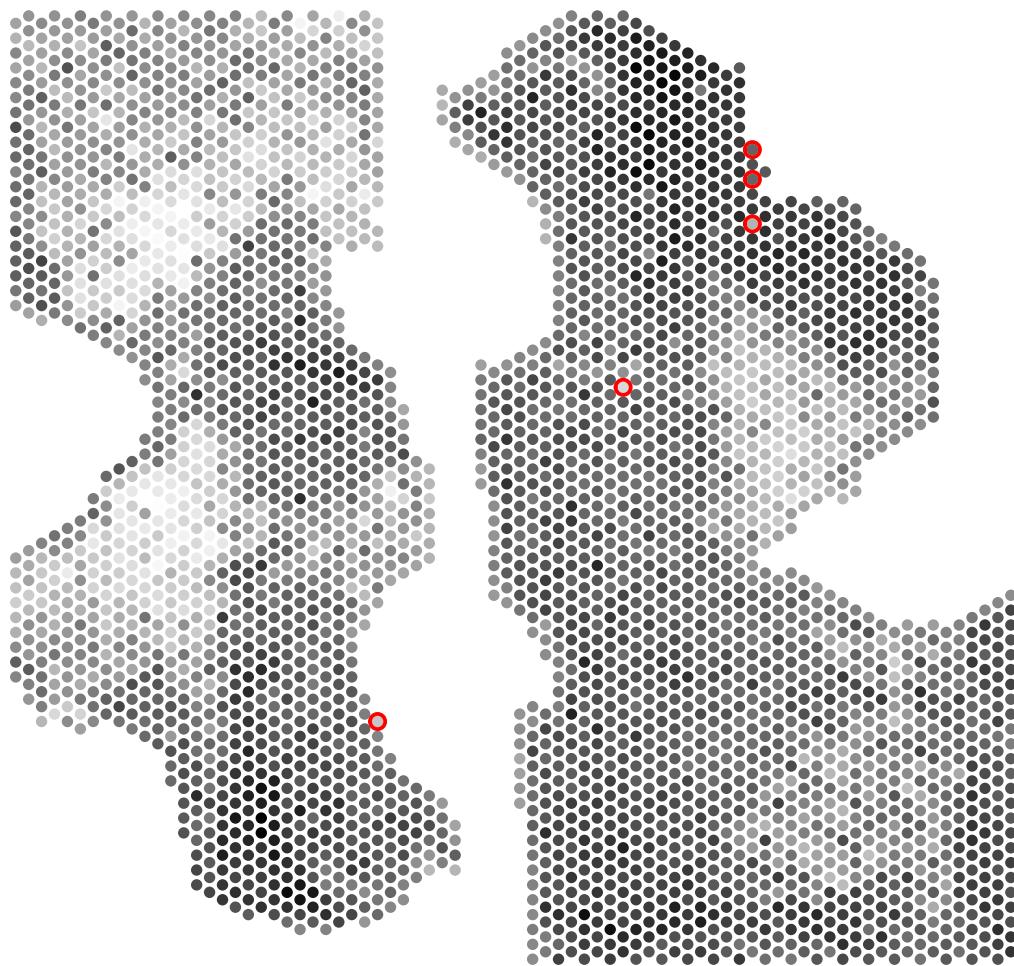
Sample: V13B23–366\_A1



Sample: V13B23–284\_A1



Sample: V13B23–284\_B1



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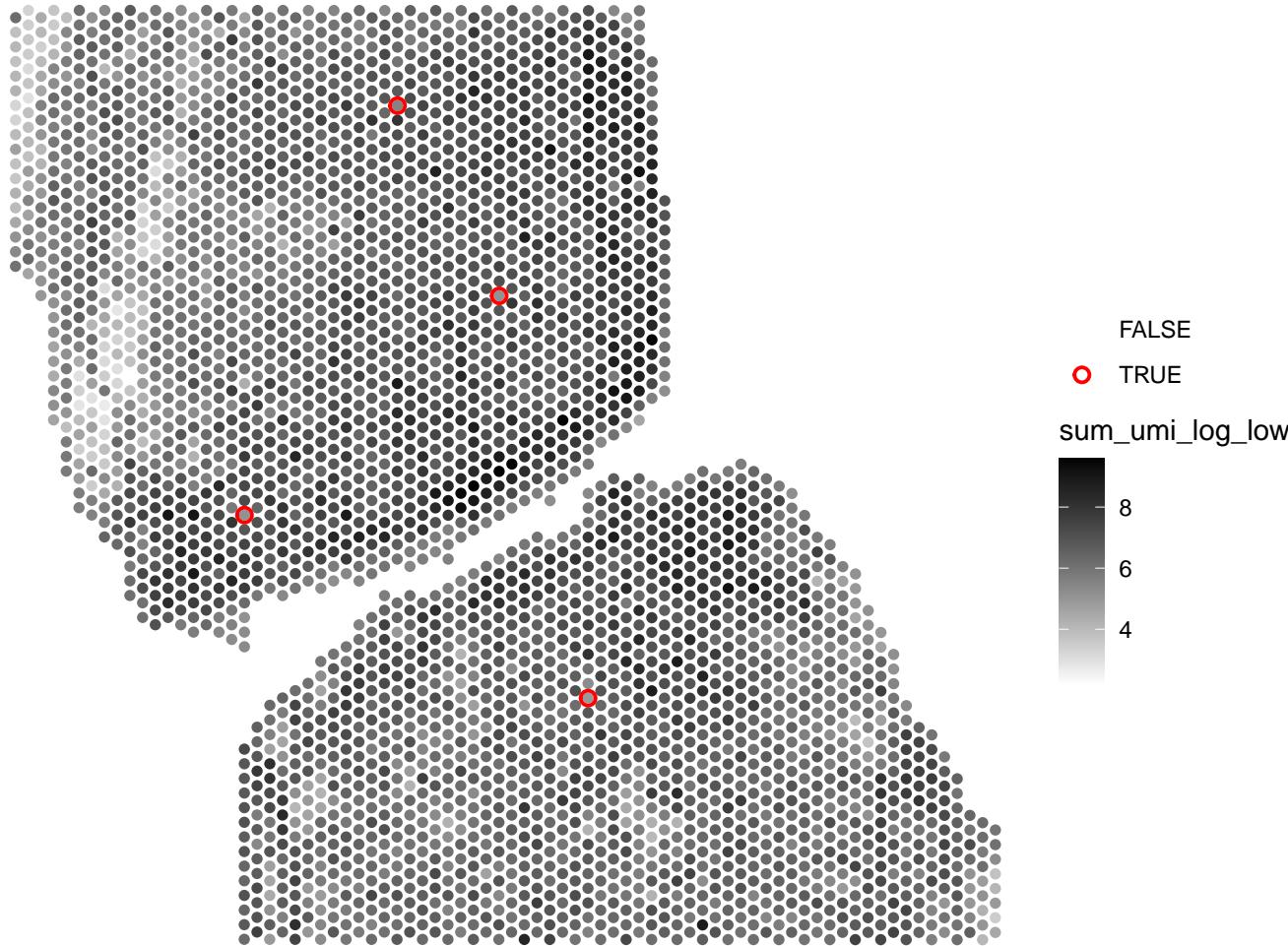


8  
6  
4

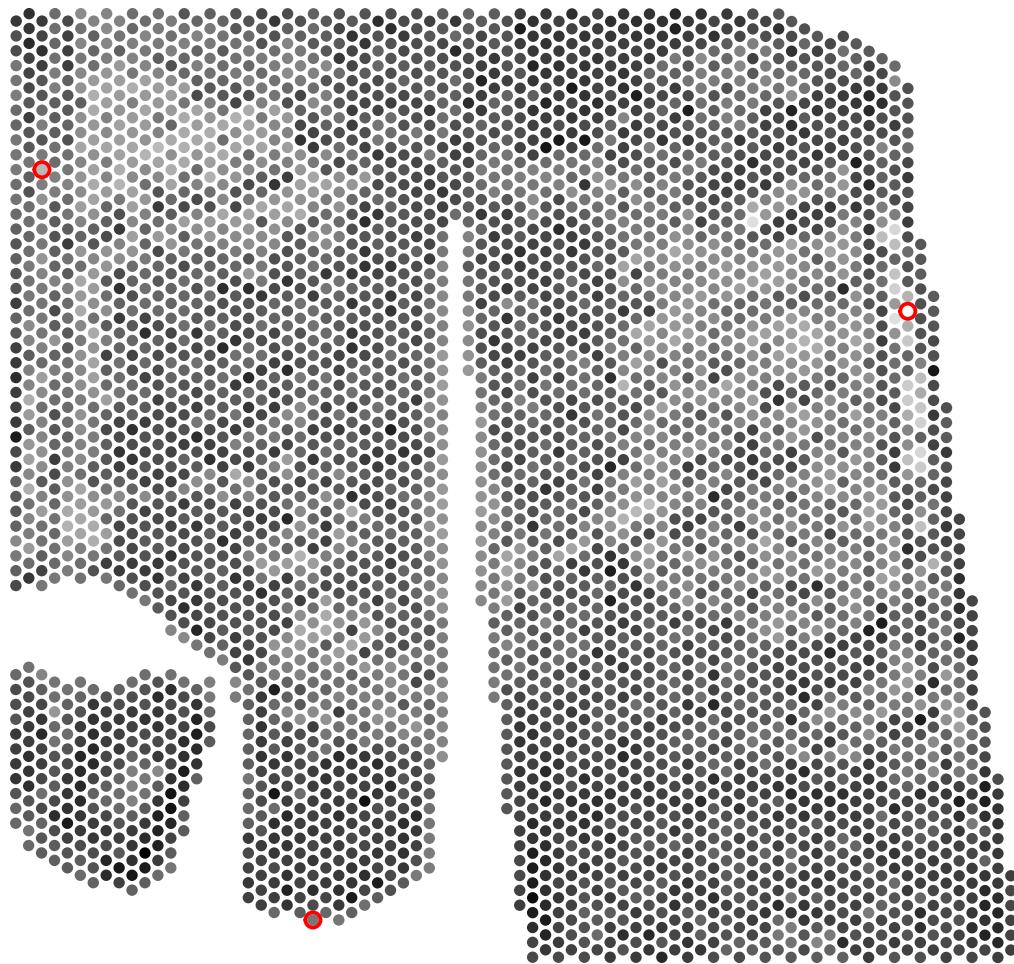
FALSE

TRUE

Sample: V13B23–284\_C1



Sample: V13B23–284\_D1



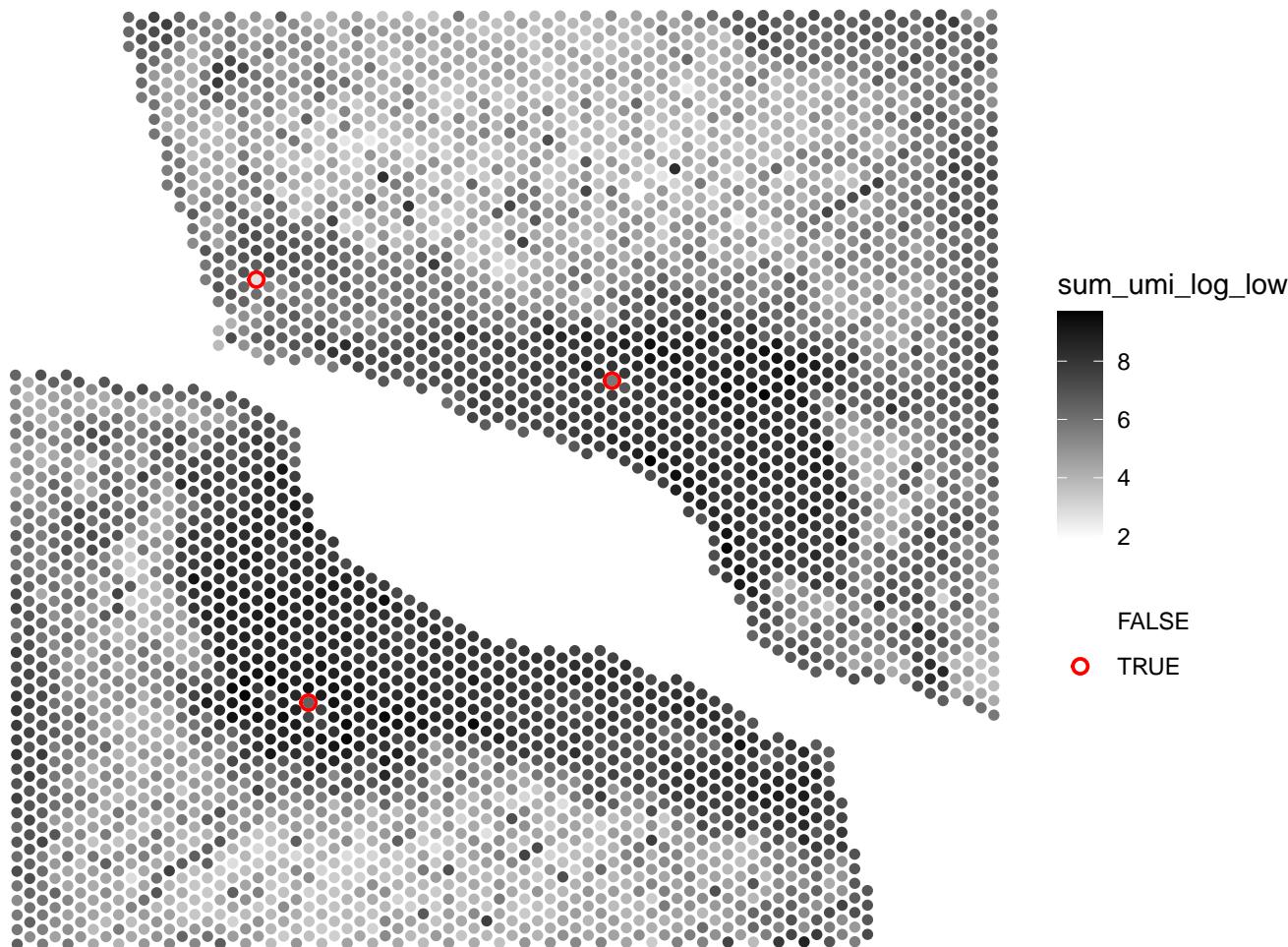
FALSE

TRUE

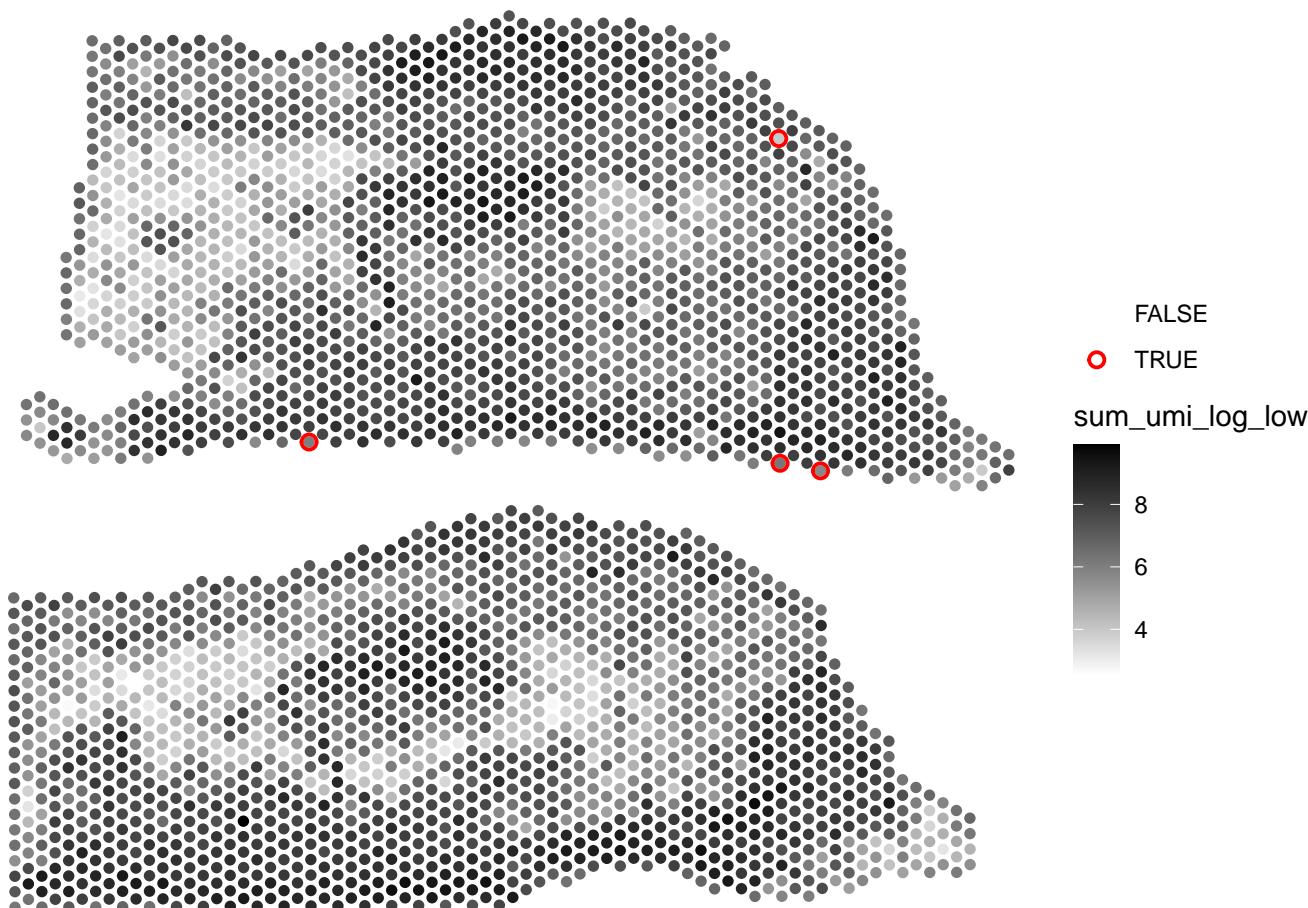
sum\_umi\_log\_low



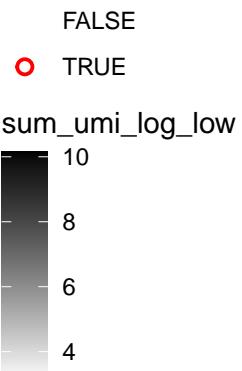
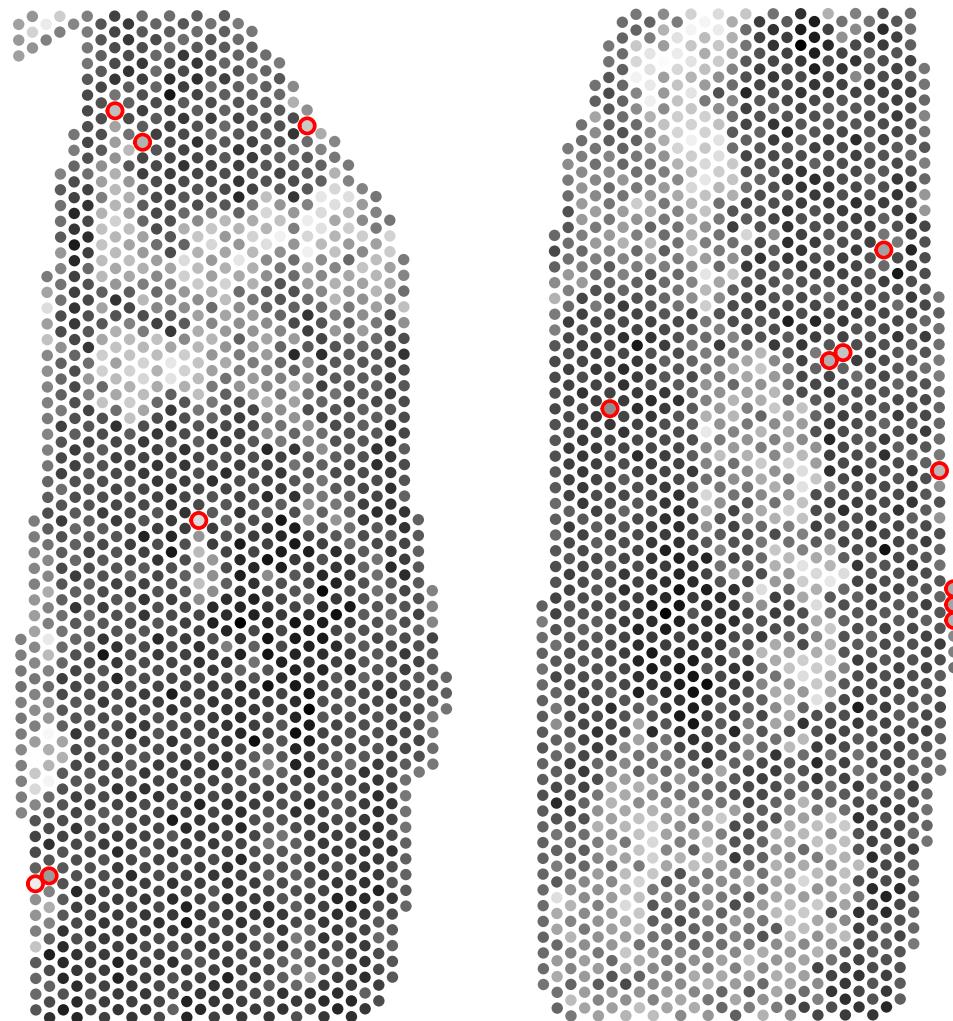
Sample: V13F27-339\_A1



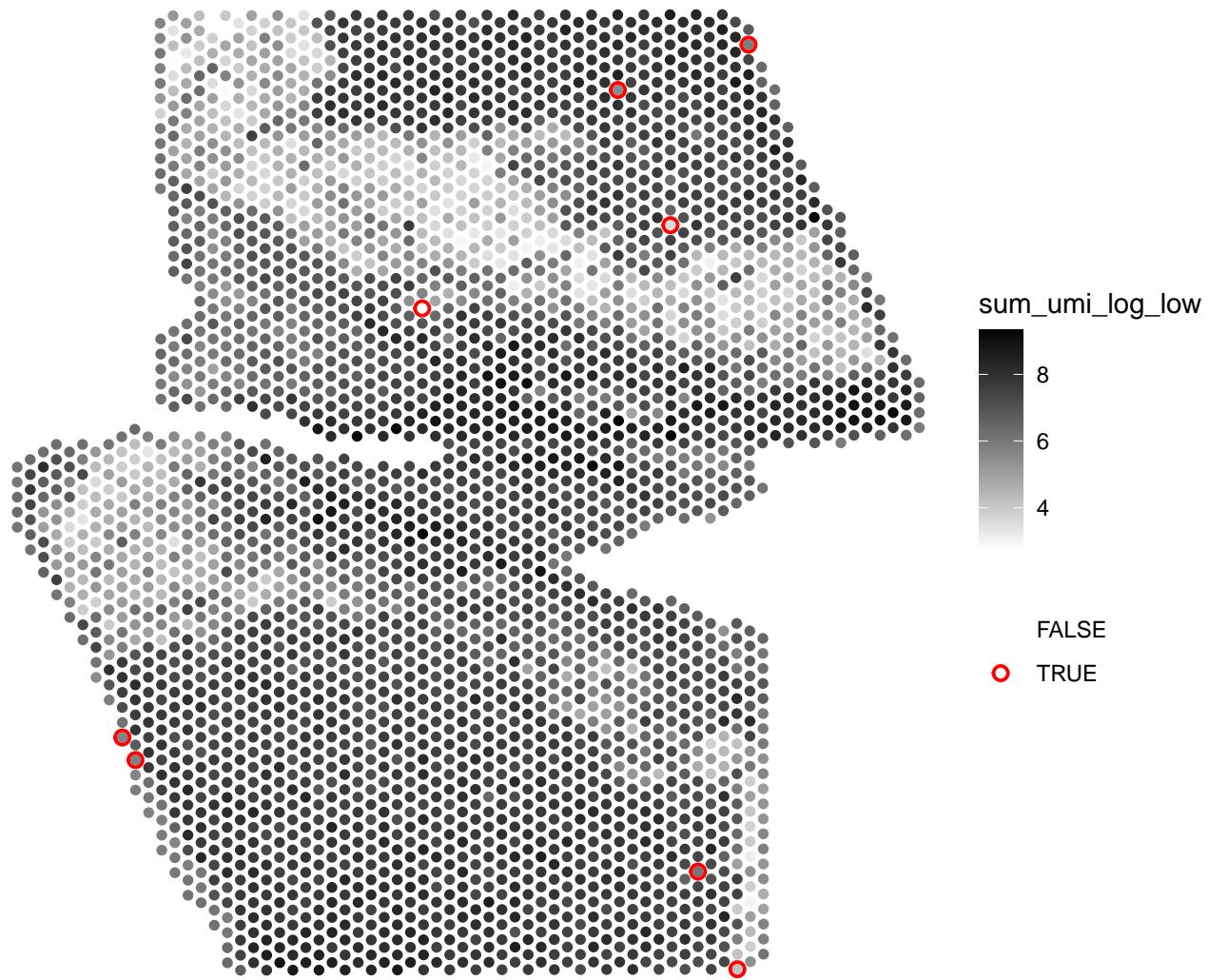
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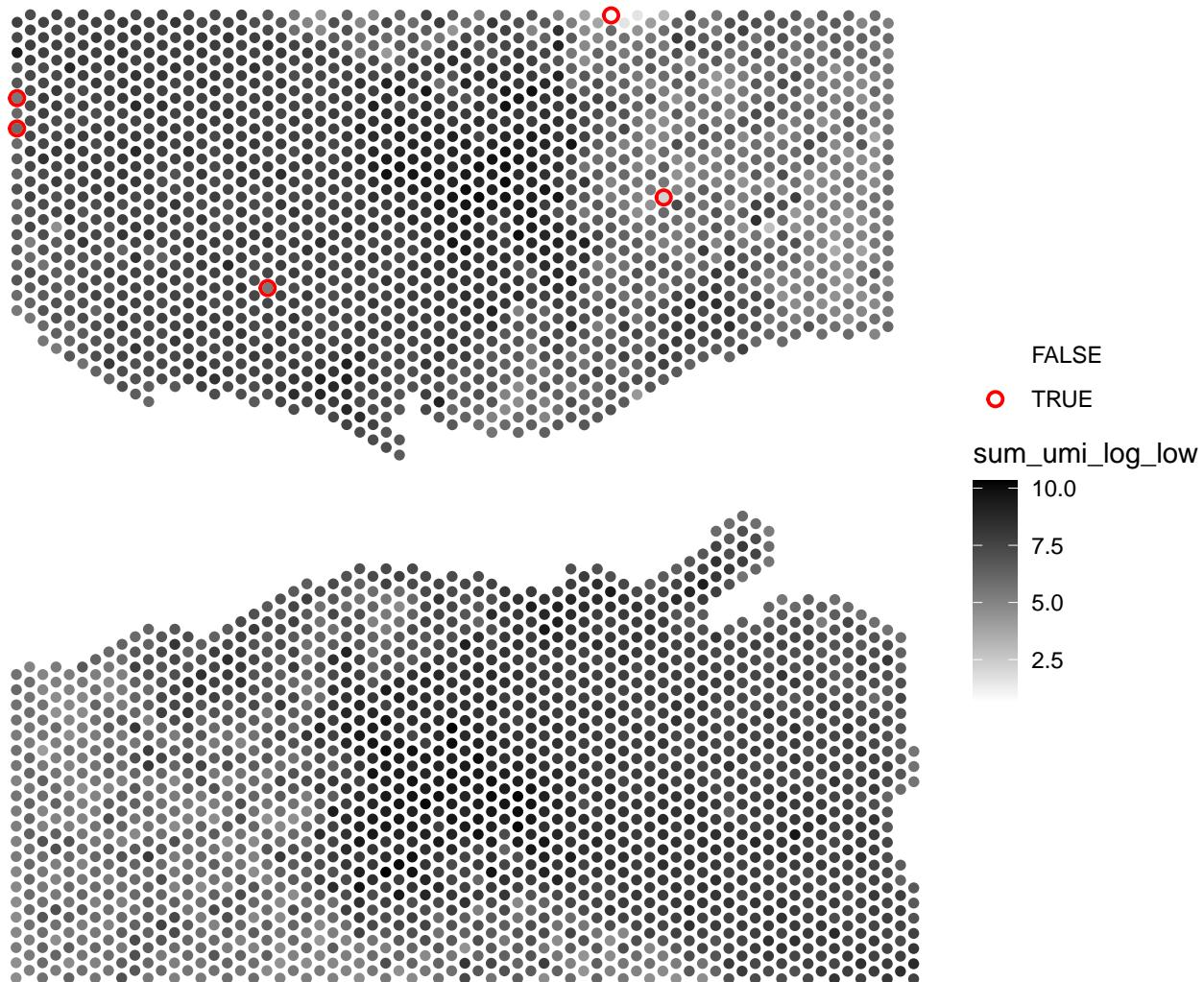
Sample: V13F27-339\_C1



Sample: V13F27-339\_D1



Sample: V13B23-286\_A1



Sample: V13B23-286\_B1

