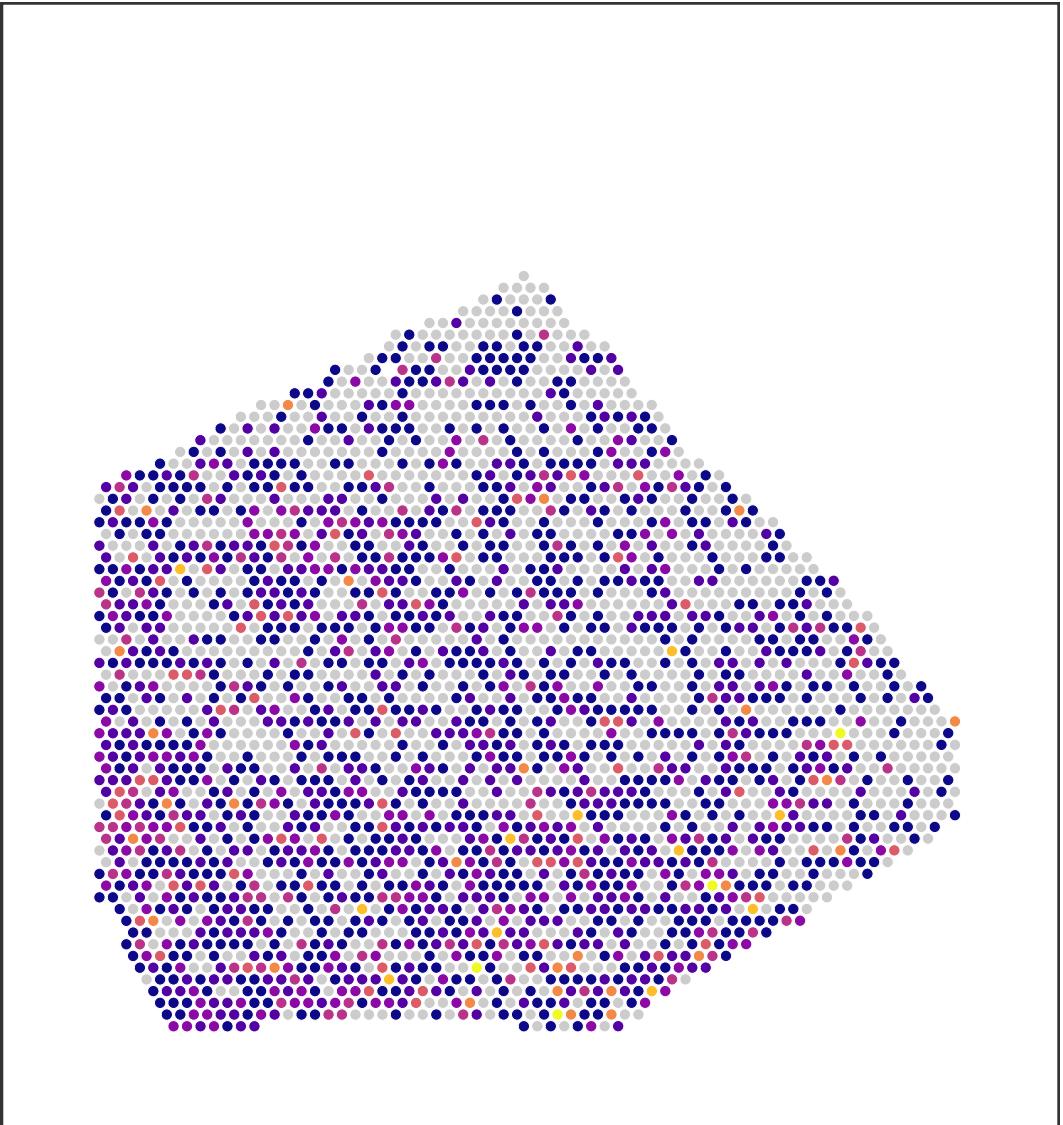
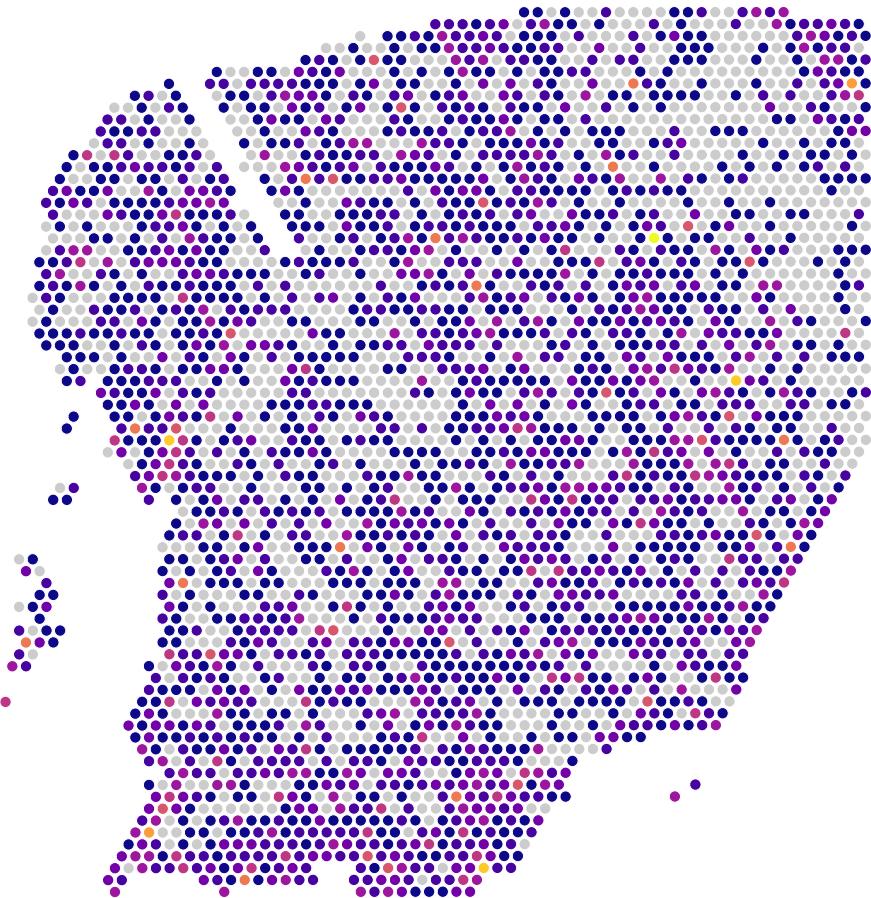


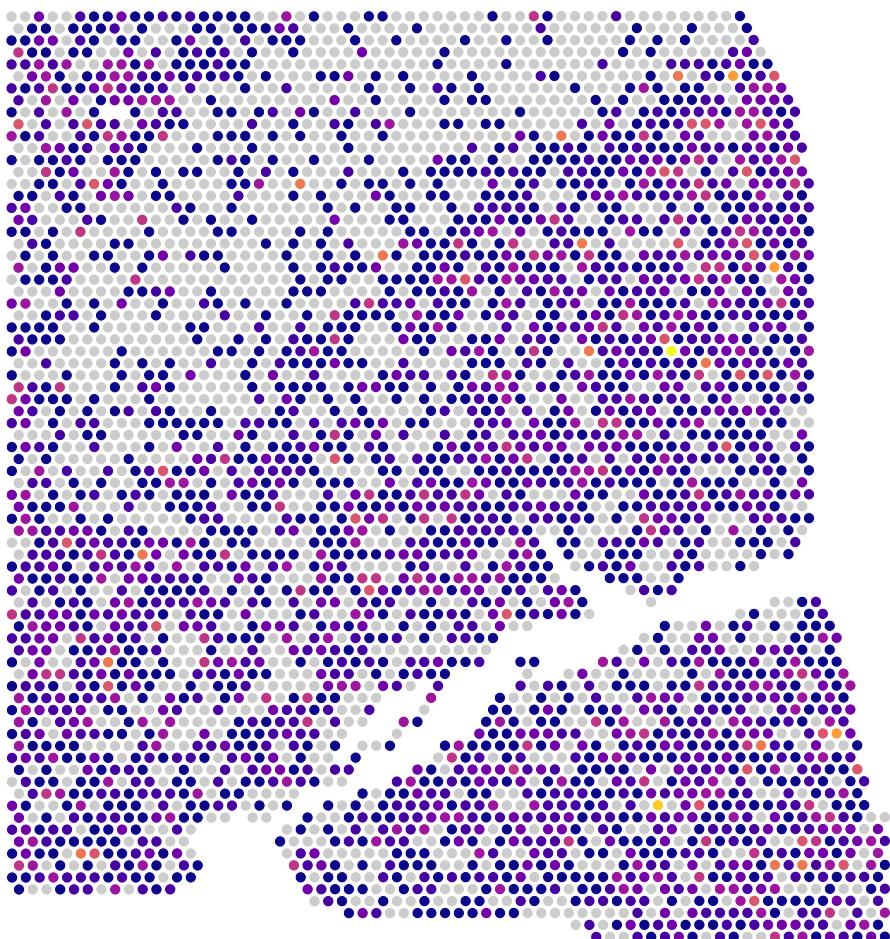
# Inhib (Br2720\_Ant\_IF)



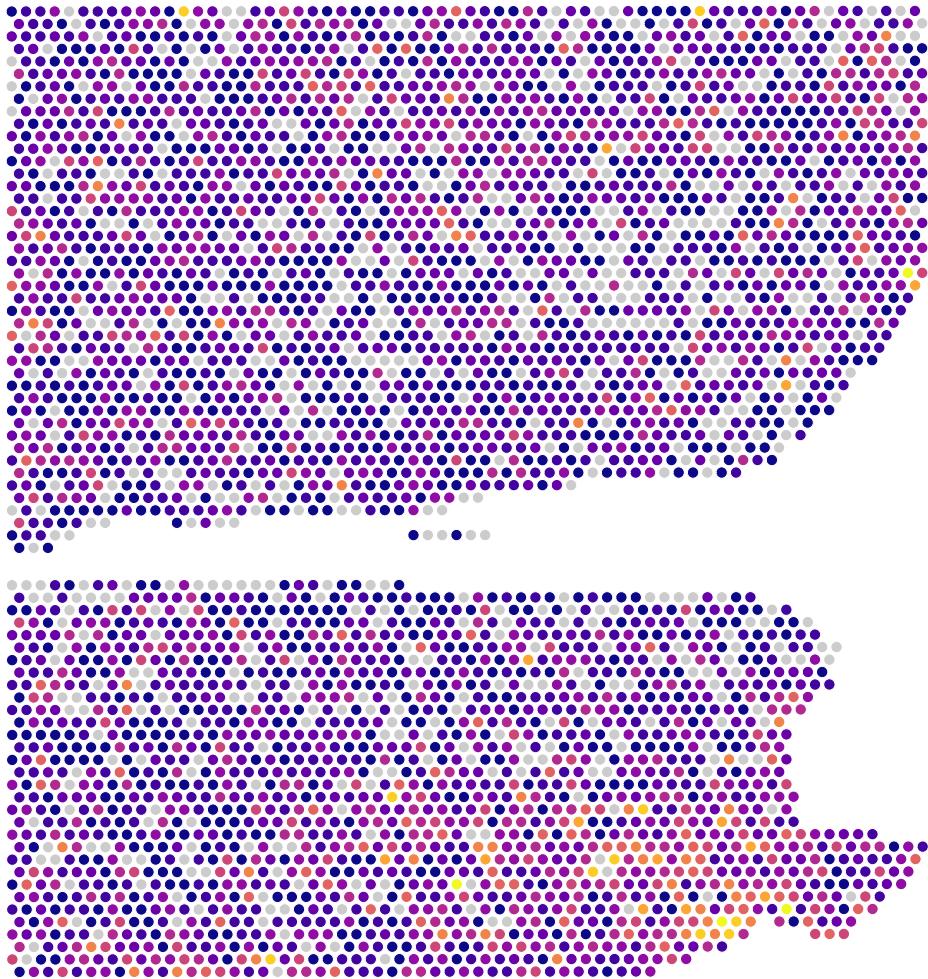
# Inhib (Br6432\_Ant\_IF)



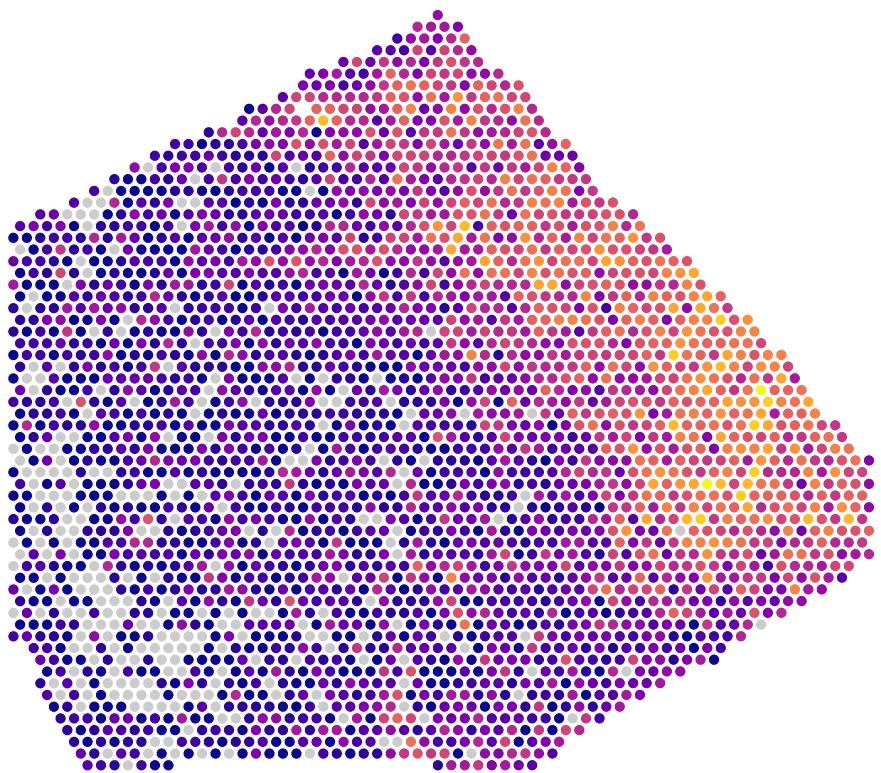
# Inhib (Br6522\_Ant\_IF)



# Inhib (Br8667\_Post\_IF)



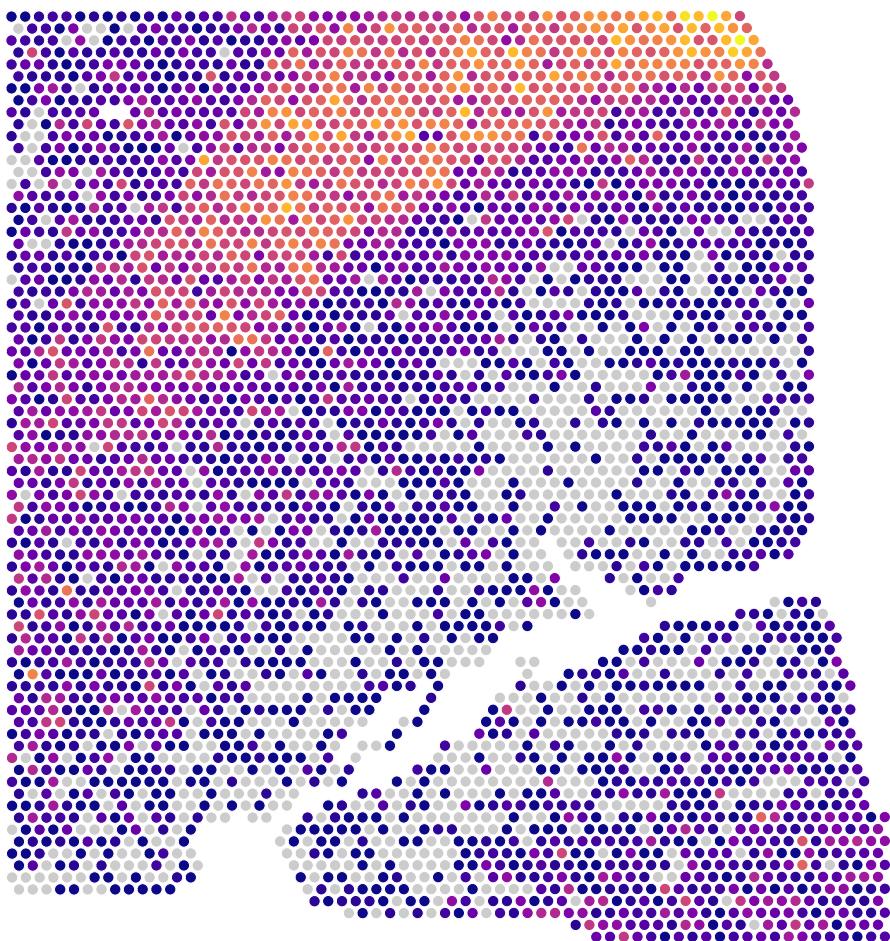
# Oligo (Br2720\_Ant\_IF)



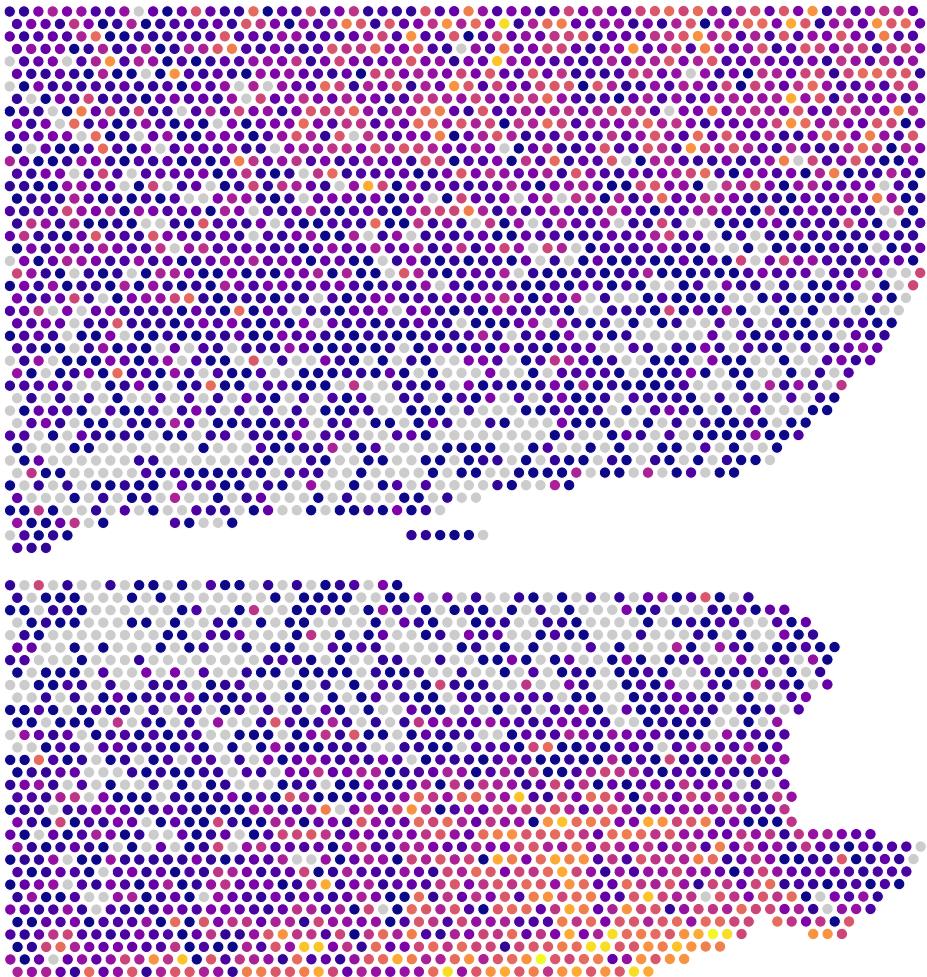
# Oligo (Br6432\_Ant\_IF)



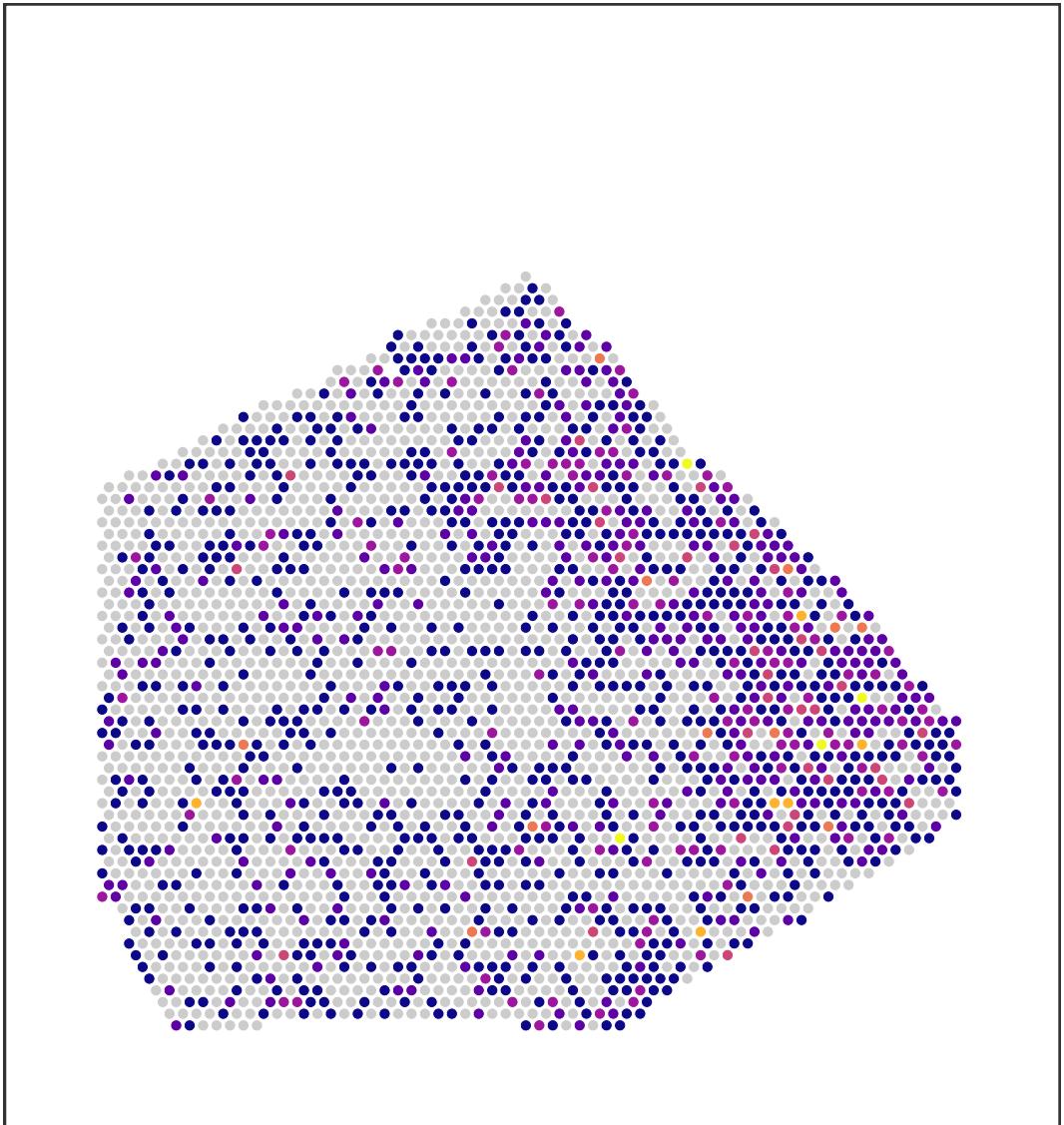
# Oligo (Br6522\_Ant\_IF)



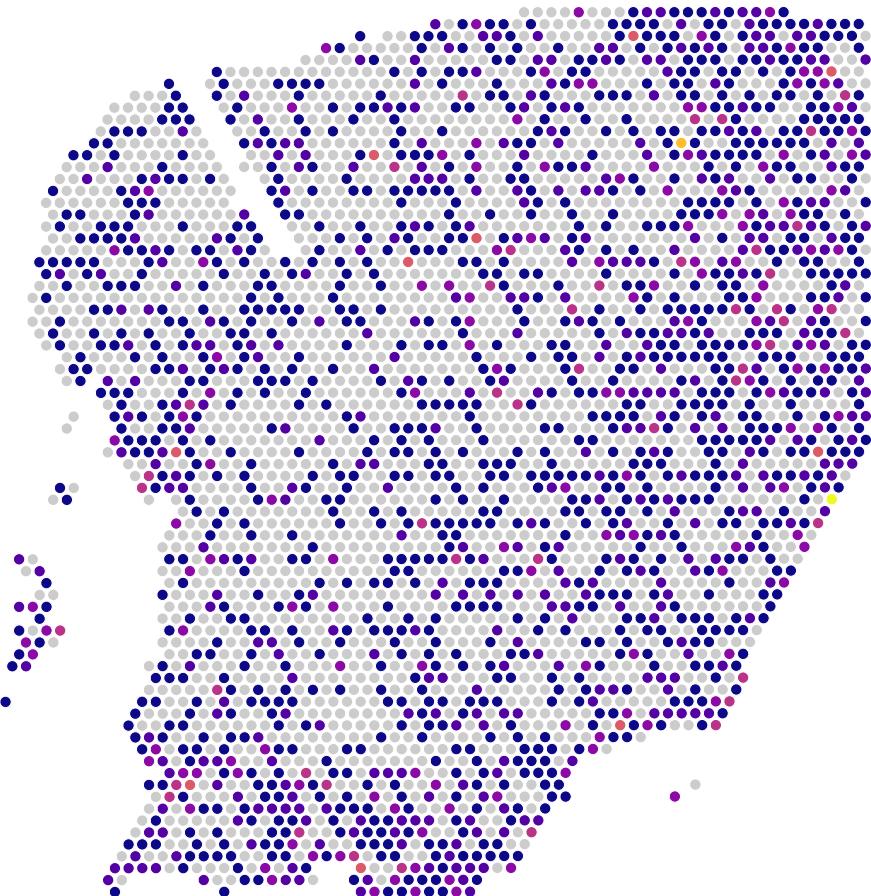
# Oligo (Br8667\_Post\_IF)



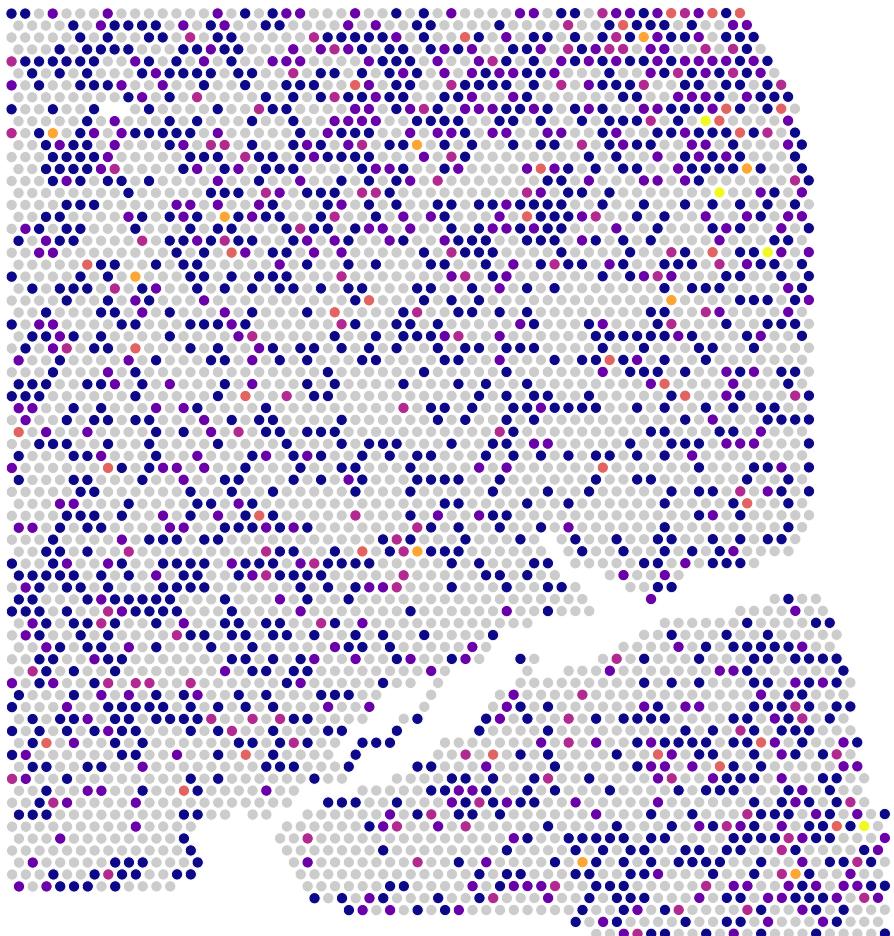
# OPC (Br2720\_Ant\_IF)



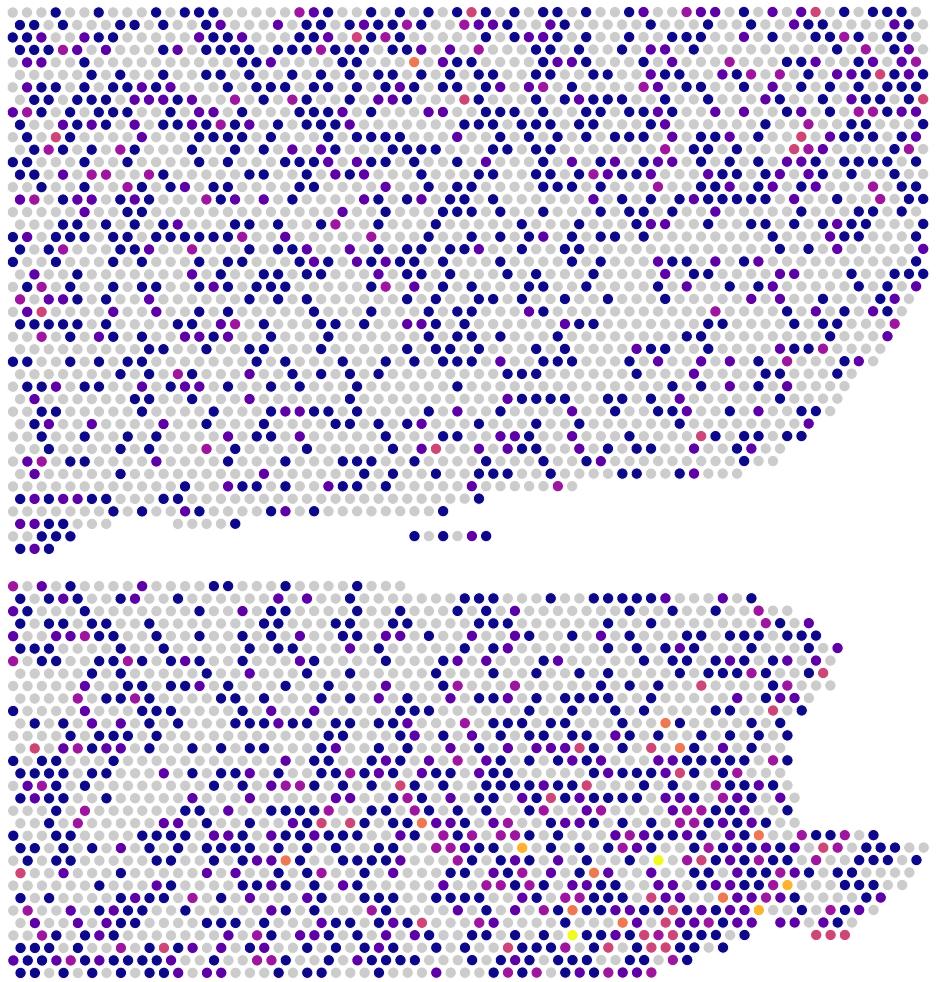
# OPC (Br6432\_Ant\_IF)



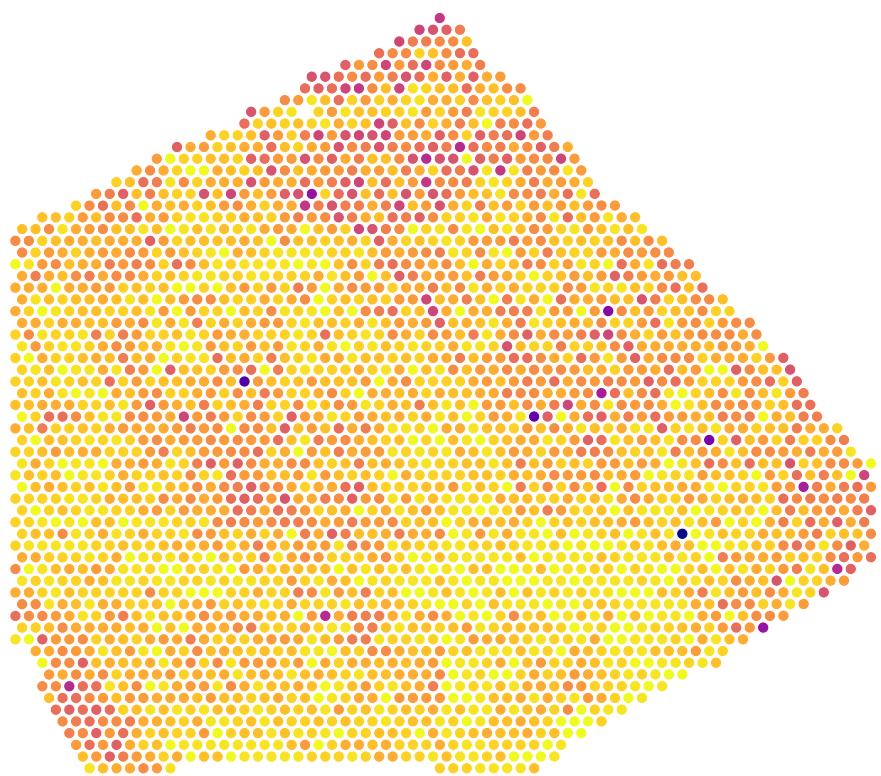
# OPC (Br6522\_Ant\_IF)



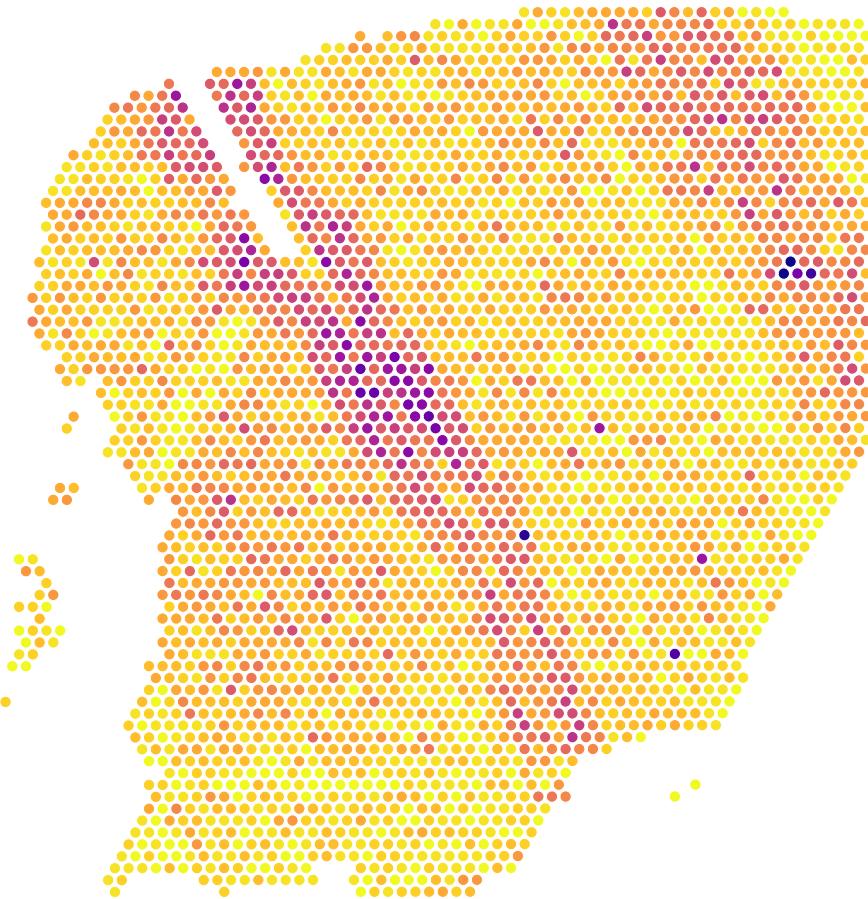
# OPC (Br8667\_Post\_IF)



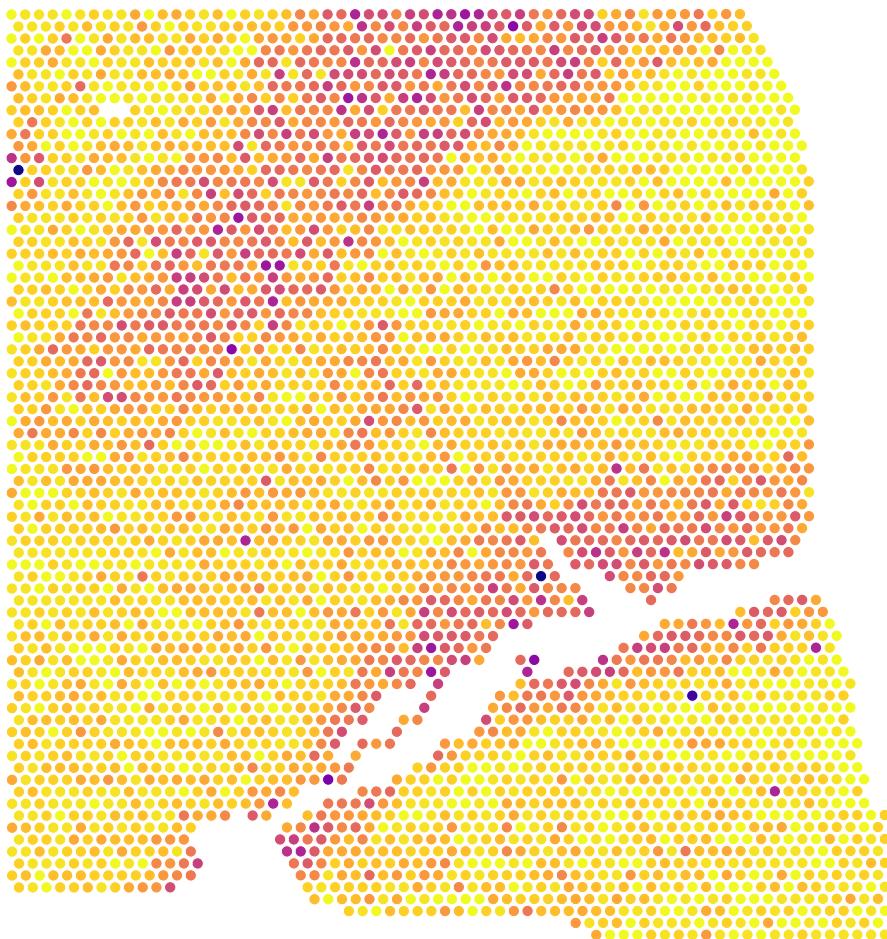
# Excit\_L3/4/5 (Br2720\_Ant\_IF)



# Excit\_L3/4/5 (Br6432\_Ant\_IF)



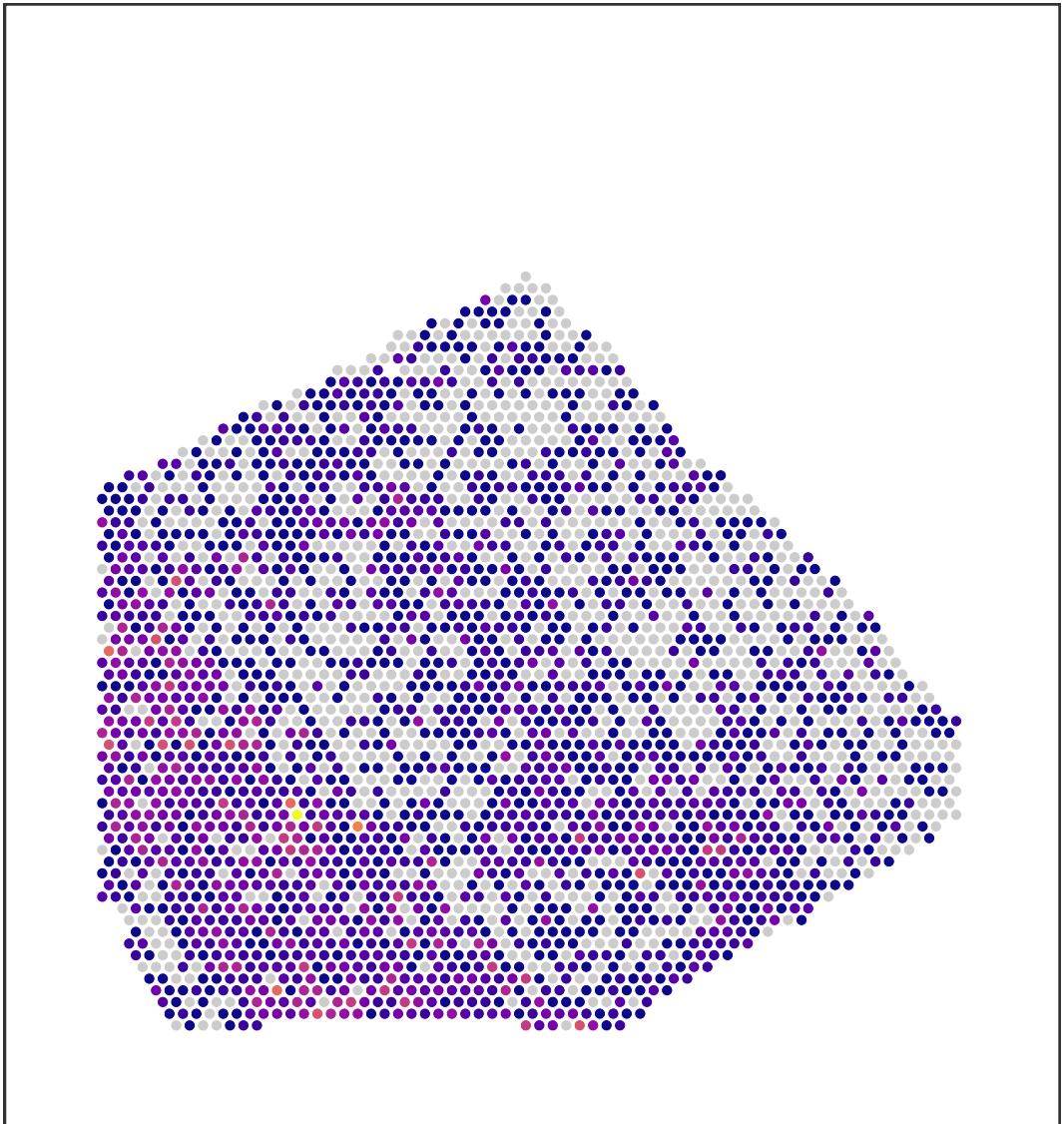
# Excit\_L3/4/5 (Br6522\_Ant\_IF)



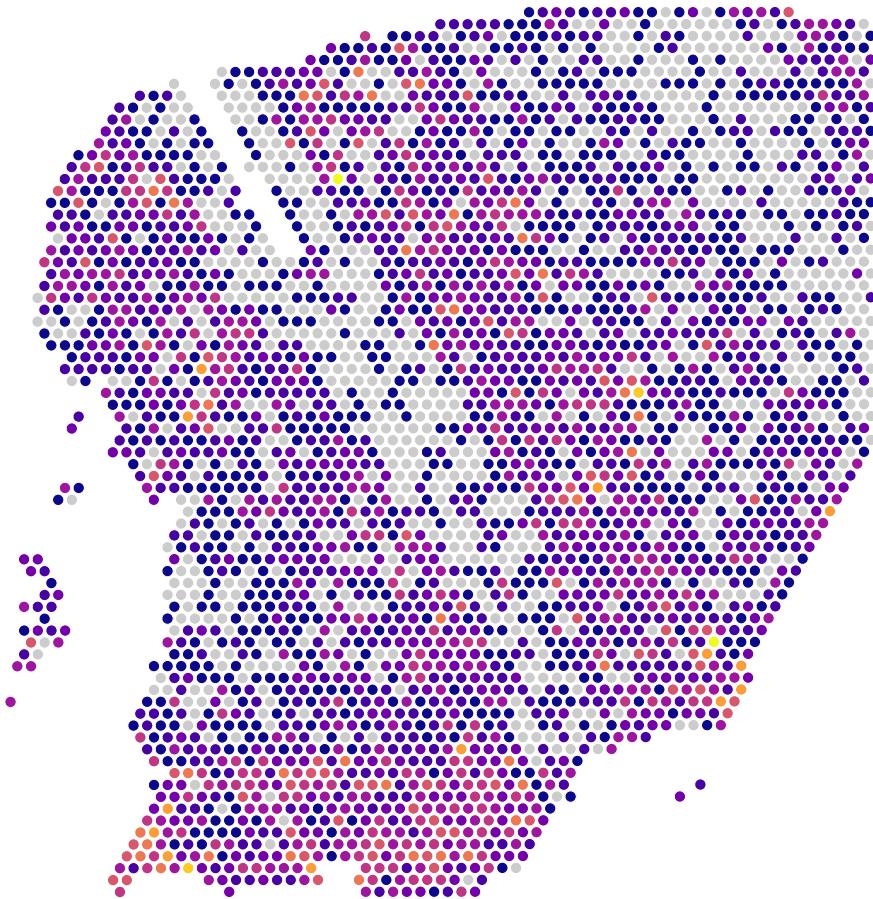
# Excit\_L3/4/5 (Br8667\_Post\_IF)



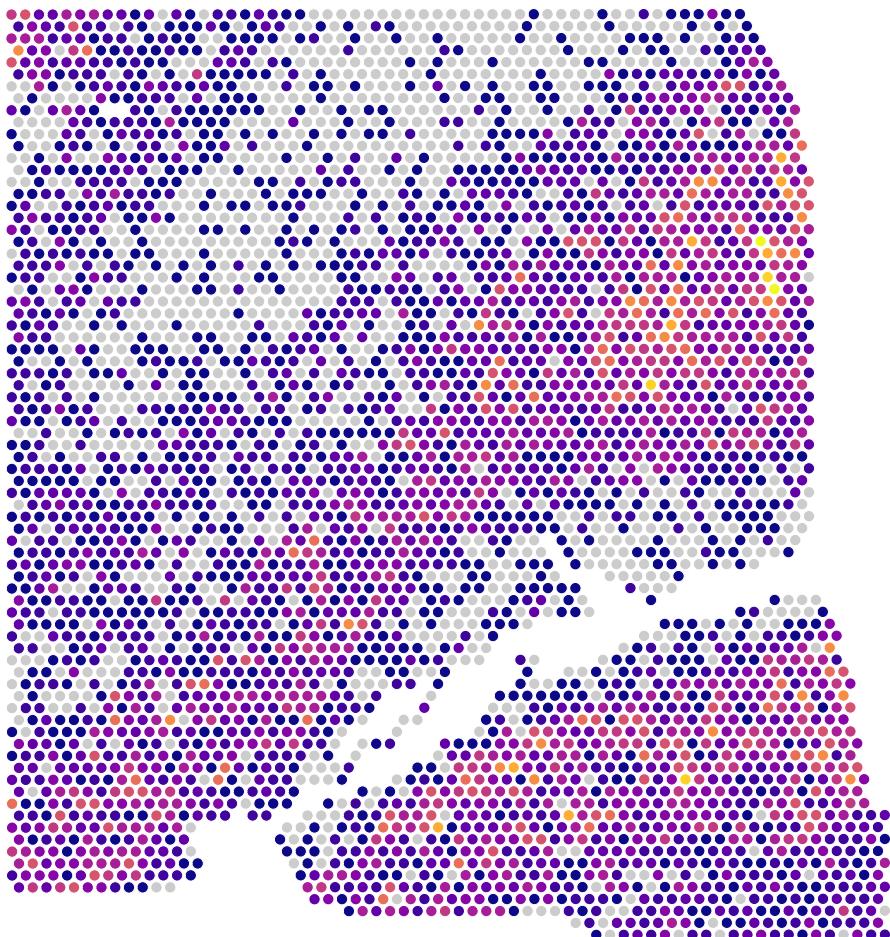
# Excit\_L3 (Br2720\_Ant\_IF)



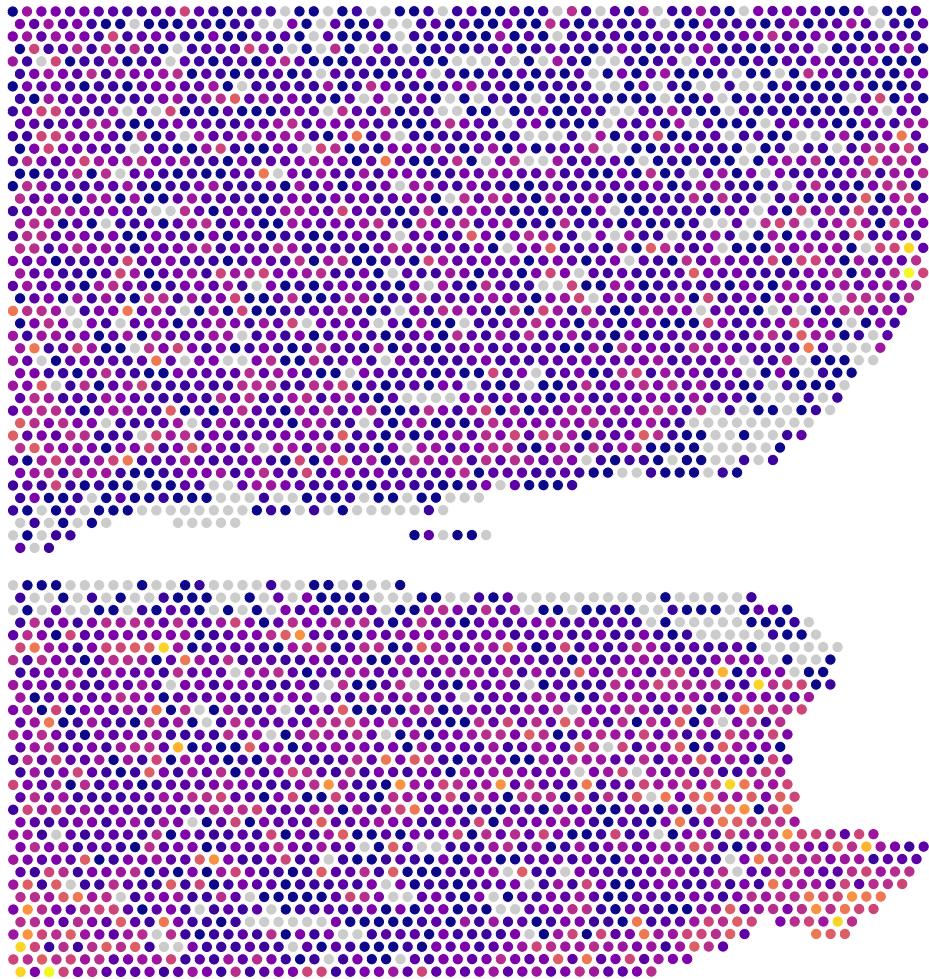
# Excit\_L3 (Br6432\_Ant\_IF)



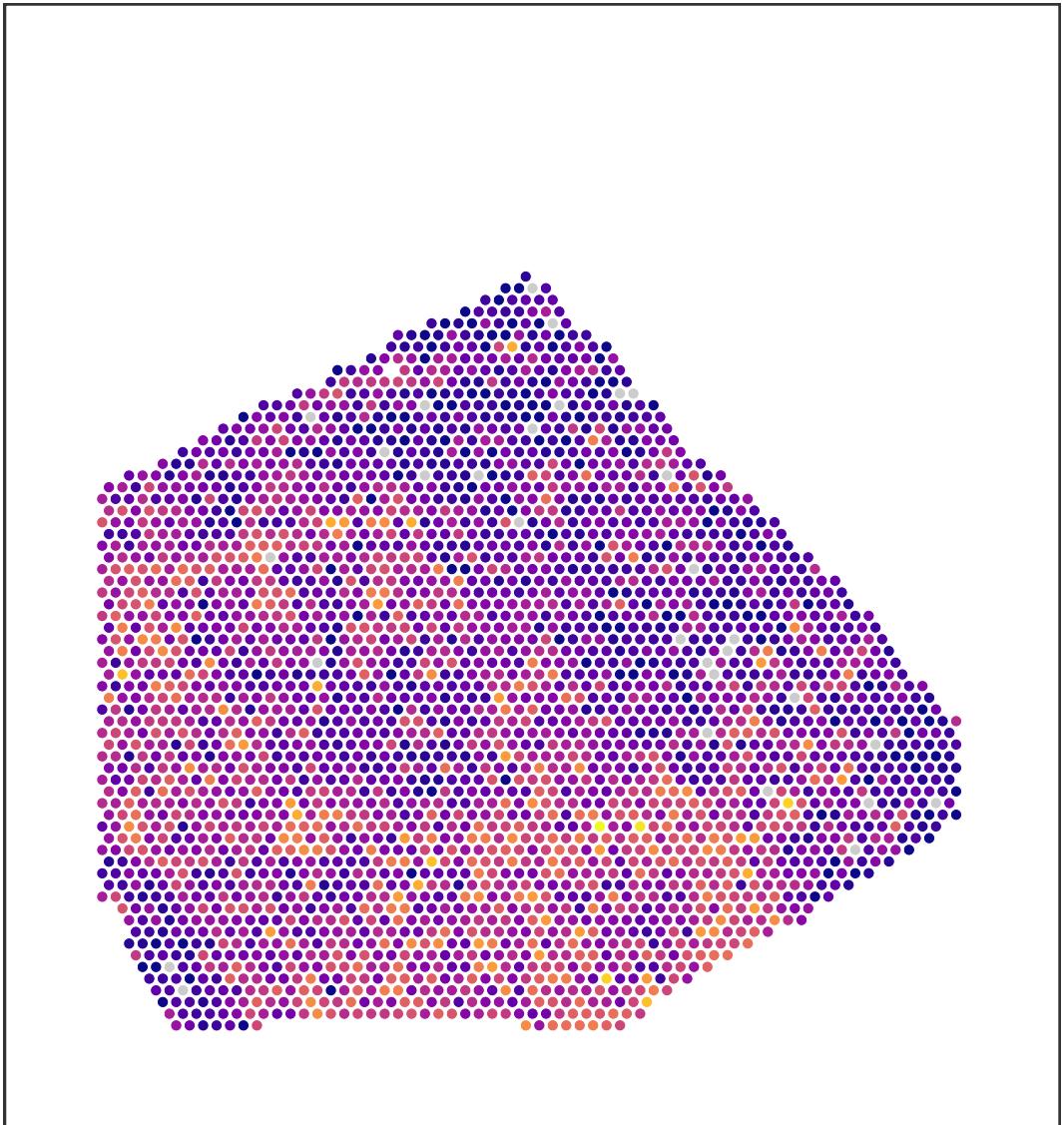
# Excit\_L3 (Br6522\_Ant\_IF)



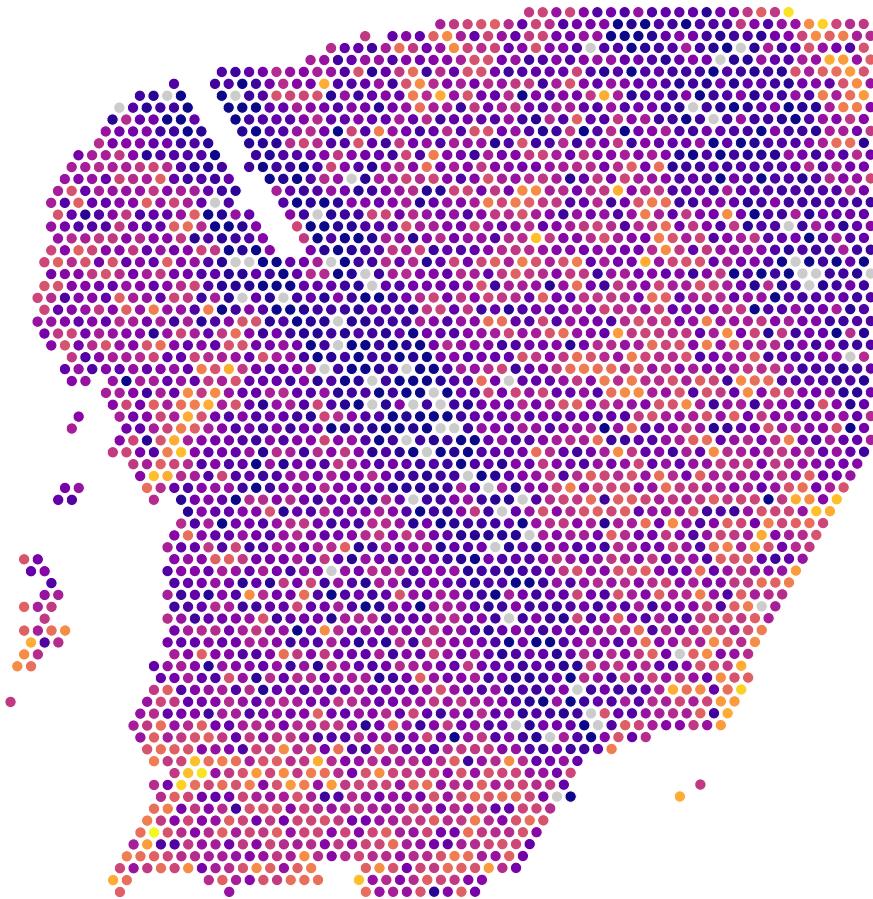
# Excit\_L3 (Br8667\_Post\_IF)



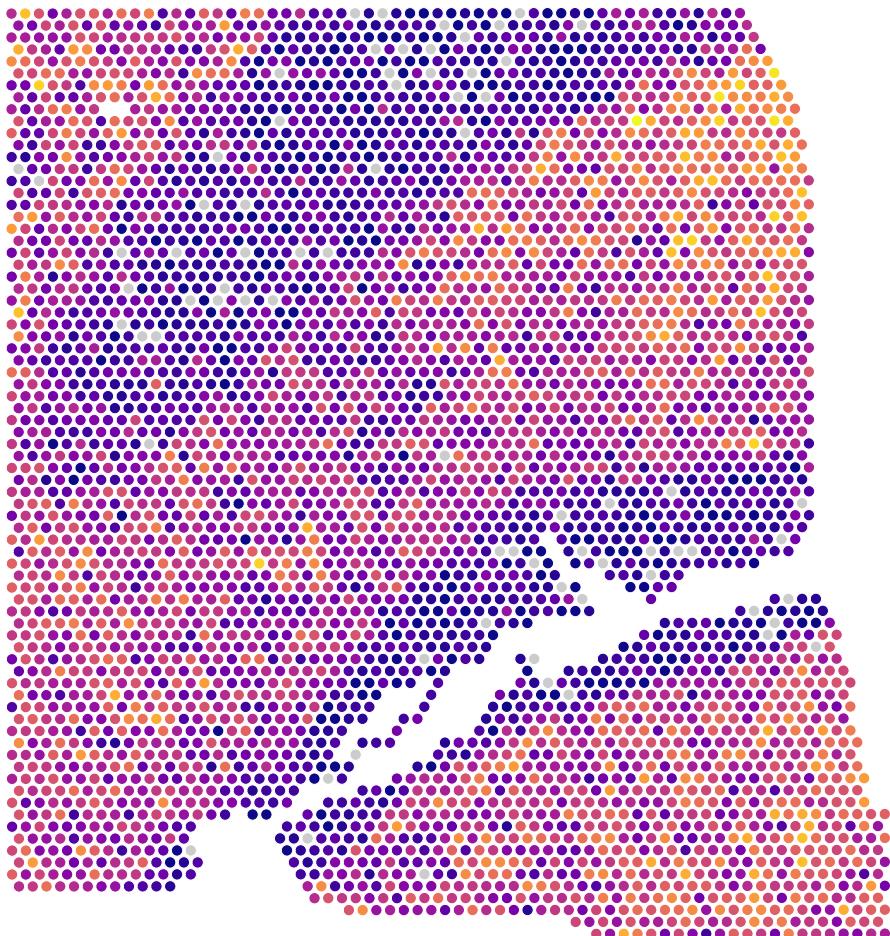
# Excit\_L4 (Br2720\_Ant\_IF)



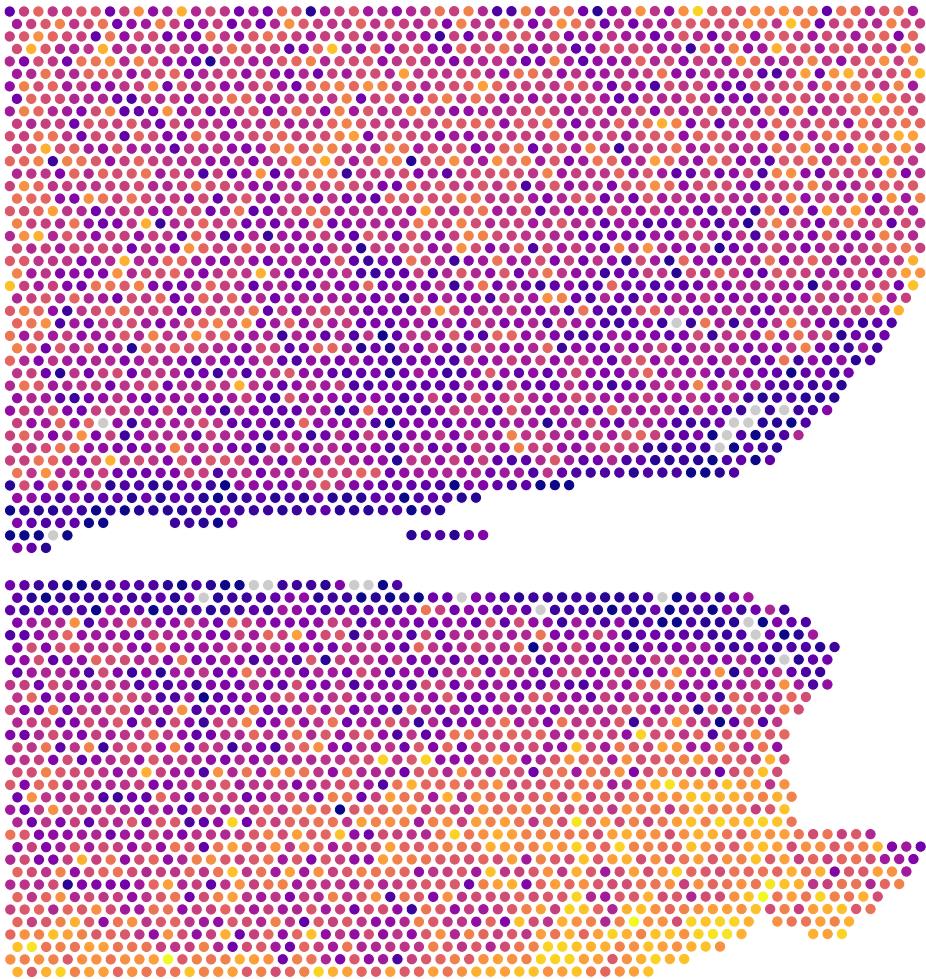
# Excit\_L4 (Br6432\_Ant\_IF)



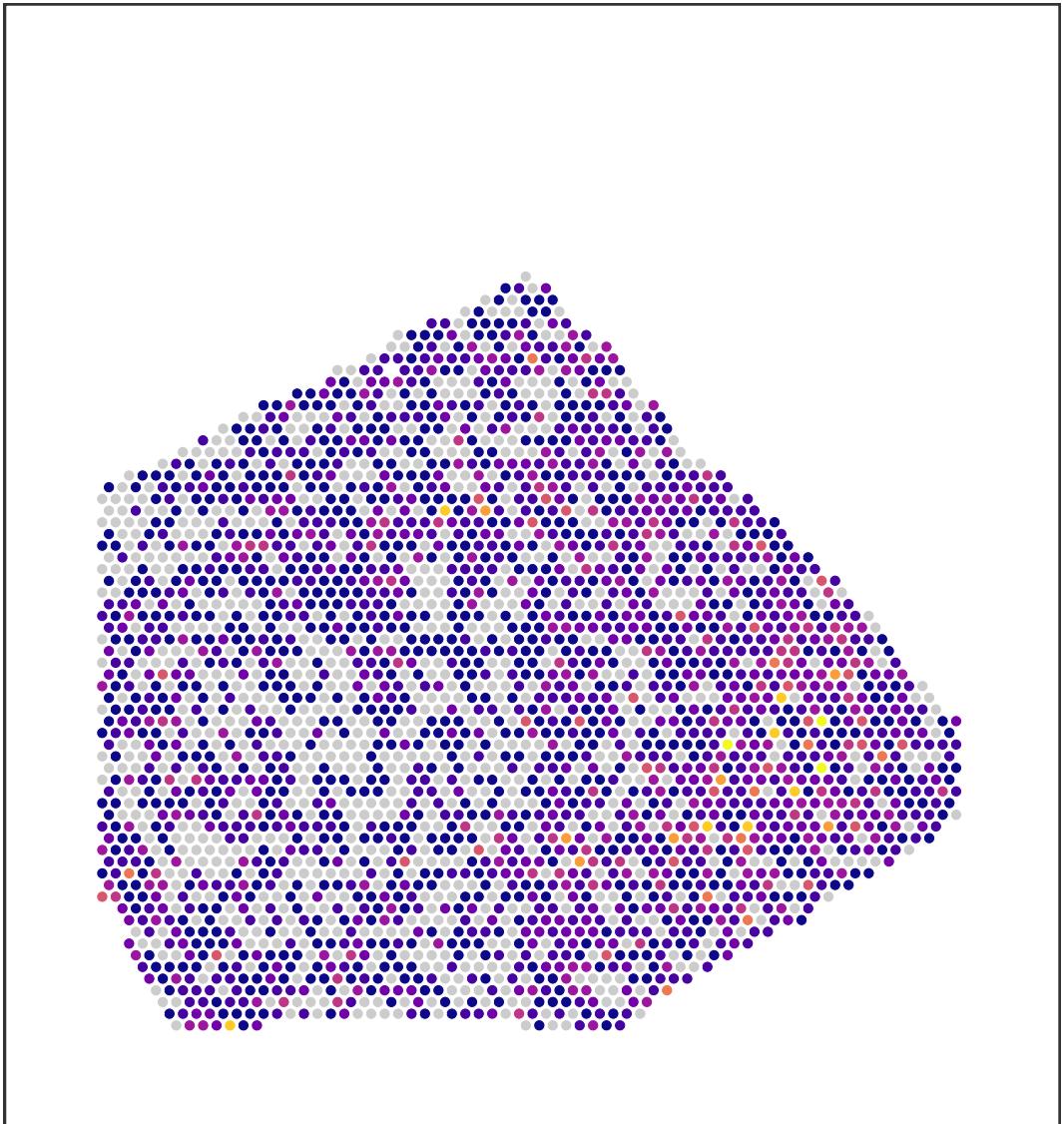
# Excit\_L4 (Br6522\_Ant\_IF)



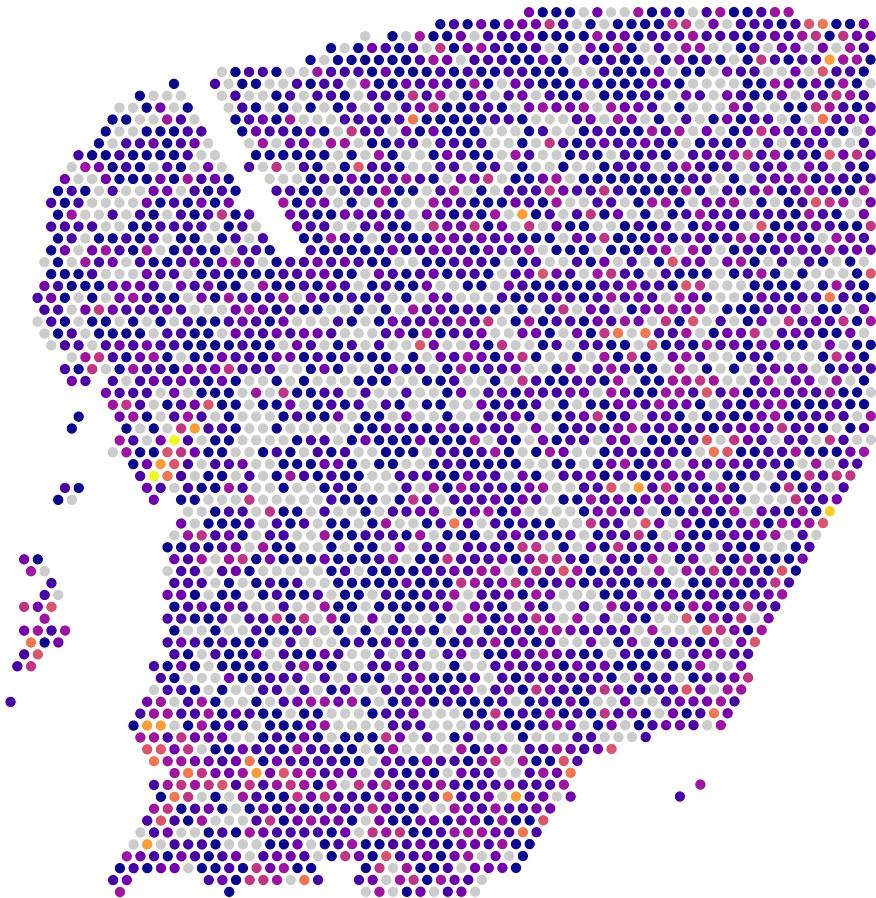
# Excit\_L4 (Br8667\_Post\_IF)



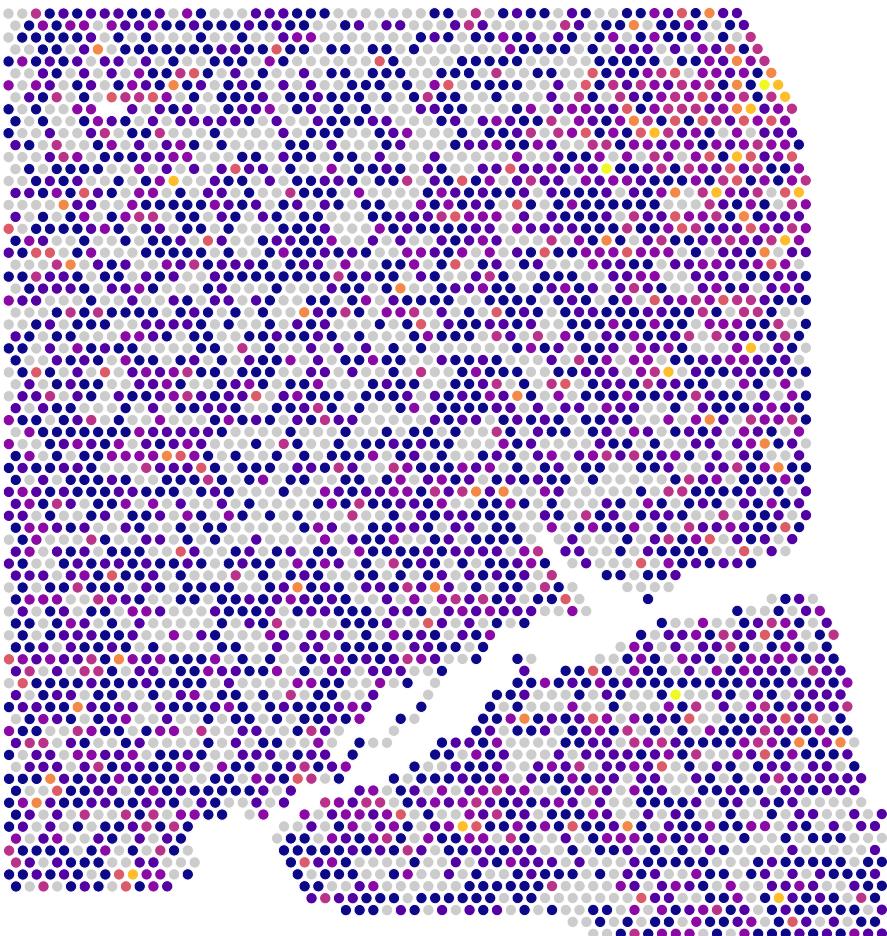
# Astro (Br2720\_Ant\_IF)



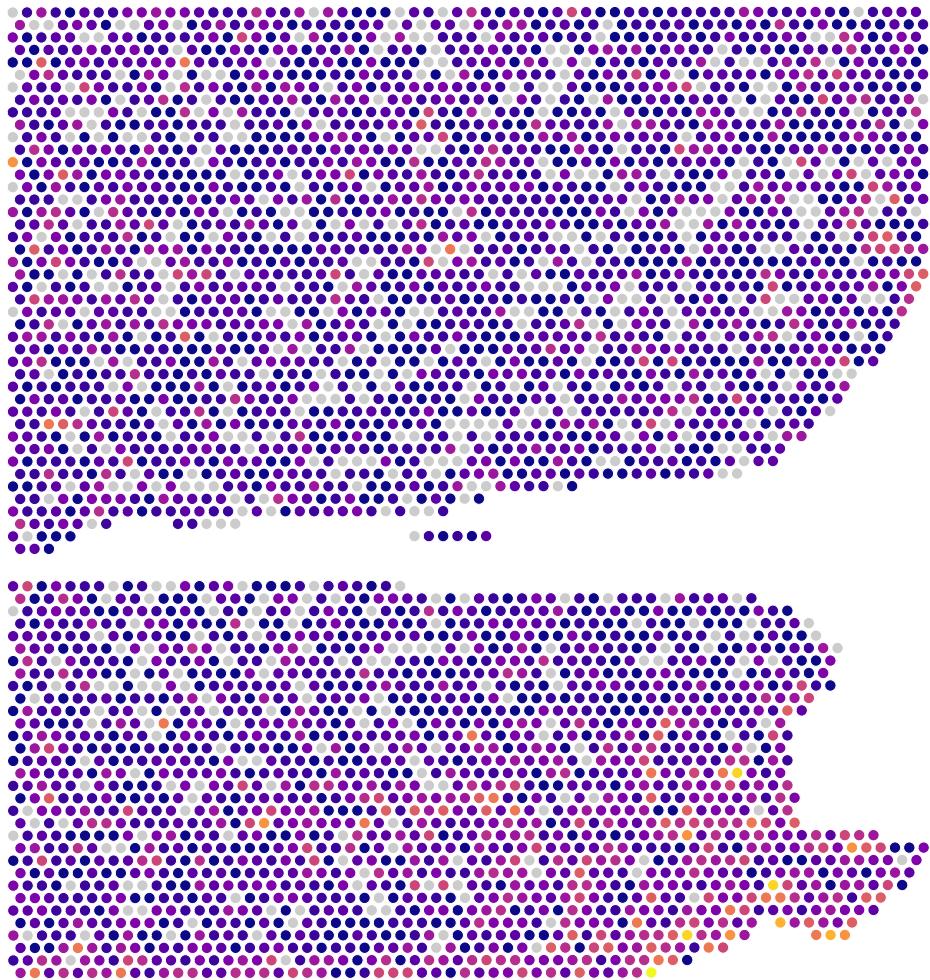
# Astro (Br6432\_Ant\_IF)



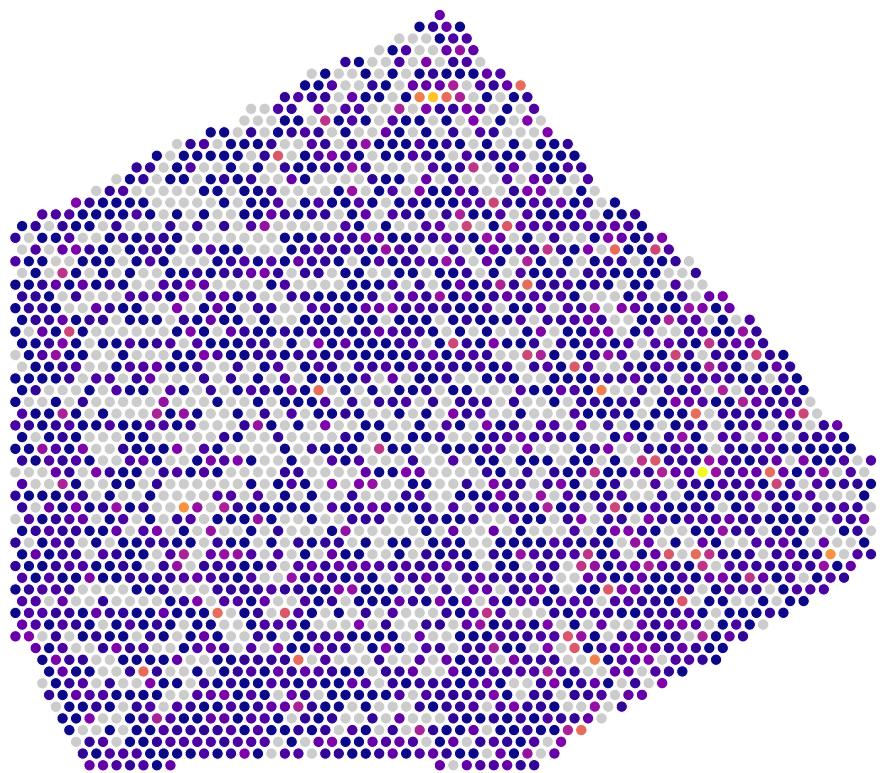
# Astro (Br6522\_Ant\_IF)



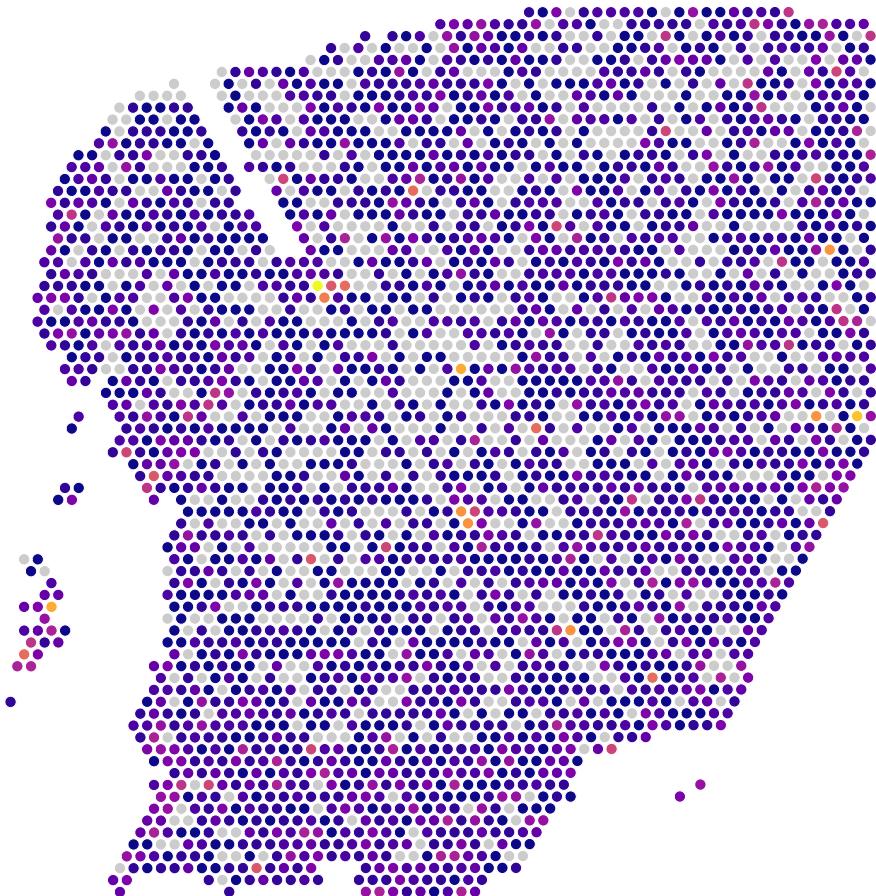
# Astro (Br8667\_Post\_IF)



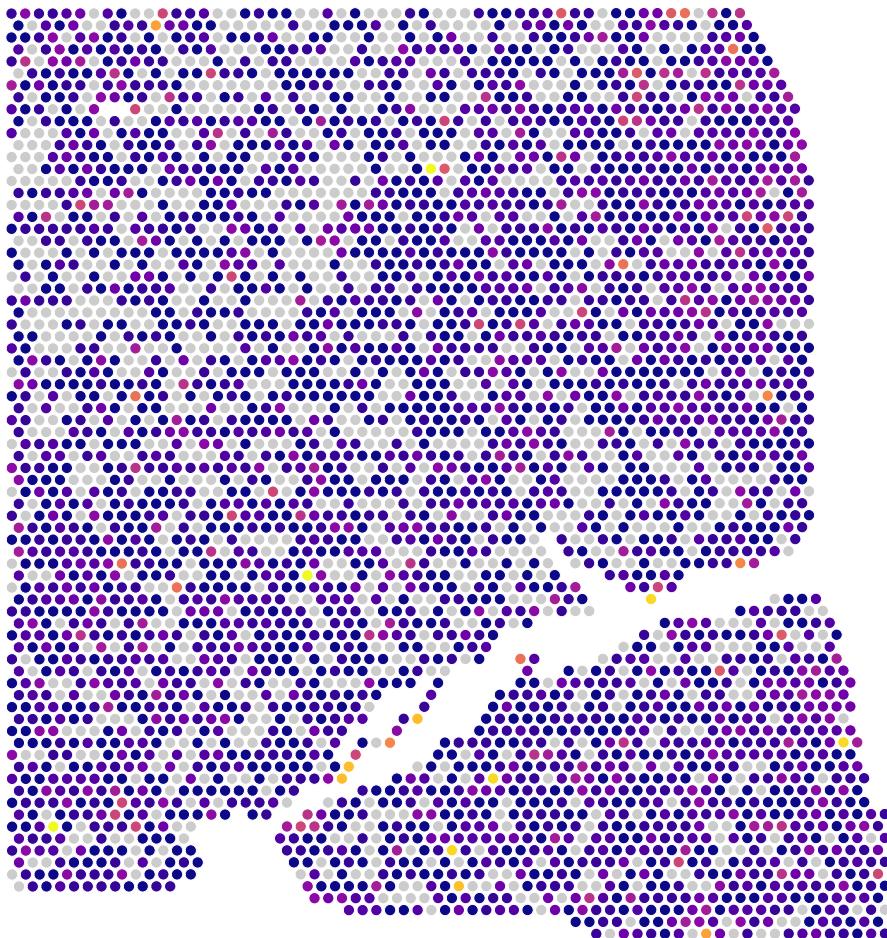
# EndoMural (Br2720\_Ant\_IF)



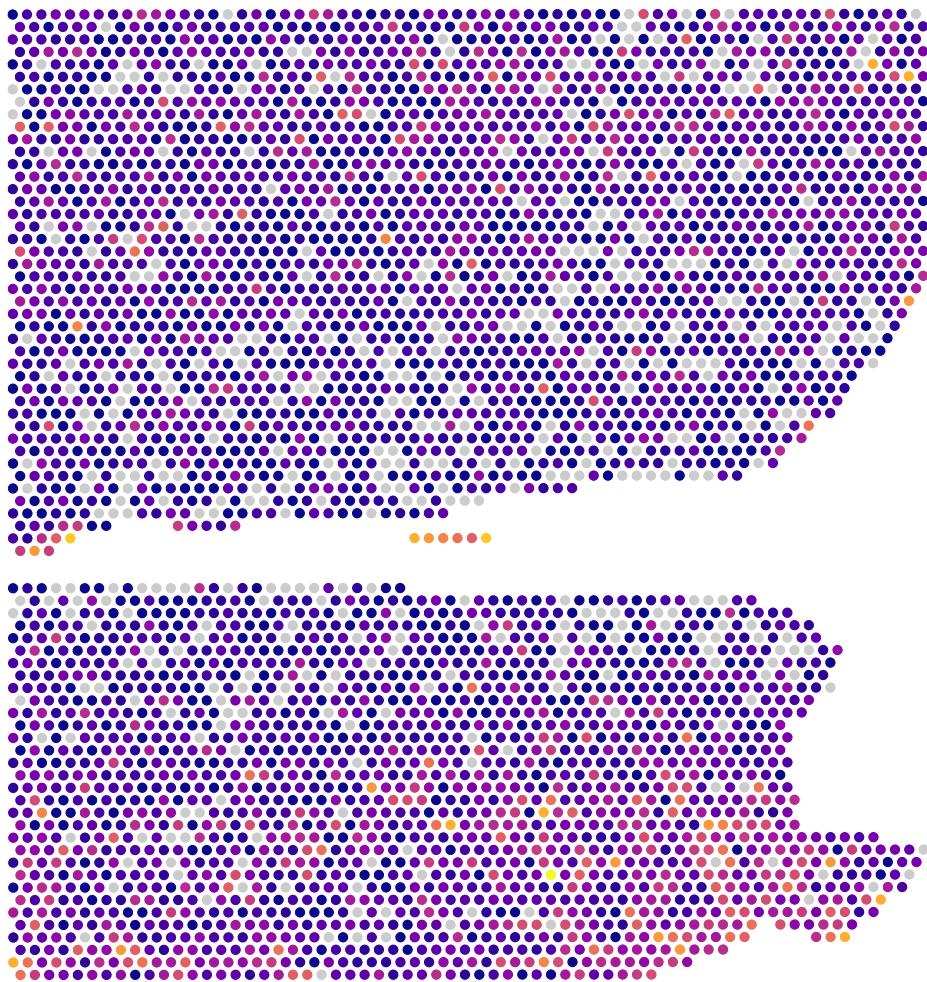
# EndoMural (Br6432\_Ant\_IF)



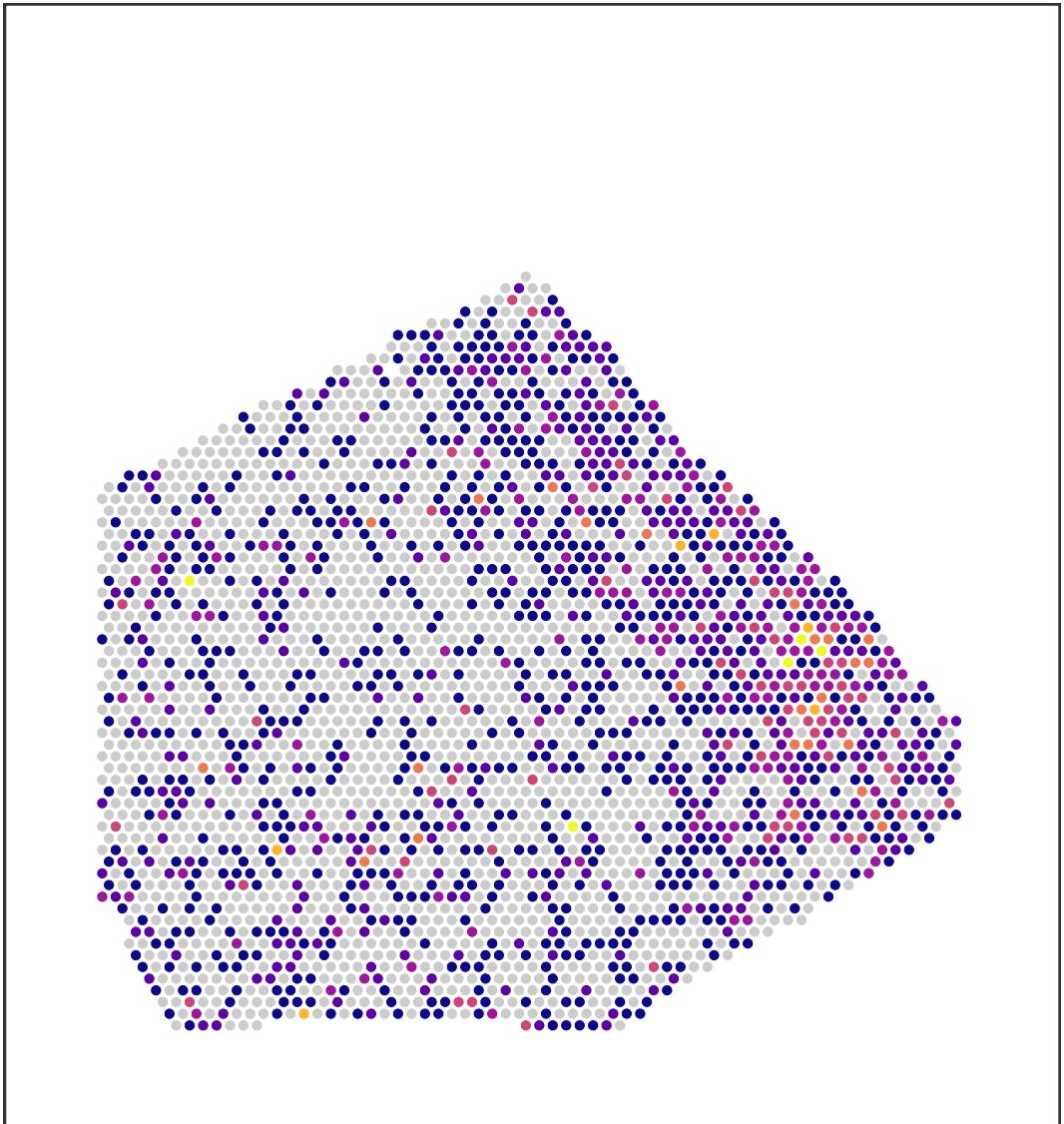
# EndoMural (Br6522\_Ant\_IF)



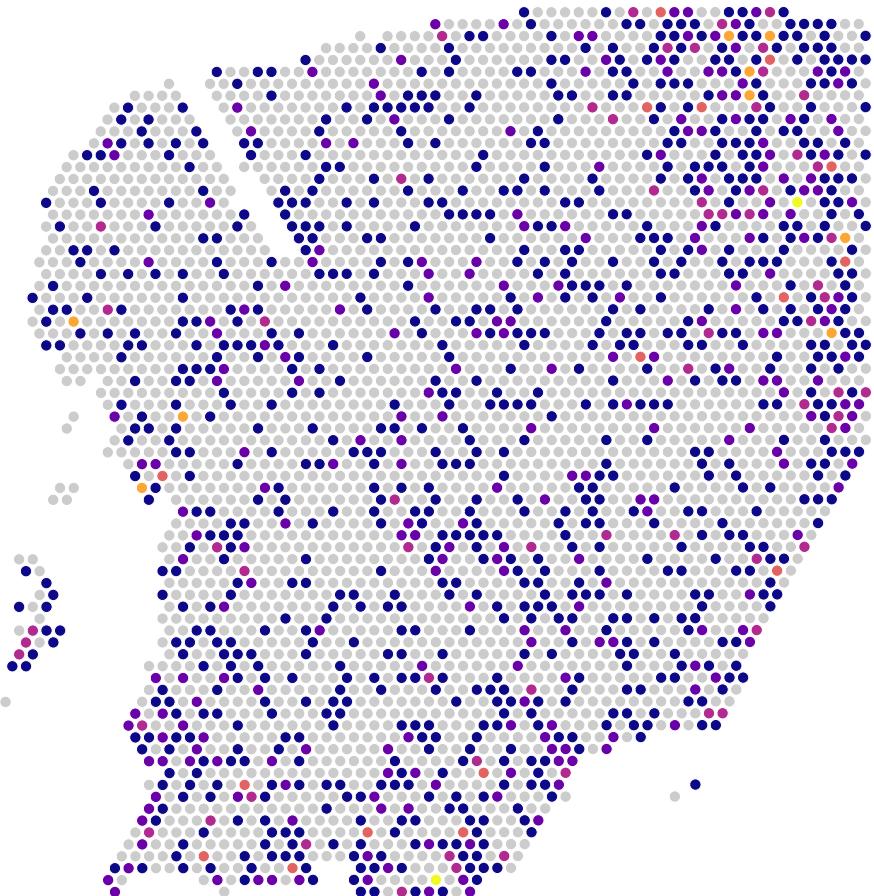
# EndoMural (Br8667\_Post\_IF)



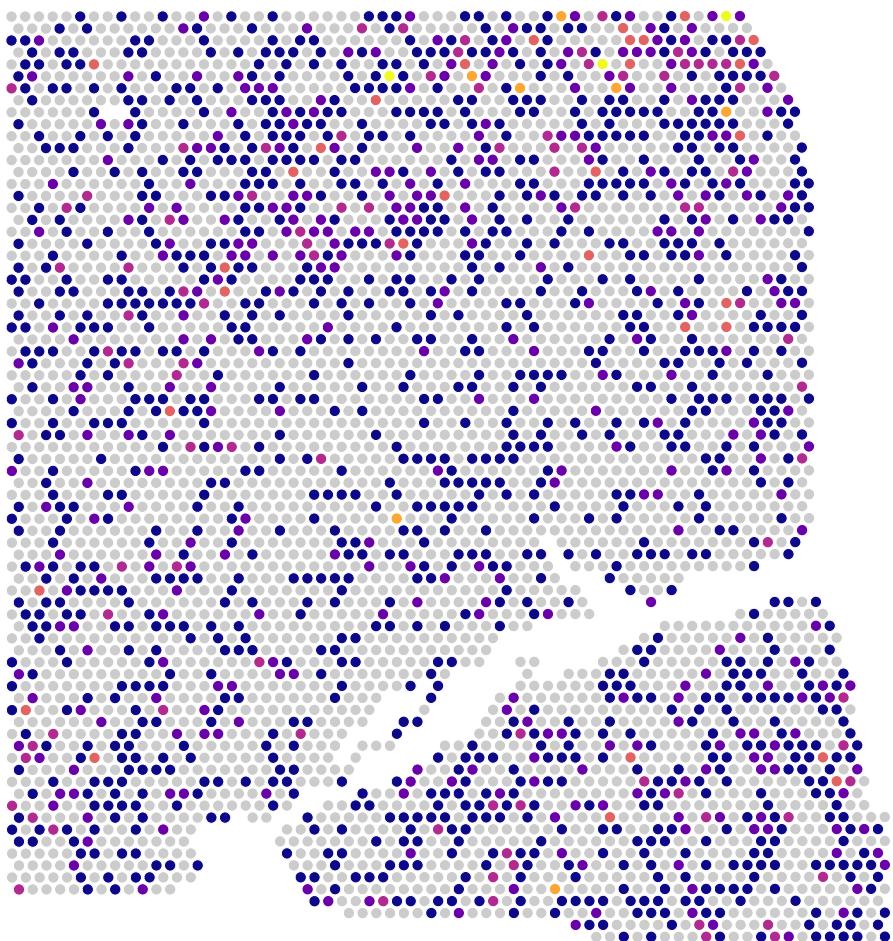
# Micro (Br2720\_Ant\_IF)



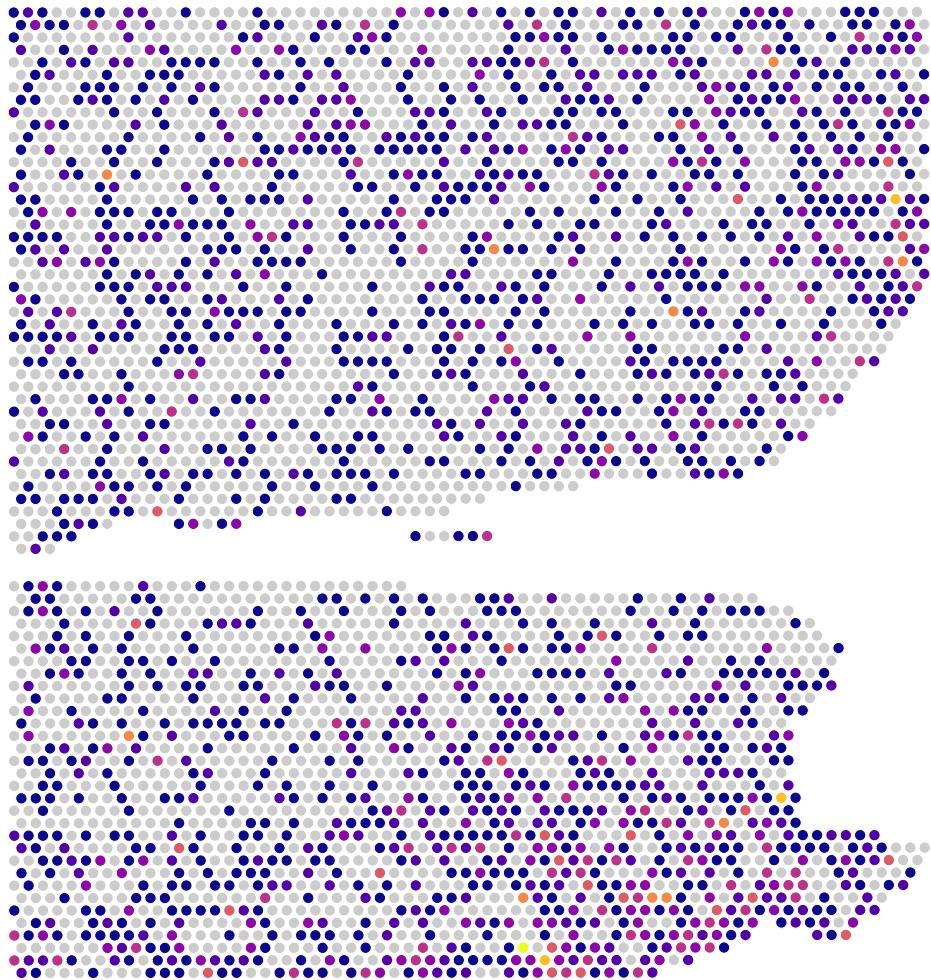
# Micro (Br6432\_Ant\_IF)



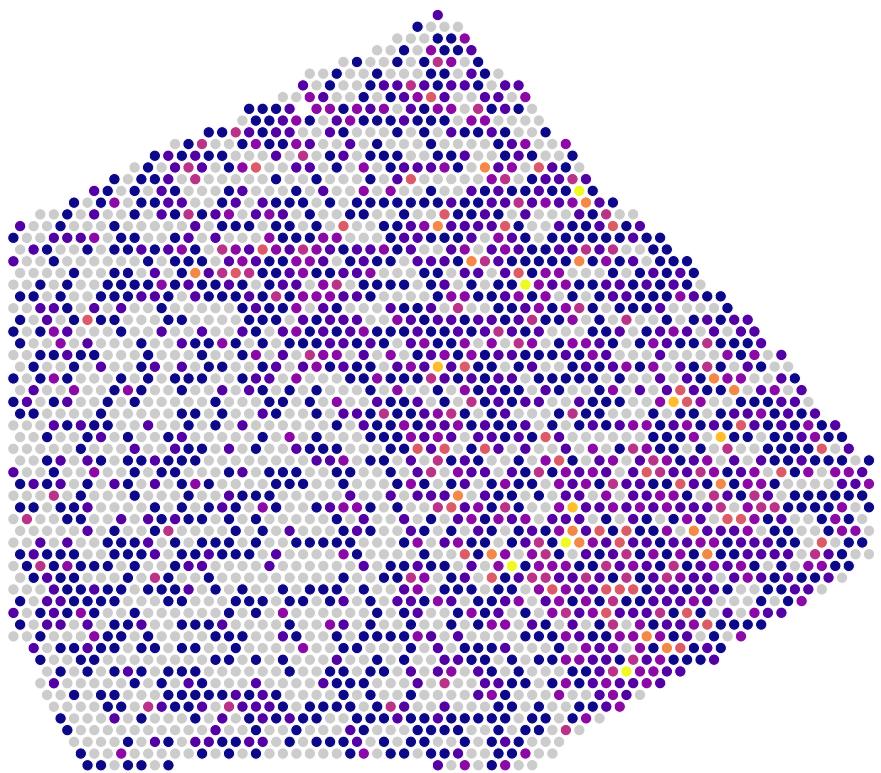
# Micro (Br6522\_Ant\_IF)



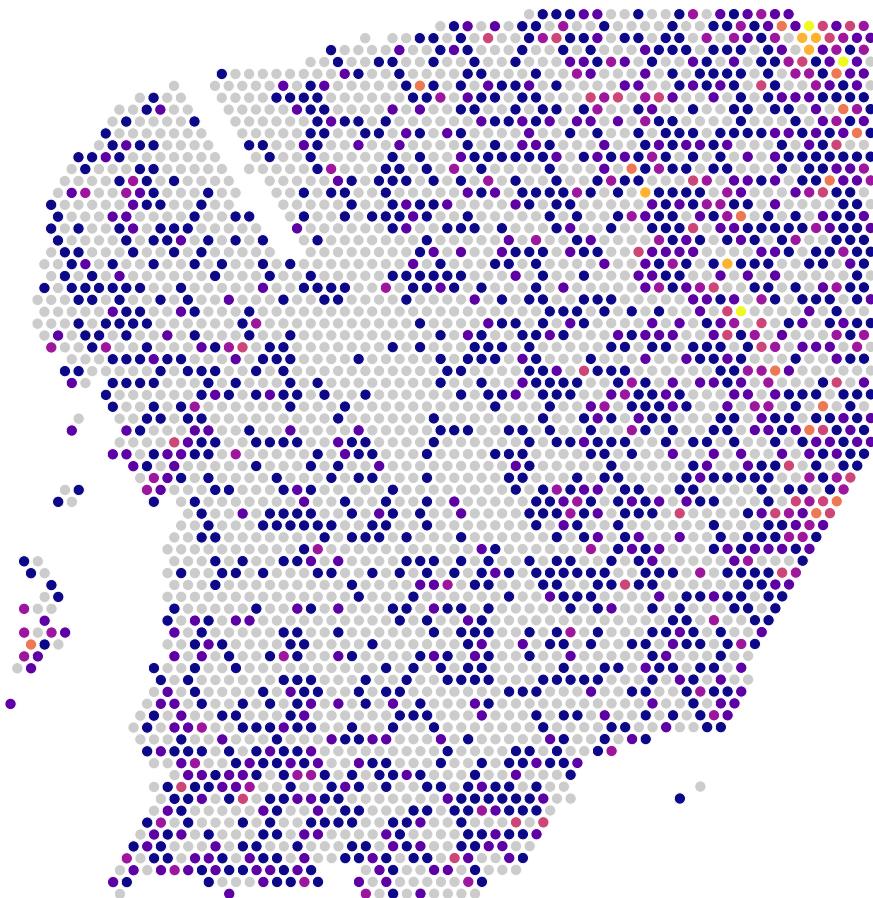
# Micro (Br8667\_Post\_IF)



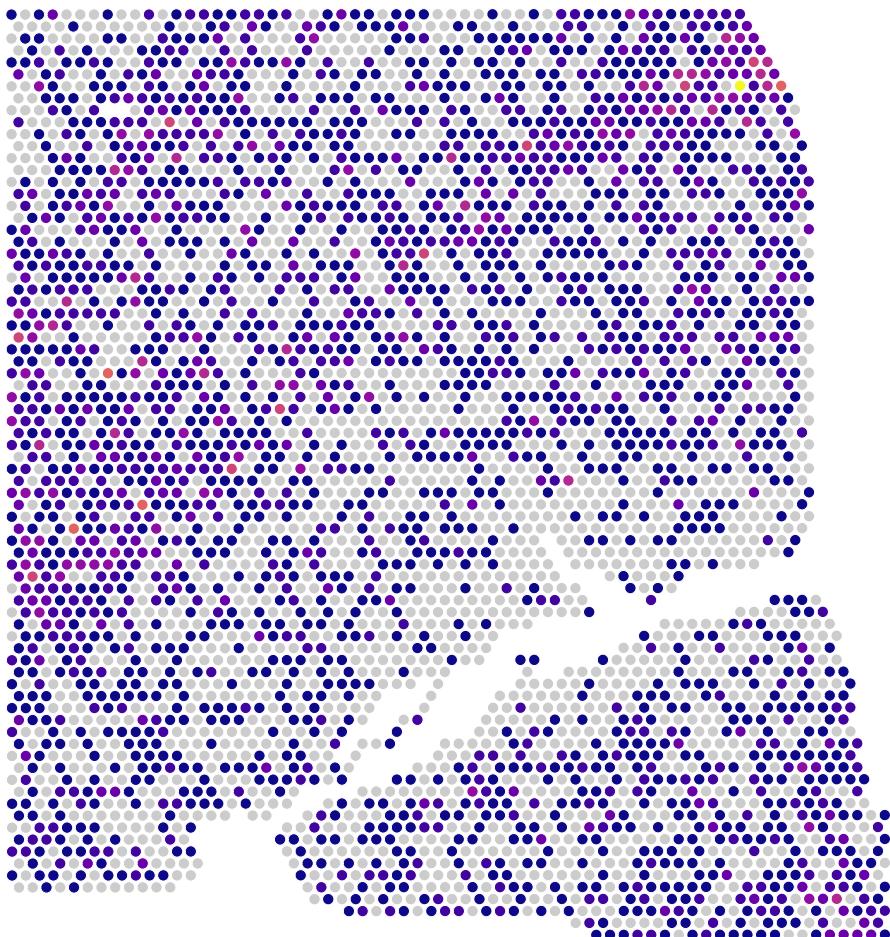
# Excit\_L6 (Br2720\_Ant\_IF)



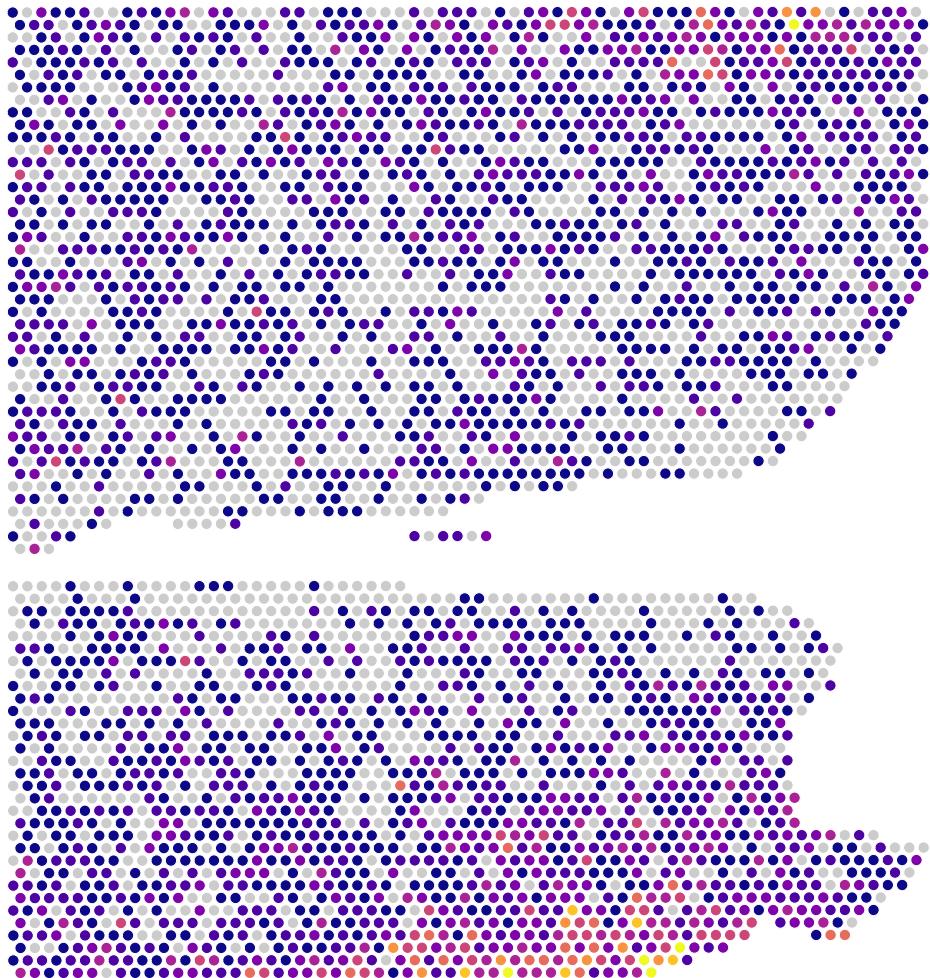
# Excit\_L6 (Br6432\_Ant\_IF)



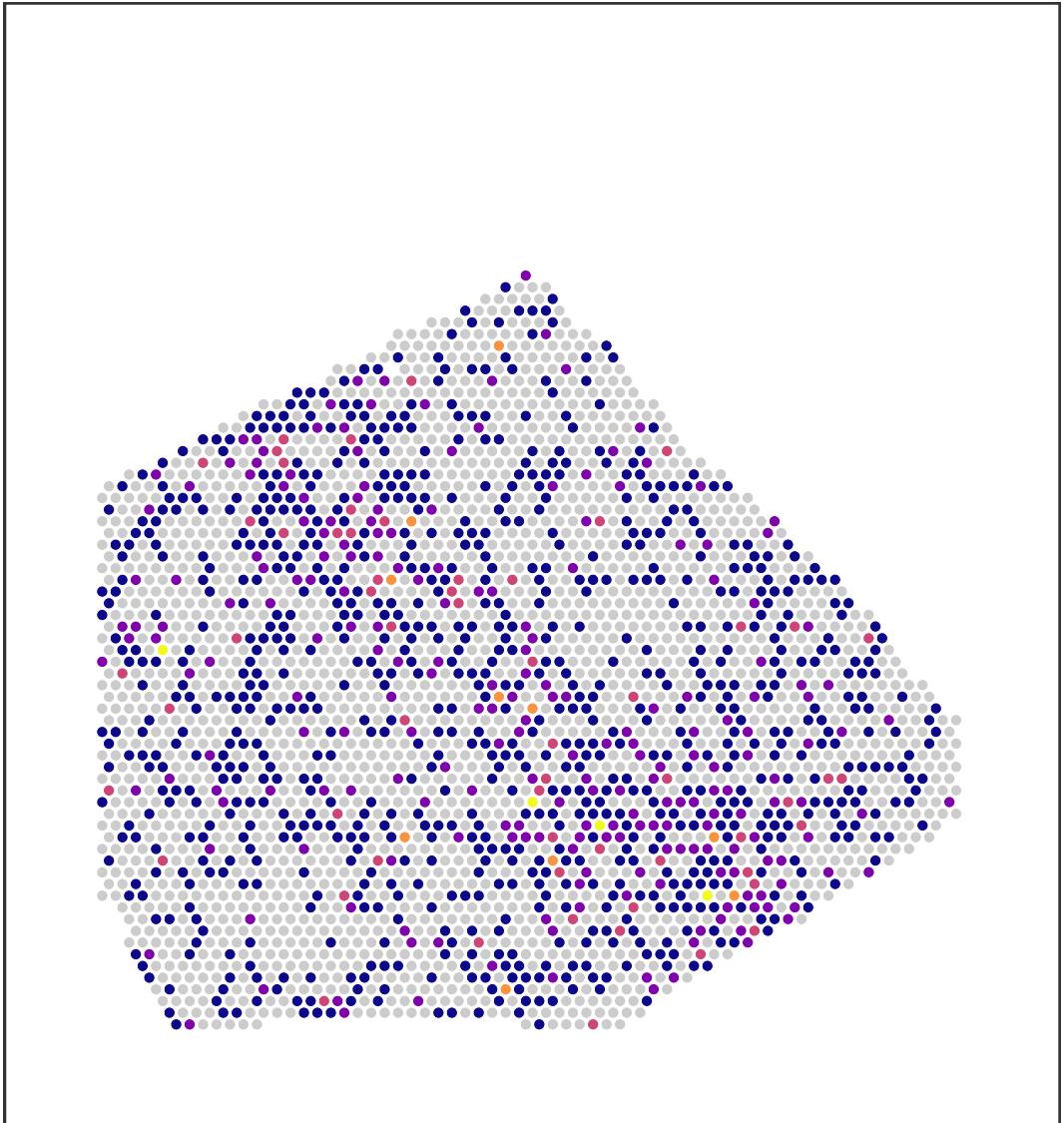
# Excit\_L6 (Br6522\_Ant\_IF)



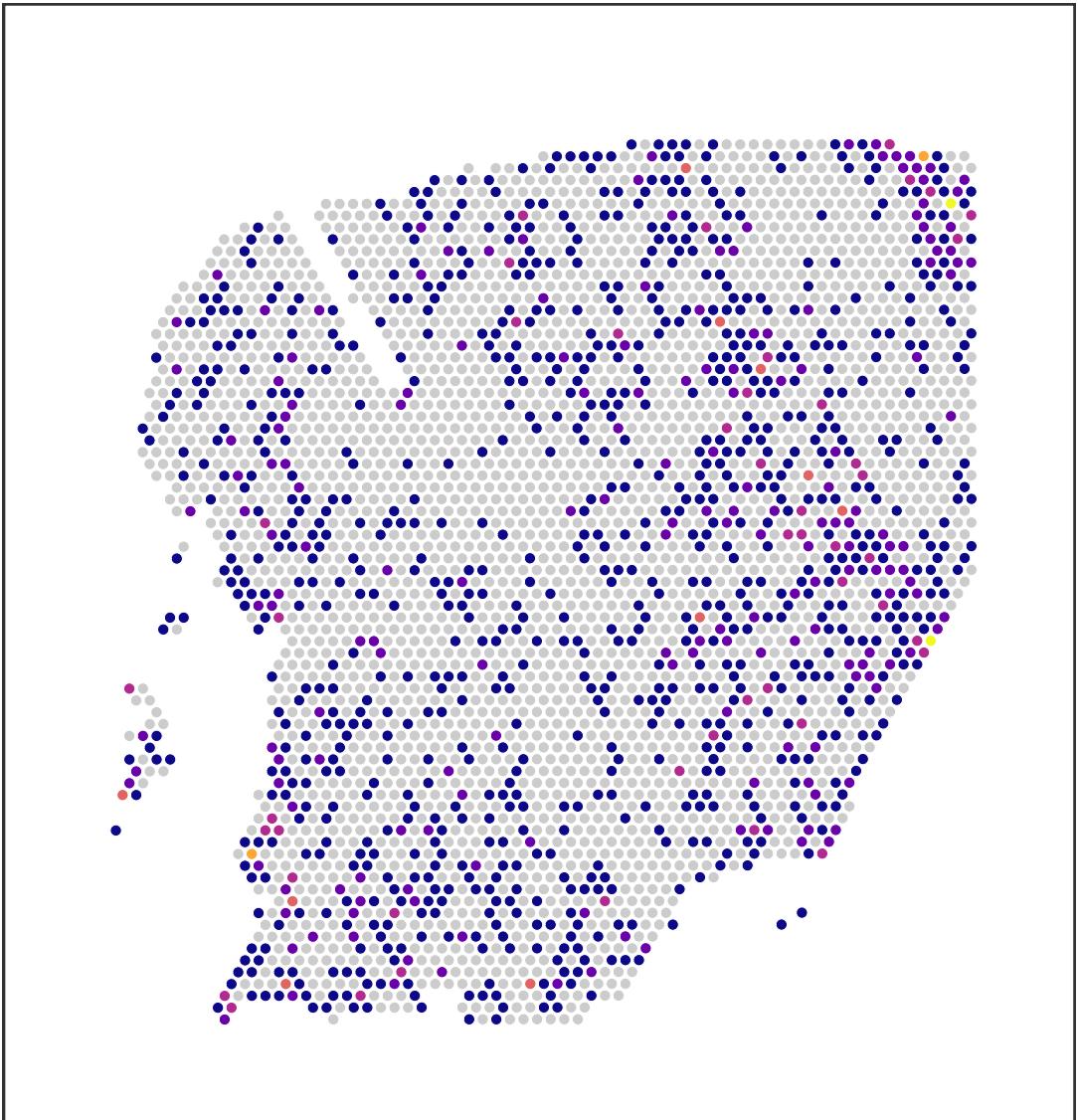
# Excit\_L6 (Br8667\_Post\_IF)



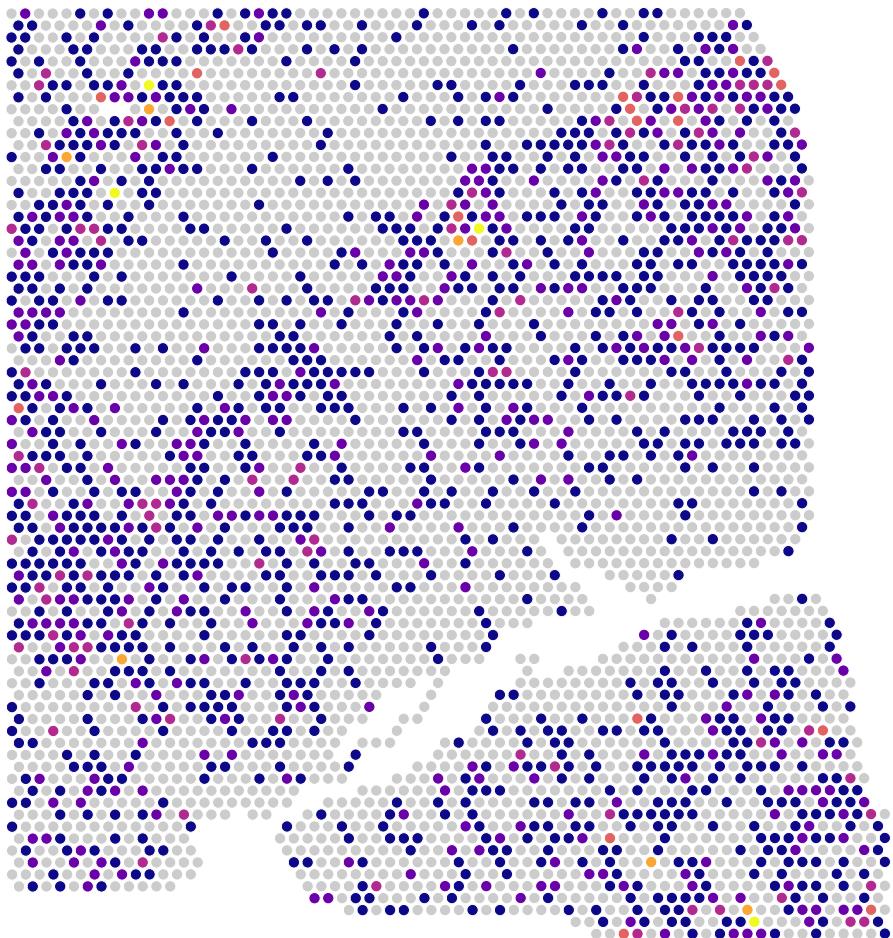
# Excit\_L5/6 (Br2720\_Ant\_IF)



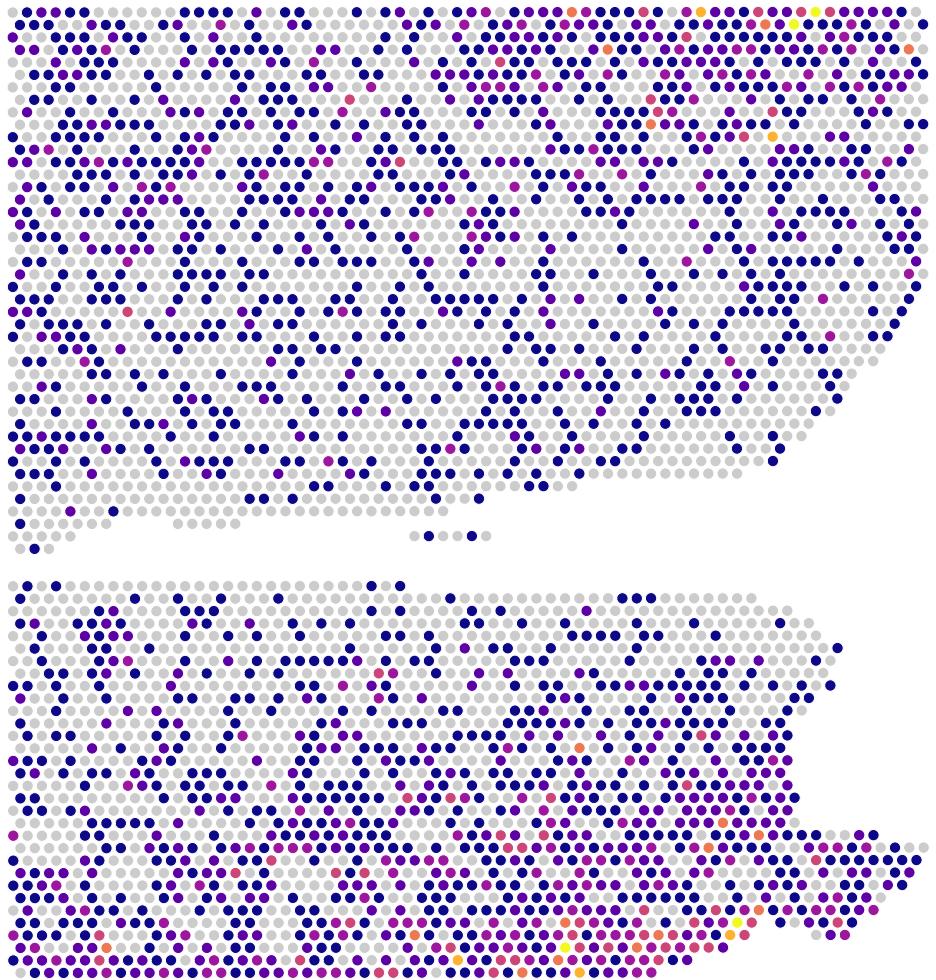
# Excit\_L5/6 (Br6432\_Ant\_IF)



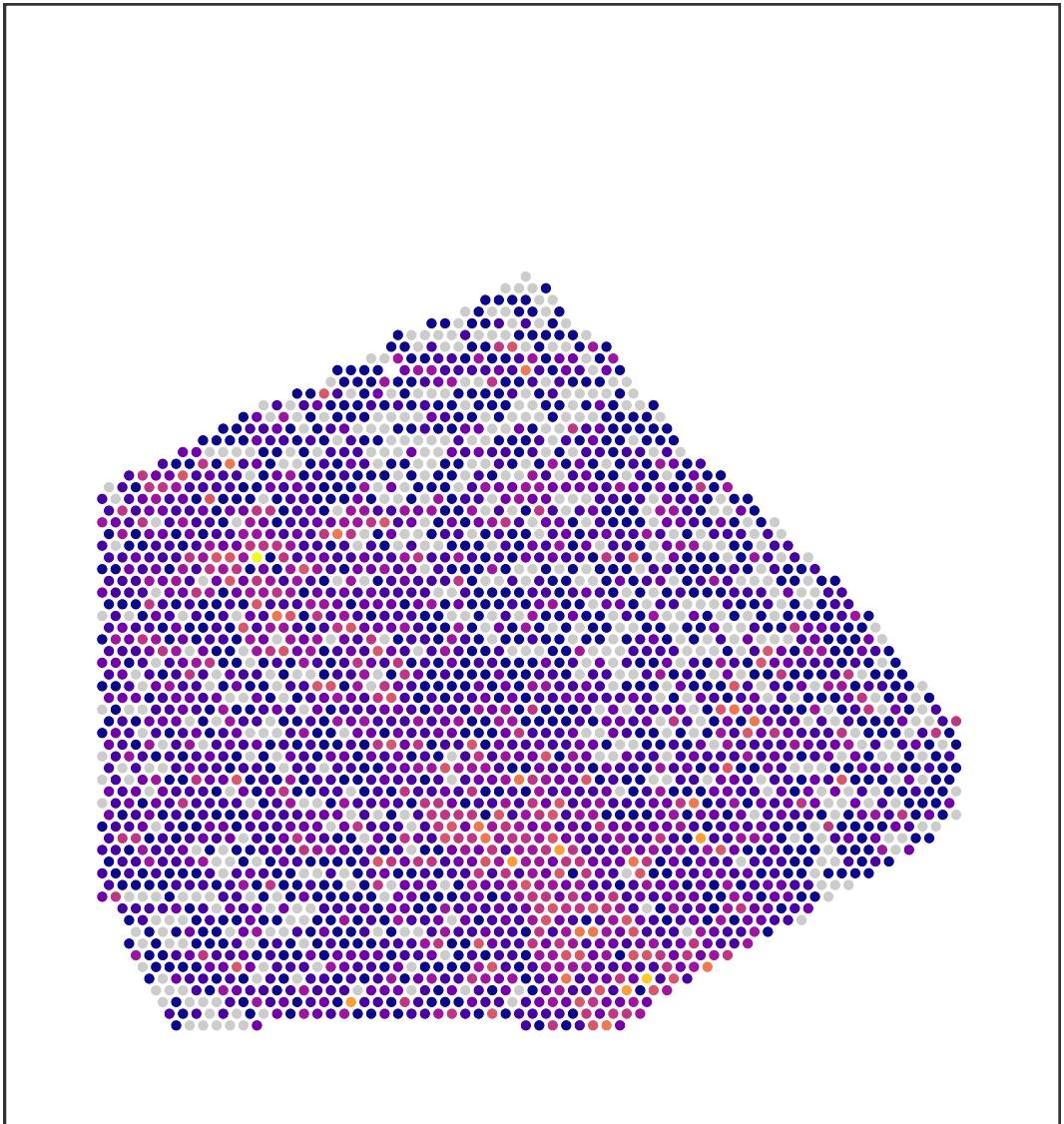
# Excit\_L5/6 (Br6522\_Ant\_IF)



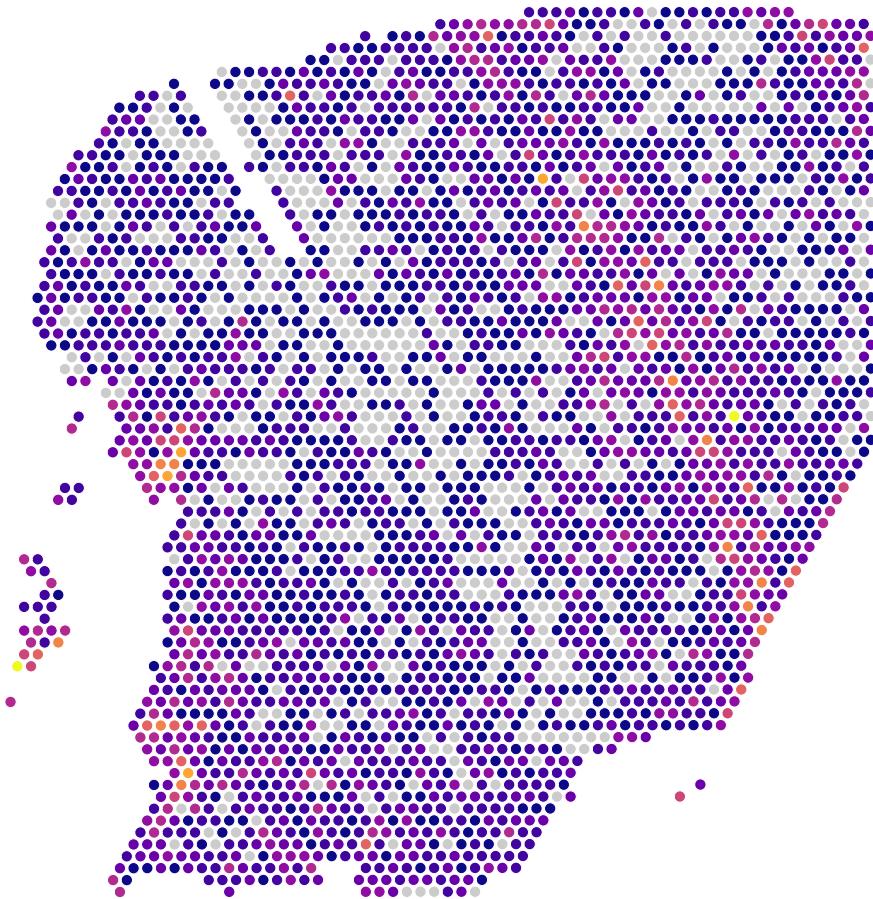
# Excit\_L5/6 (Br8667\_Post\_IF)



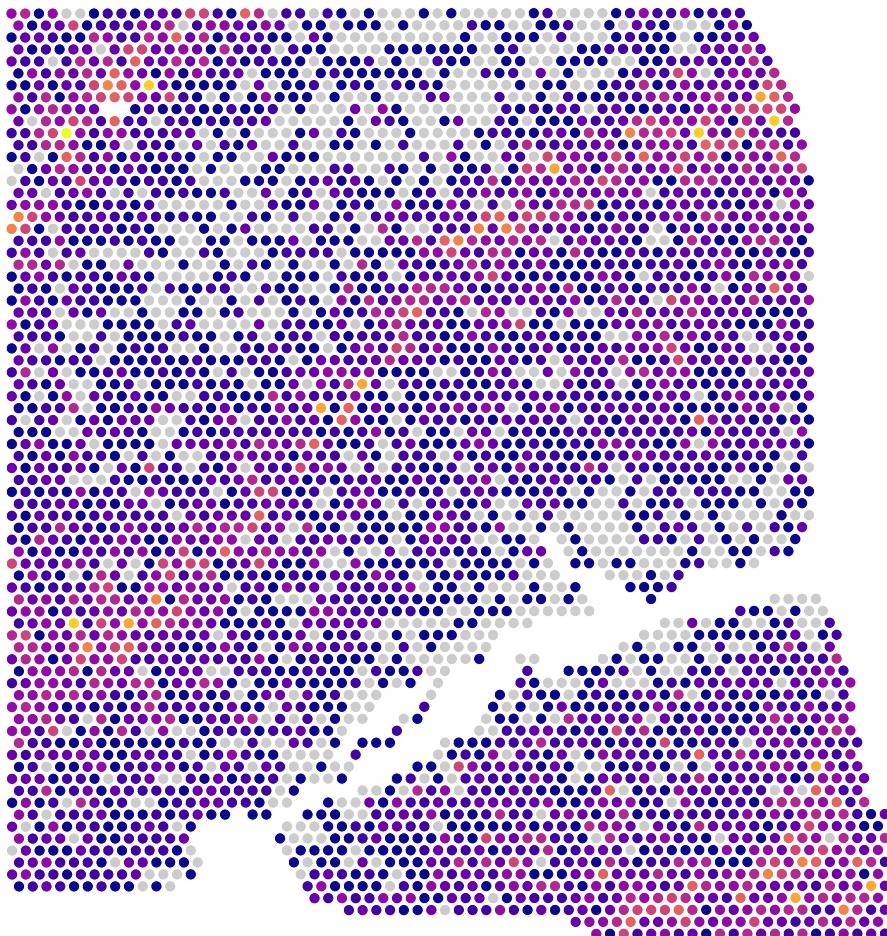
# Excit\_L5 (Br2720\_Ant\_IF)



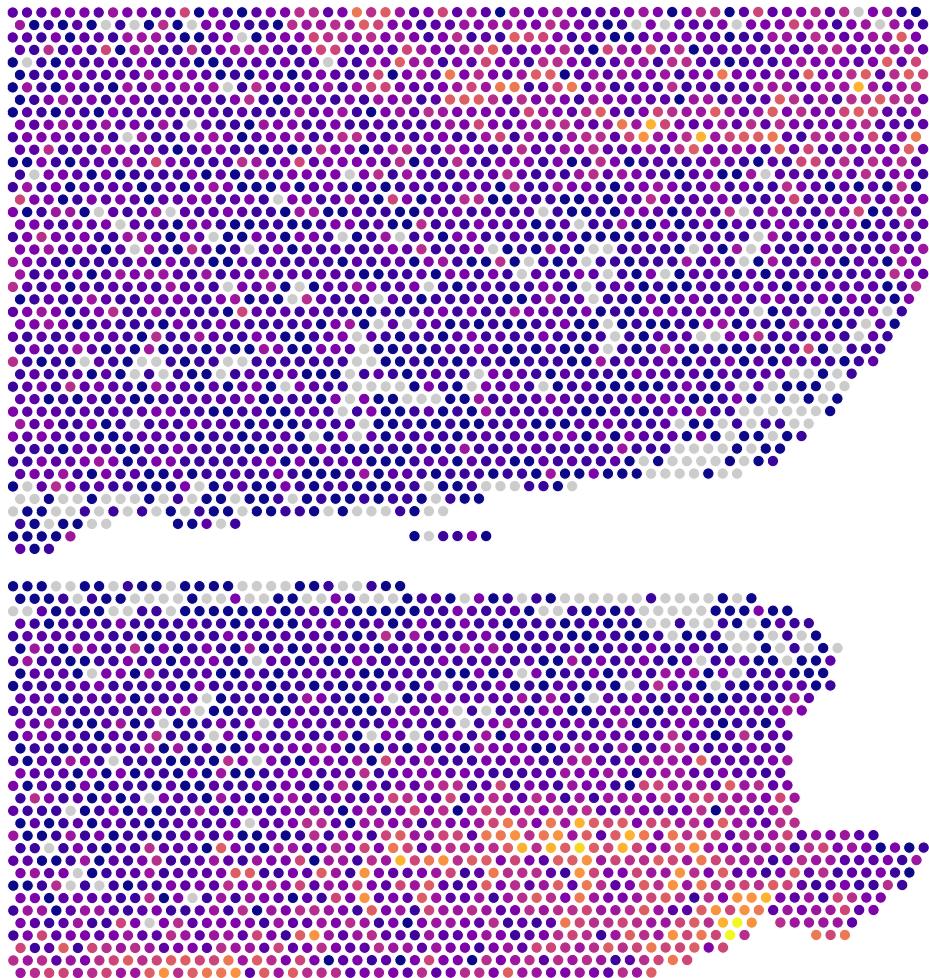
# Excit\_L5 (Br6432\_Ant\_IF)



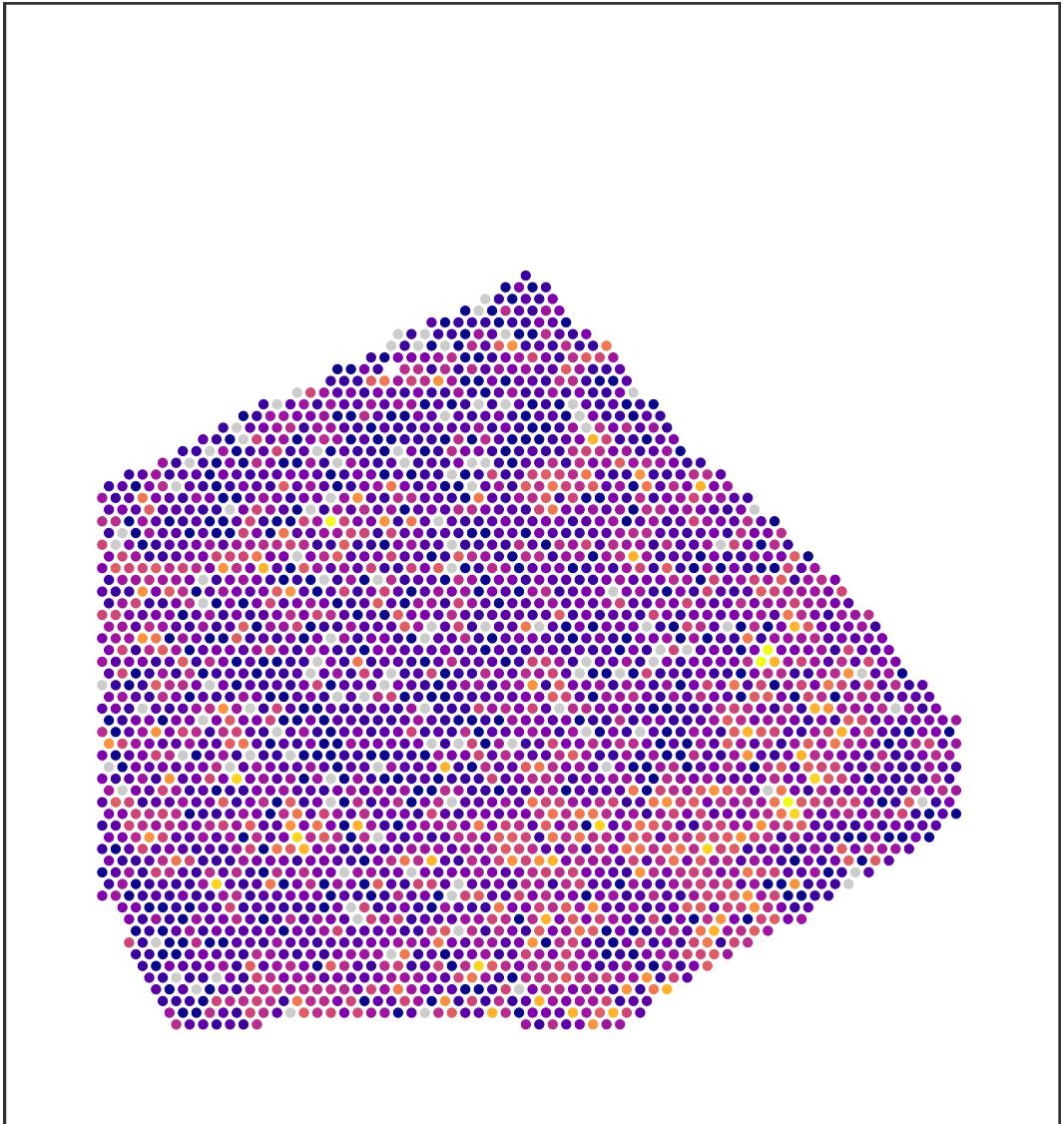
# Excit\_L5 (Br6522\_Ant\_IF)



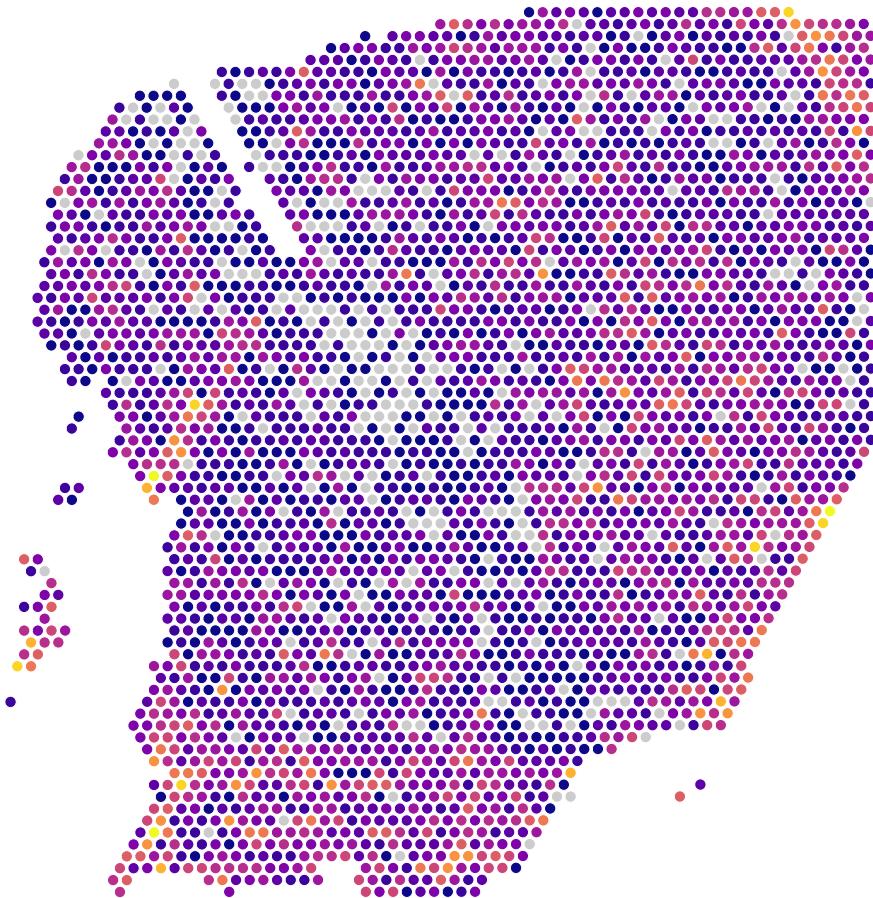
# Excit\_L5 (Br8667\_Post\_IF)



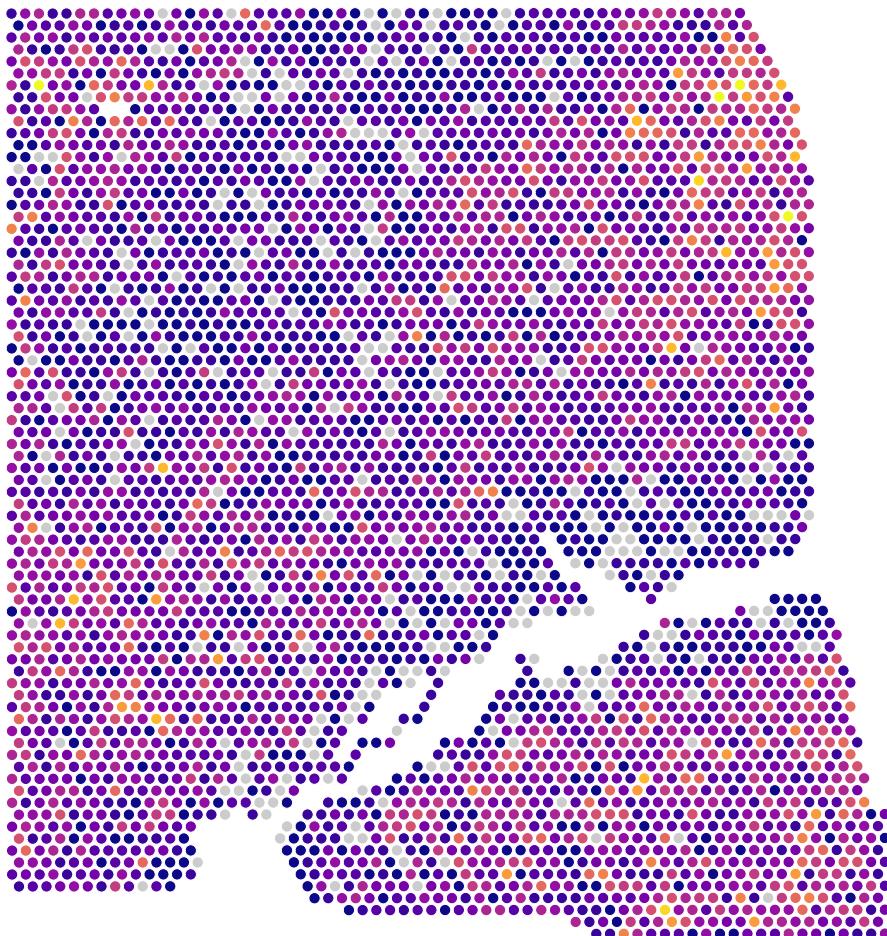
# Excit\_L2/3 (Br2720\_Ant\_IF)



# Excit\_L2/3 (Br6432\_Ant\_IF)



# Excit\_L2/3 (Br6522\_Ant\_IF)



# Excit\_L2/3 (Br8667\_Post\_IF)

