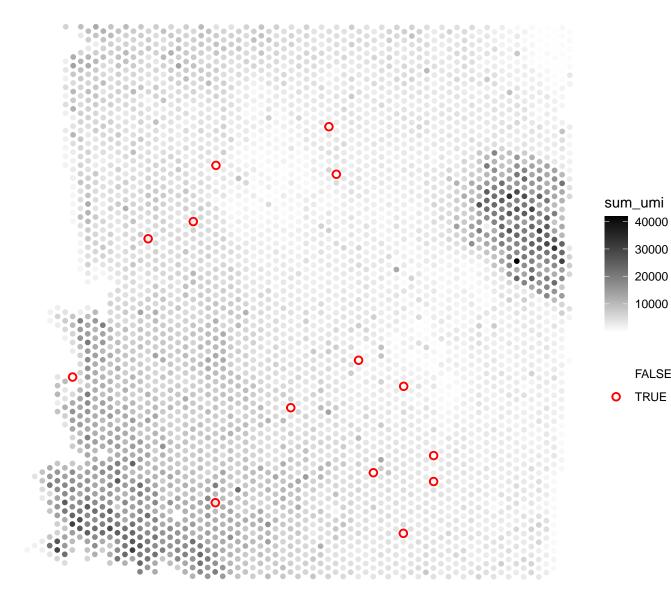
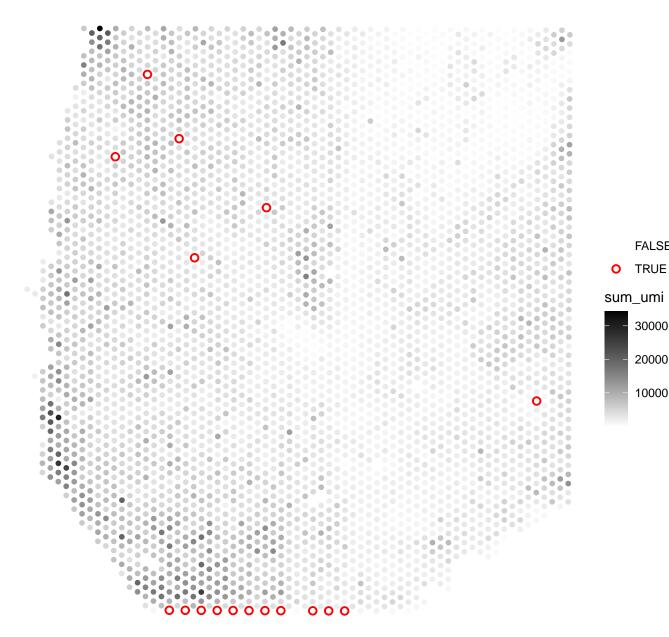
Sample: V12D07-075_D1



Sample: V13M13-362_A1



FALSE

30000

20000

10000

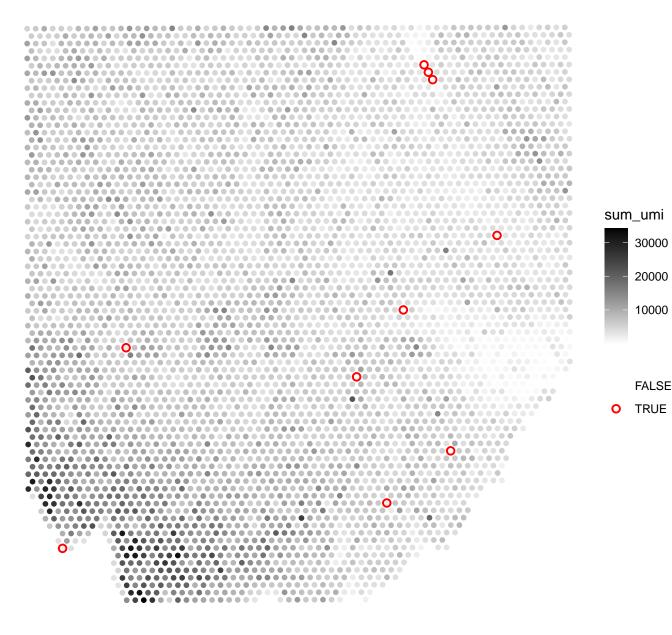
Sample: V12D07-075 A1



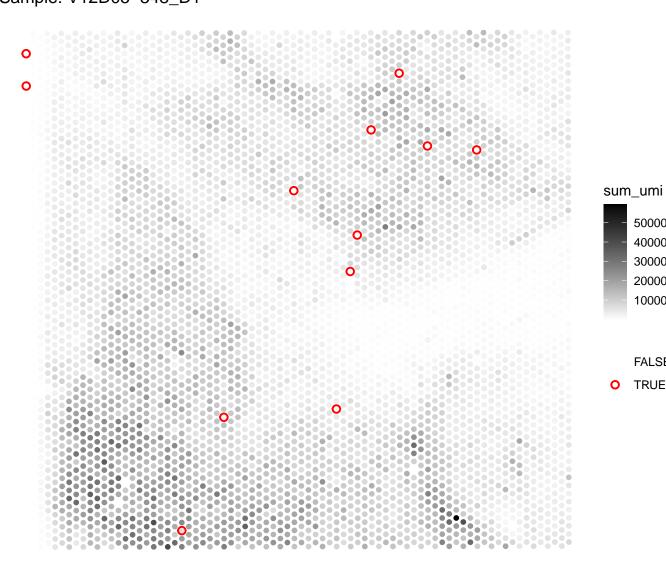




Sample: V12Y31-080_A1

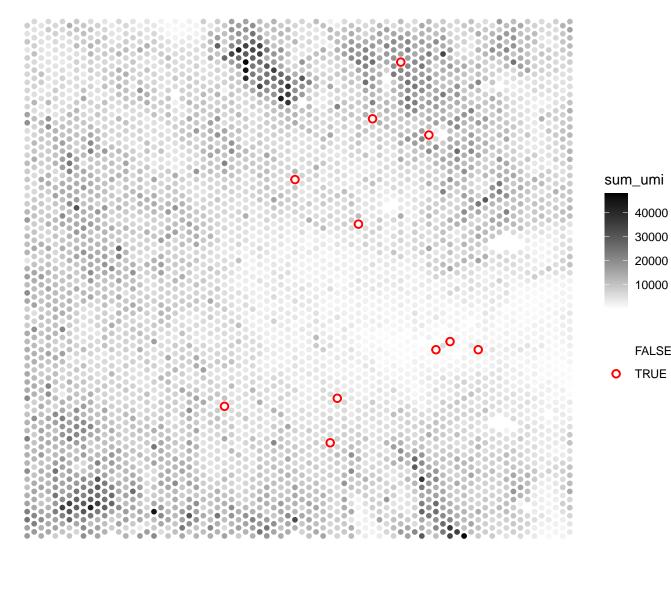


Sample: V12D05-348_D1

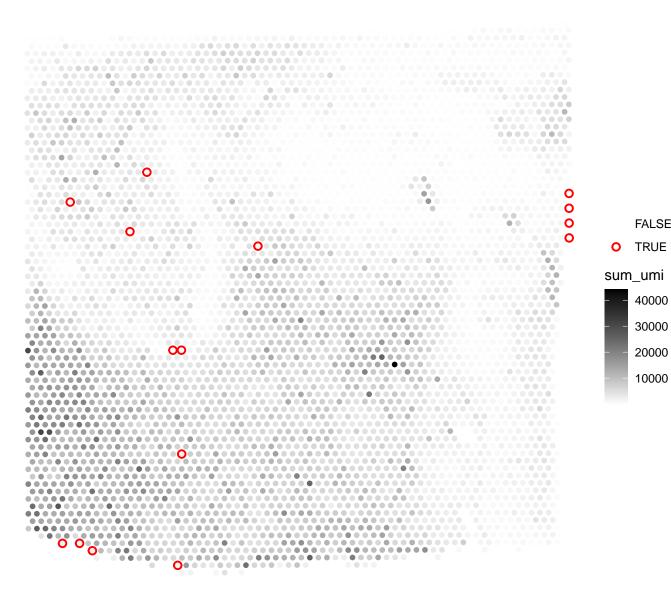


FALSE TRUE

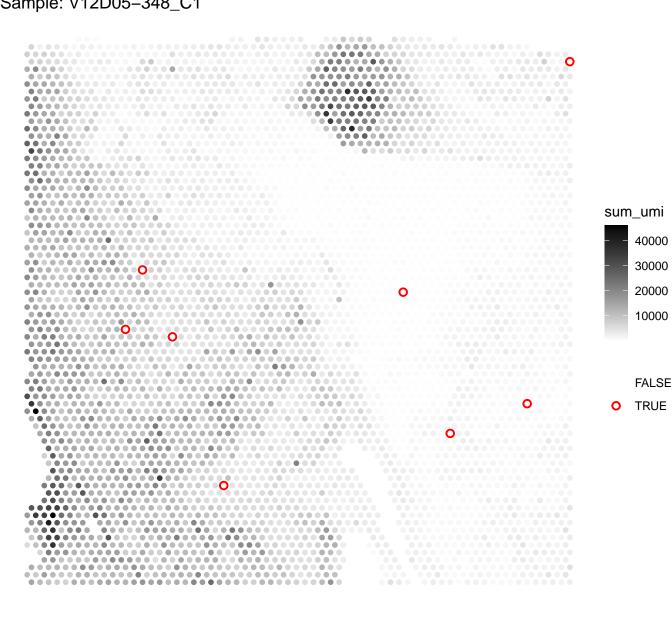
Sample: V12D05-350_D1



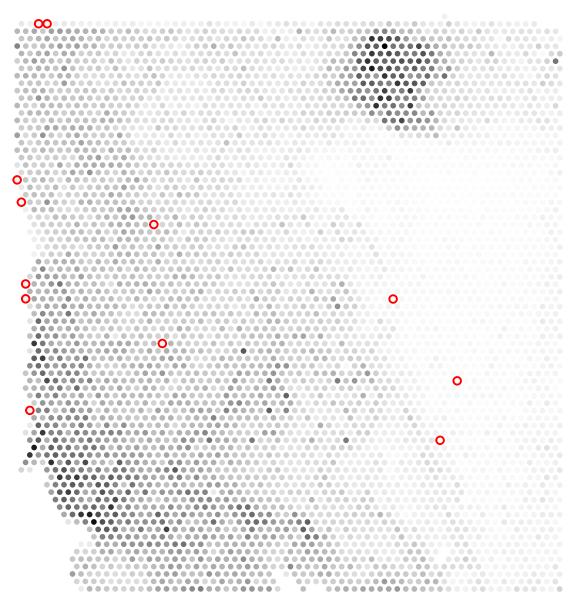
Sample: V13M13-362_D1

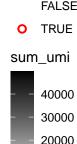


Sample: V12D05-348_C1



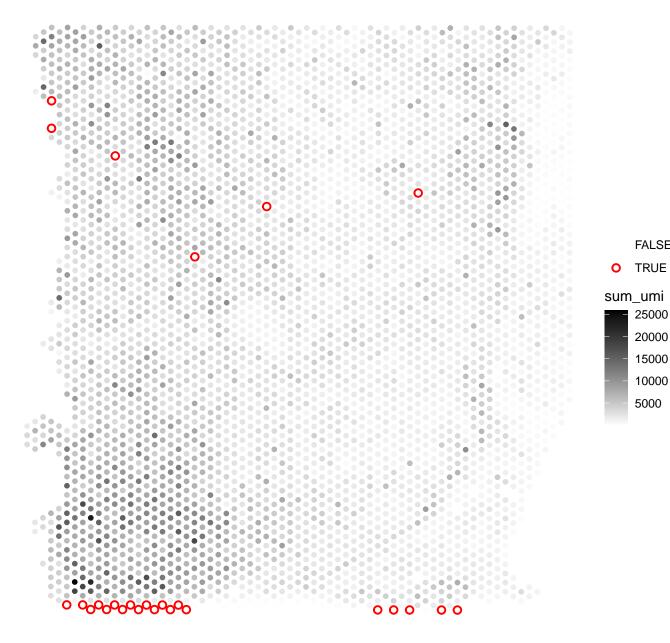
Sample: V12D05-350_C1





10000

Sample: V13Y24-346_C1



FALSE

25000

10000