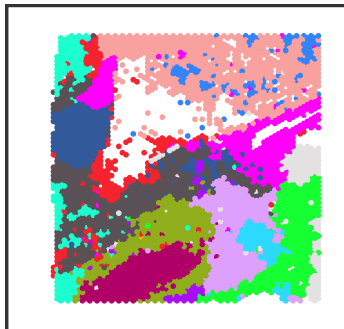
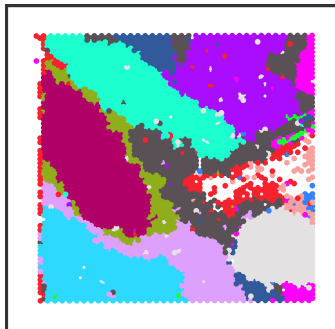


V12D05-348\_C1



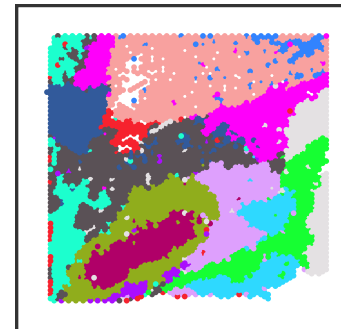
• 2  
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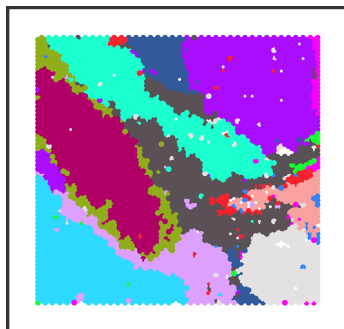
• 2  
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V12D05-350\_C1



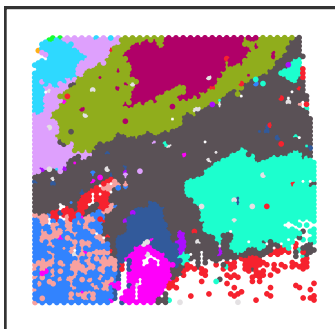
• 2  
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V12D05-350\_D1



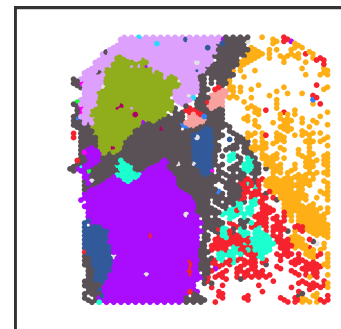
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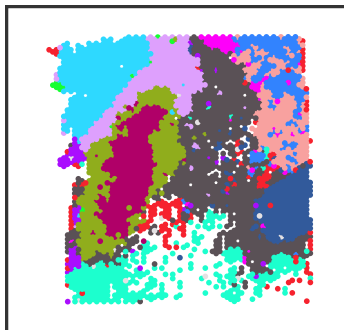
• 1  
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V12D07-075\_B1



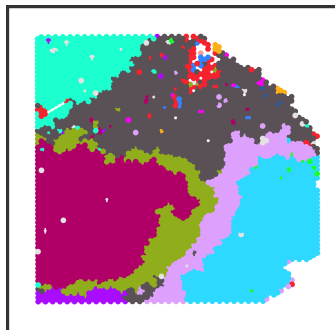
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V12D07-075\_D1



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V12Y31-080\_A1



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