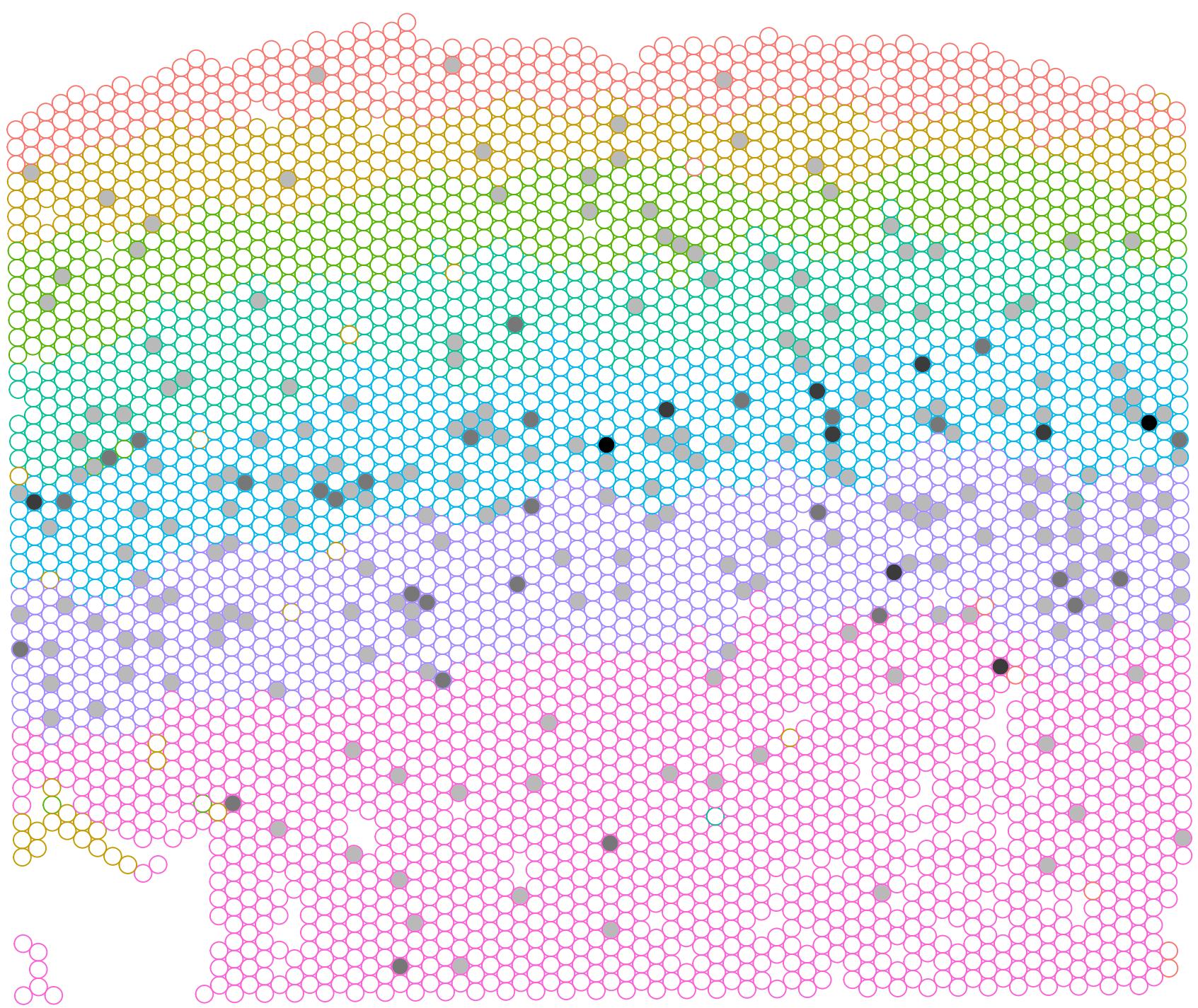


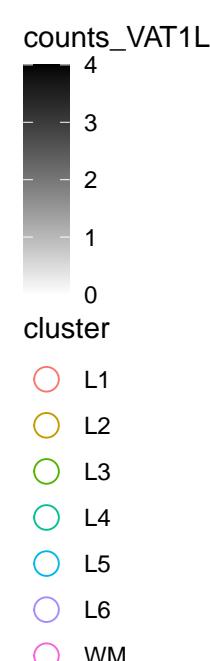
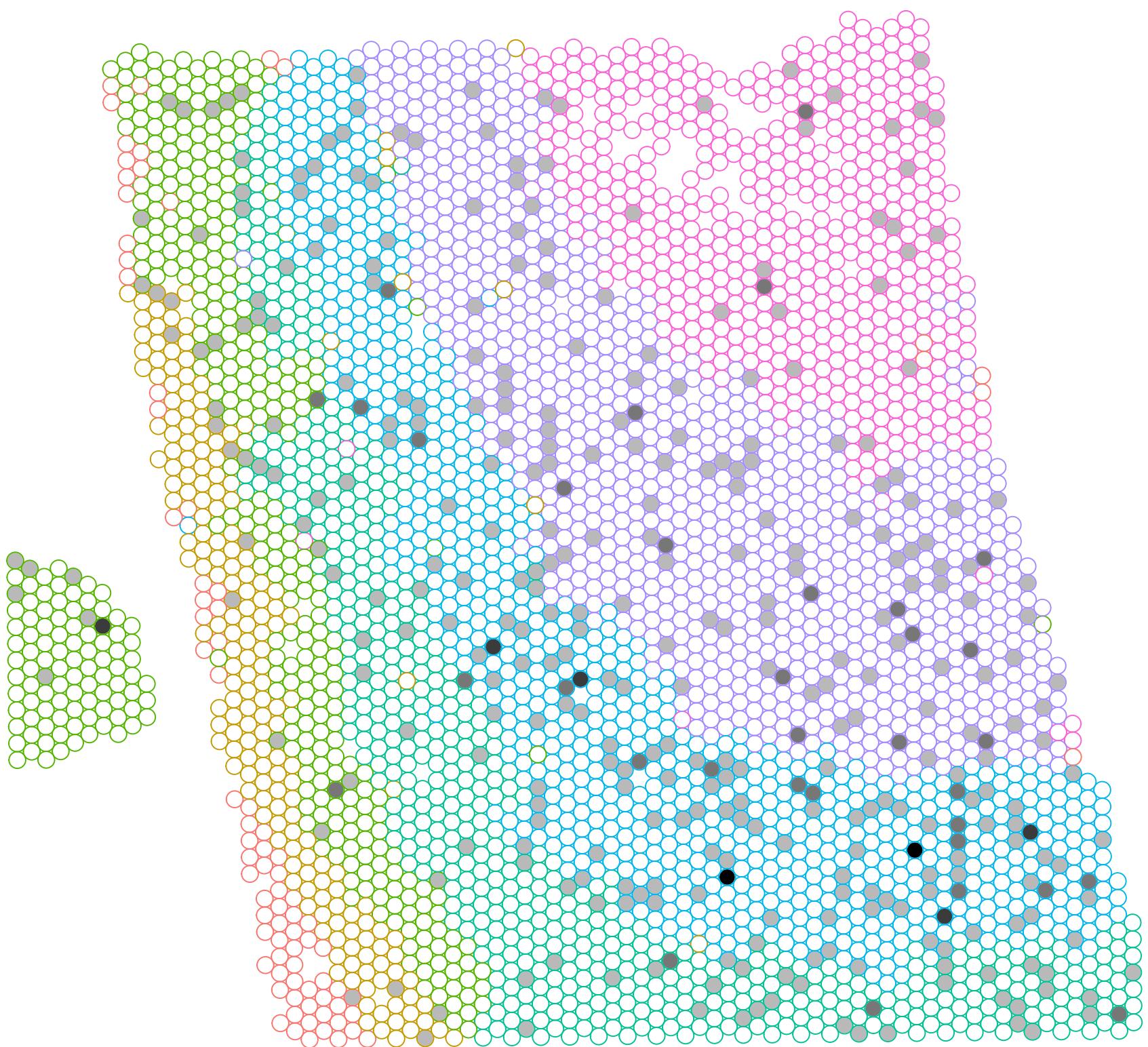
Sample Br2743\_ant



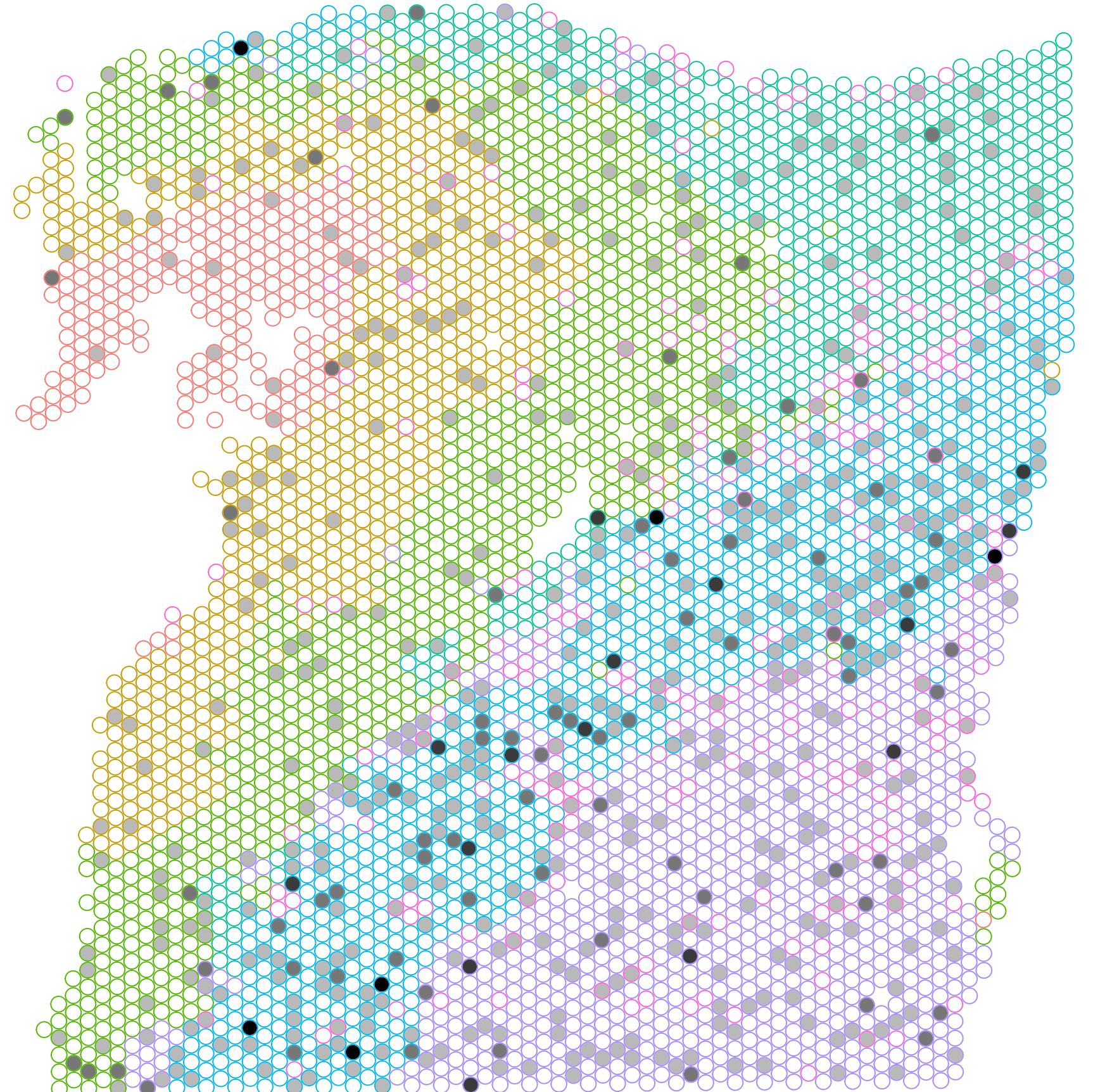
Sample Br2743\_mid



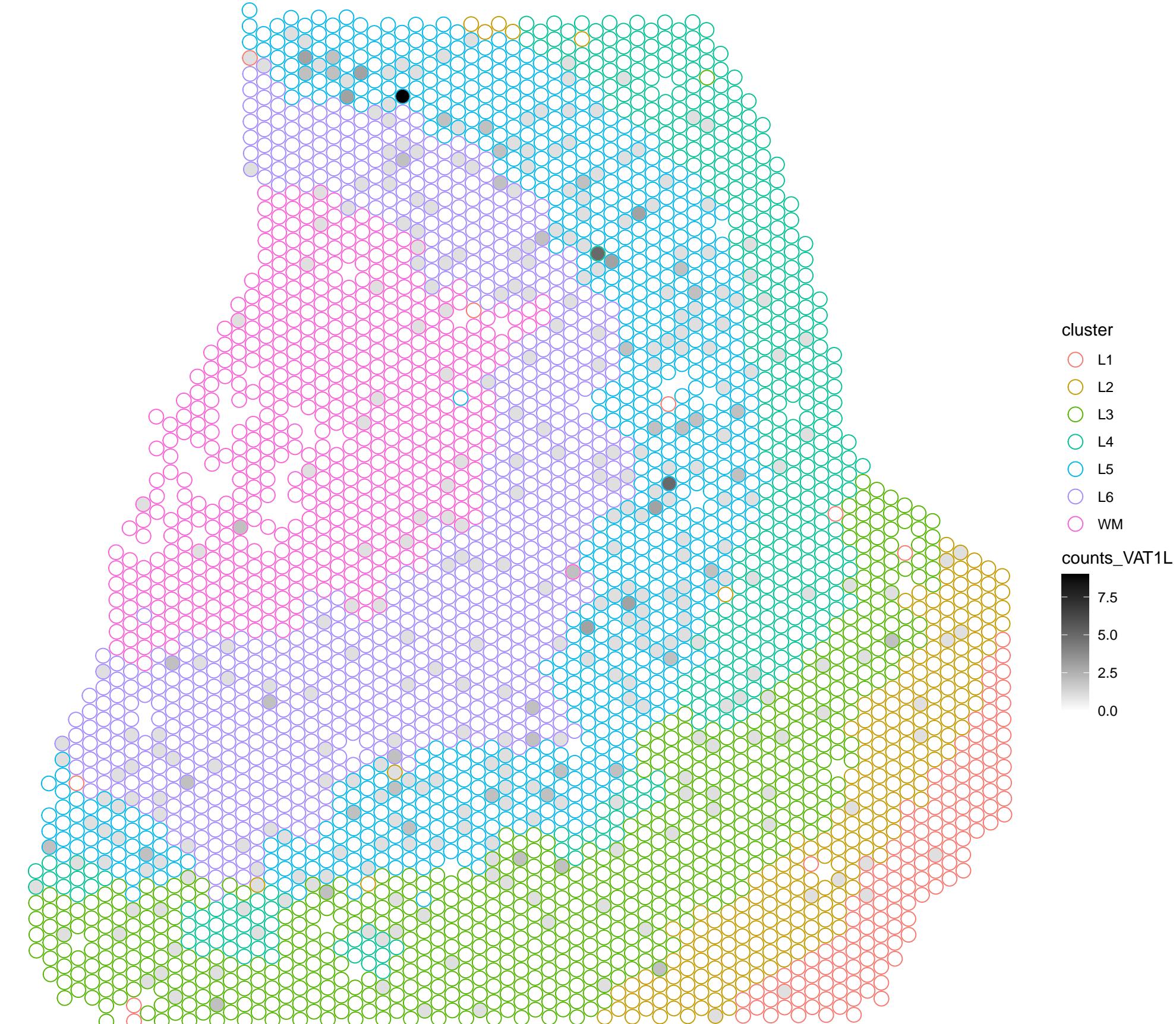
Sample Br2743\_post



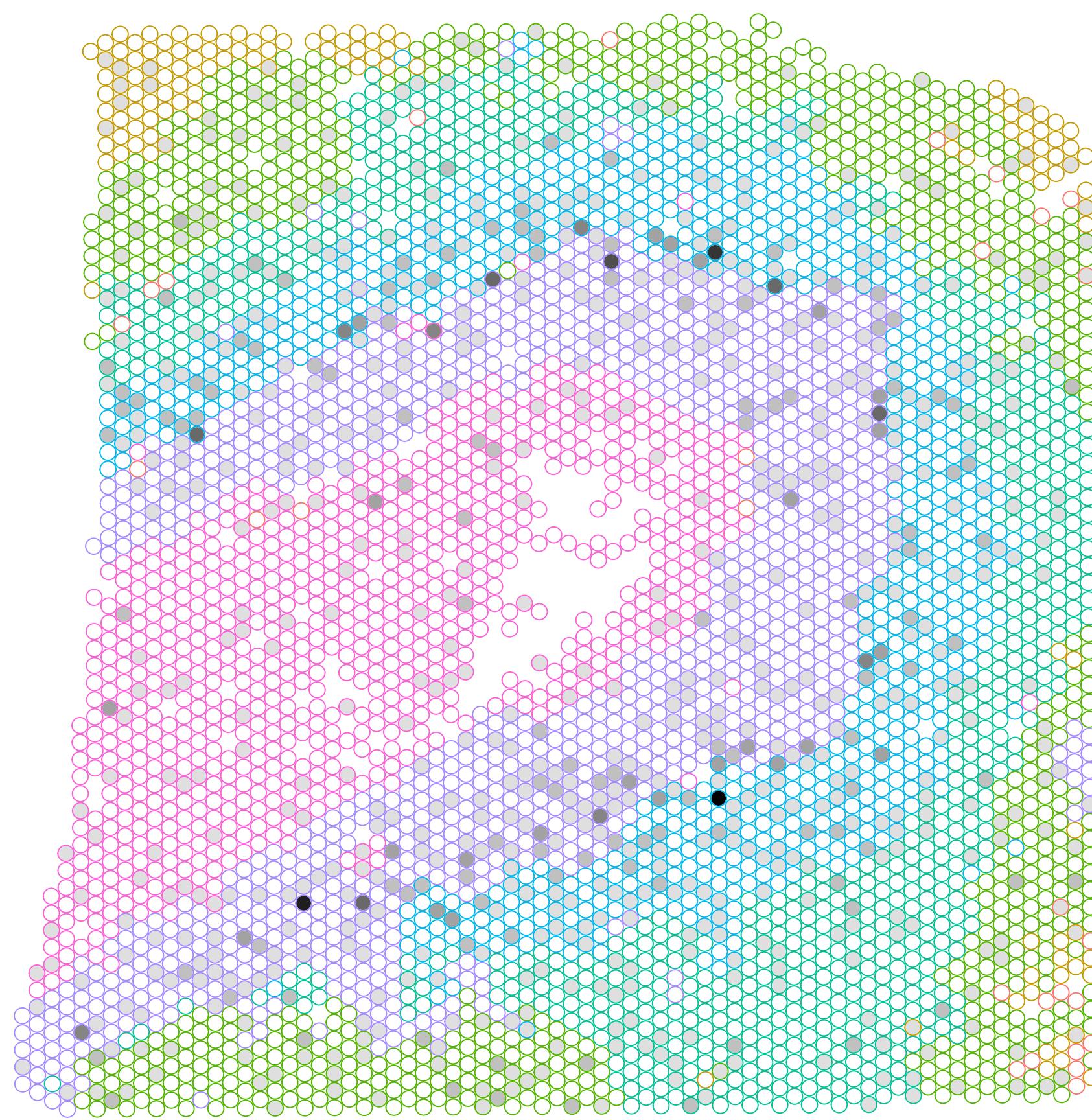
Sample Br3942\_ant



Sample Br3942\_mid



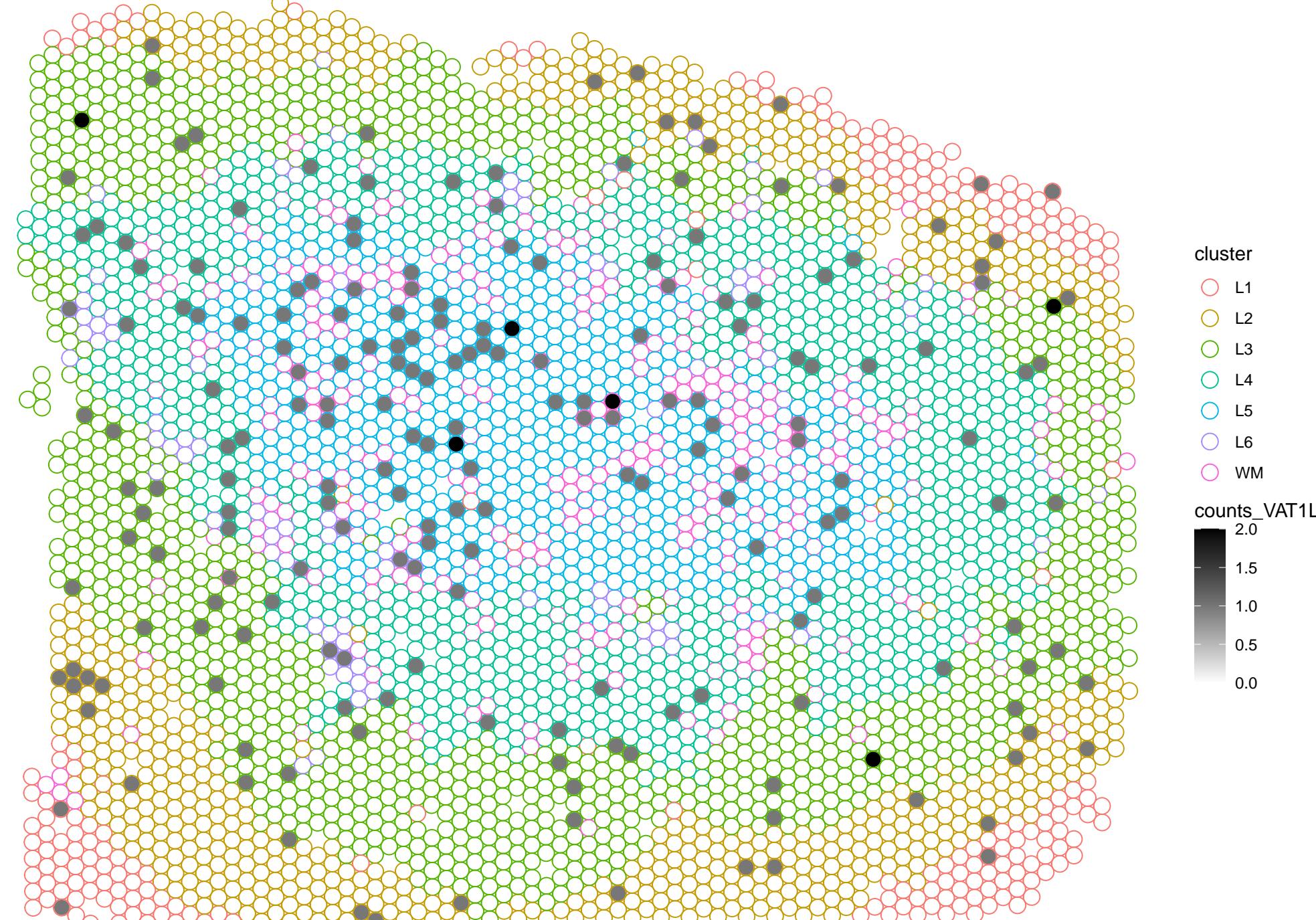
Sample Br3942\_post



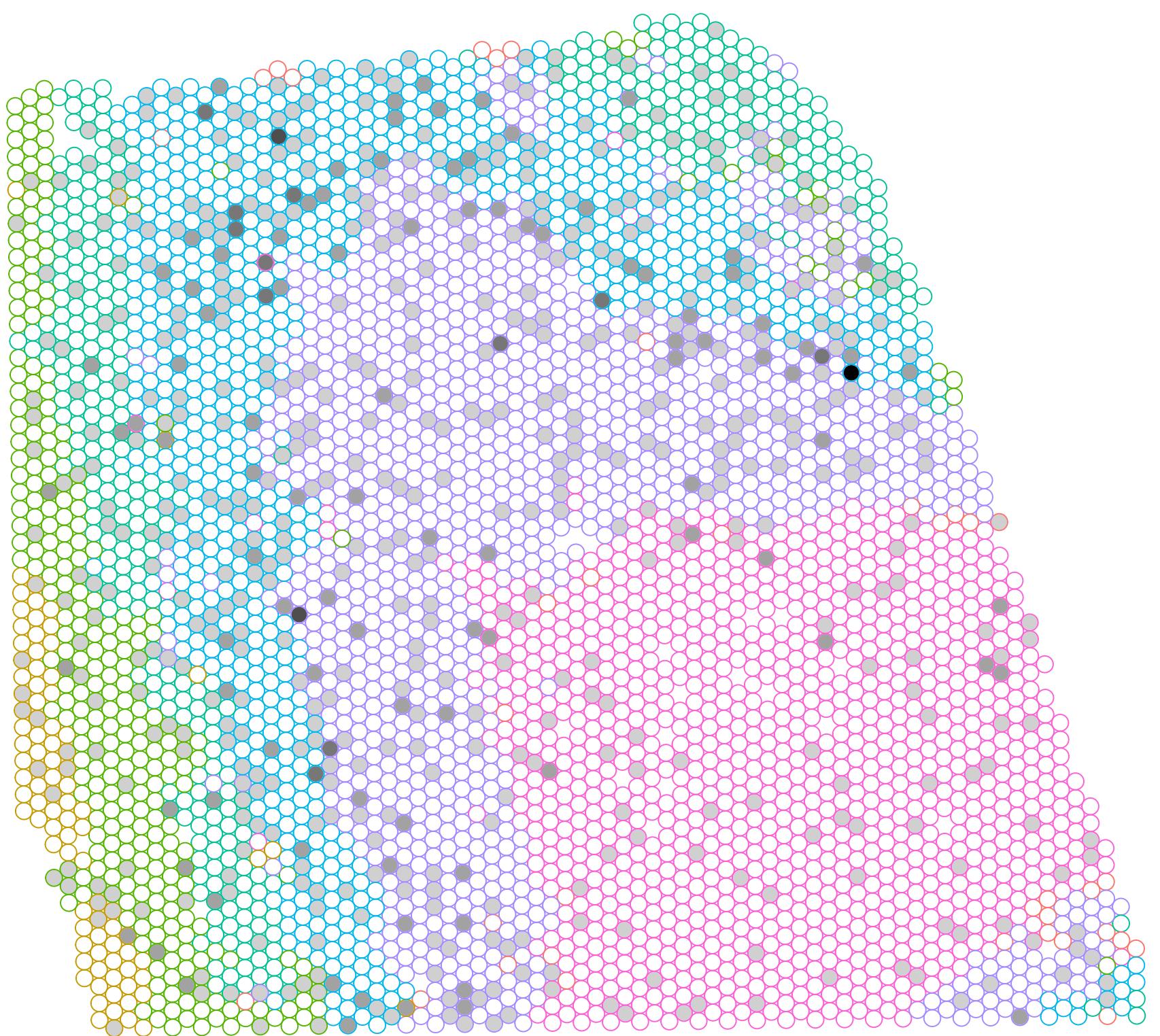
Sample Br6423\_ant



Sample Br6423\_mid



Sample Br6423\_post



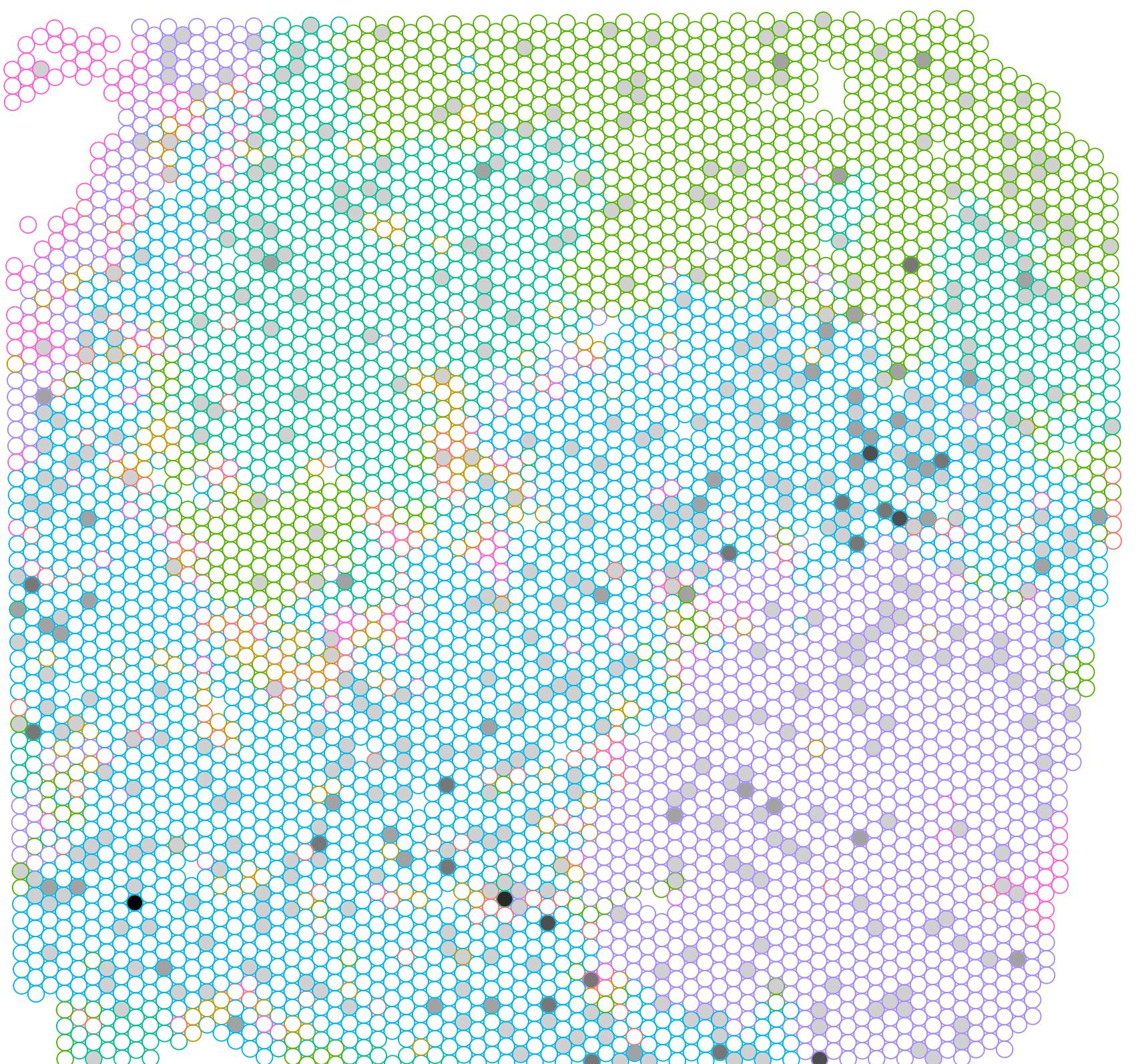
cluster

- L1
- L2
- L3
- L4
- L5
- L6
- WM

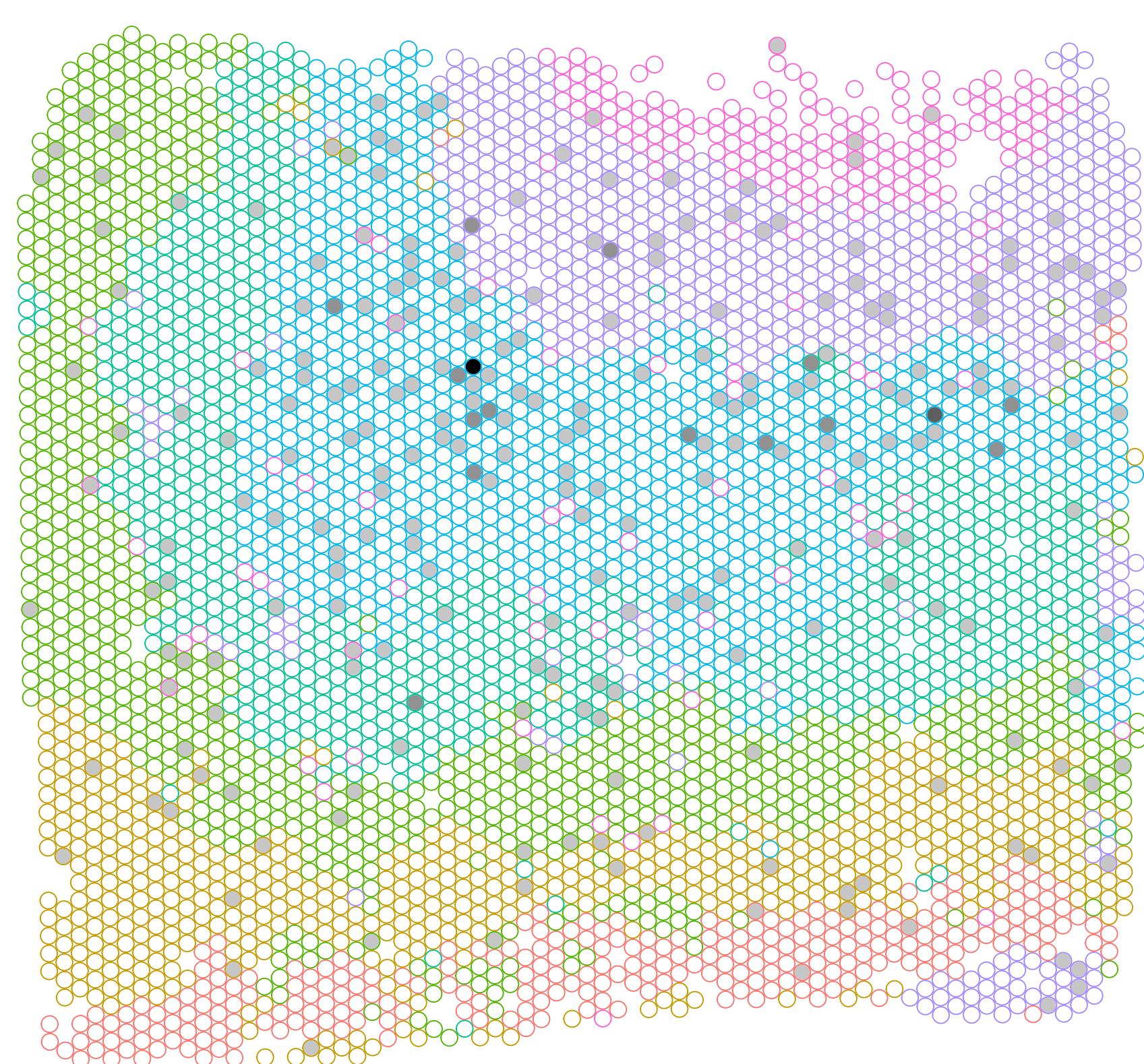
counts\_VAT1L

6  
5  
4  
3  
2  
1  
0

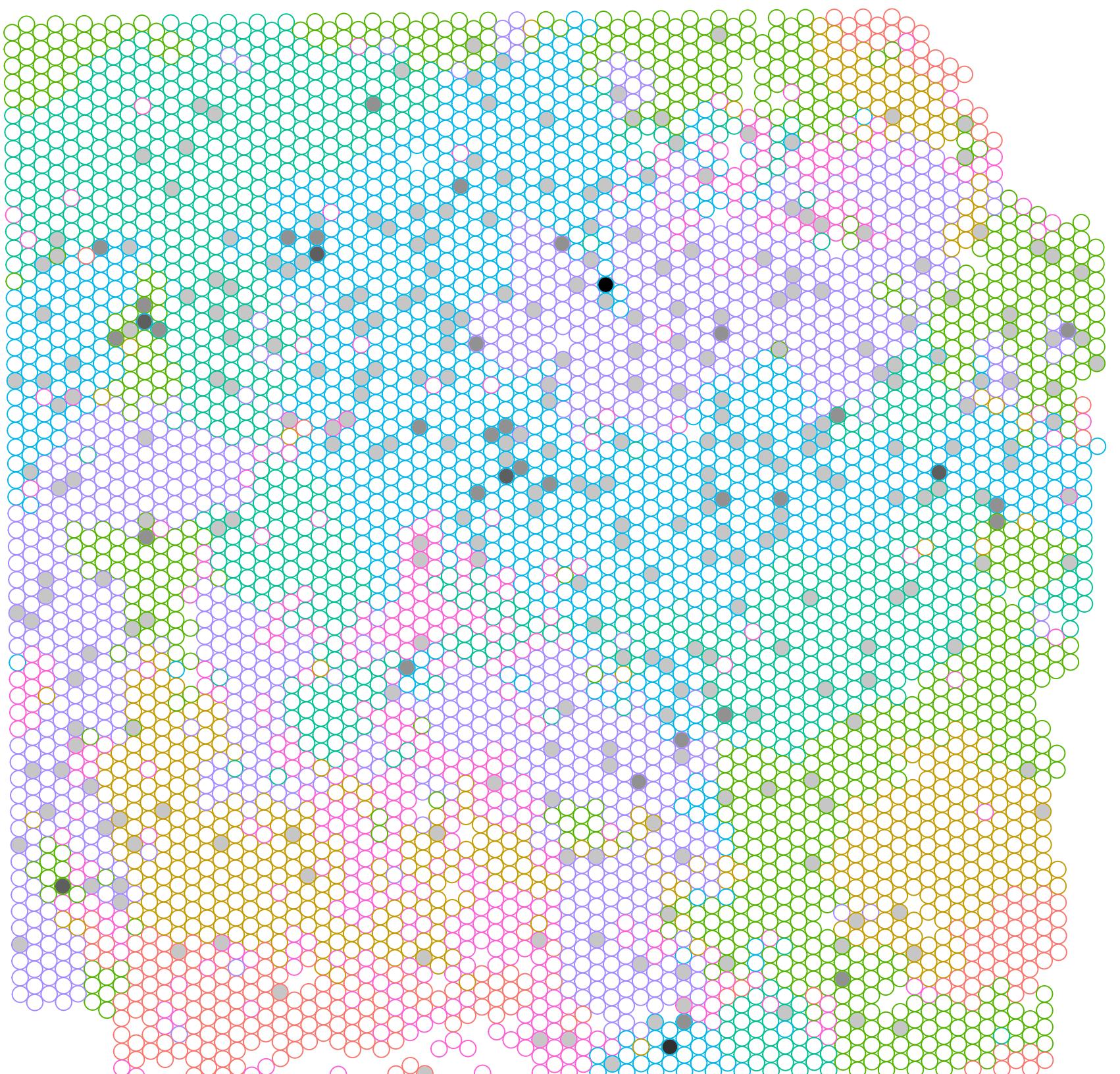
Sample Br8492\_ant



Sample Br8492\_mid



Sample Br8492\_post



cluster

- L1
- L2
- L3
- L4
- L5
- L6
- WM

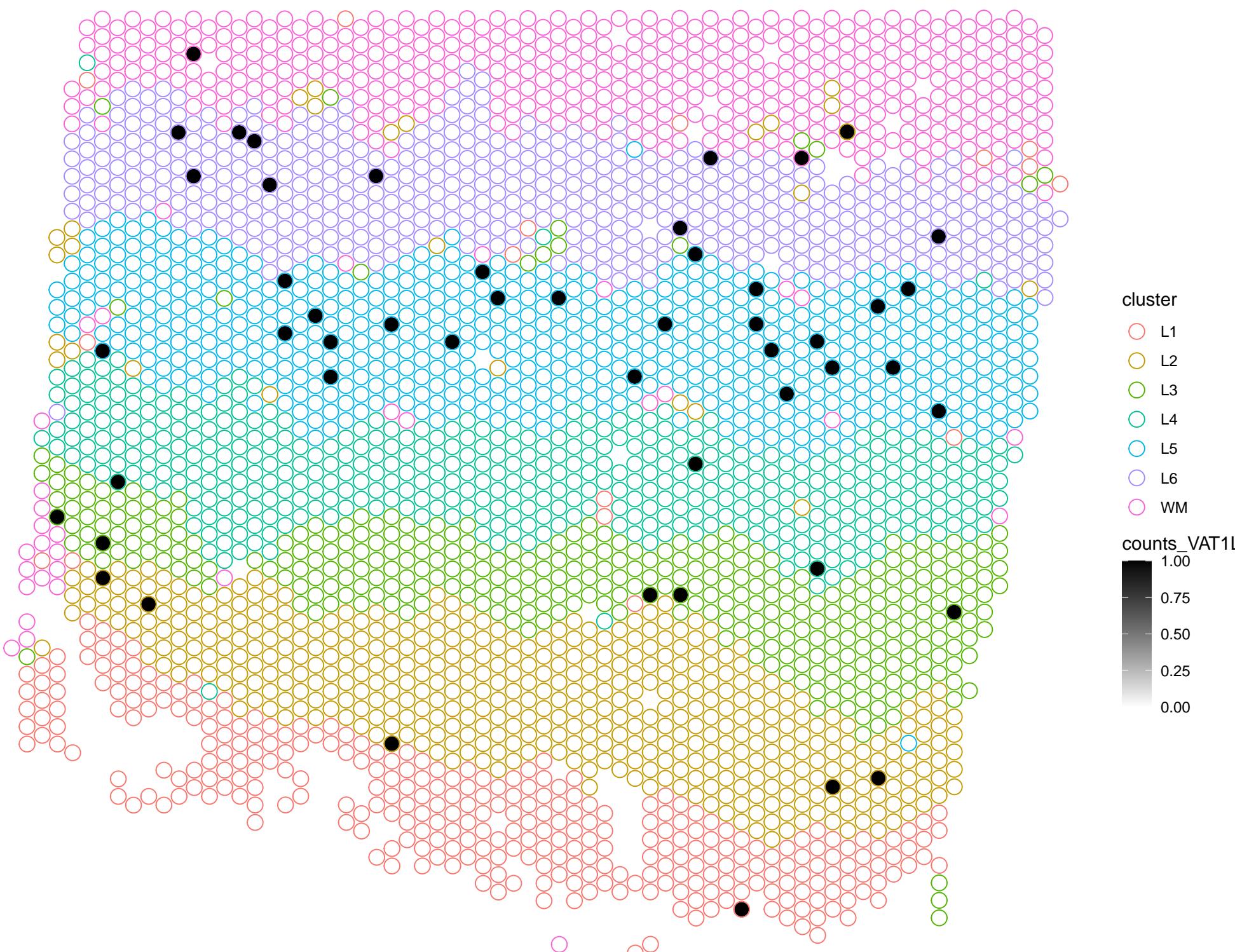
counts\_VAT1L

5  
4  
3  
2  
1  
0

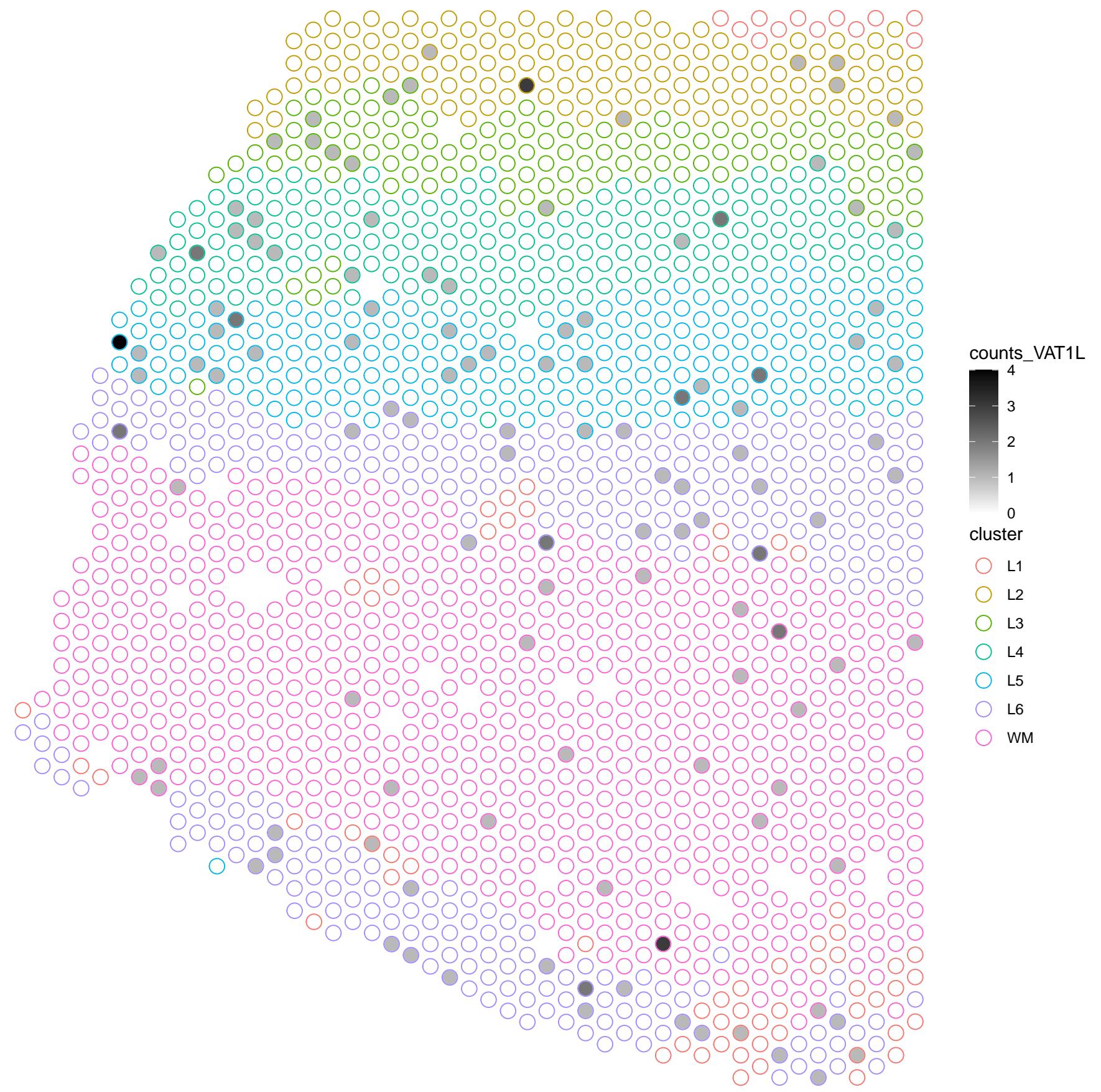
cluster  
L1  
L2  
L3  
L4  
L5  
L6  
WM

counts\_VAT1L  
5  
4  
3  
2  
1  
0

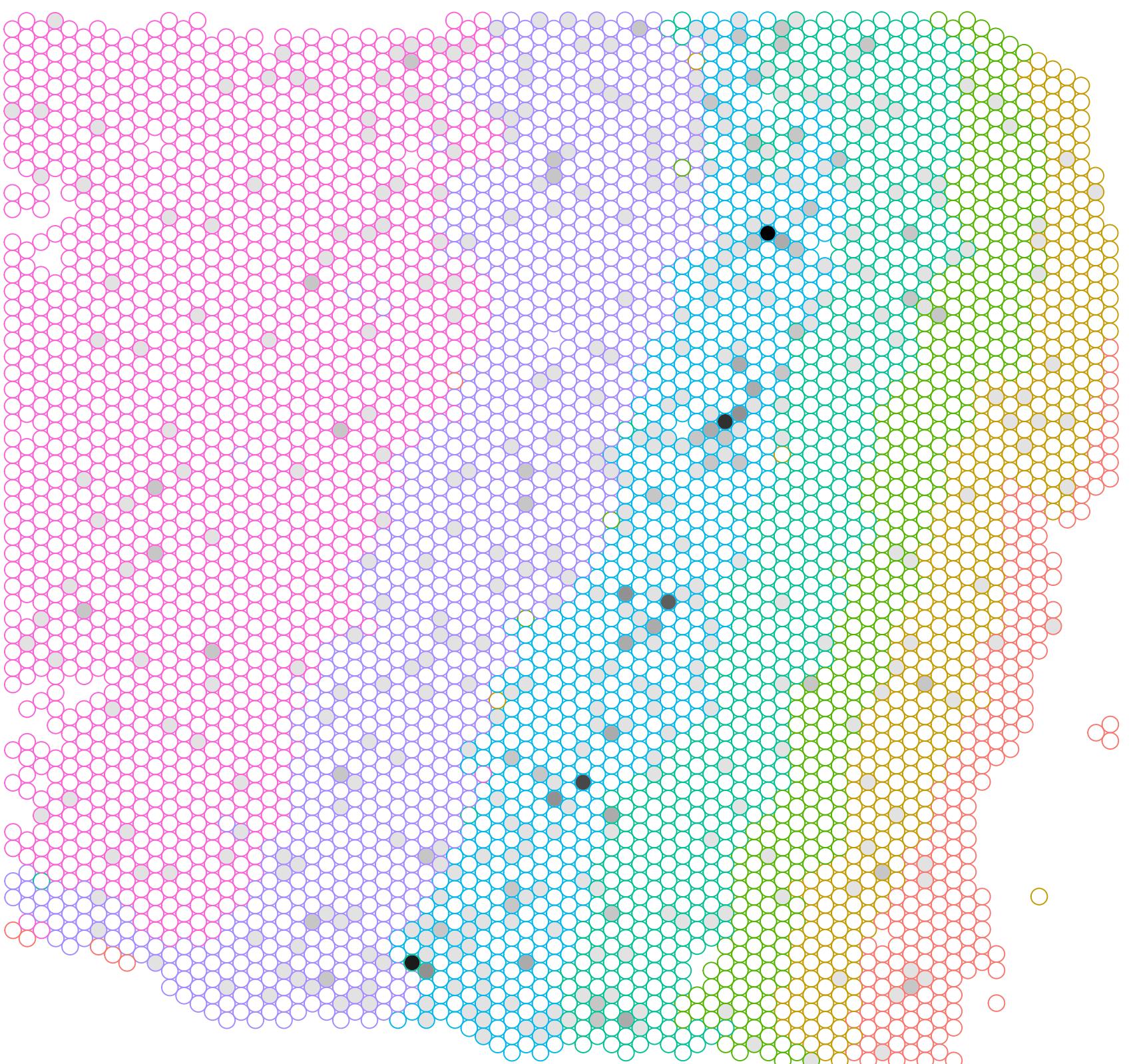
Sample Br2720\_ant



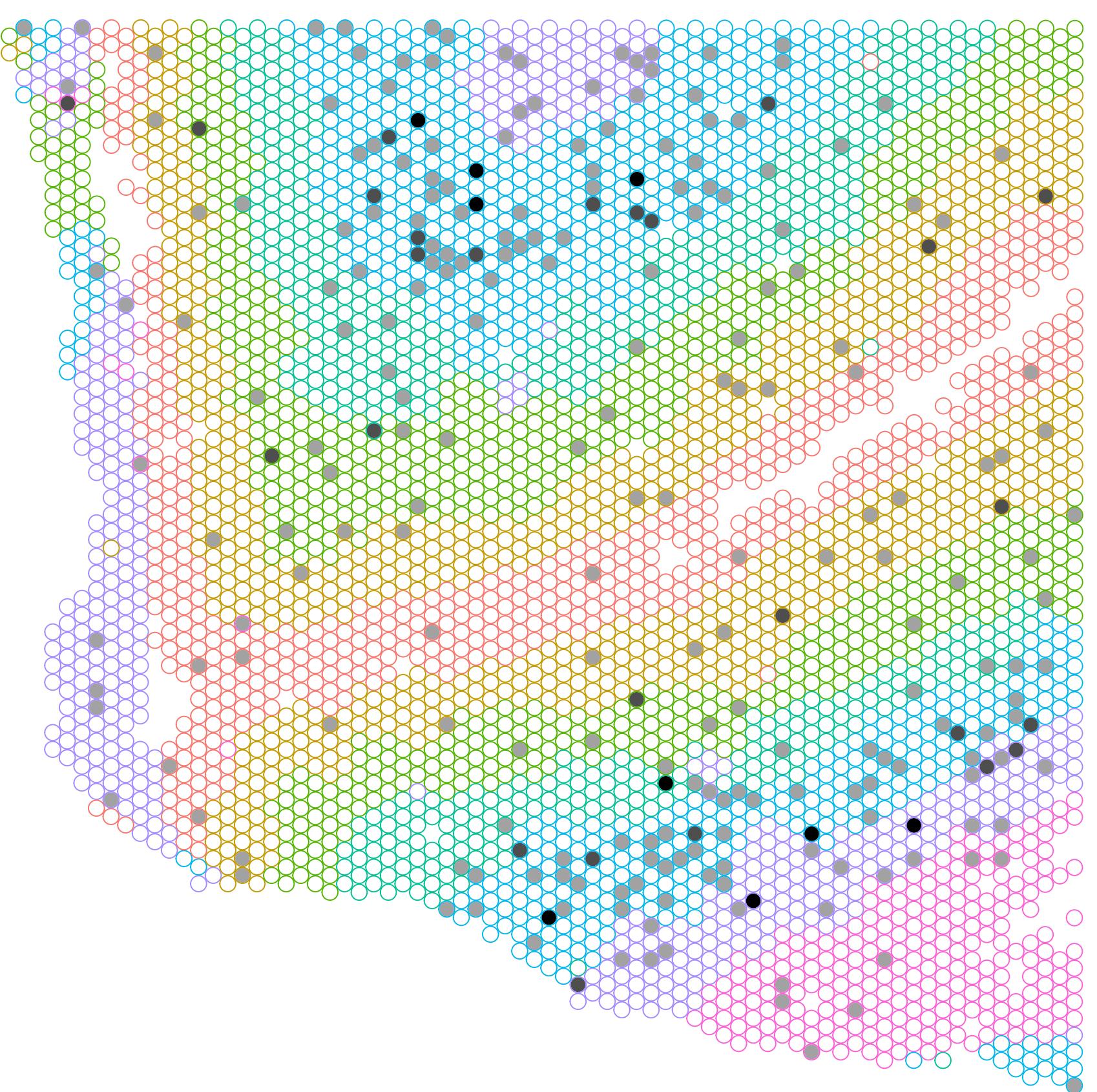
Sample Br2720\_mid



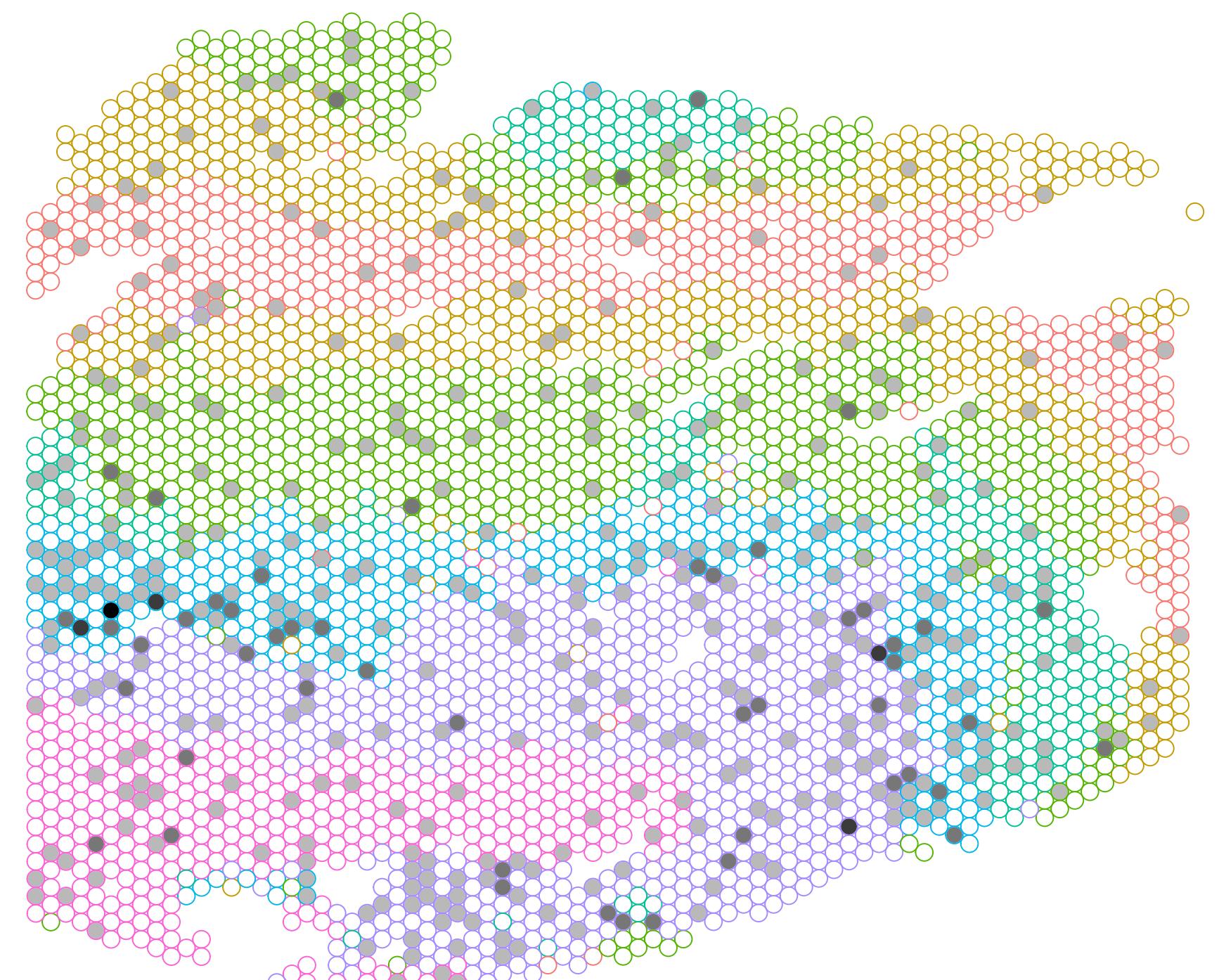
Sample Br2720\_post



Sample Br6432\_ant

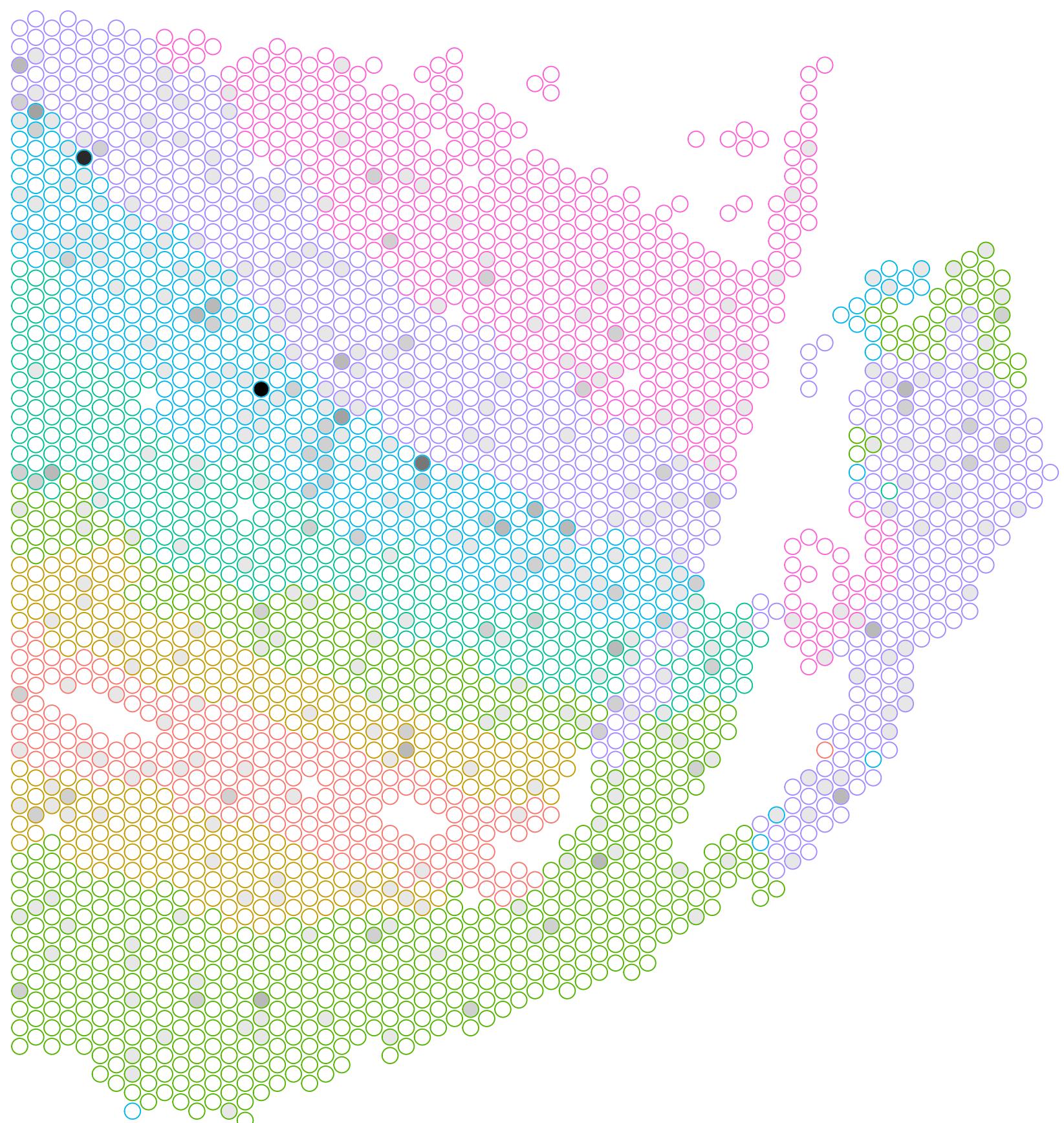


Sample Br6432\_mid



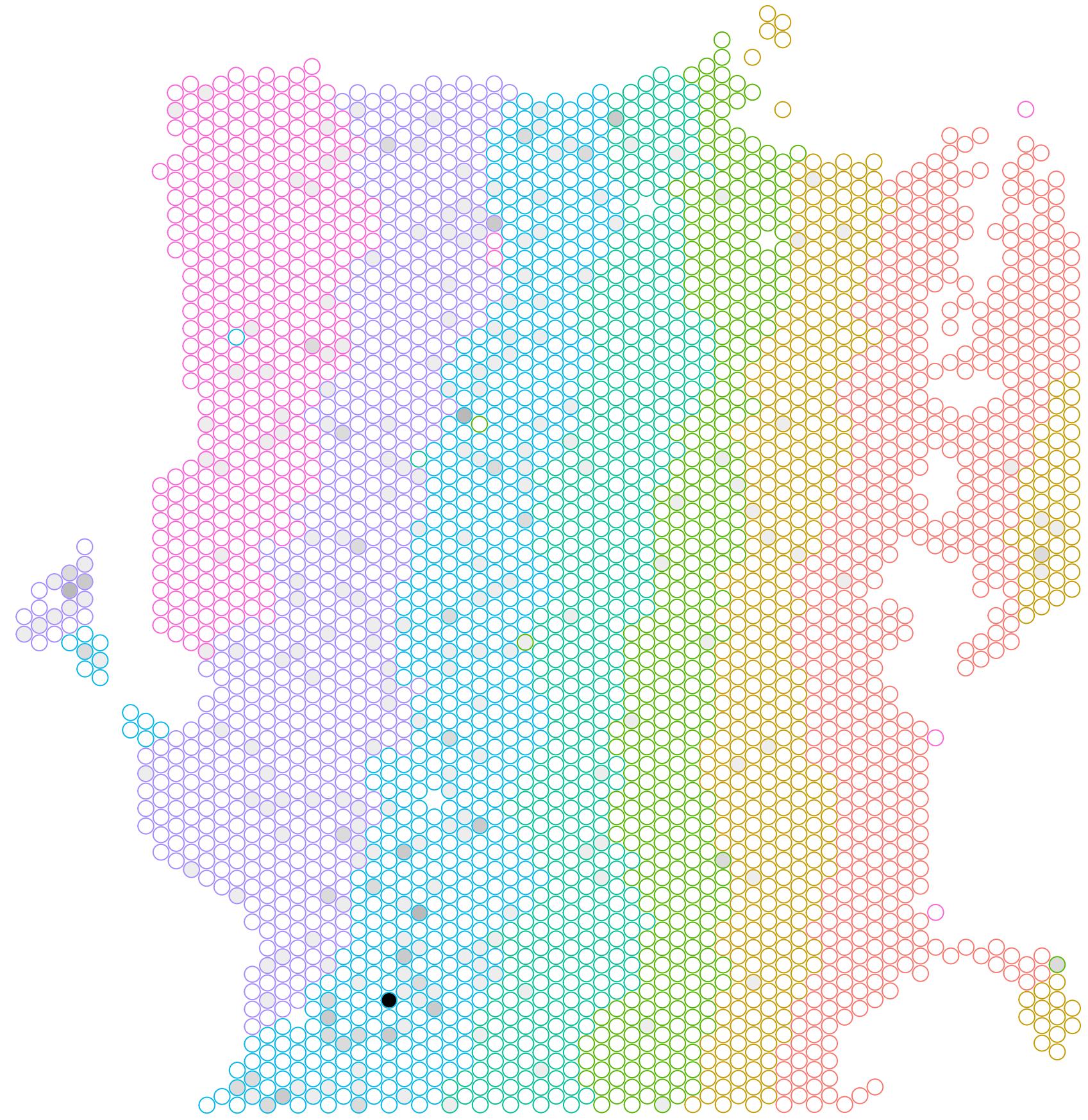
counts\_VAT1L  
cluster  
L1  
L2  
L3  
L4  
L5  
L6  
WM

Sample Br6432\_post

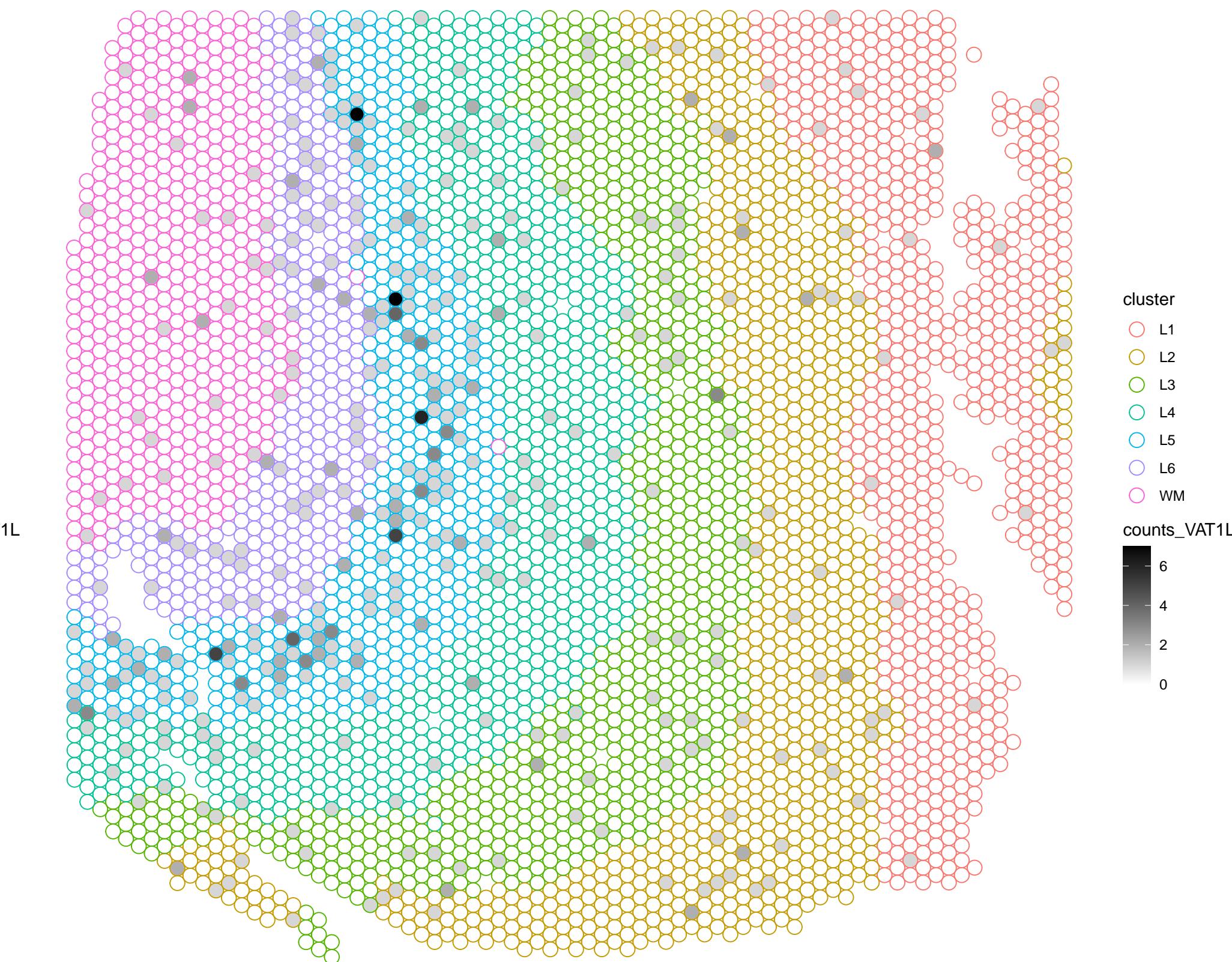


cluster  
L1  
L2  
L3  
L4  
L5  
L6  
WM  
counts\_VAT1L  
12  
9  
6  
3  
0

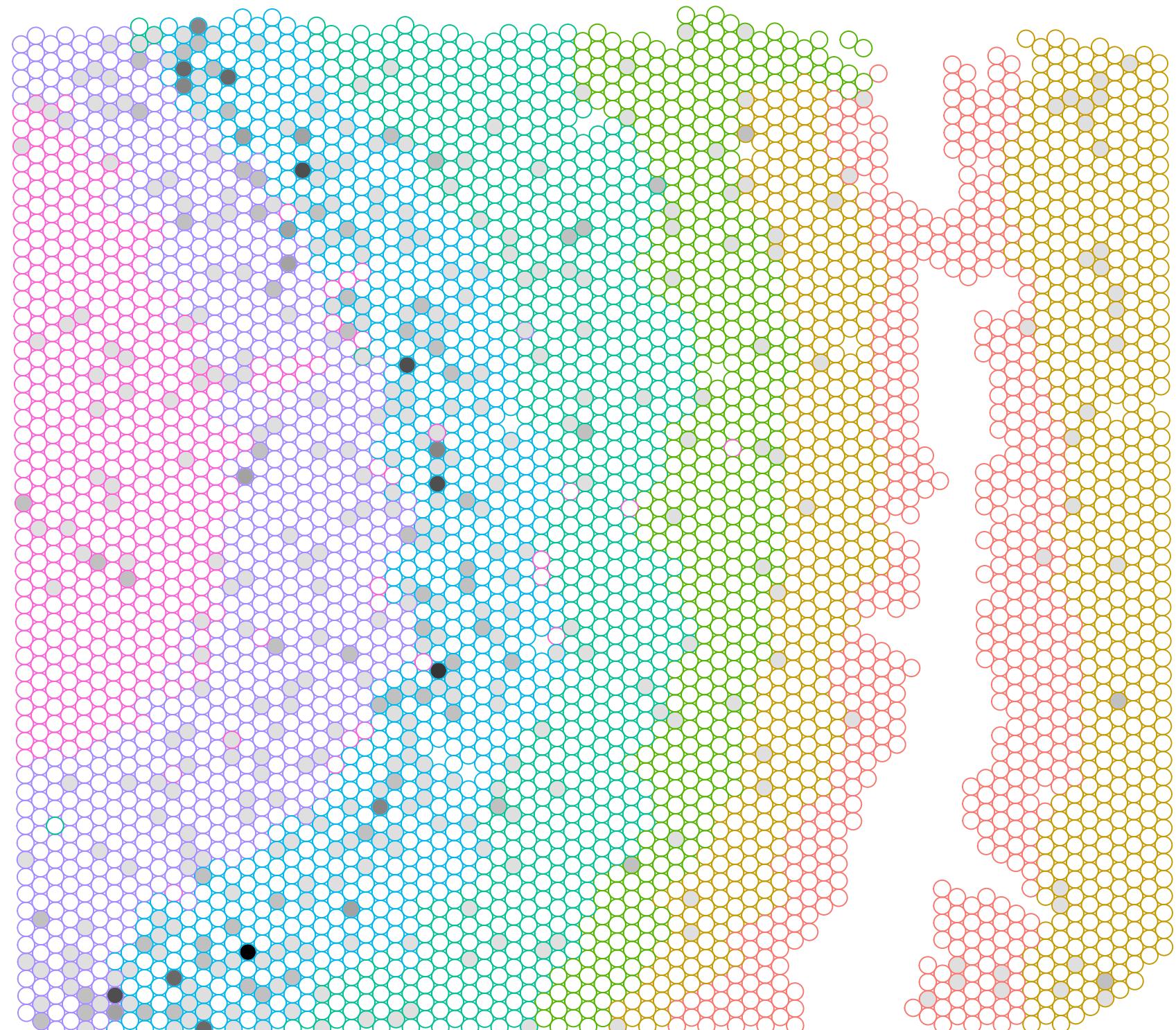
Sample Br6471\_ant



Sample Br6471\_mid



Sample Br6471\_post



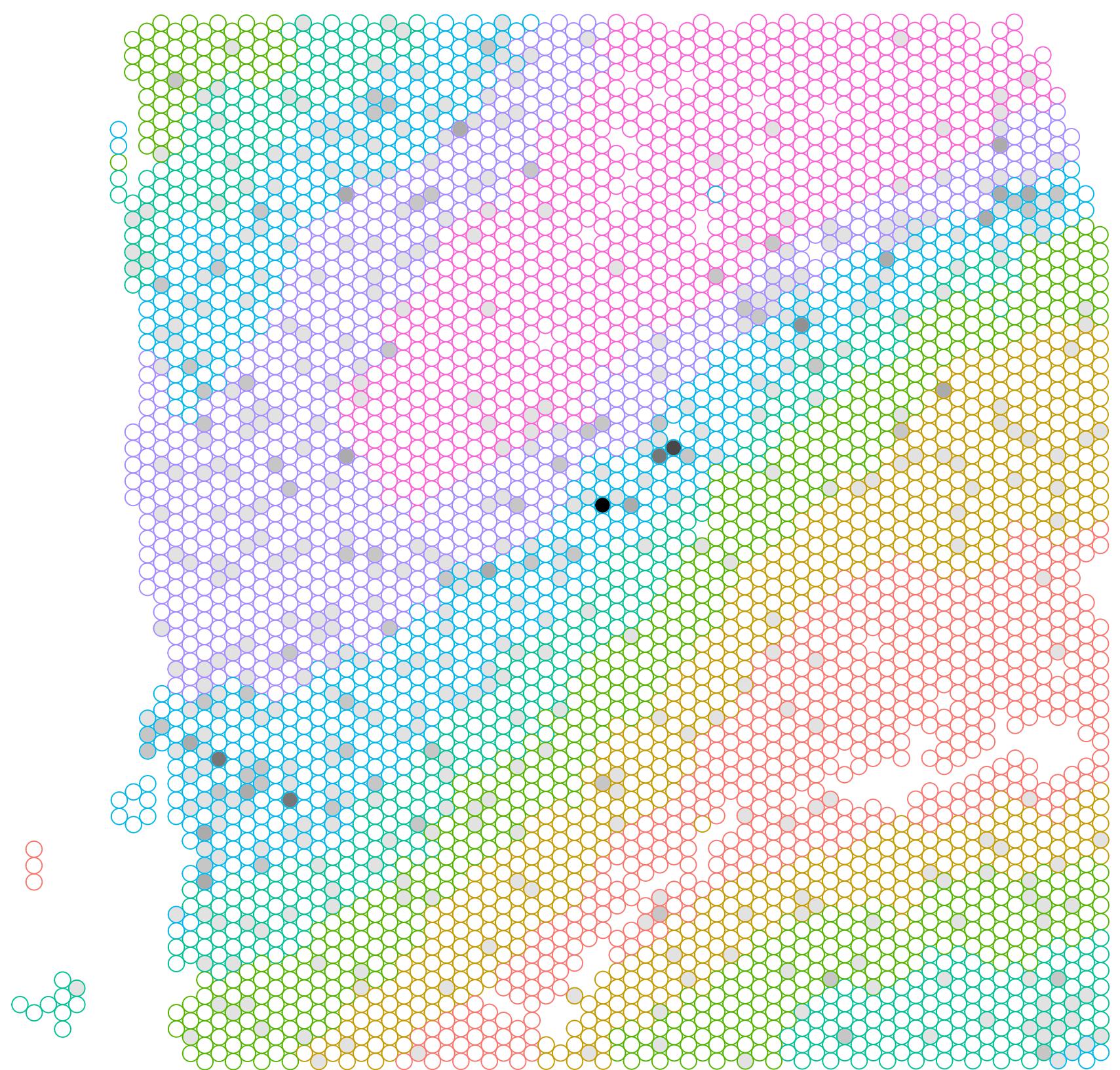
cluster

- L1
- L2
- L3
- L4
- L5
- L6
- WM

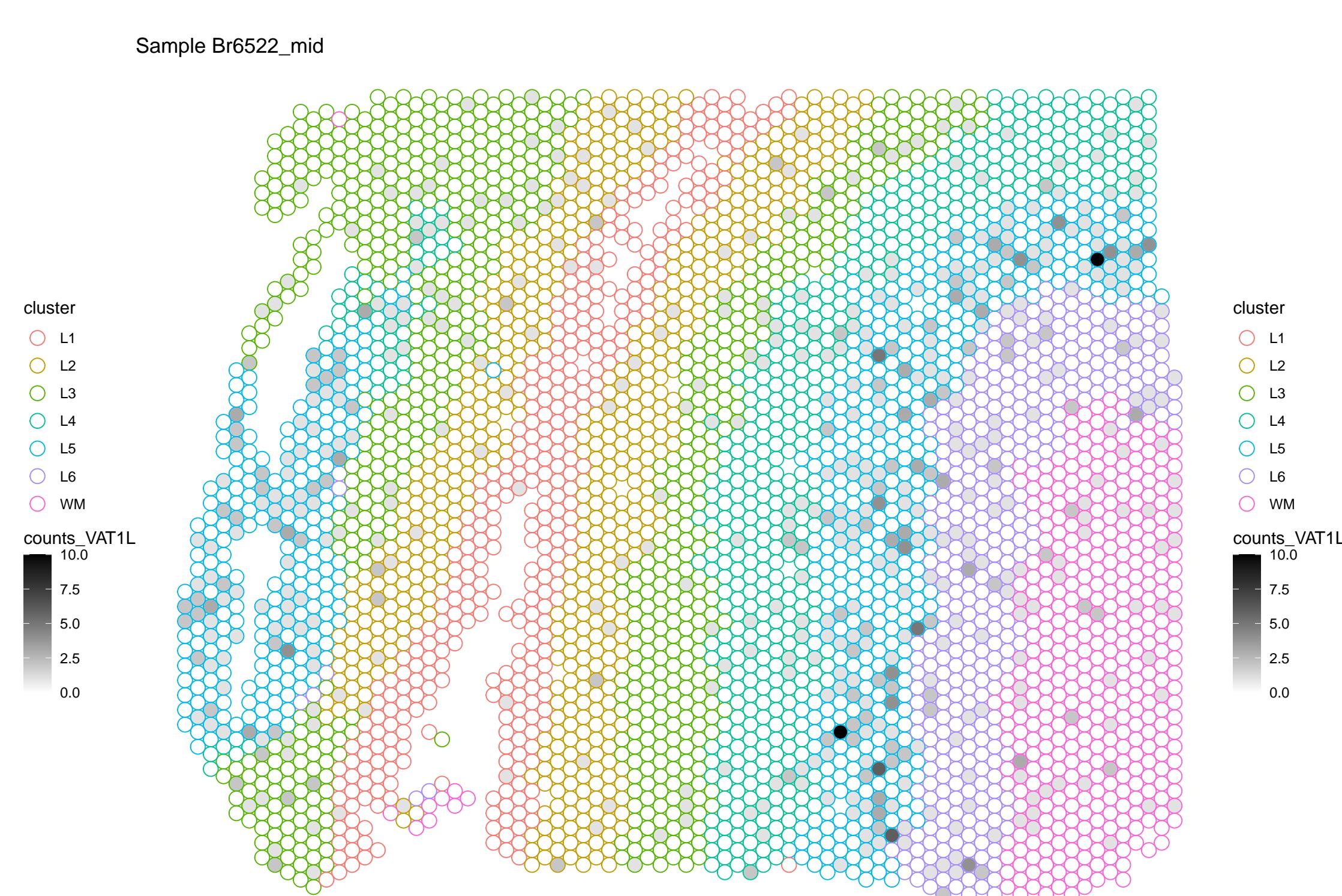
counts\_VAT1L



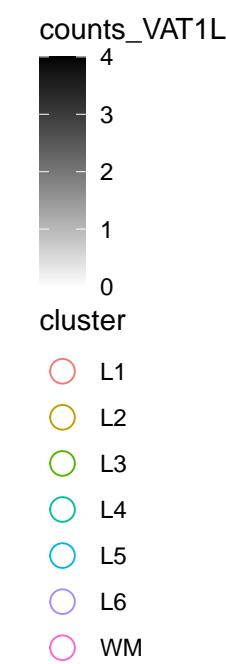
Sample Br6522\_ant



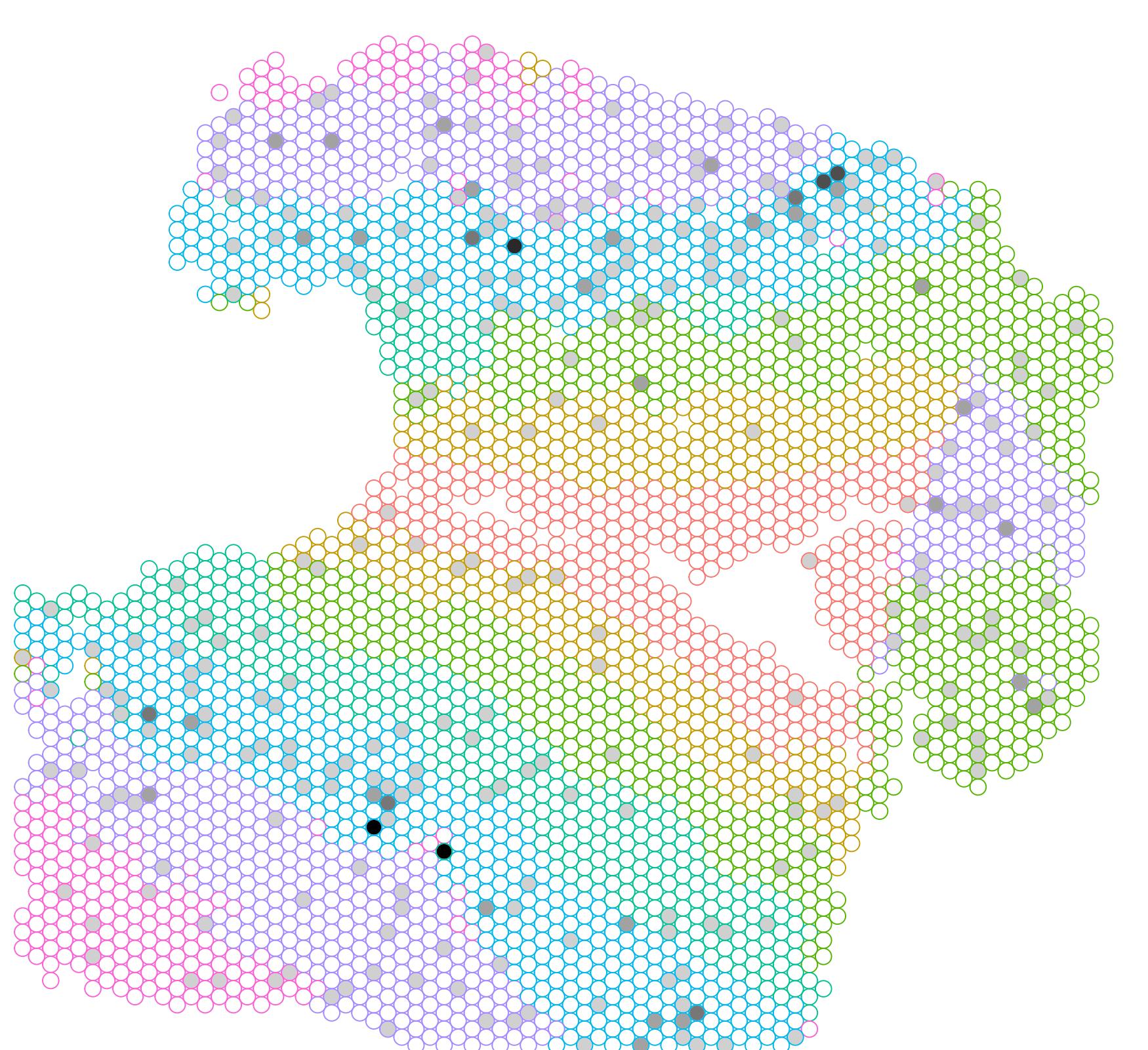
Sample Br6522\_mid



Sample Br6522\_post



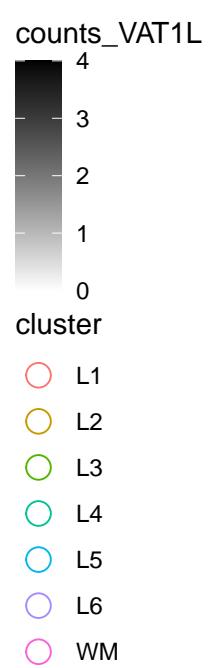
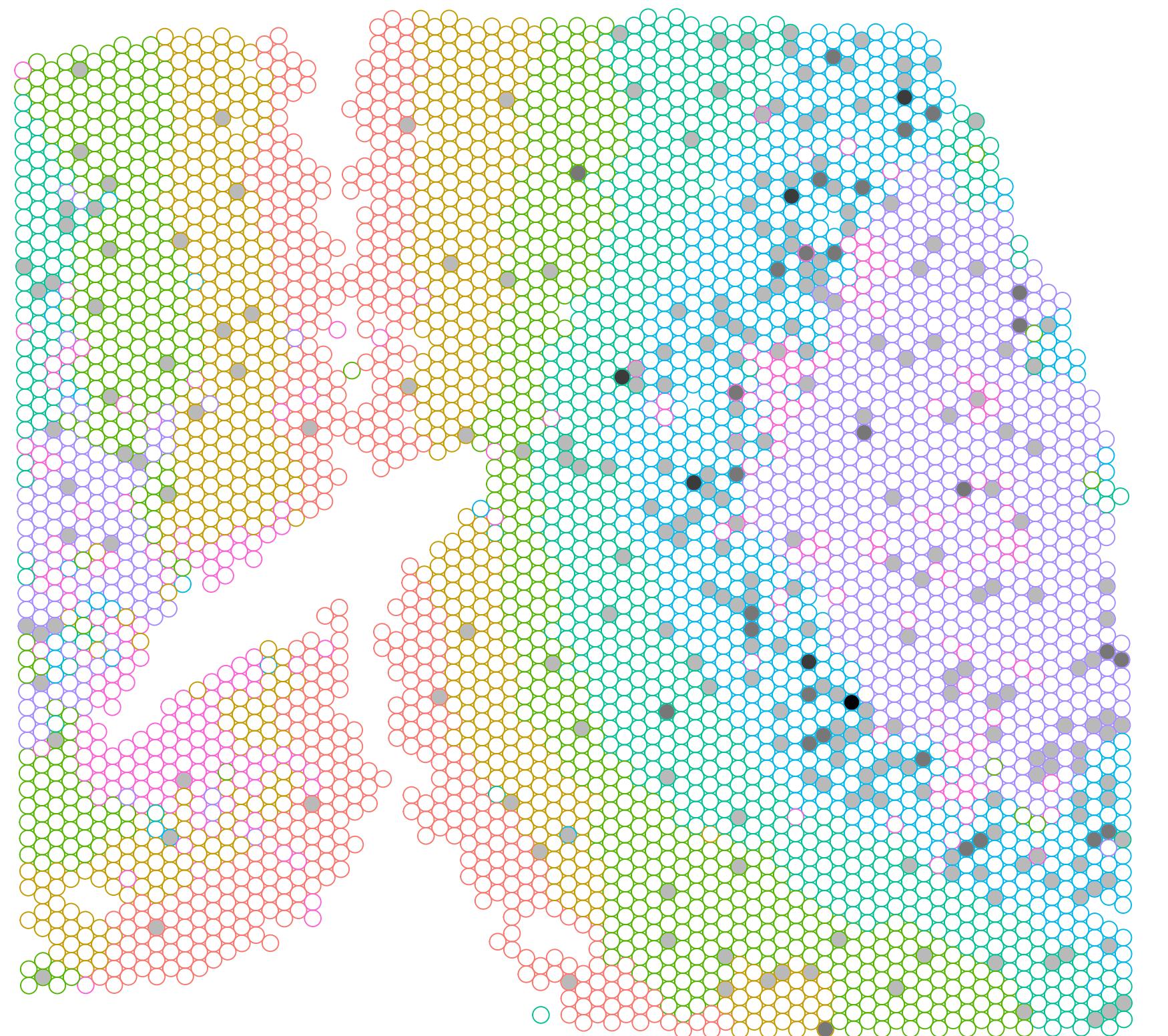
Sample Br8325\_ant



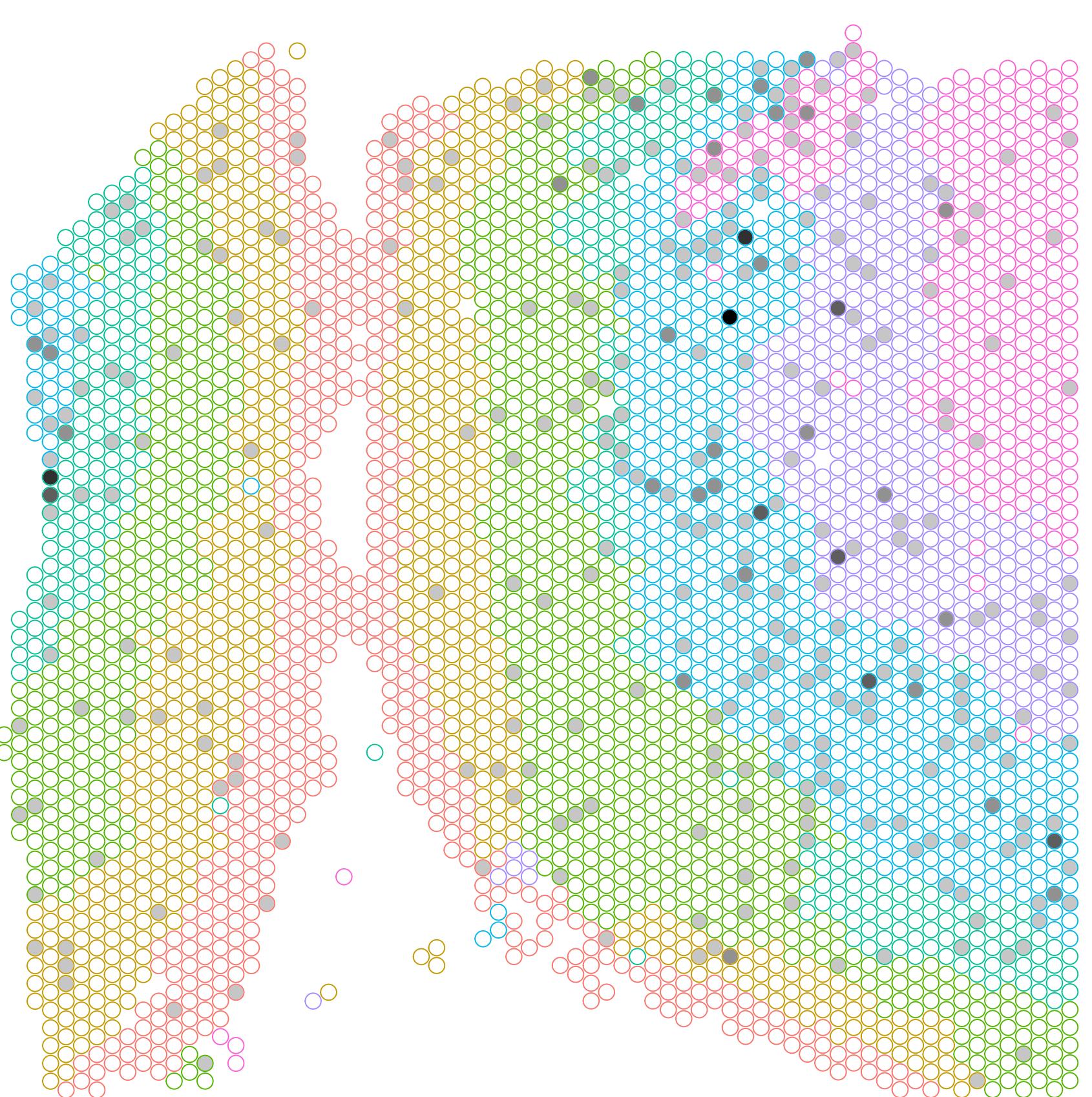
Sample Br8325\_mid



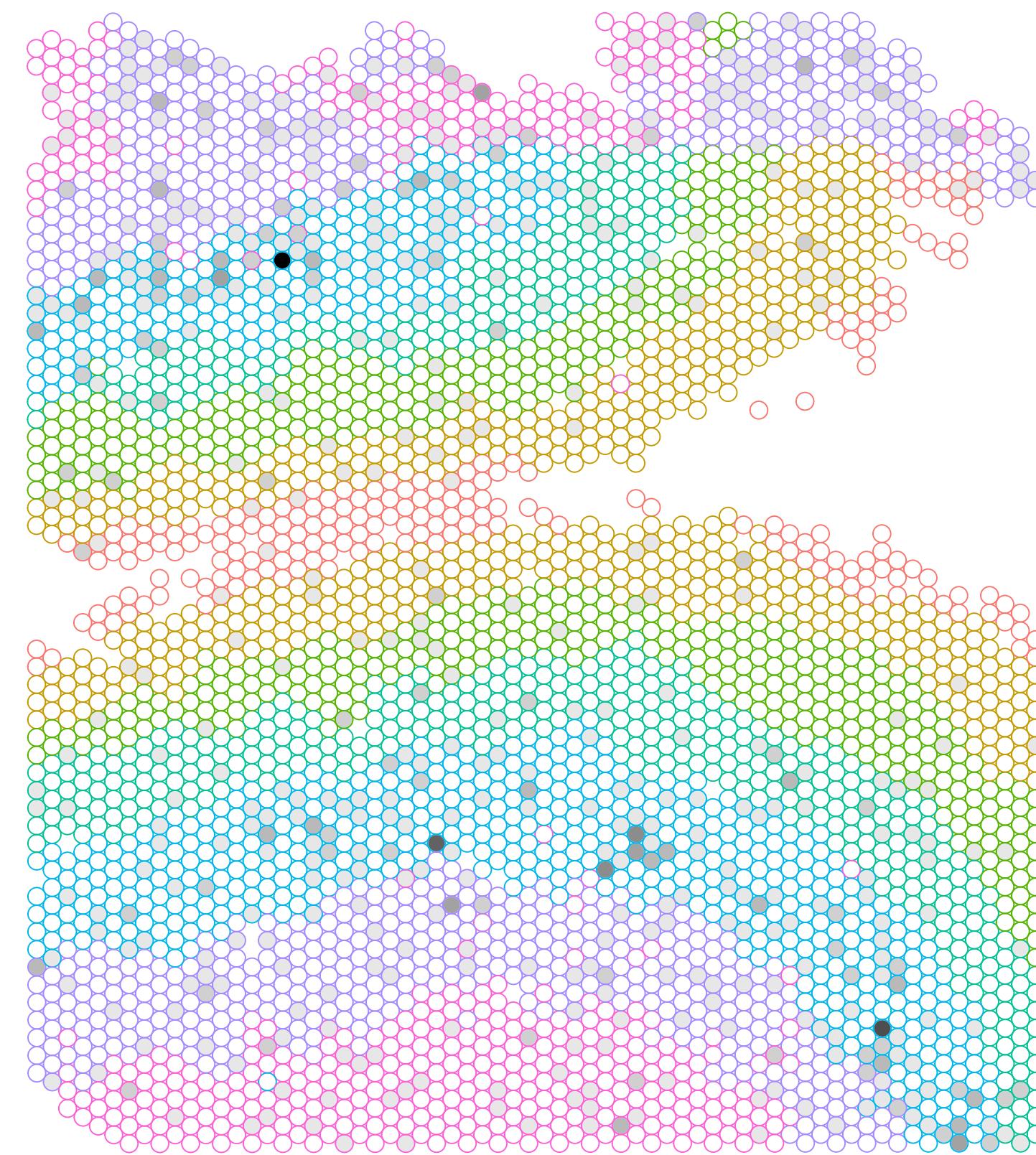
Sample Br8325\_post



Sample Br8667\_ant



Sample Br8667\_mid



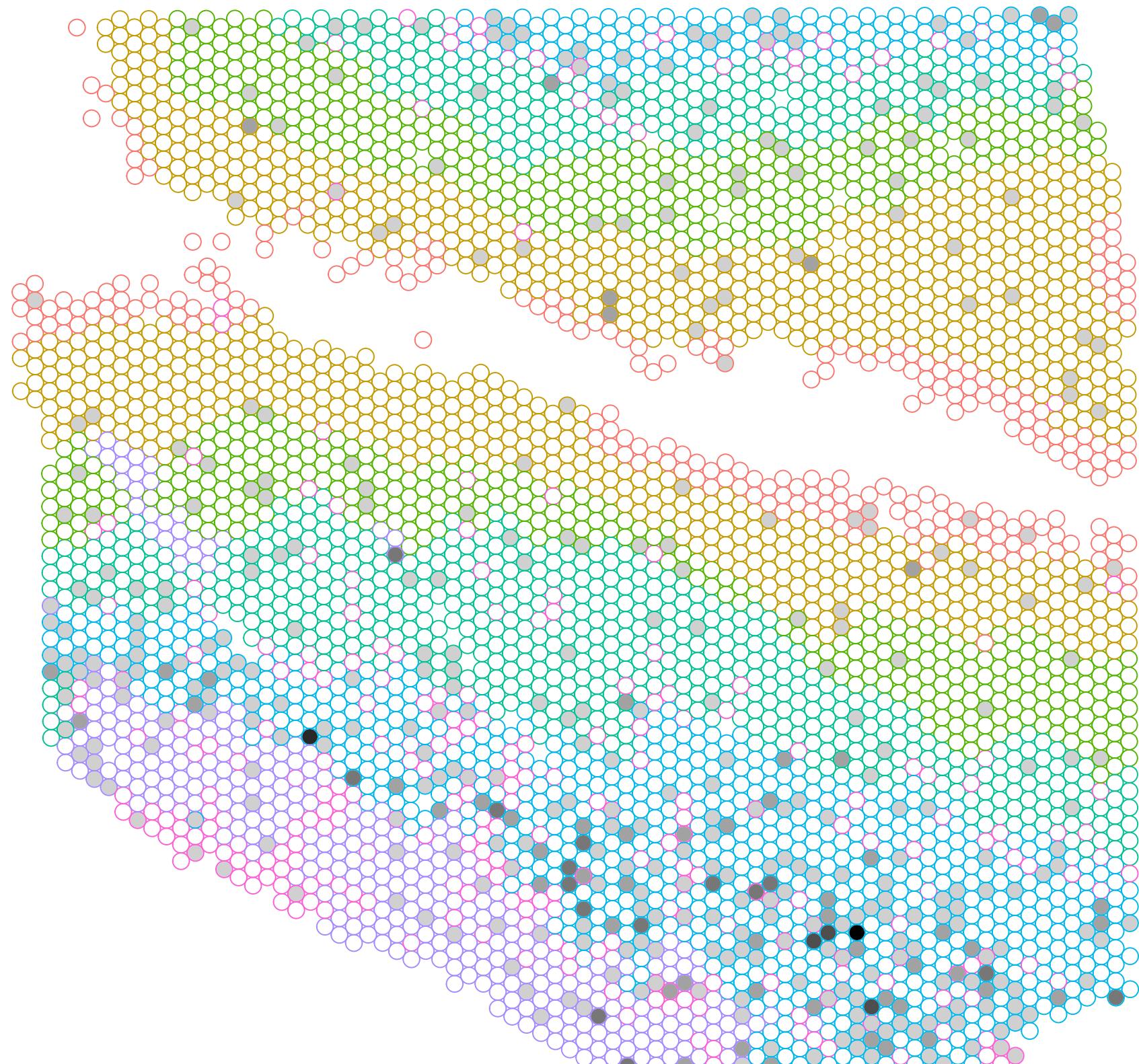
cluster  
L1  
L2  
L3  
L4  
L5  
L6  
WM

cluster  
L1  
L2  
L3  
L4  
L5  
L6  
WM

counts\_VAT1L  
5  
4  
3  
2  
1  
0

counts\_VAT1L  
12  
9  
6  
3  
0

Sample Br8667\_post



cluster  
L1  
L2  
L3  
L4  
L5  
L6  
WM

counts\_VAT1L  
6  
5  
4  
3  
2  
1  
0