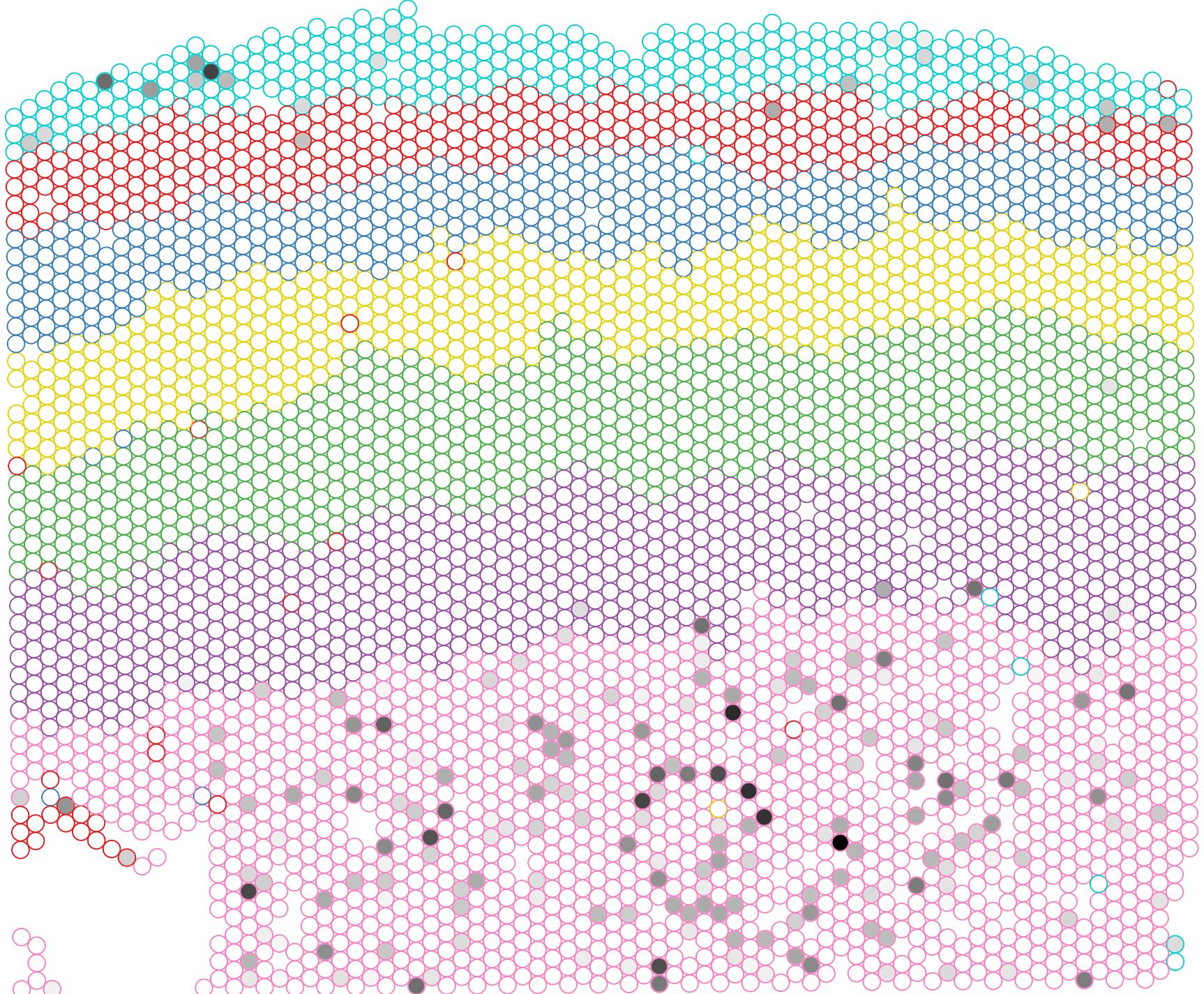
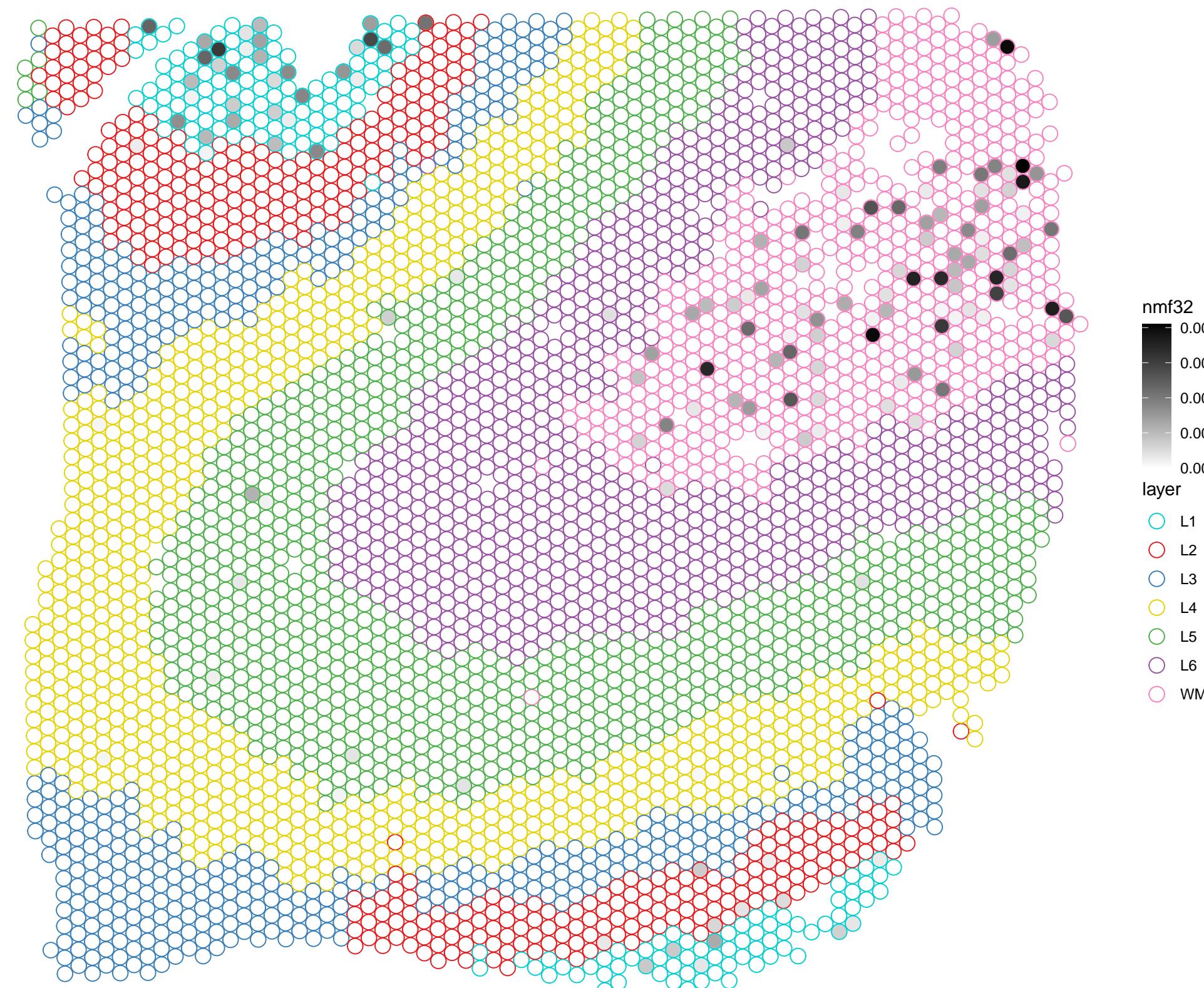


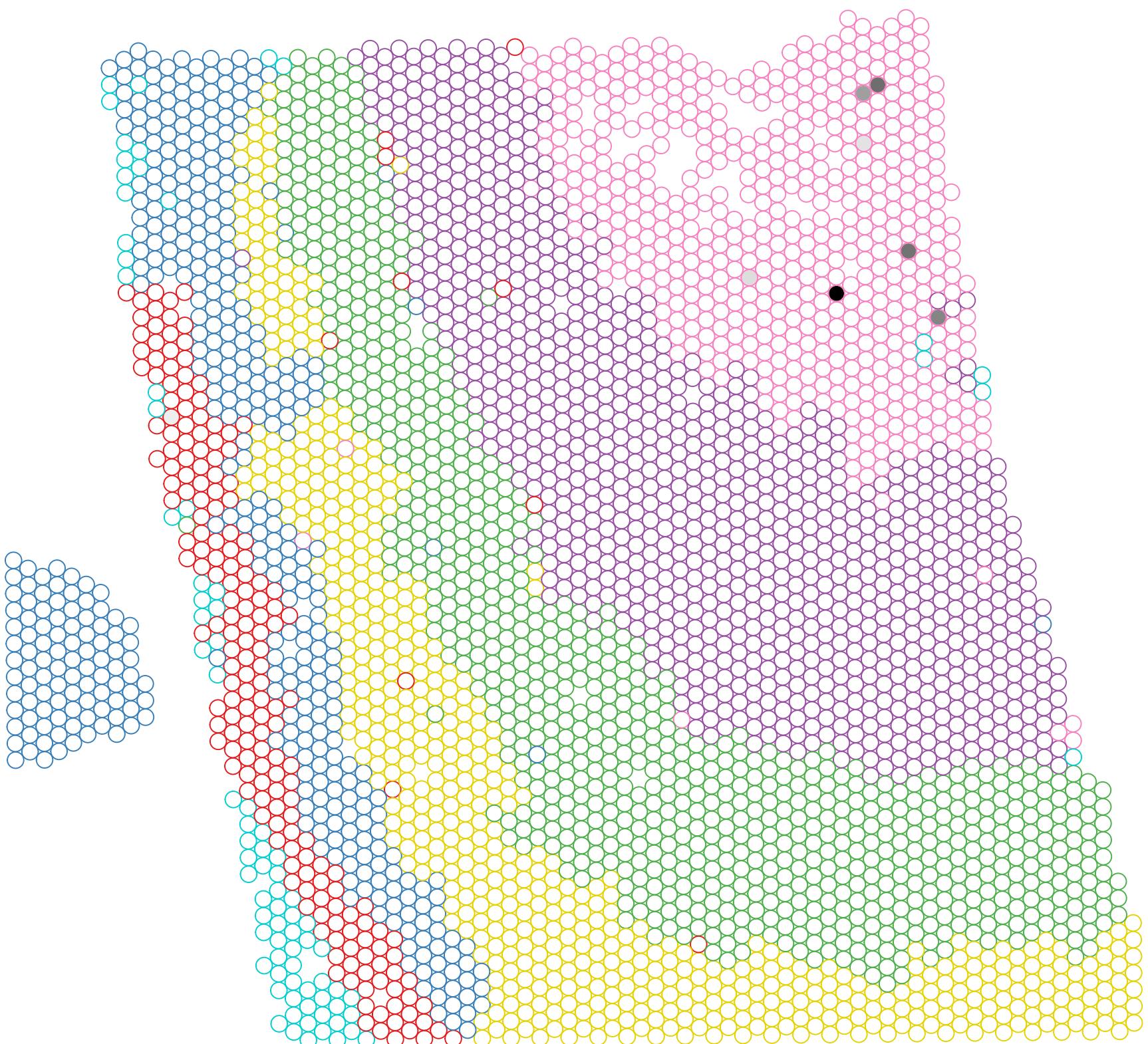
Sample Br2743\_ant



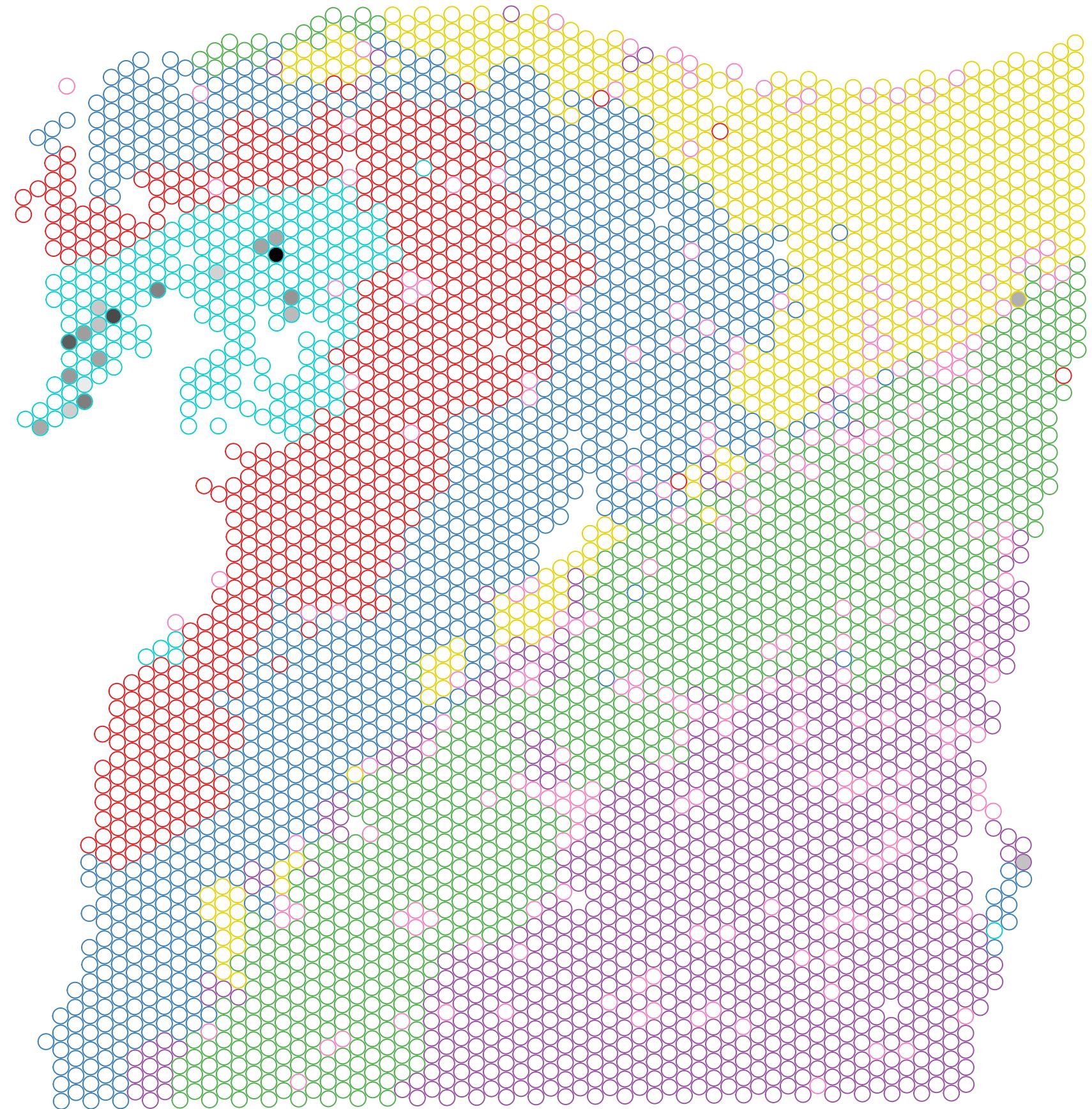
Sample Br2743\_mid



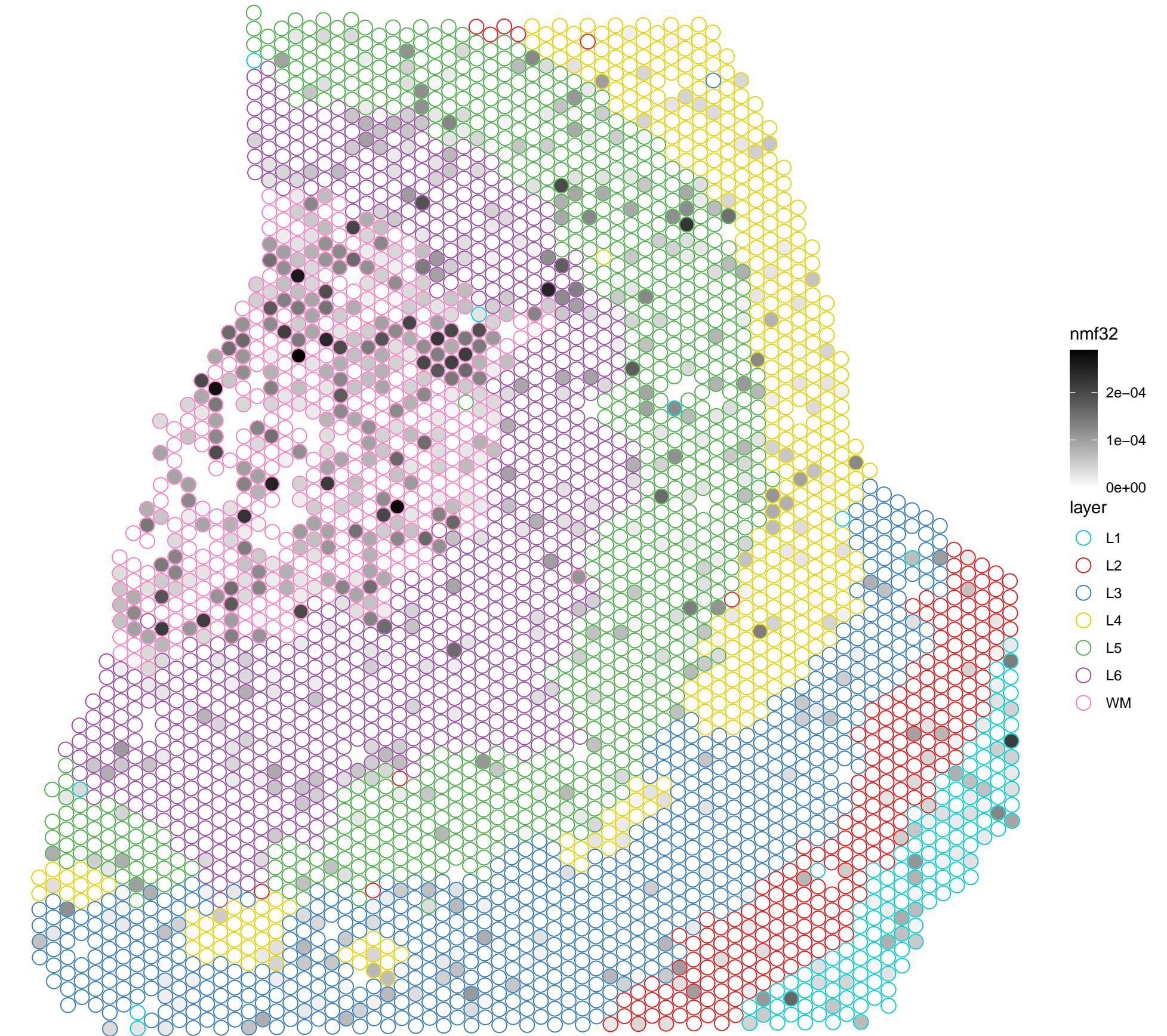
Sample Br2743\_post



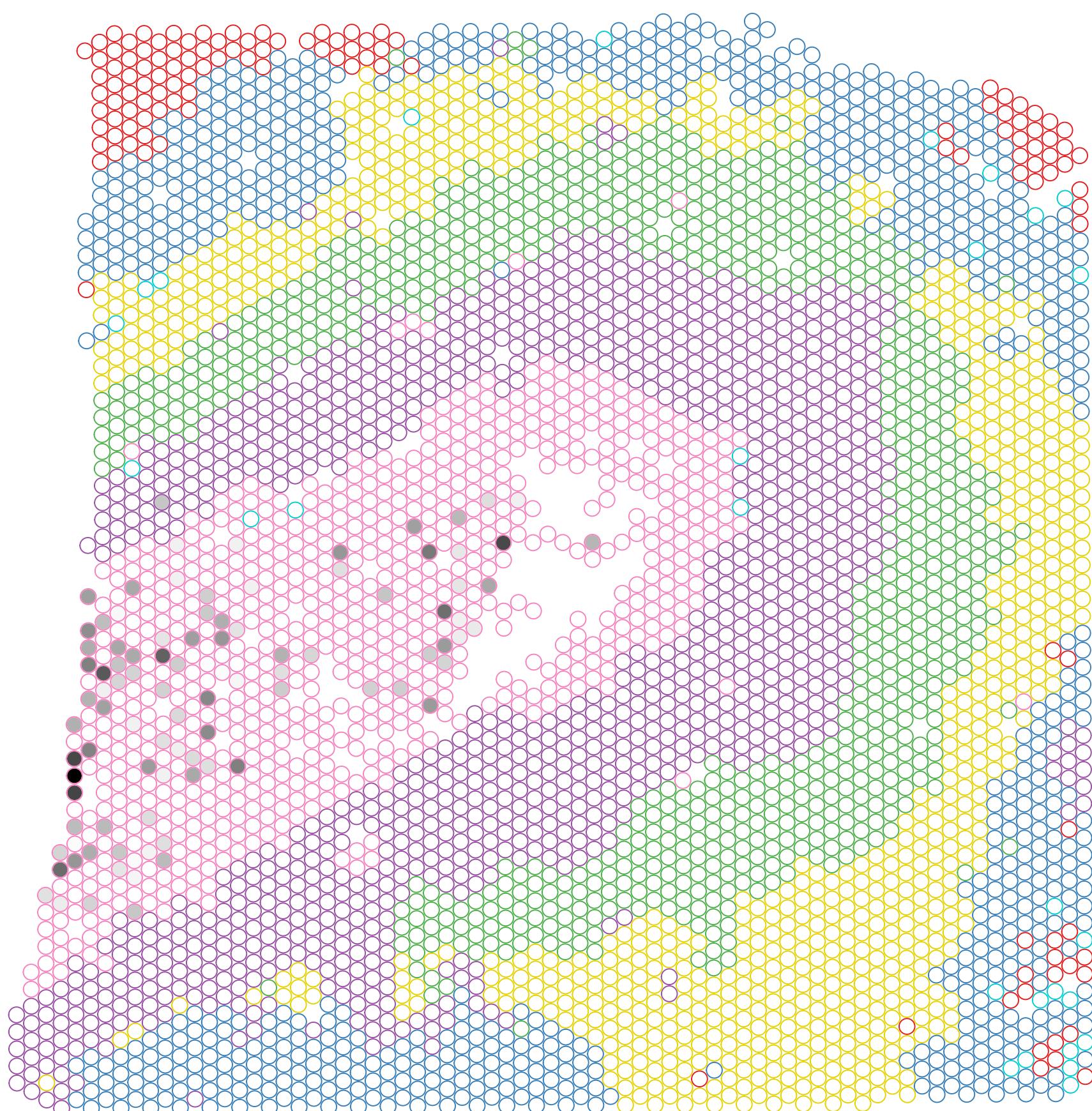
Sample Br3942\_ant



Sample Br3942\_mid



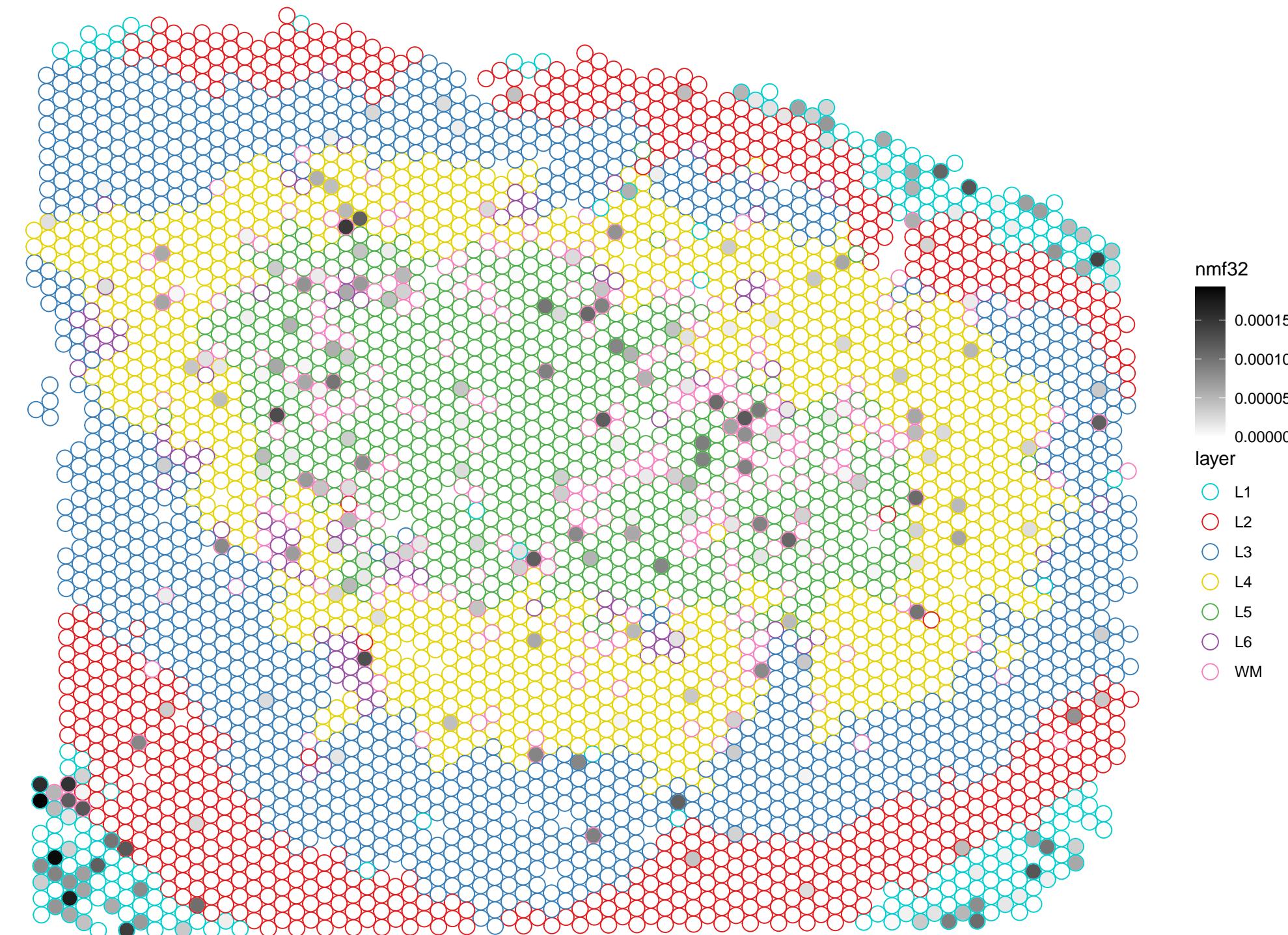
Sample Br3942\_post



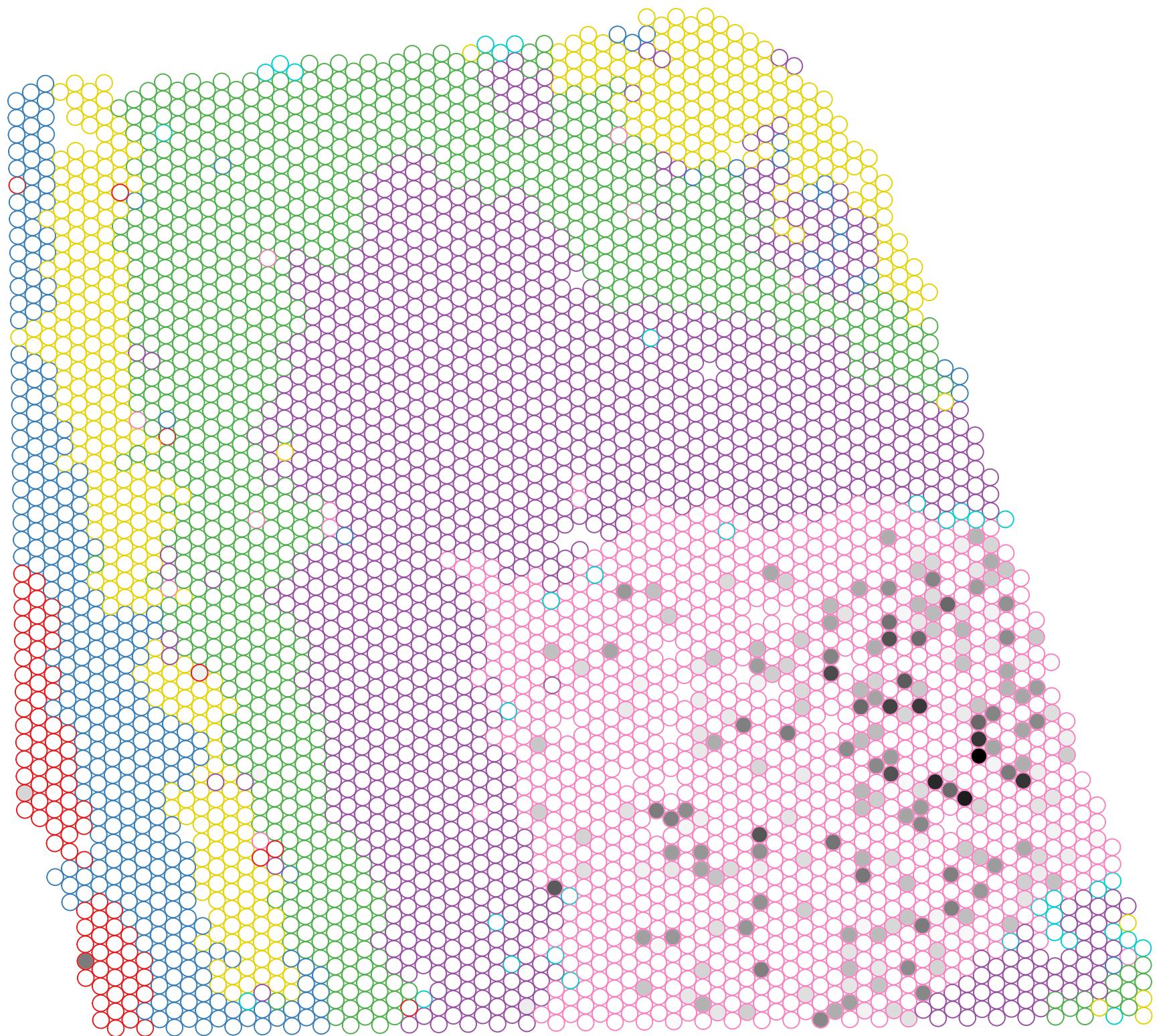
Sample Br6423\_ant



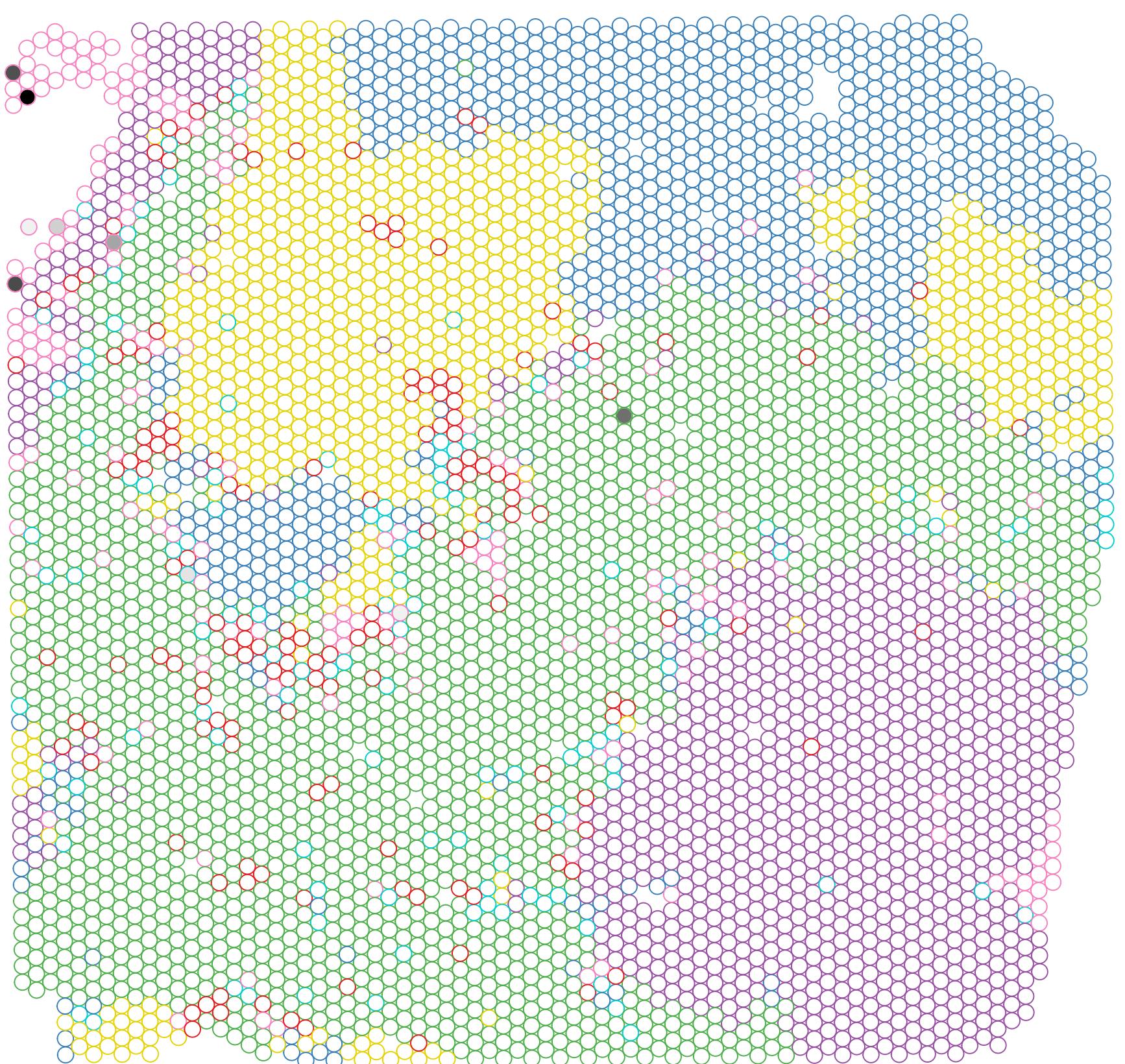
Sample Br6423\_mid



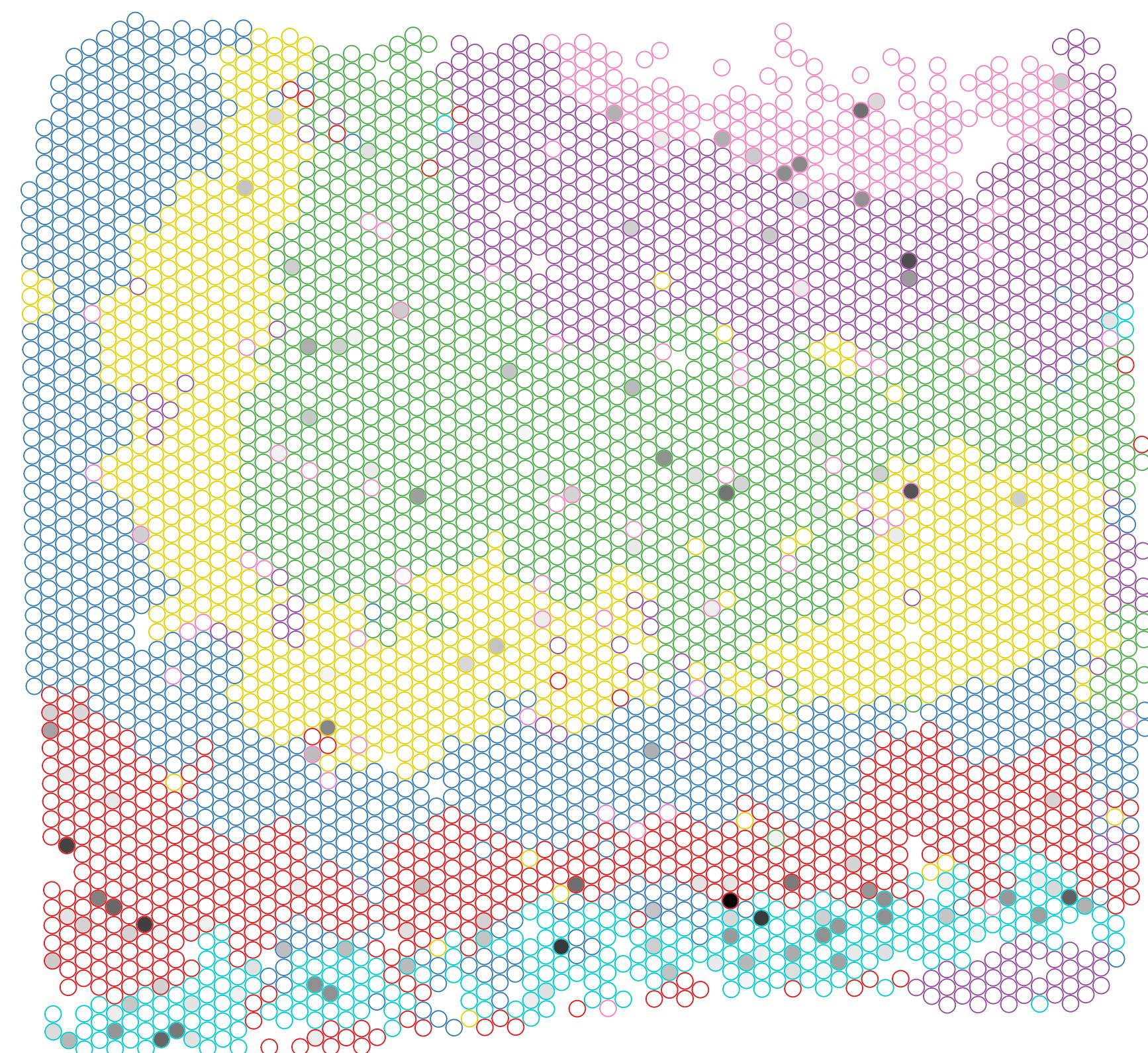
Sample Br6423\_post



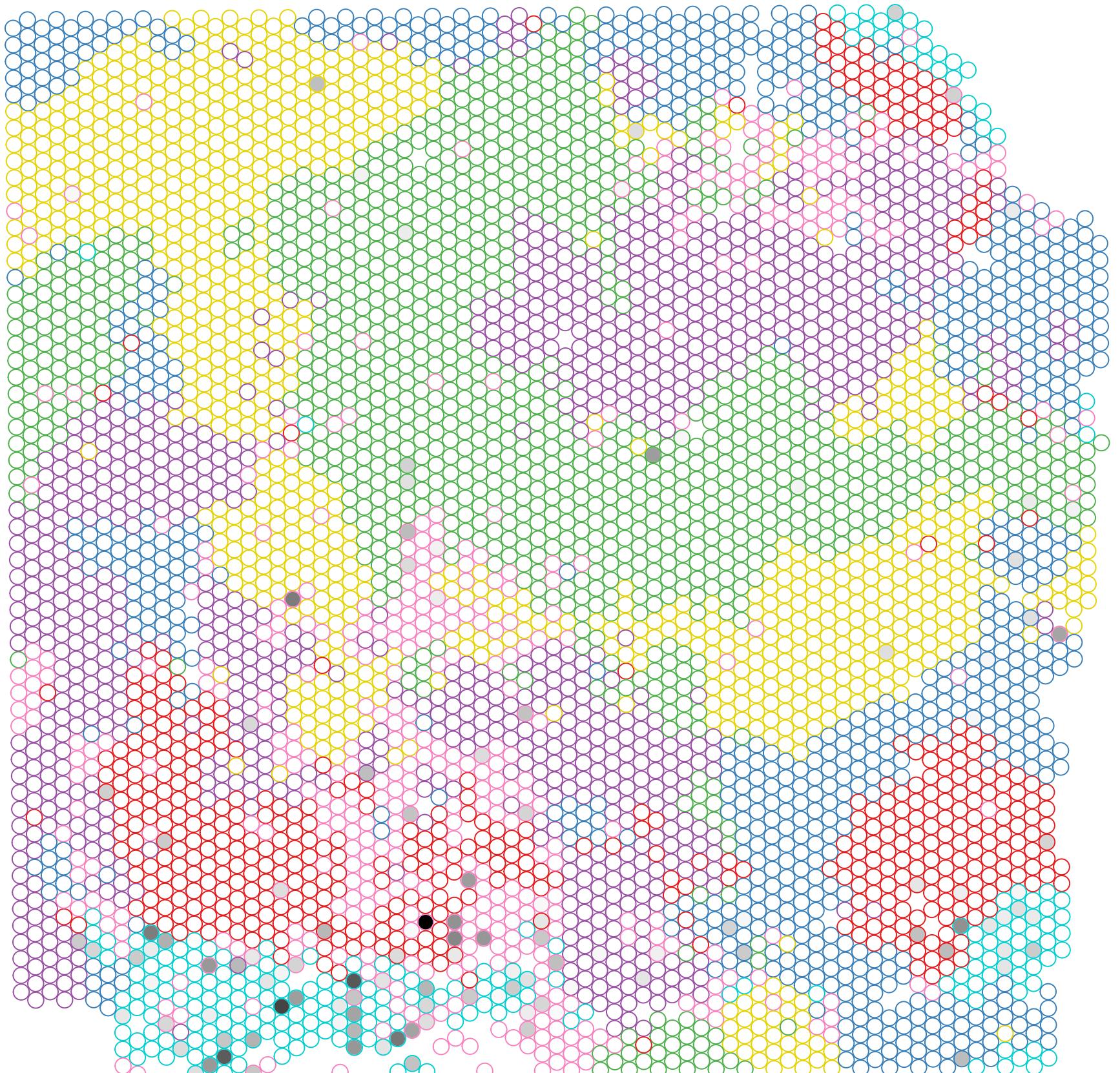
Sample Br8492\_ant



Sample Br8492\_mid



Sample Br8492\_post

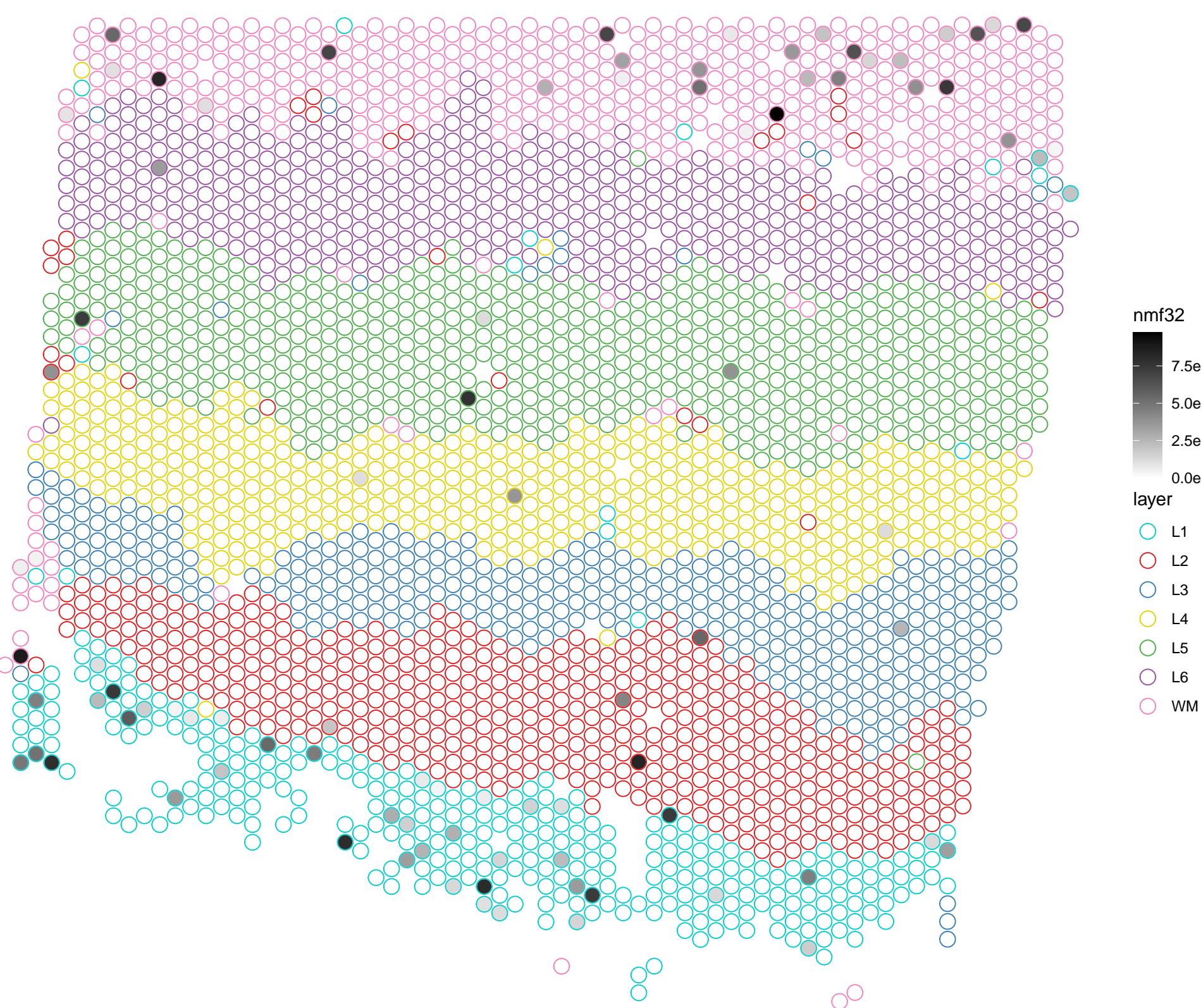


layer

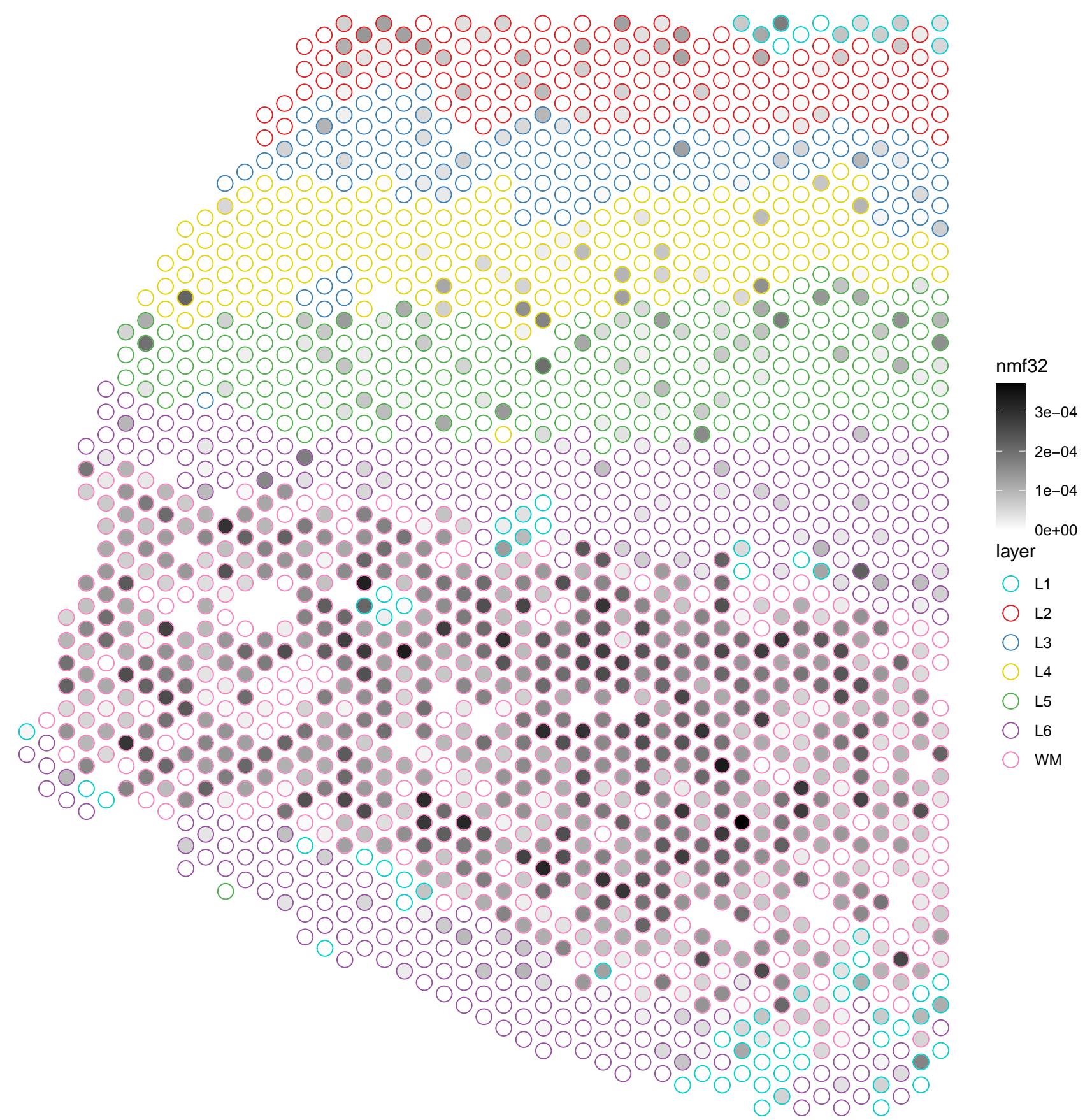
- L1
- L2
- L3
- L4
- L5
- L6
- WM

nmf32  
0.00025  
0.00020  
0.00015  
0.00010  
0.00005  
0.00000

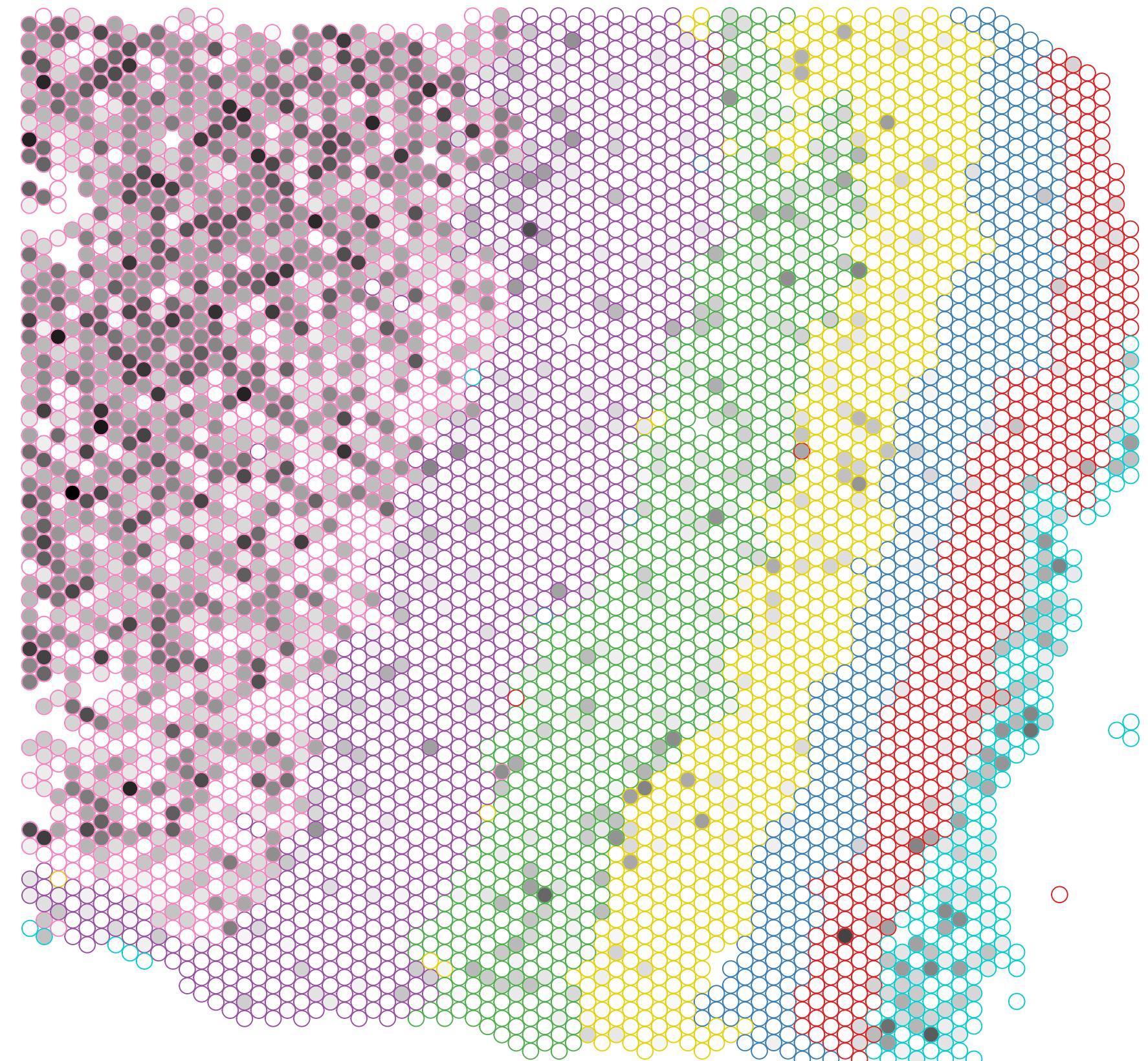
Sample Br2720\_ant



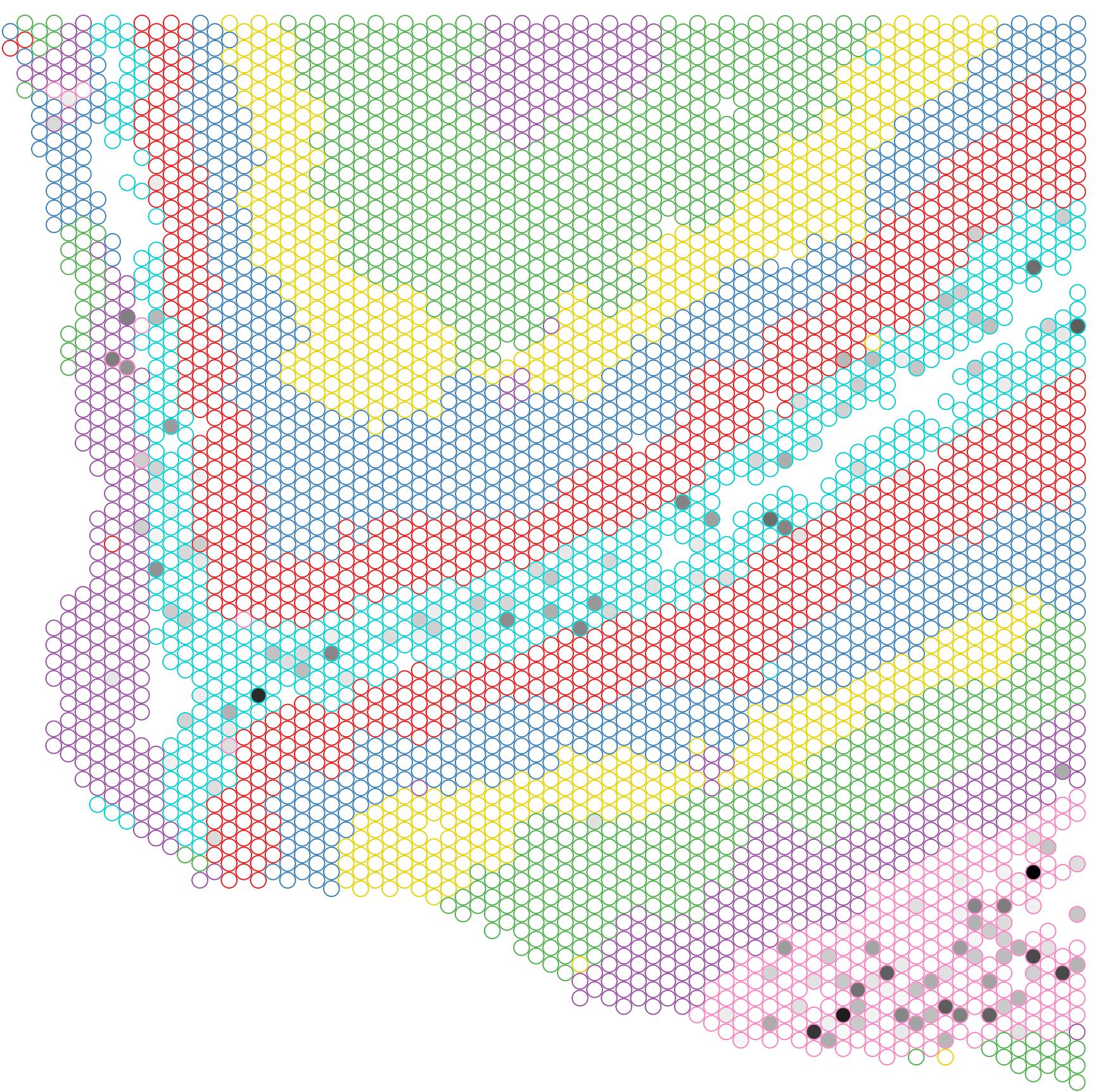
Sample Br2720\_mid



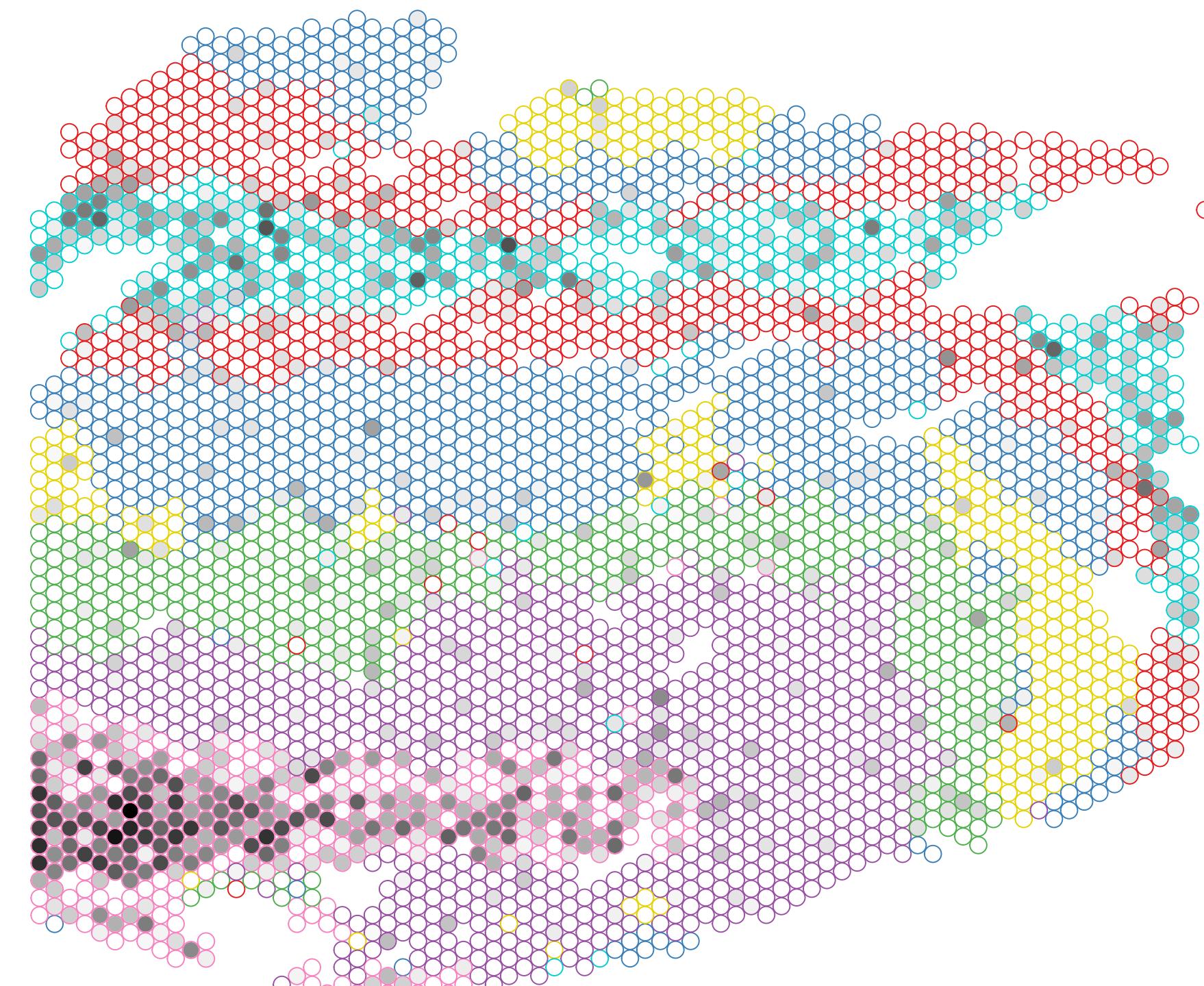
Sample Br2720\_post



Sample Br6432\_ant



Sample Br6432\_mid



Sample Br6432\_post



layer

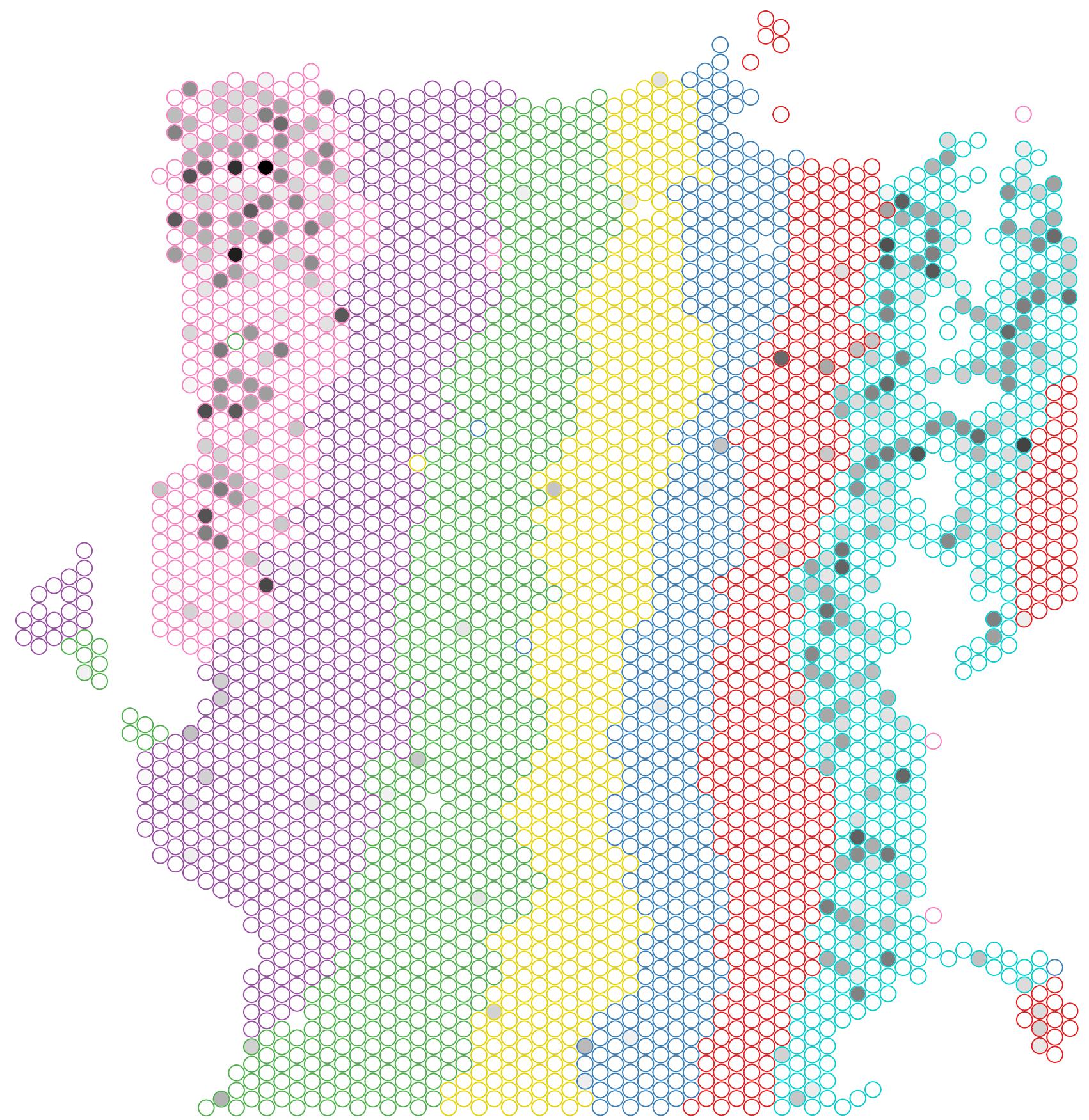
- L1
- L2
- L3
- L4
- L5
- L6
- WM

nmf32

- 3e-04
- 2e-04
- 1e-04
- 0e+00

Sample Br6471\_ant

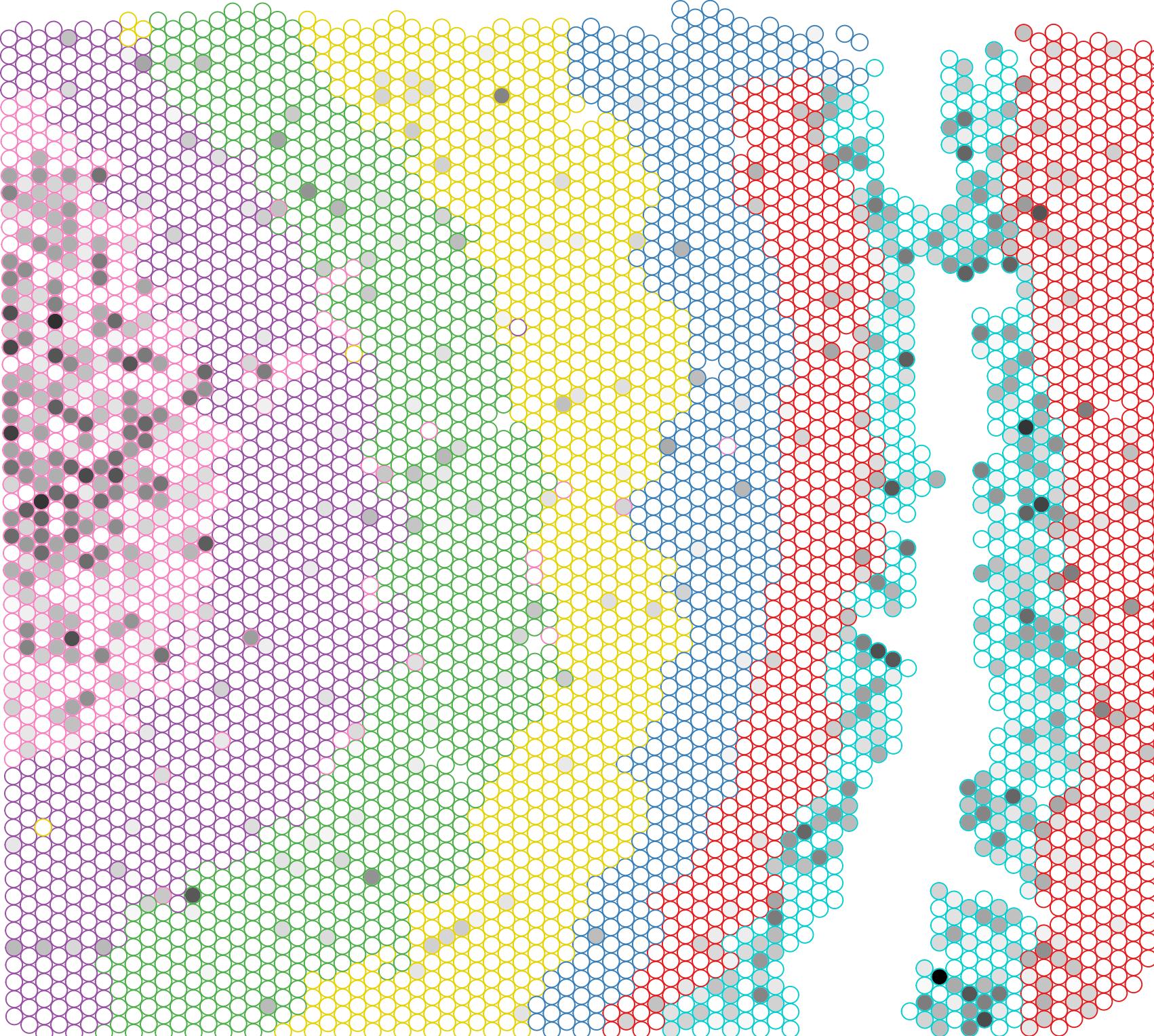
Sample Br6471\_mid



nmf32  
layer  
L1  
L2  
L3  
L4  
L5  
L6  
WM

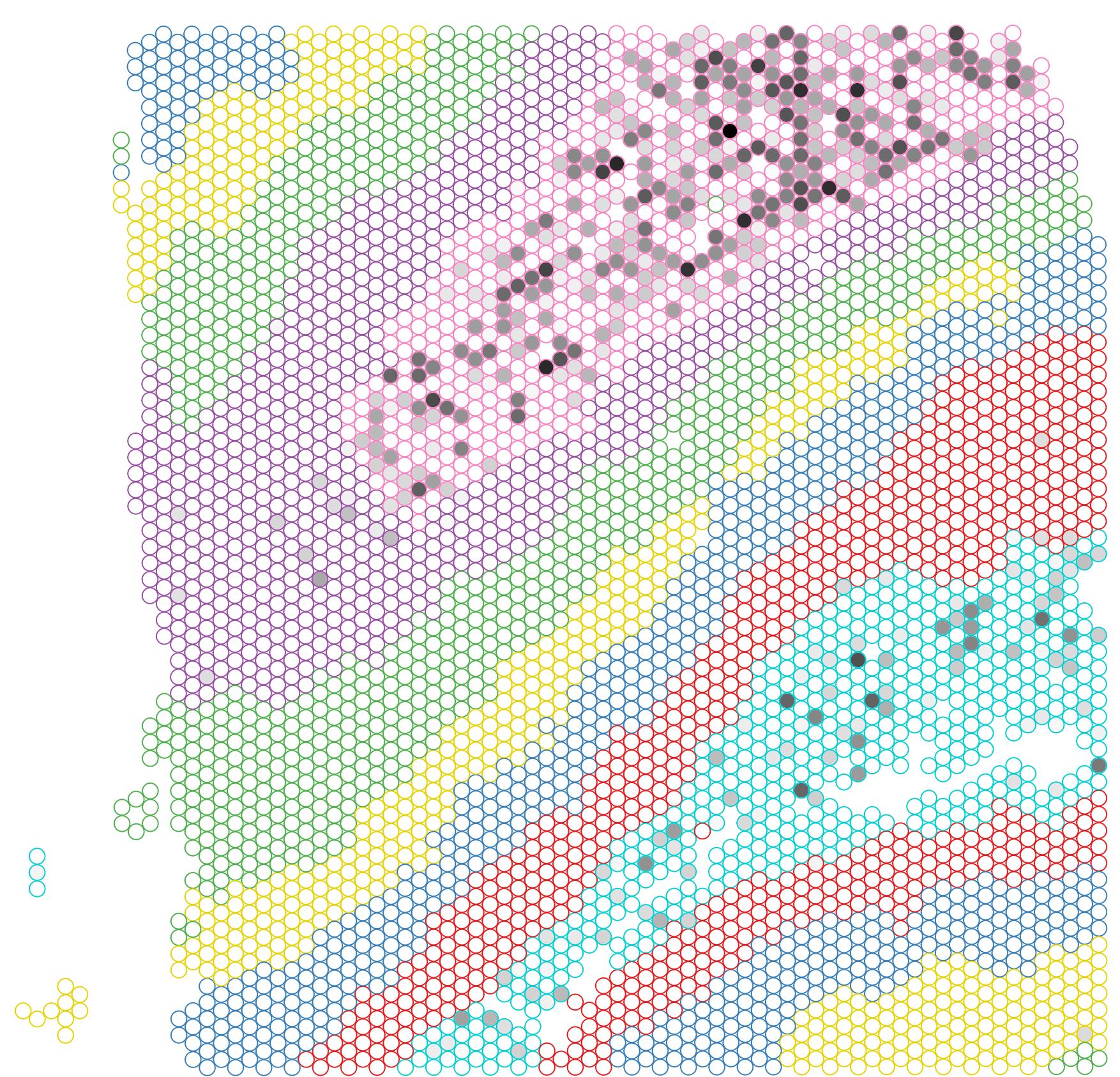
nmf32  
layer  
L1  
L2  
L3  
L4  
L5  
L6  
WM

Sample Br6471\_post



nmf32  
layer  
L1  
L2  
L3  
L4  
L5  
L6  
WM

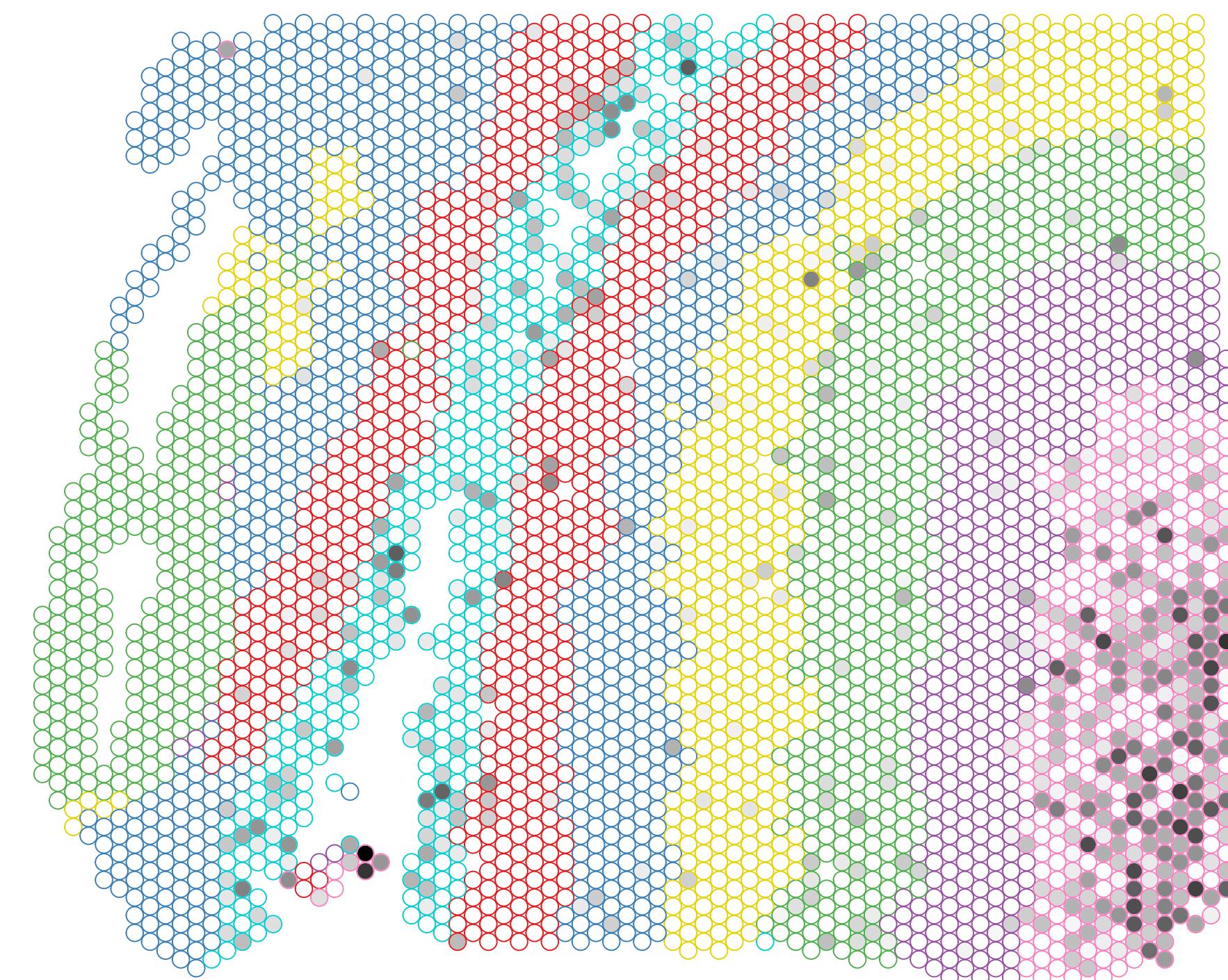
Sample Br6522\_ant



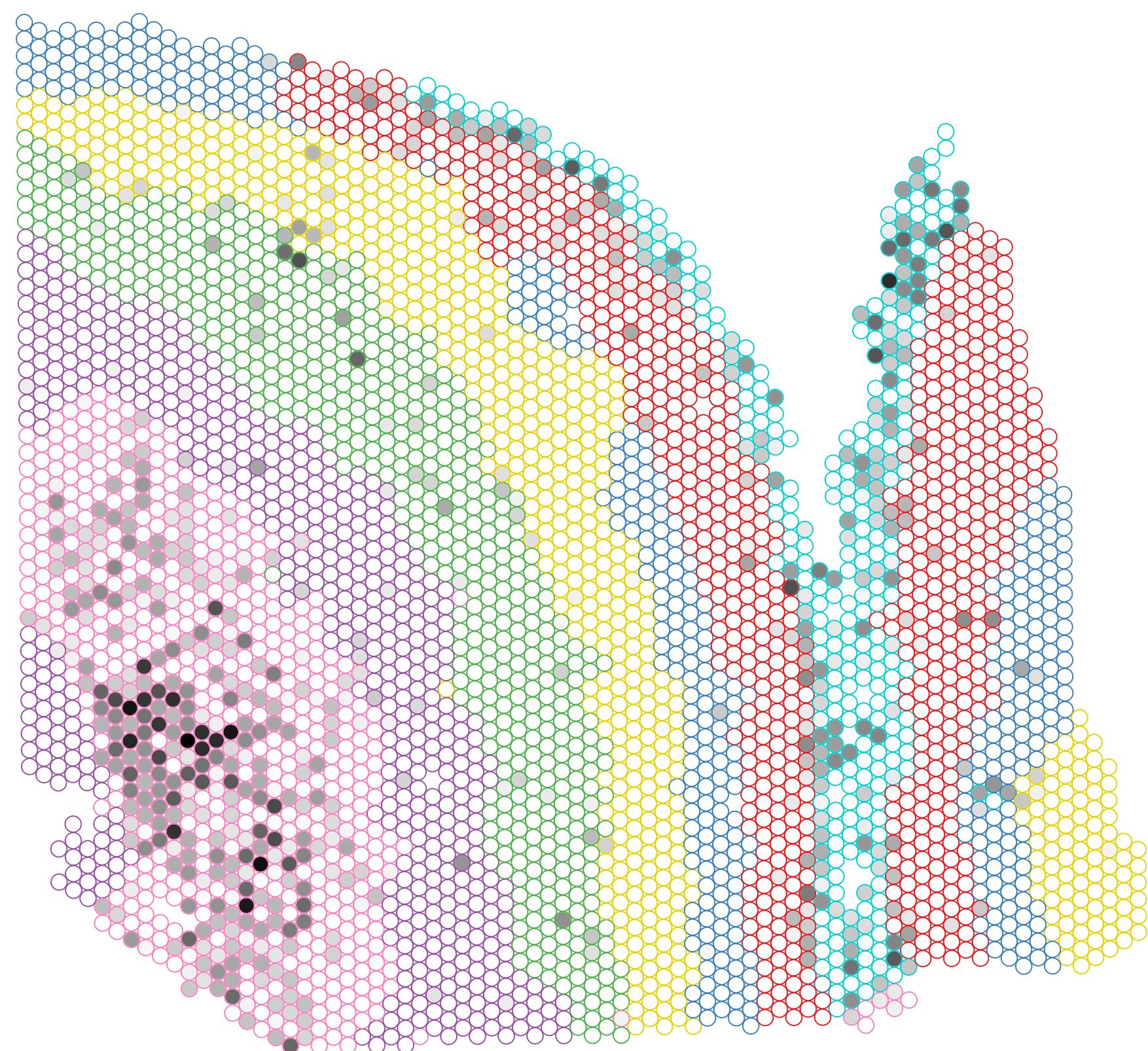
Sample Br6522\_mid

nmf32  
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM



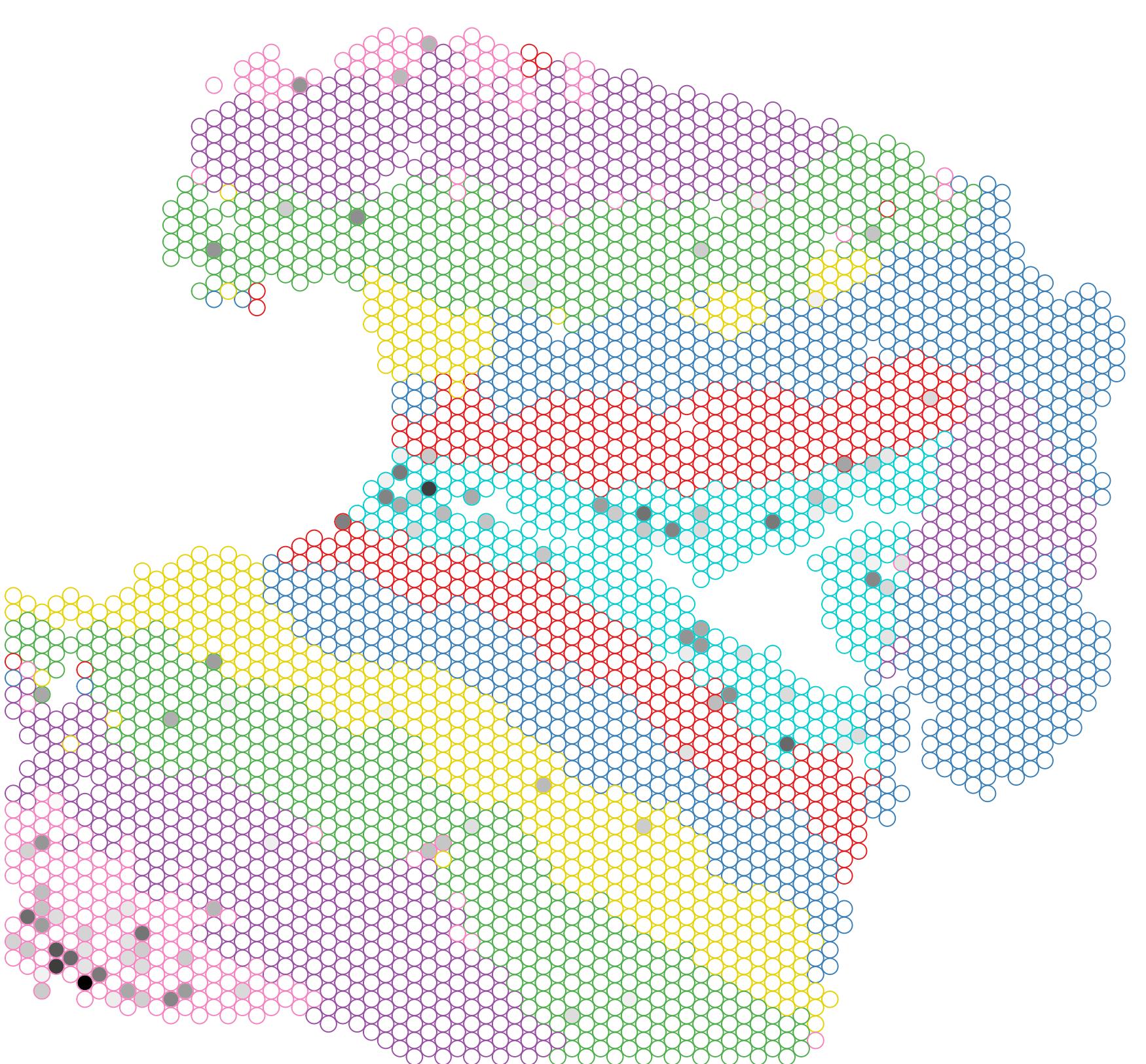
Sample Br6522\_post



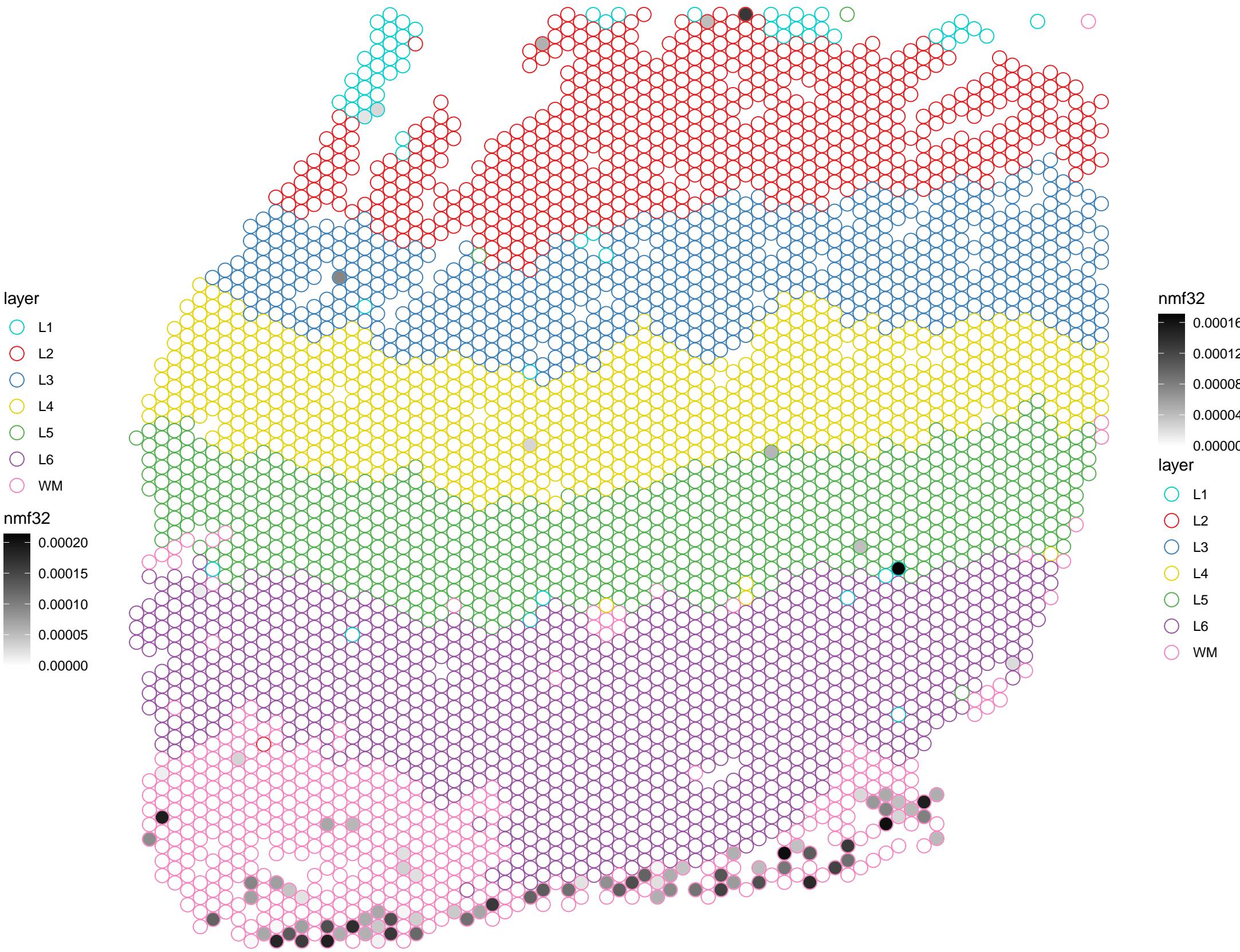
nmf32  
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

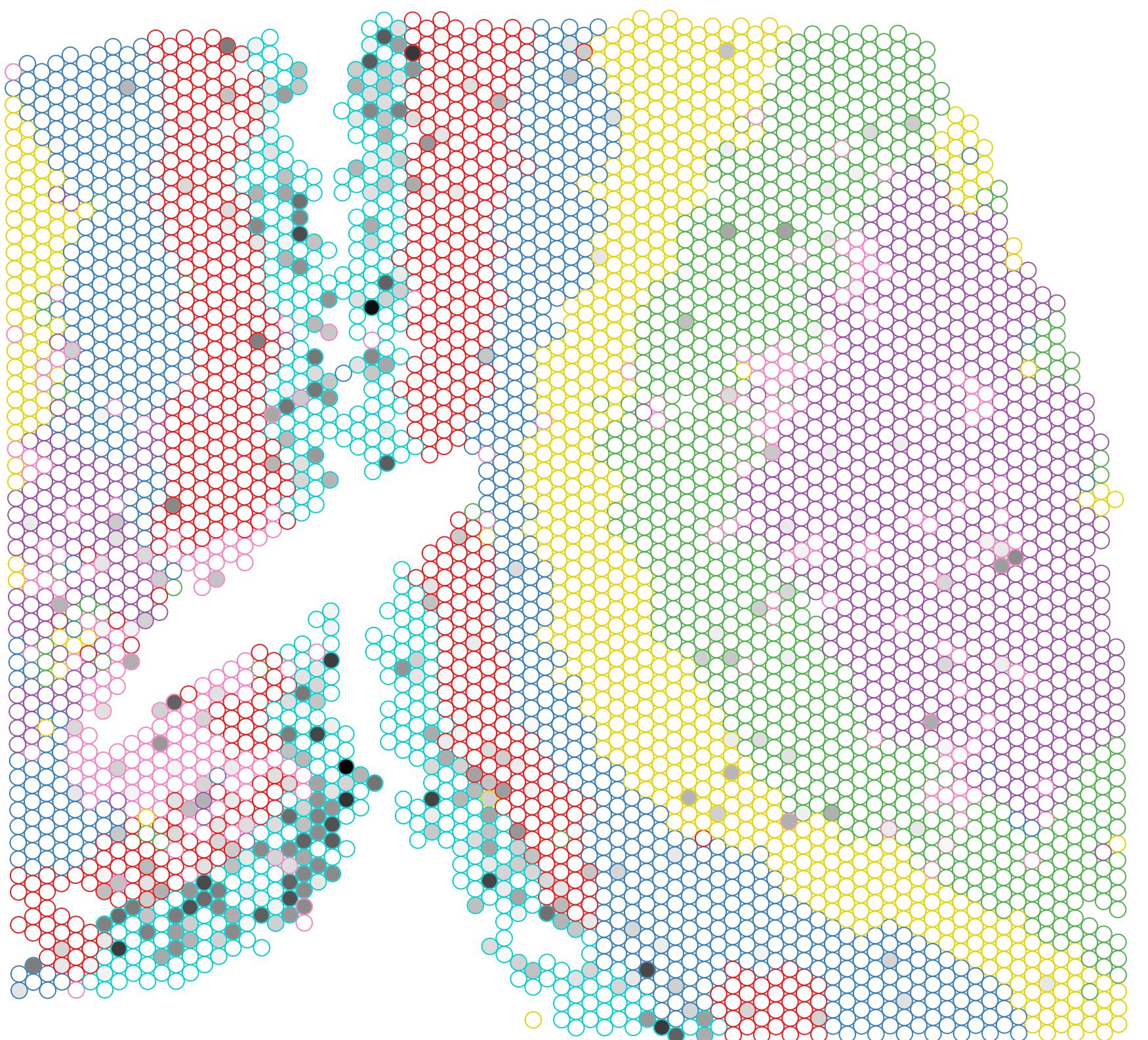
Sample Br8325\_ant



Sample Br8325\_mid



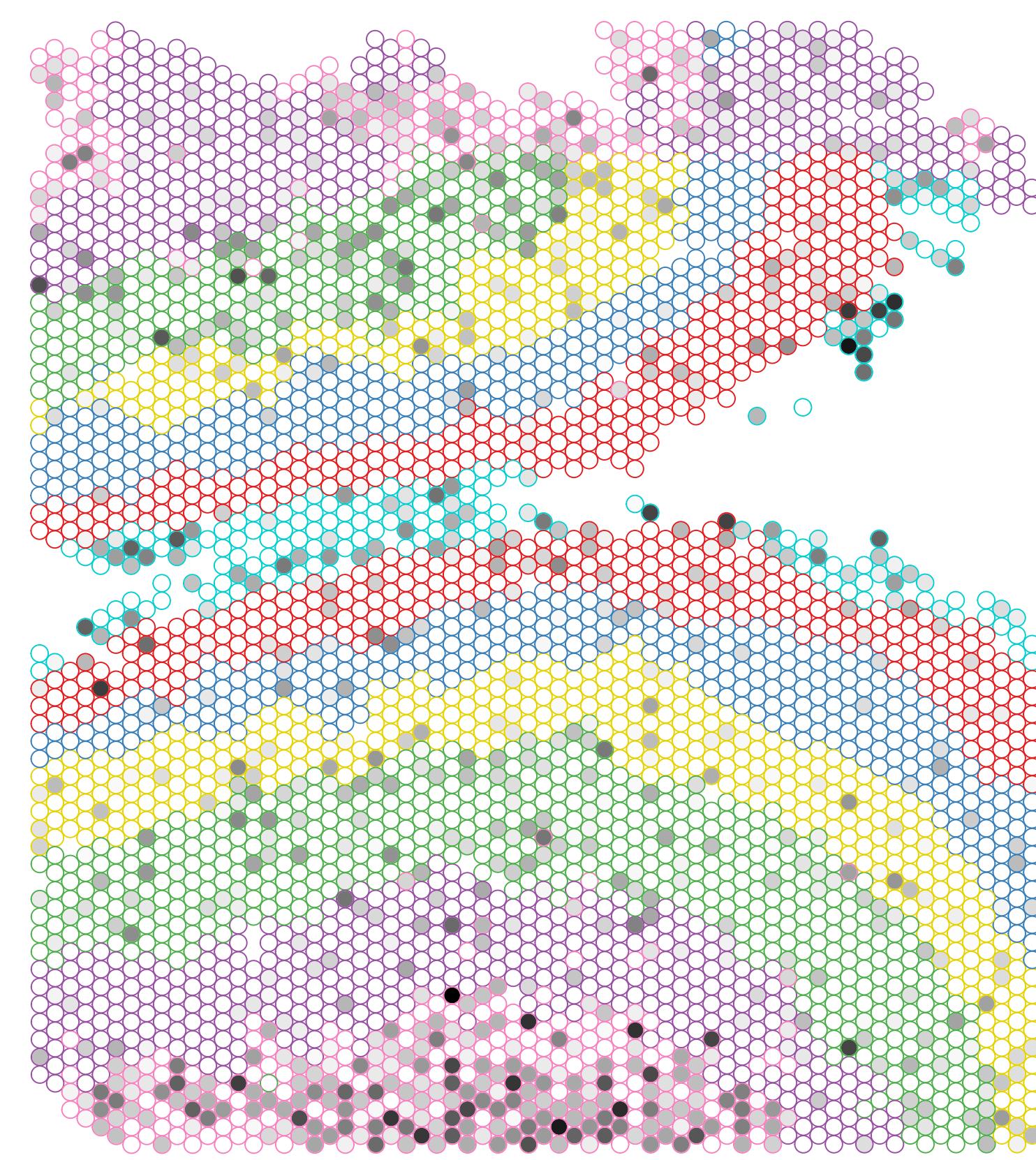
Sample Br8325\_post



Sample Br8667\_ant



Sample Br8667\_mid



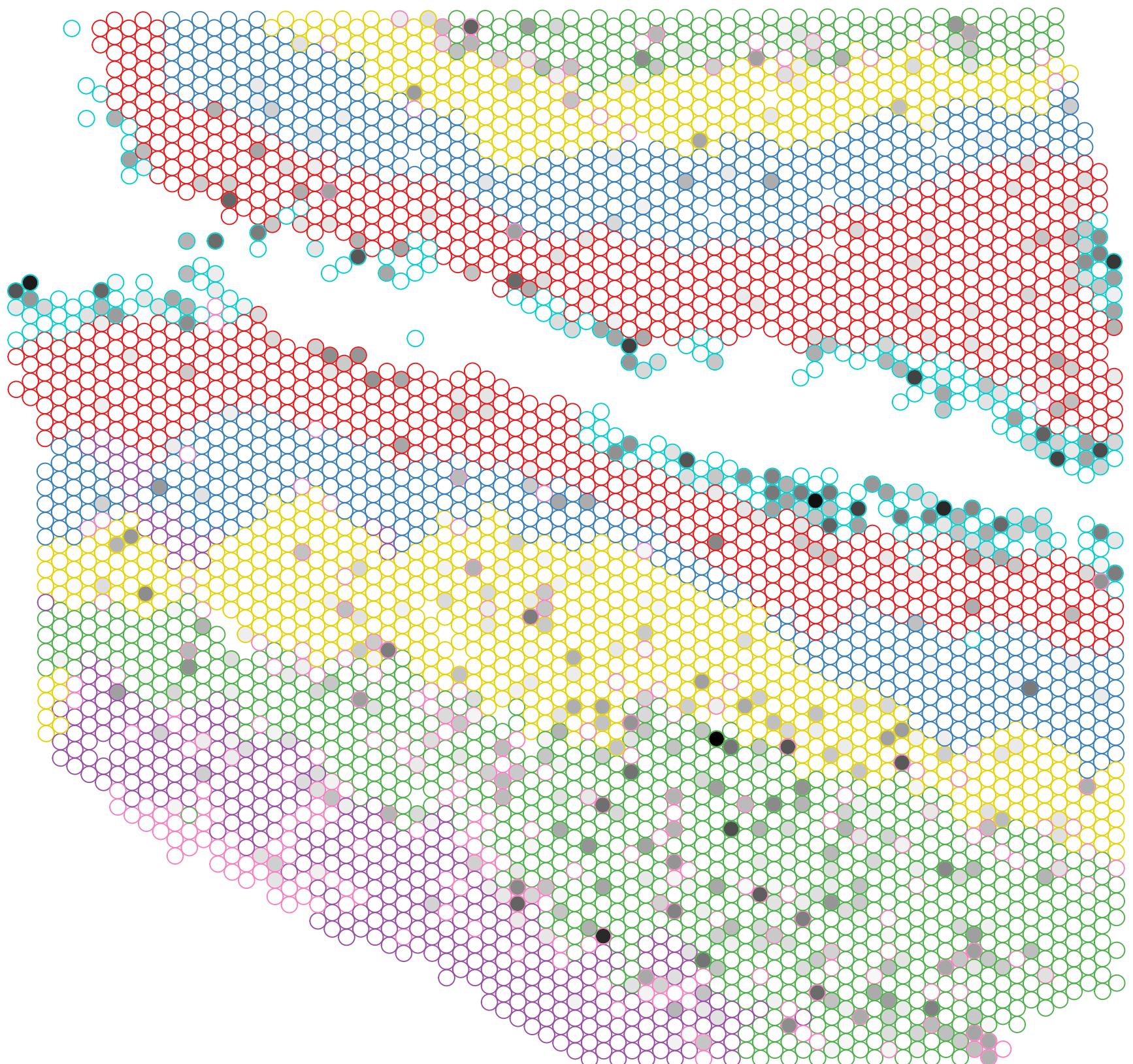
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

nmf32

0.00025
0.00020
0.00015
0.00010
0.00005
0.00000

Sample Br8667\_post



nmf32

0.00020
0.00015
0.00010
0.00005
0.00000

layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM