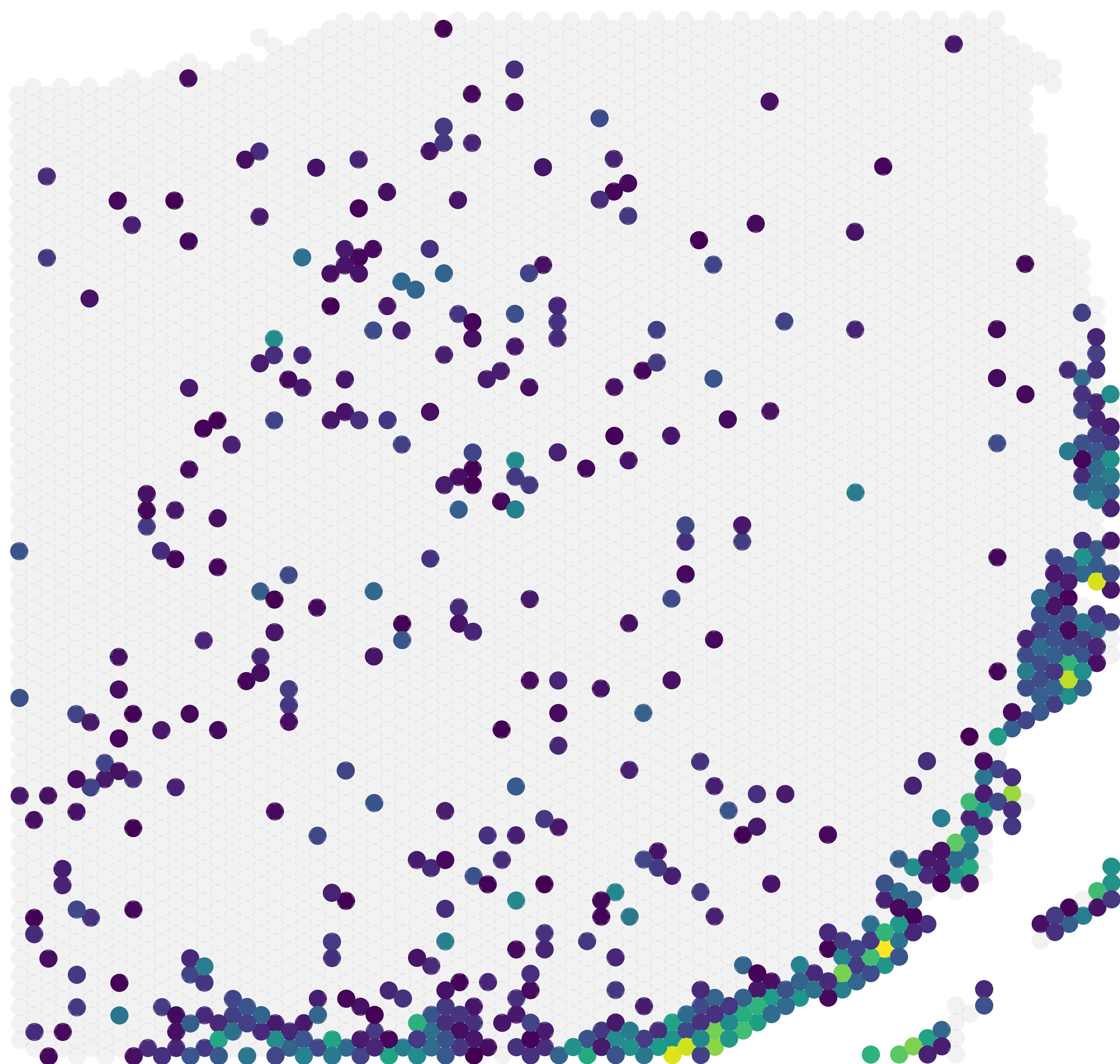
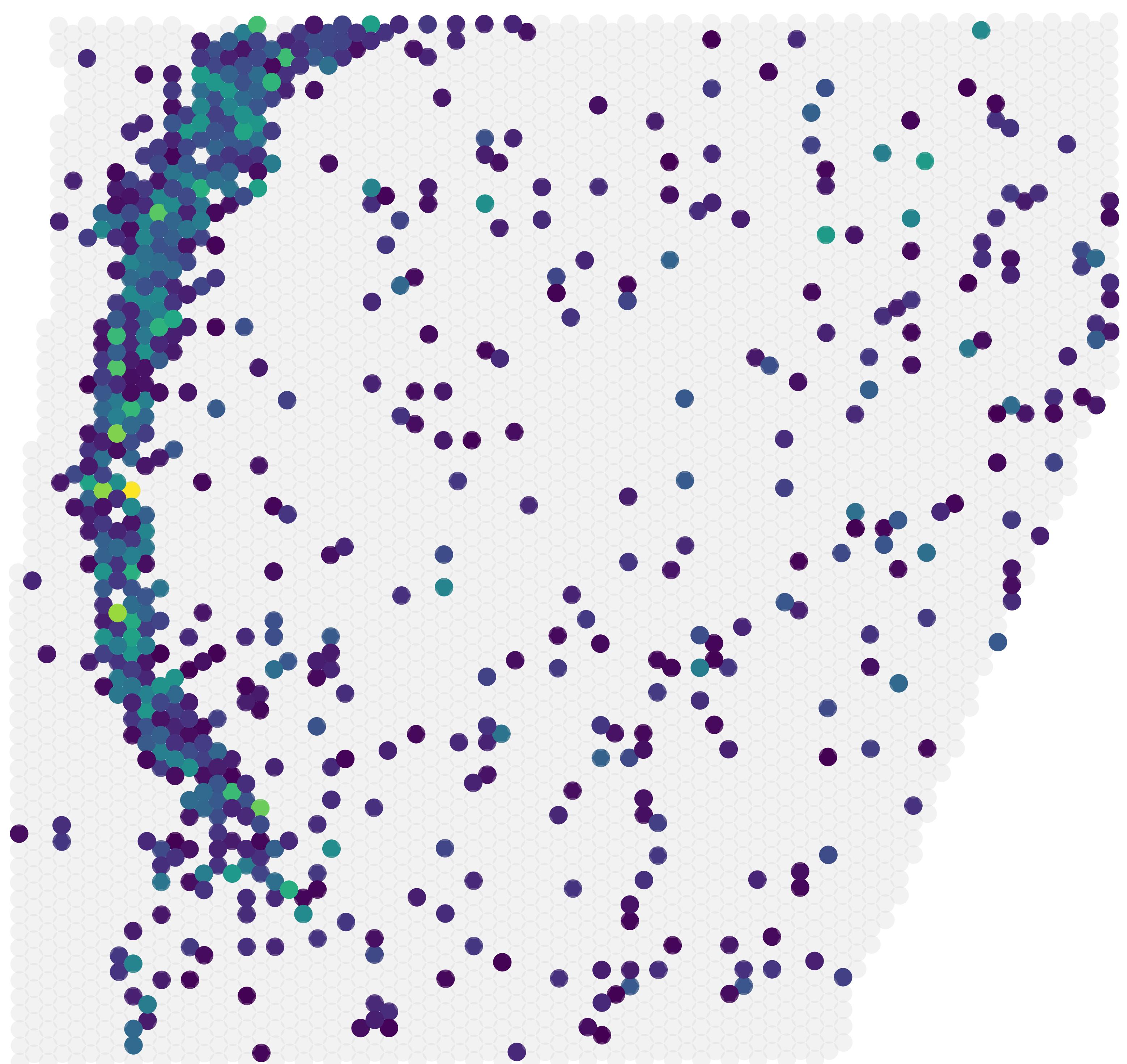
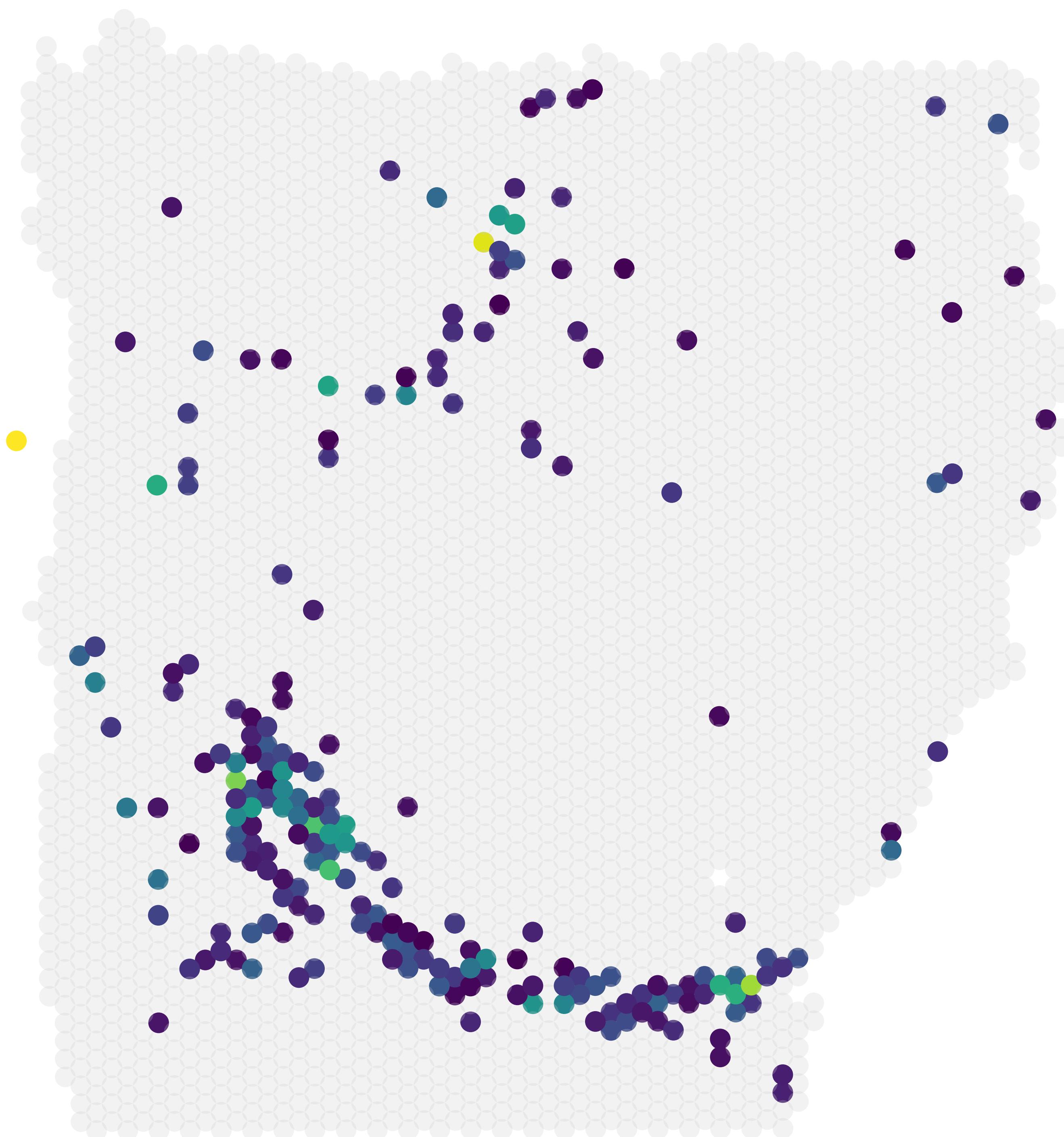


min > 0  
300  
200  
100

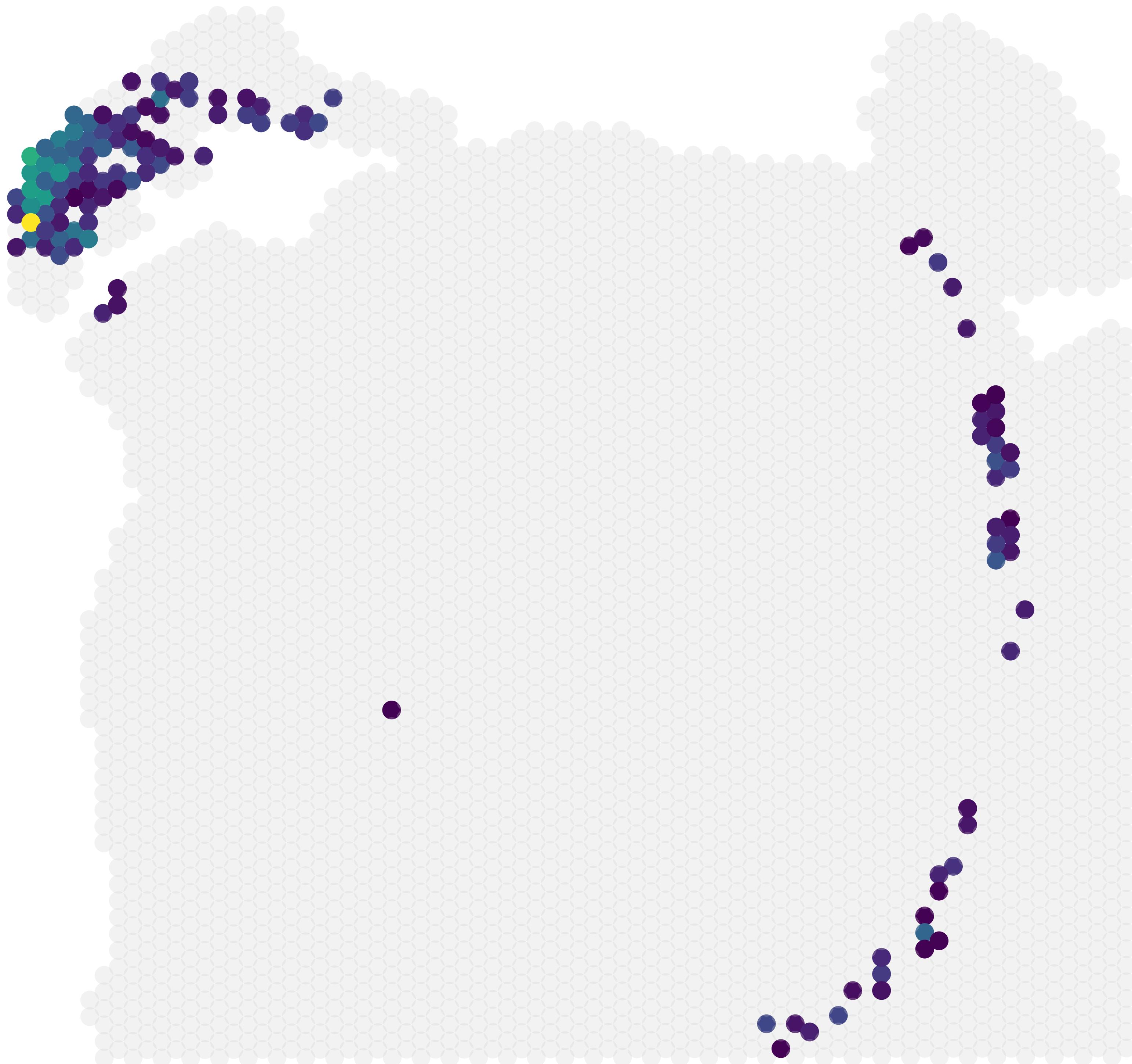


V12N28-334\_D1 NMF\_15

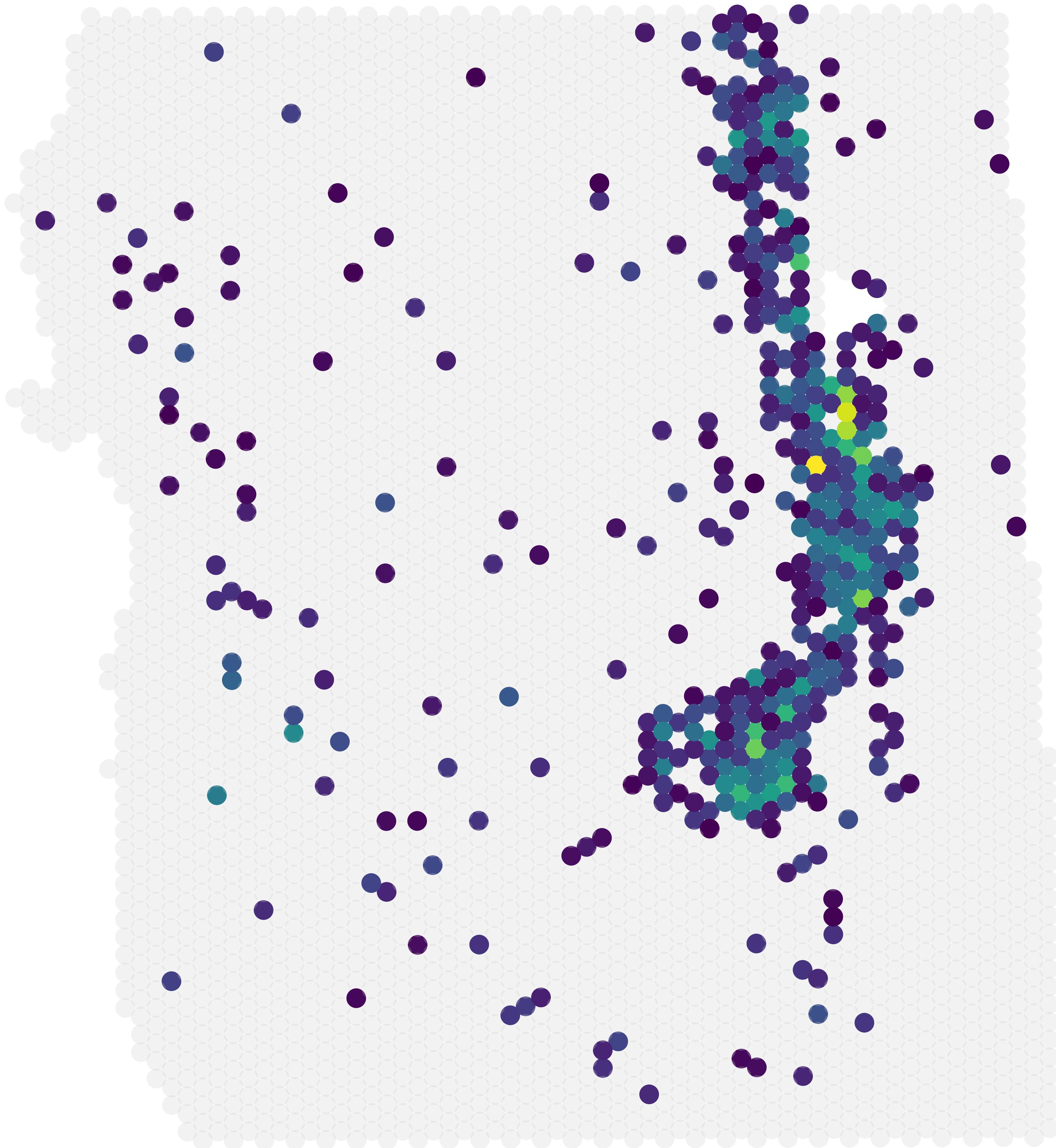




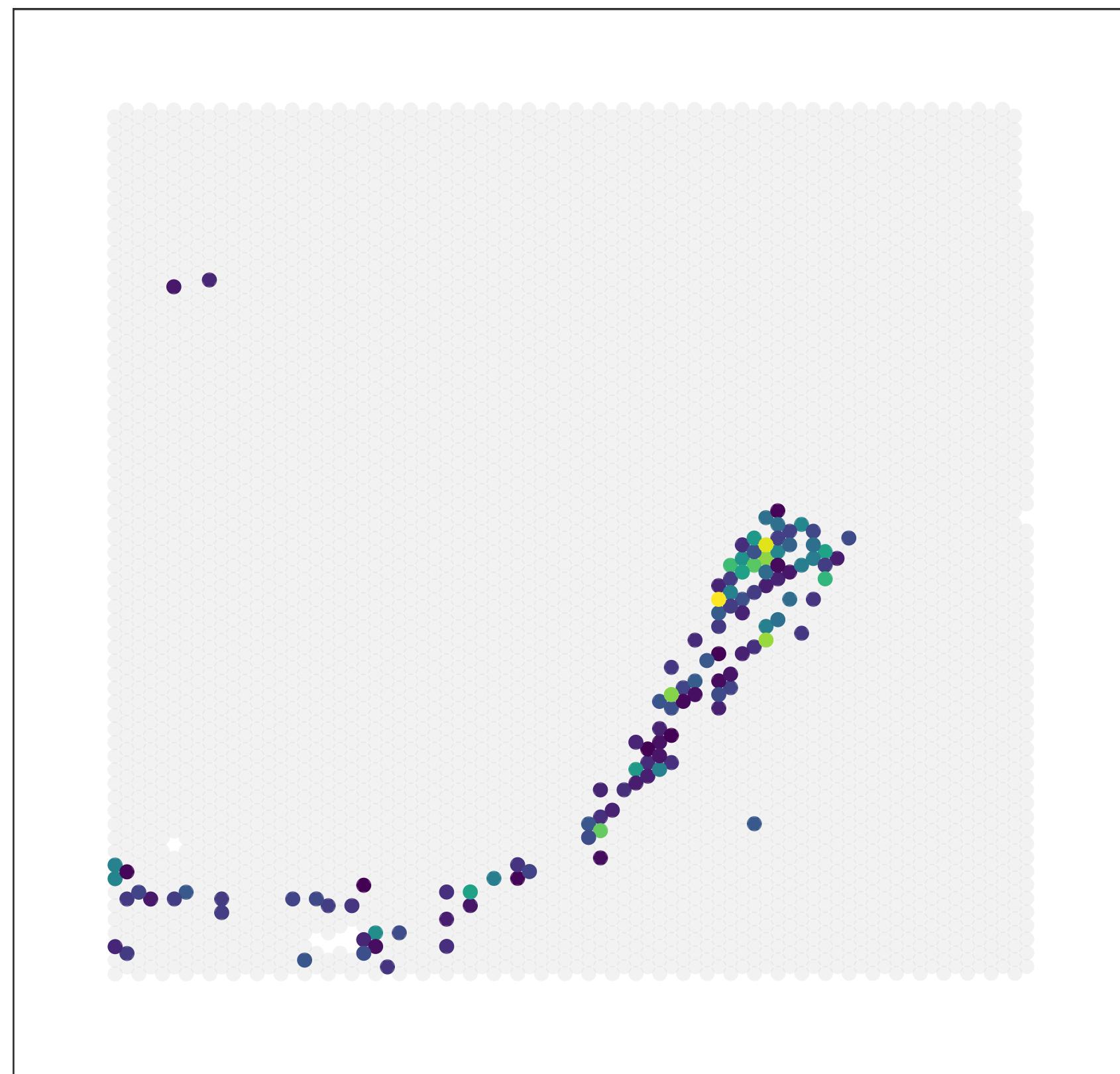
min > 0  
400  
300  
200  
100



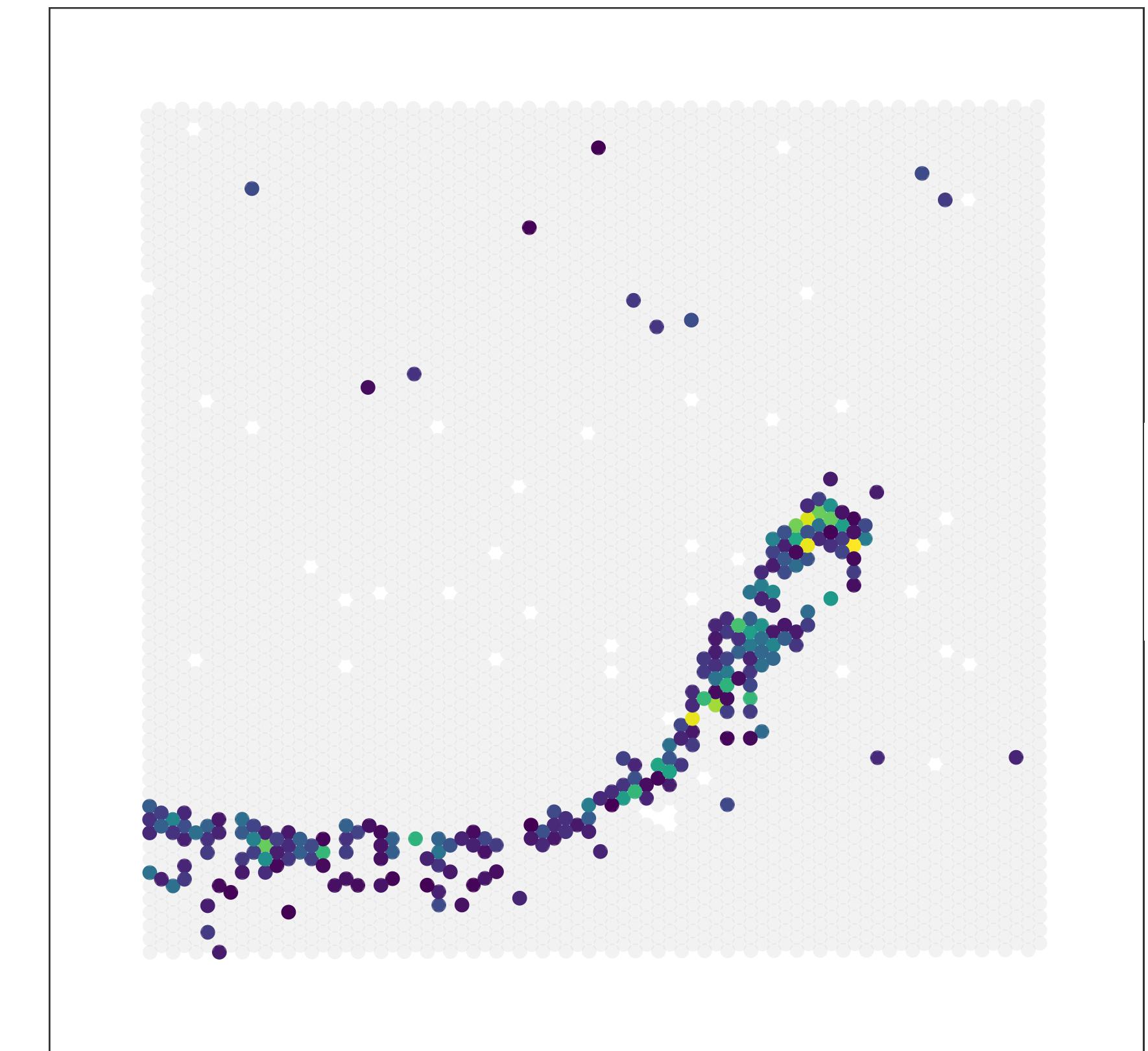
min > 0  
600  
400  
200



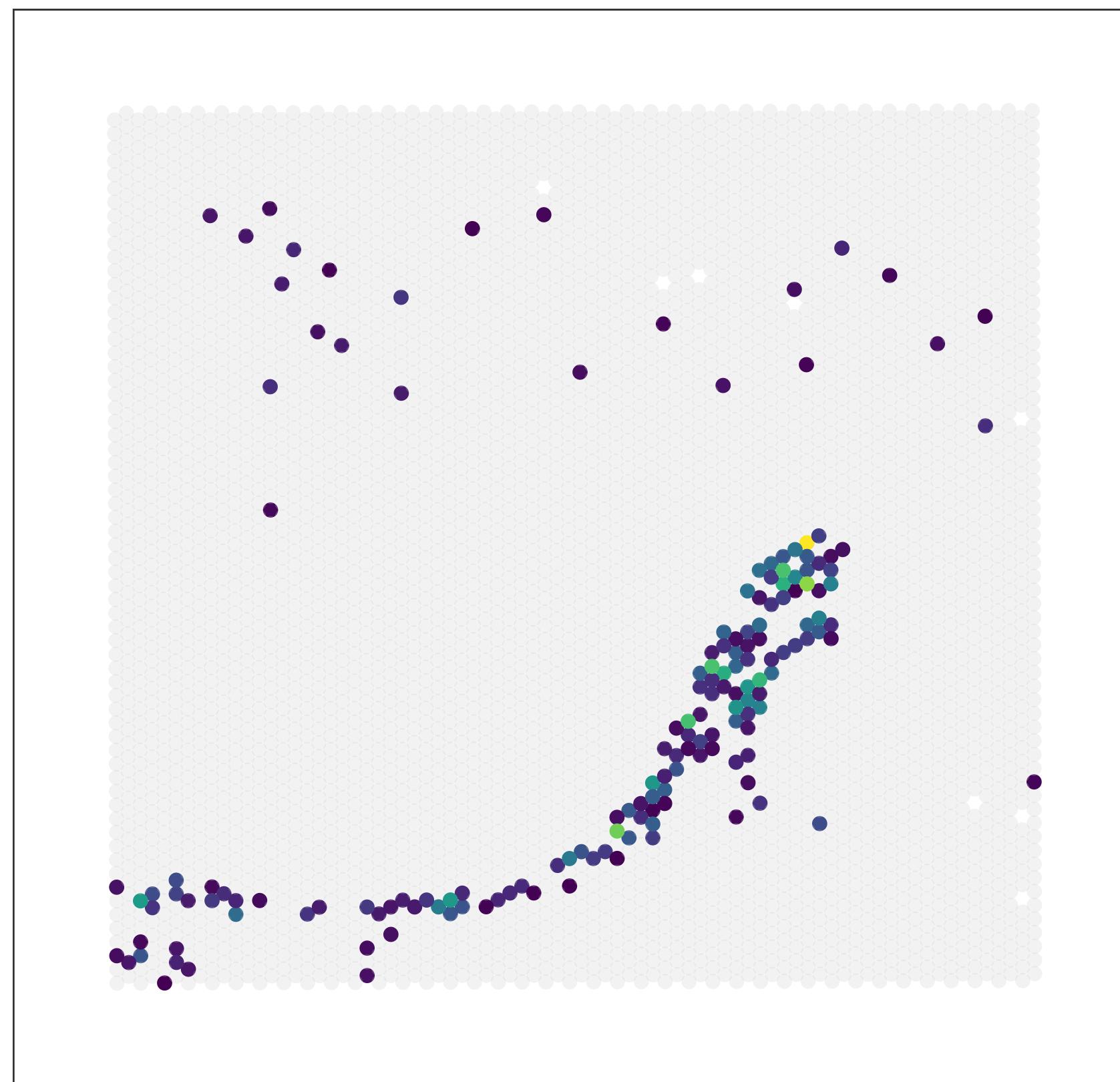
V12J03–002\_A1 NMF\_15



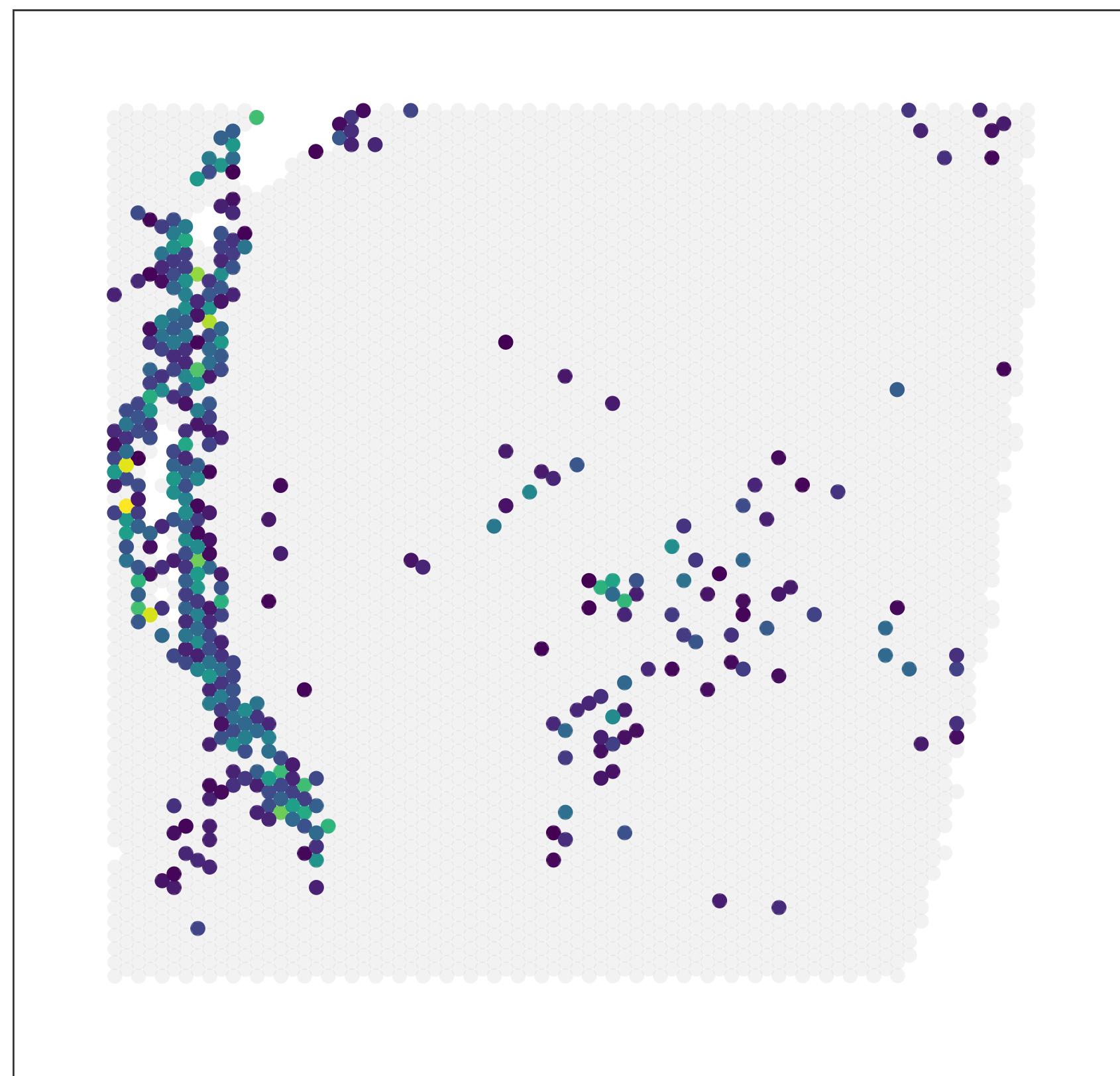
V12N28–331\_A1 NMF\_15



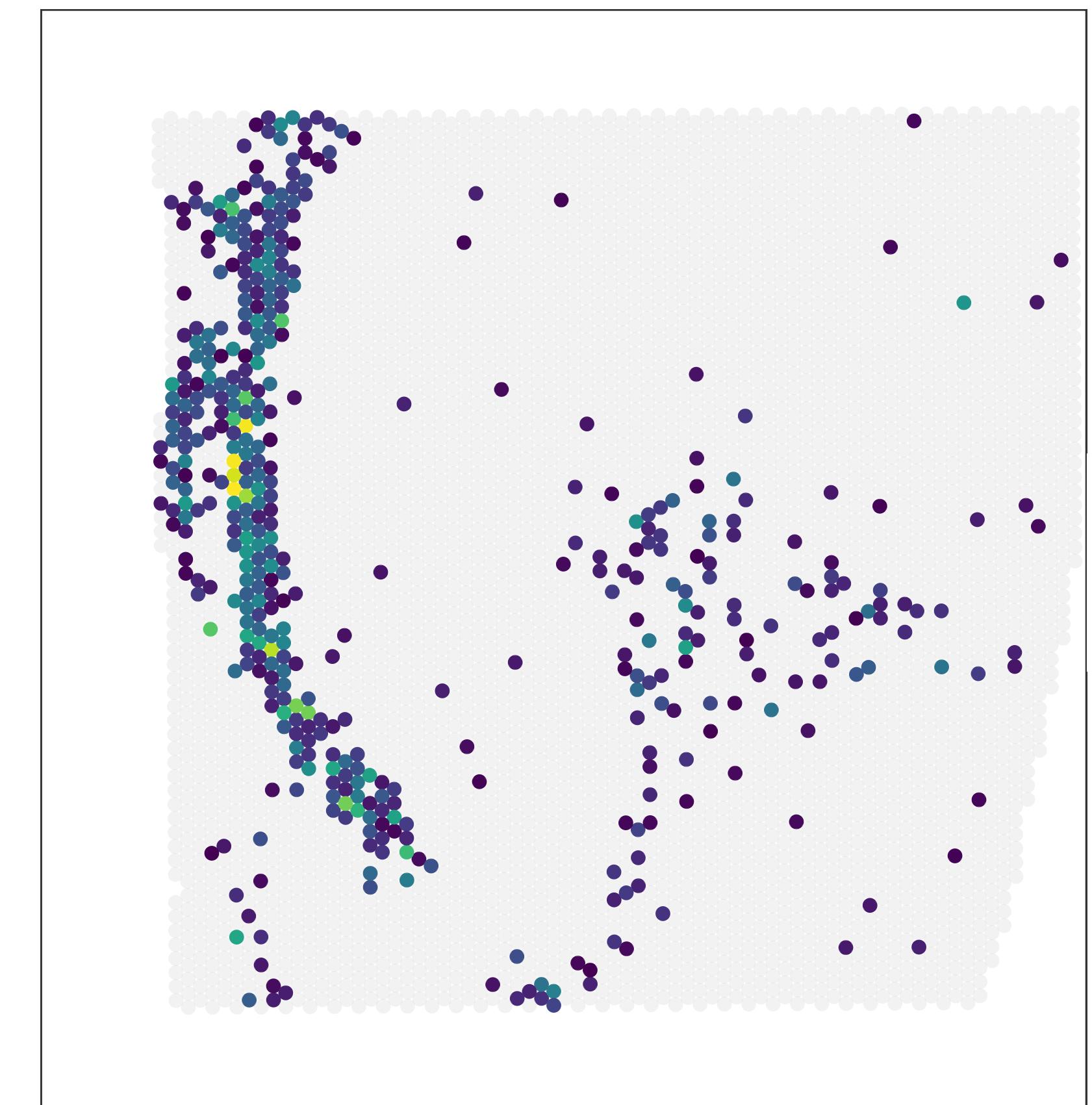
V12N28–332\_A1 NMF\_15



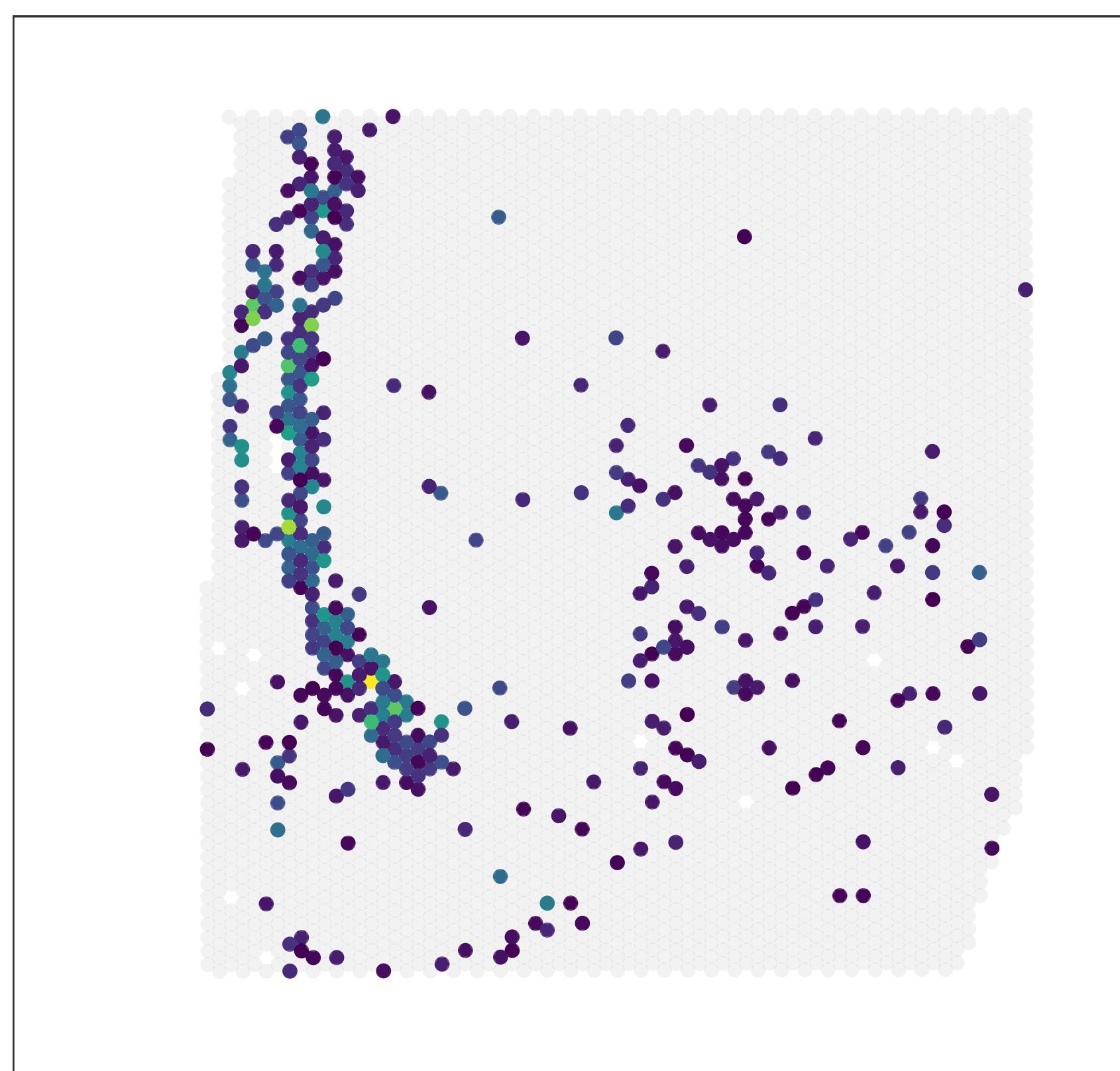
V12J03–002\_B1 NMF\_15



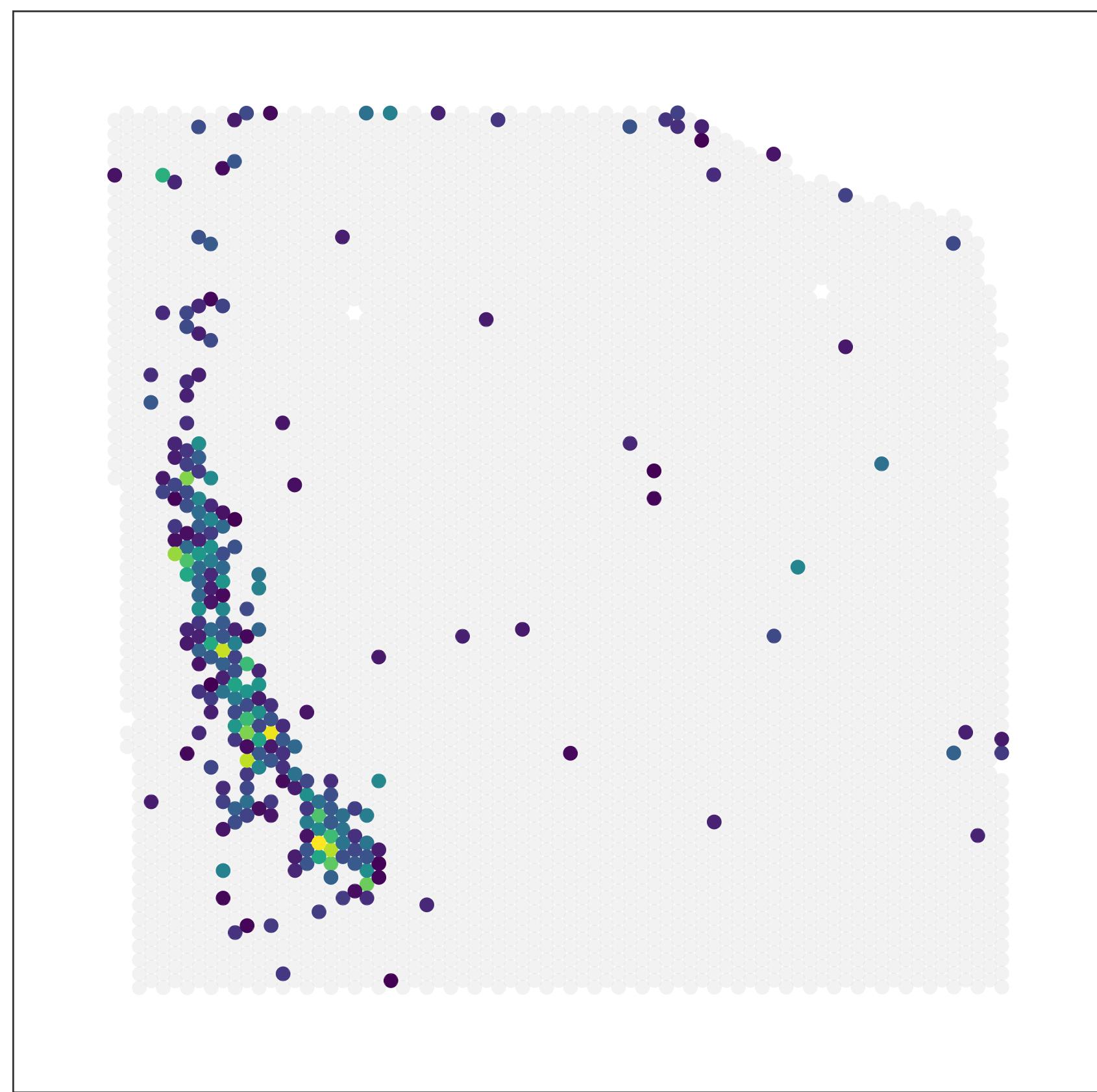
V12N28–331\_B1 NMF\_15



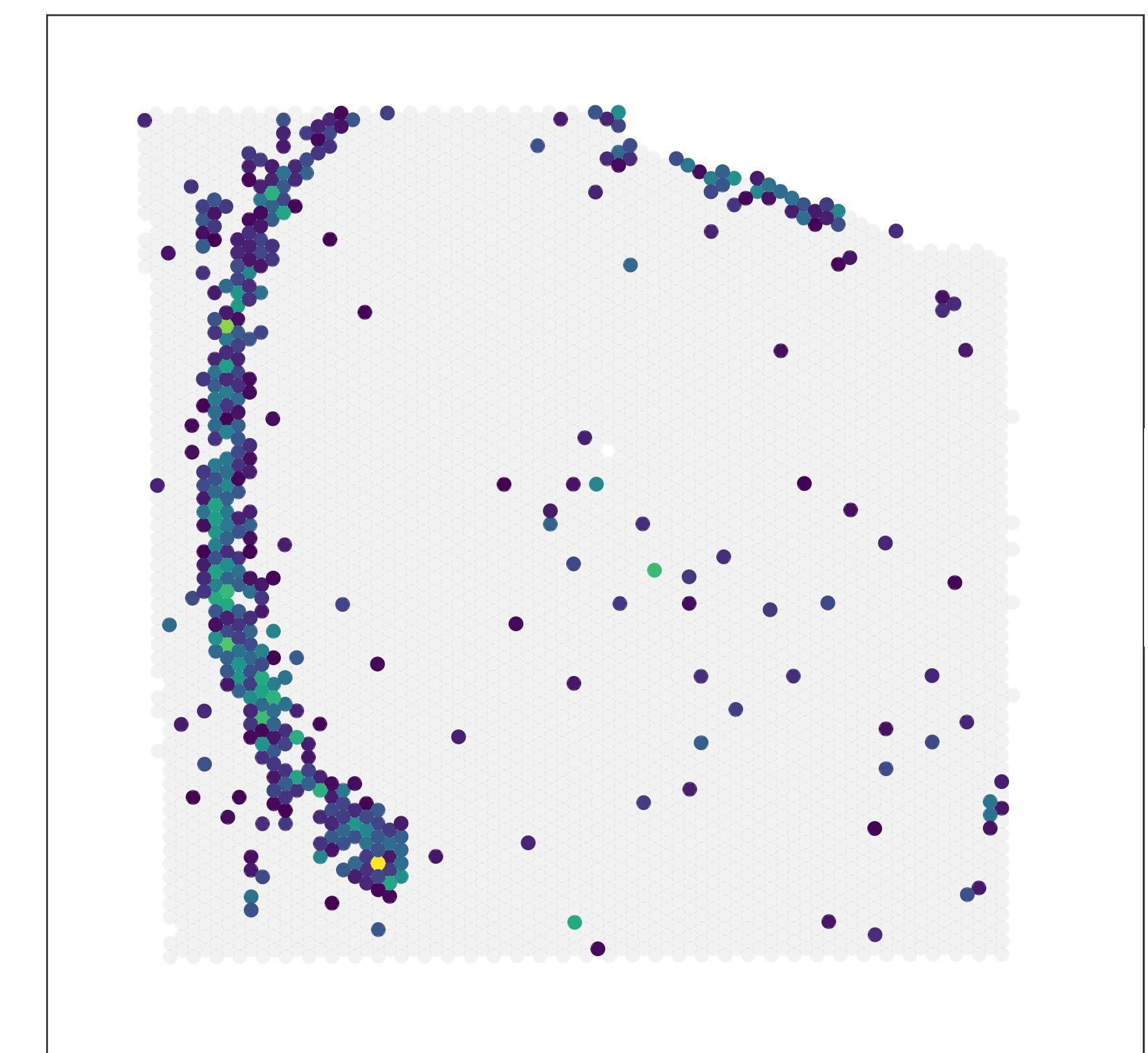
V12N28–332\_B1 NMF\_15



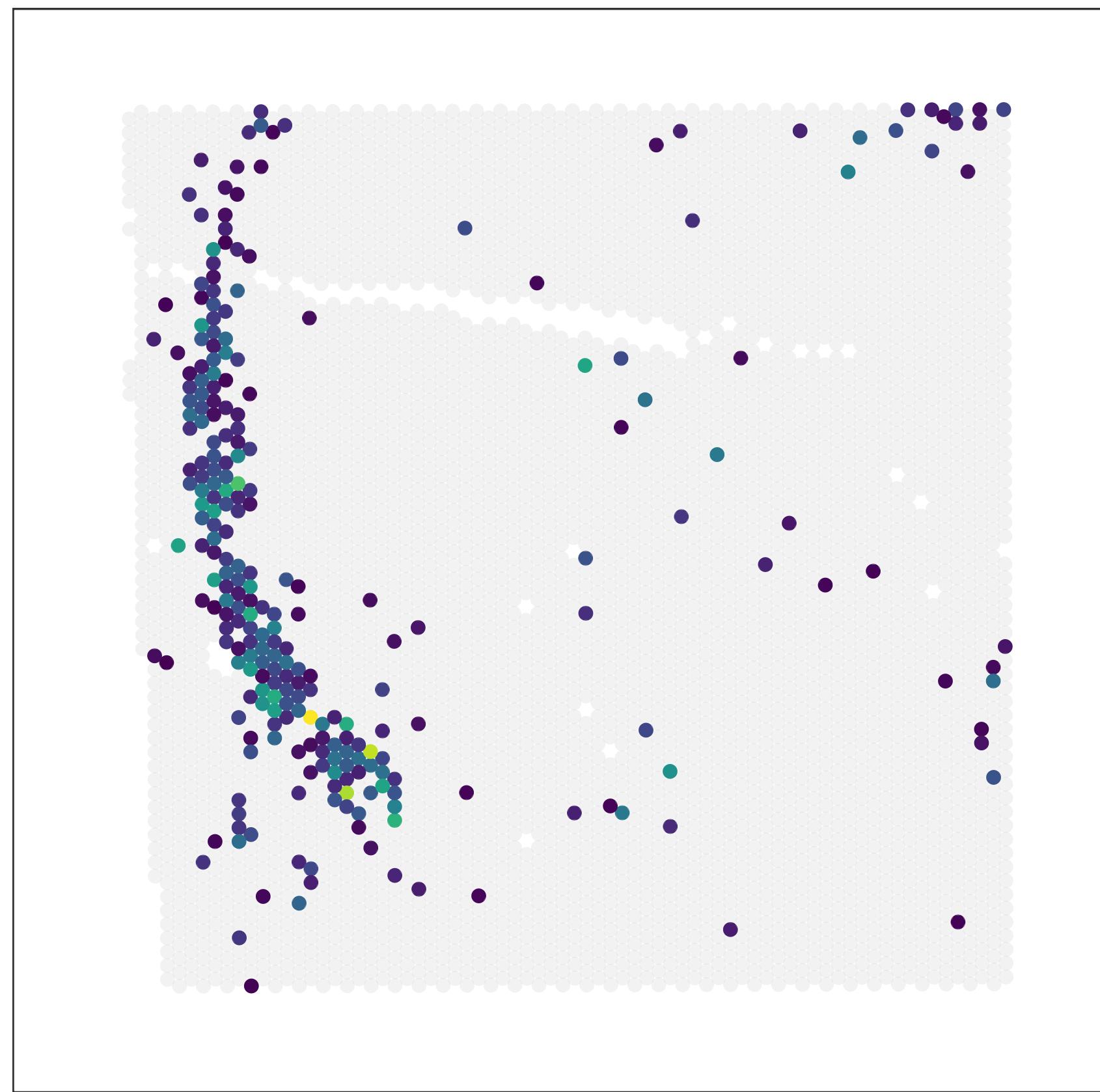
V12J03–002\_C1 NMF\_15



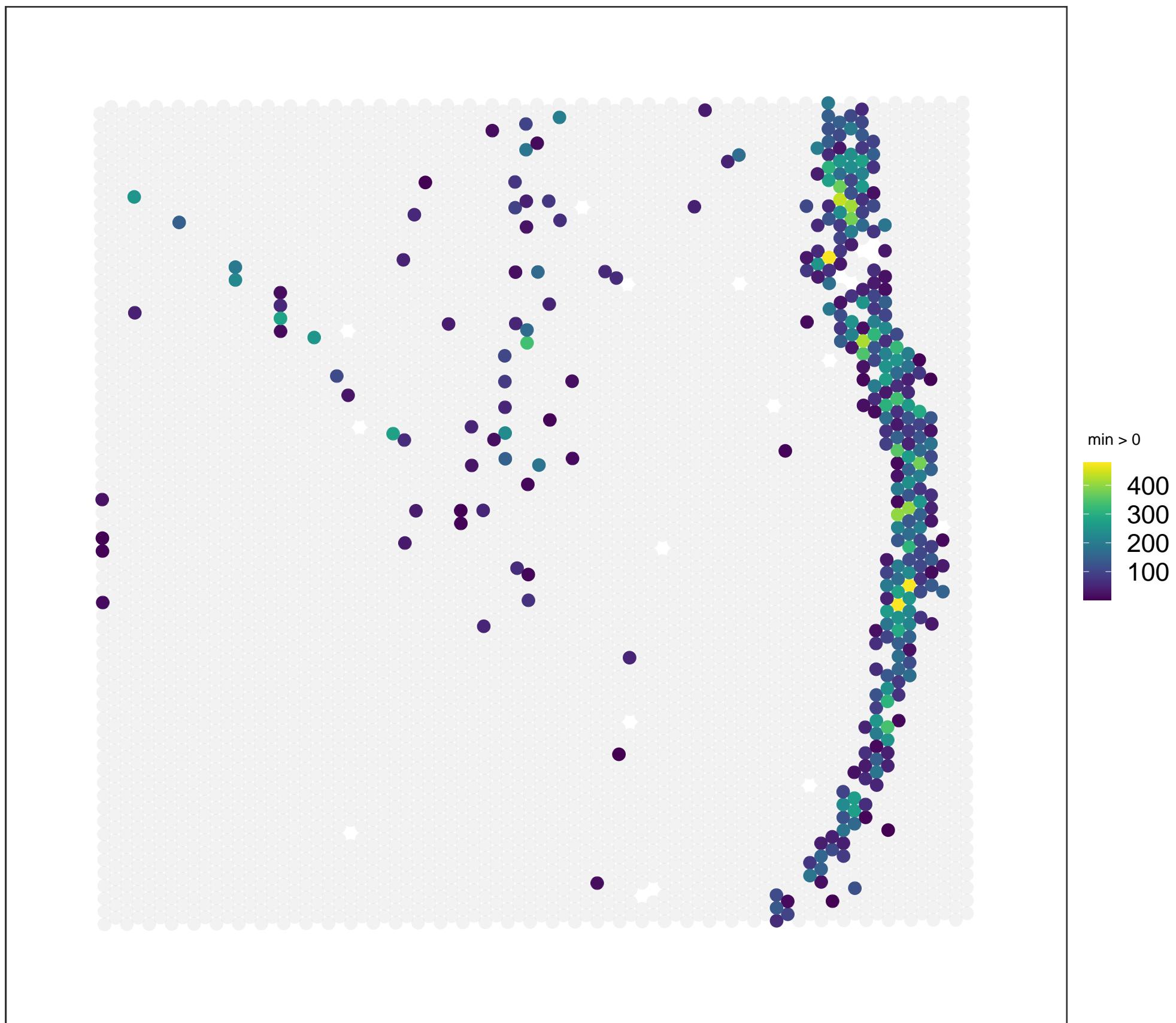
V12N28–331\_C1 NMF\_15



V12N28–332\_C1 NMF\_15



V12N28–331\_D1 NMF\_15 V12N28–331\_D1



V12N28–332\_D1 NMF\_15

