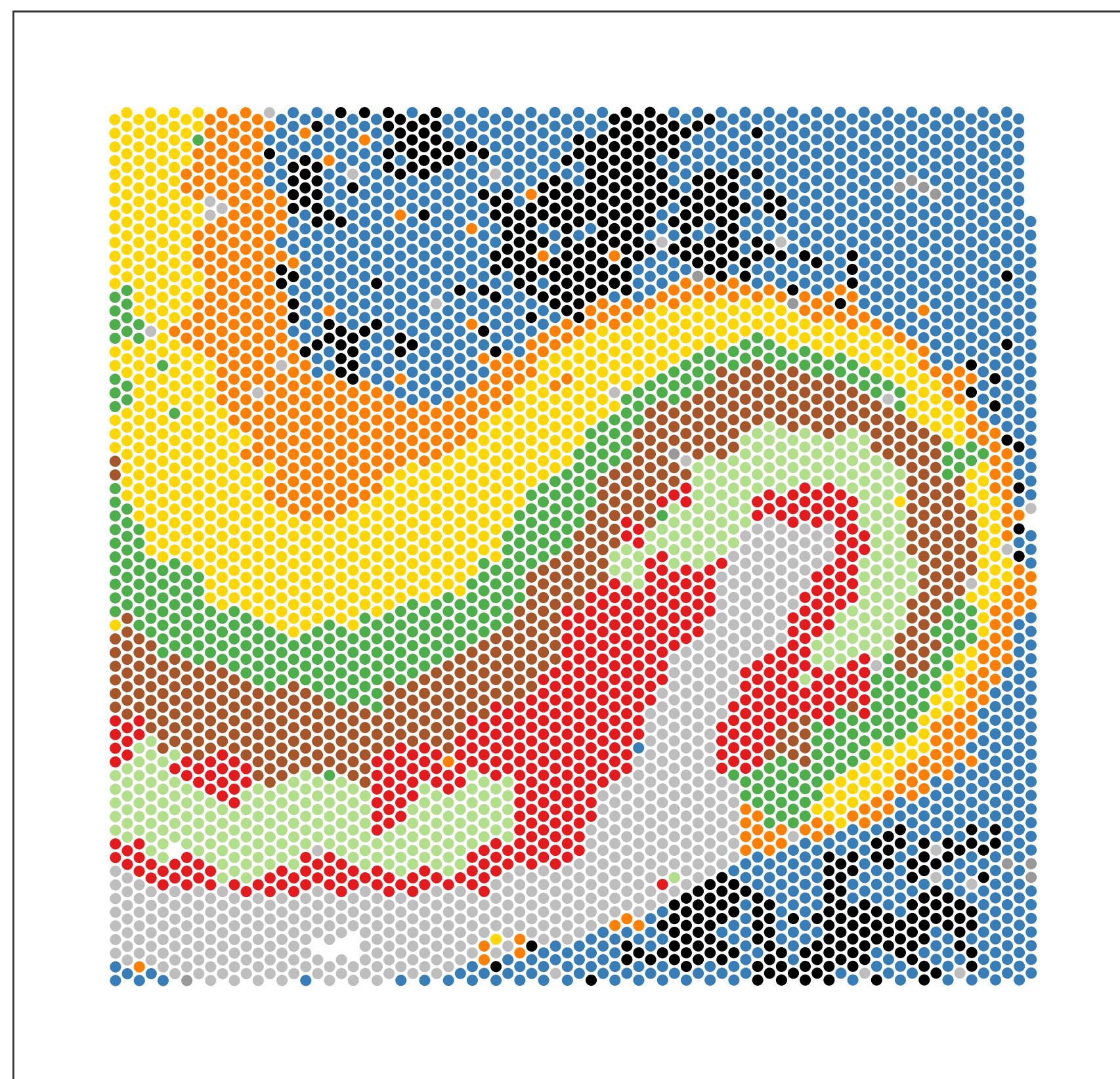


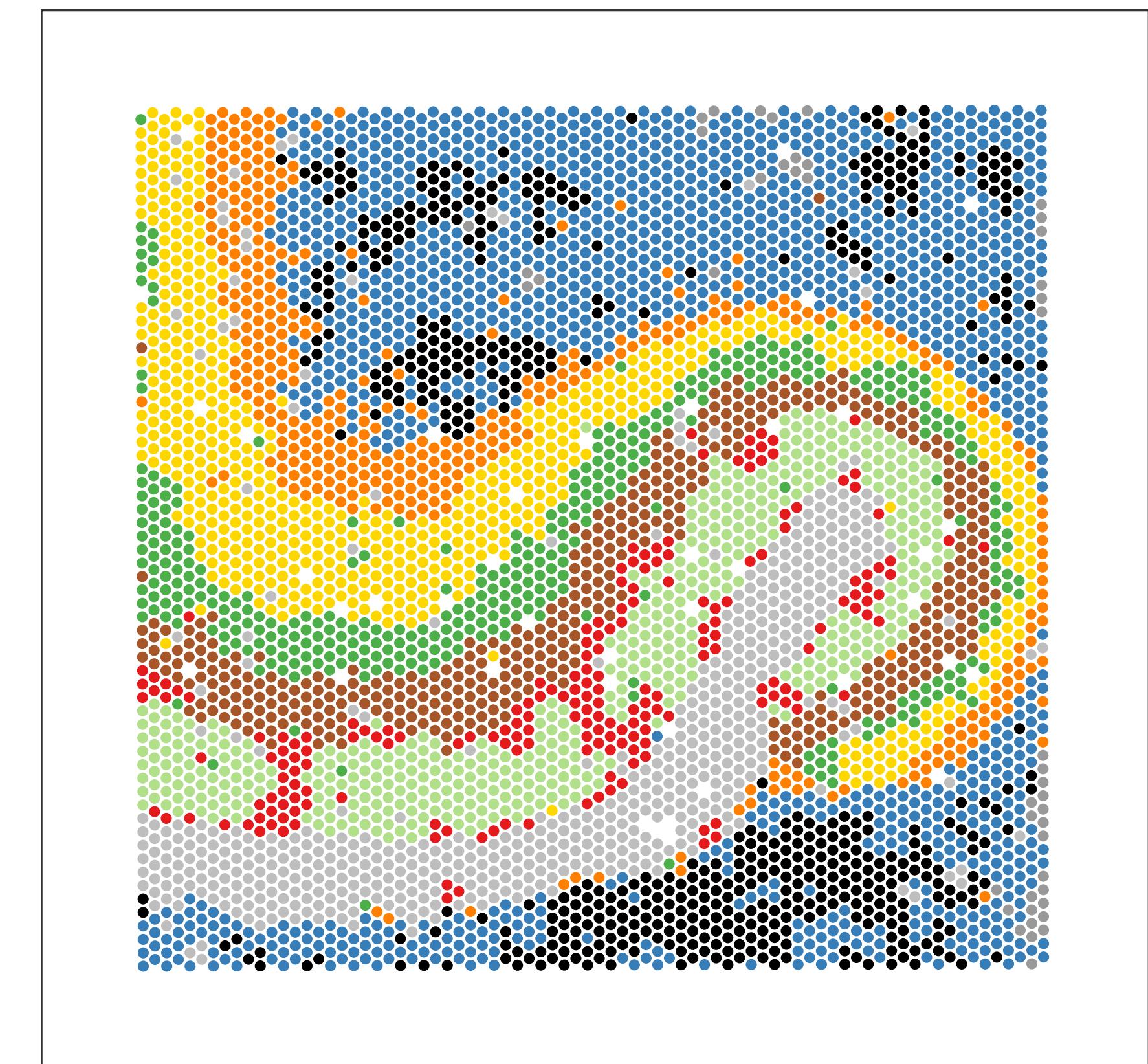
1  
2  
3  
4  
5  
6  
7  
8  
9  
10



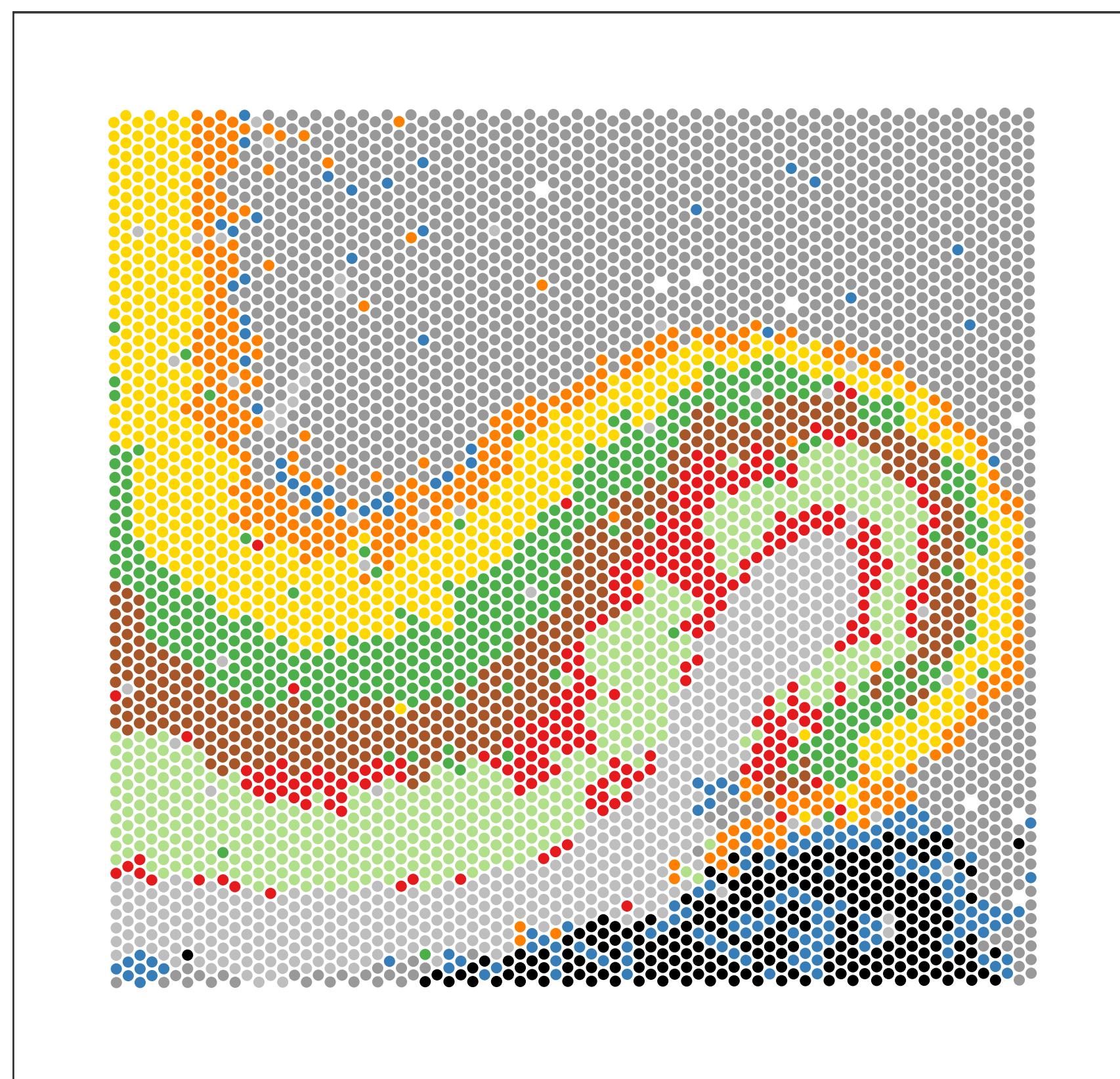
V12J03–002\_A1\_Br2720



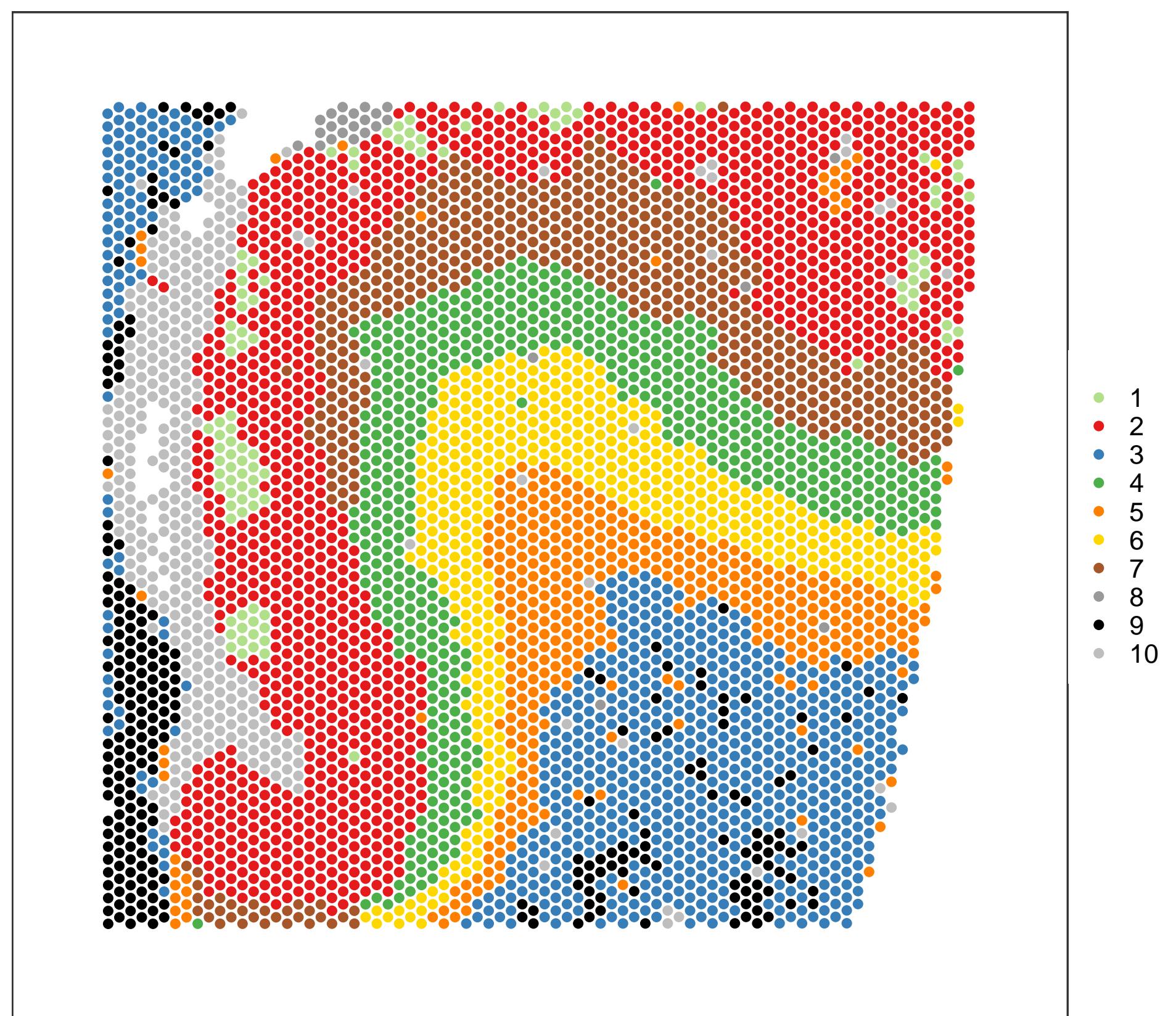
V12N28–331\_A1\_Br2720



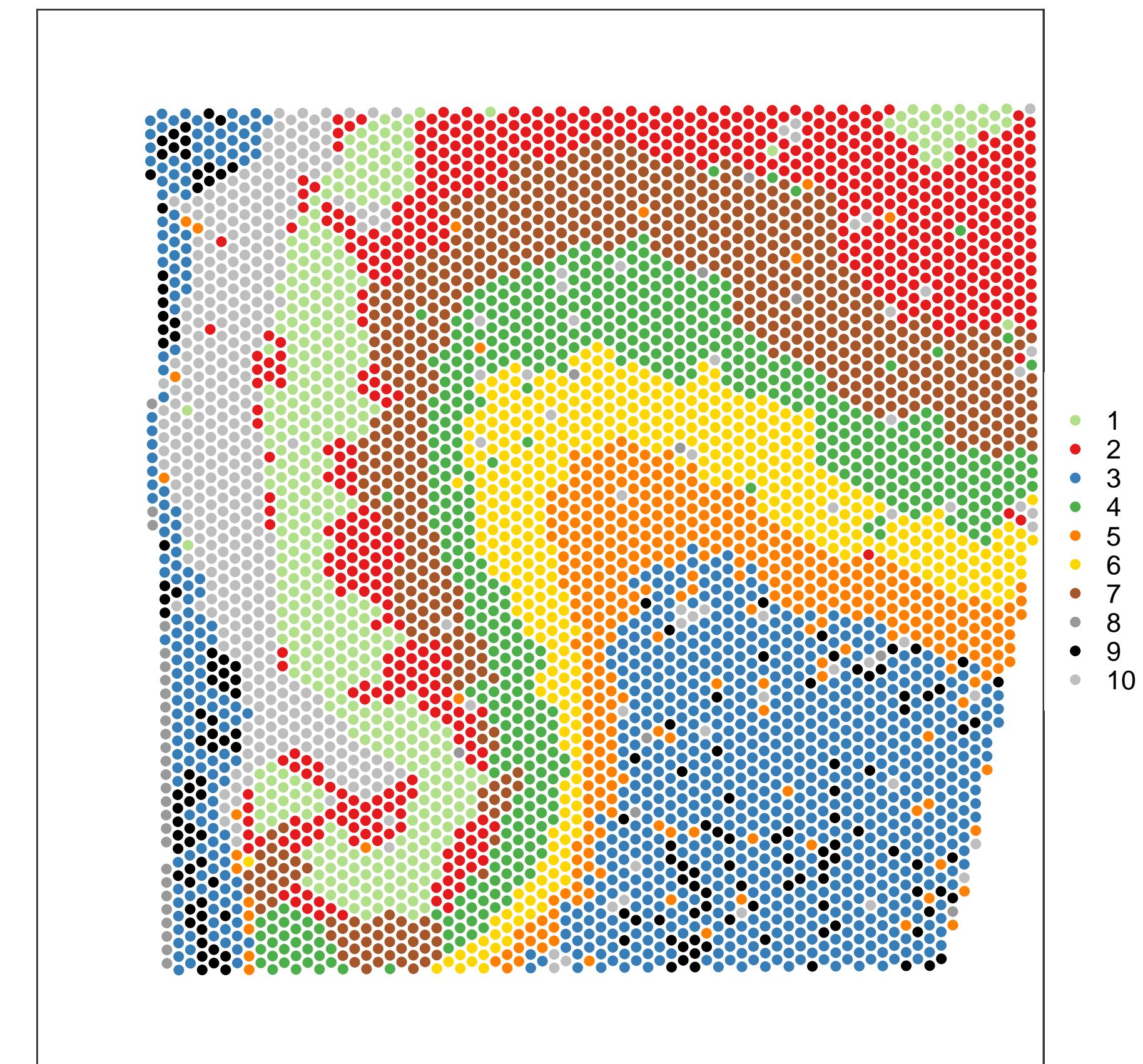
V12N28–332\_A1\_Br2720



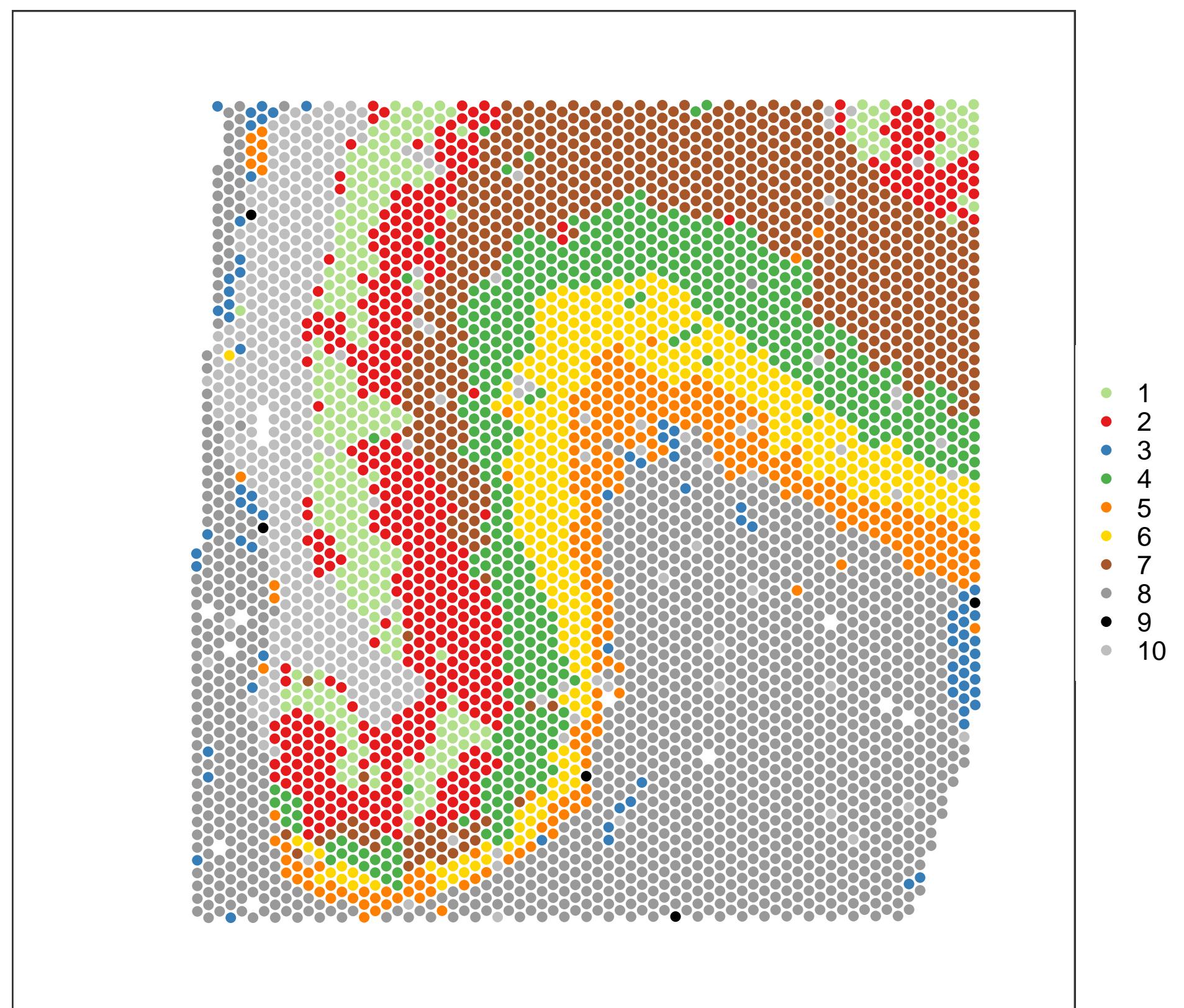
V12J03–002\_B1\_Br6432



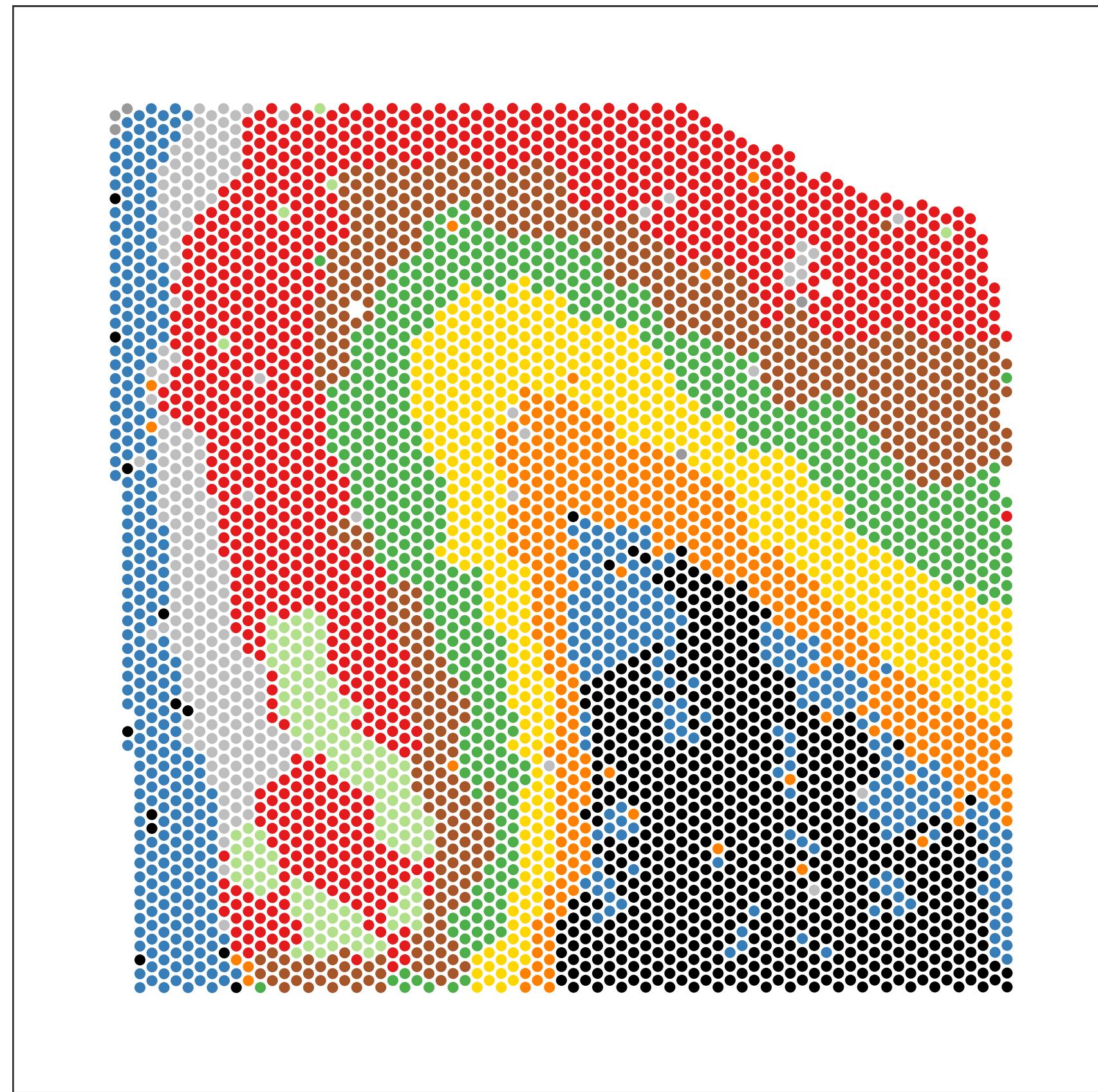
V12N28–331\_B1\_Br6432



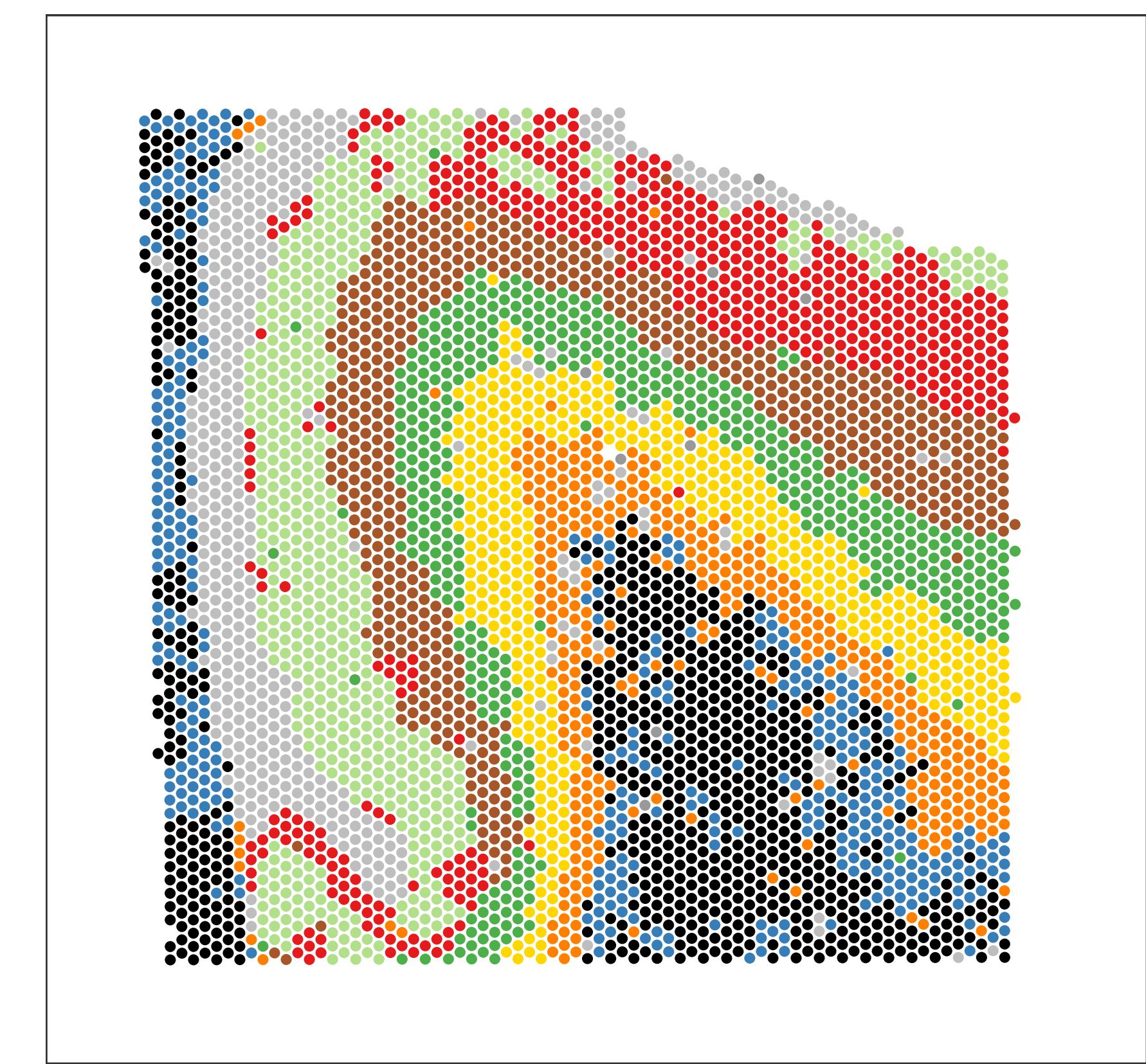
V12N28–332\_B1\_Br6432



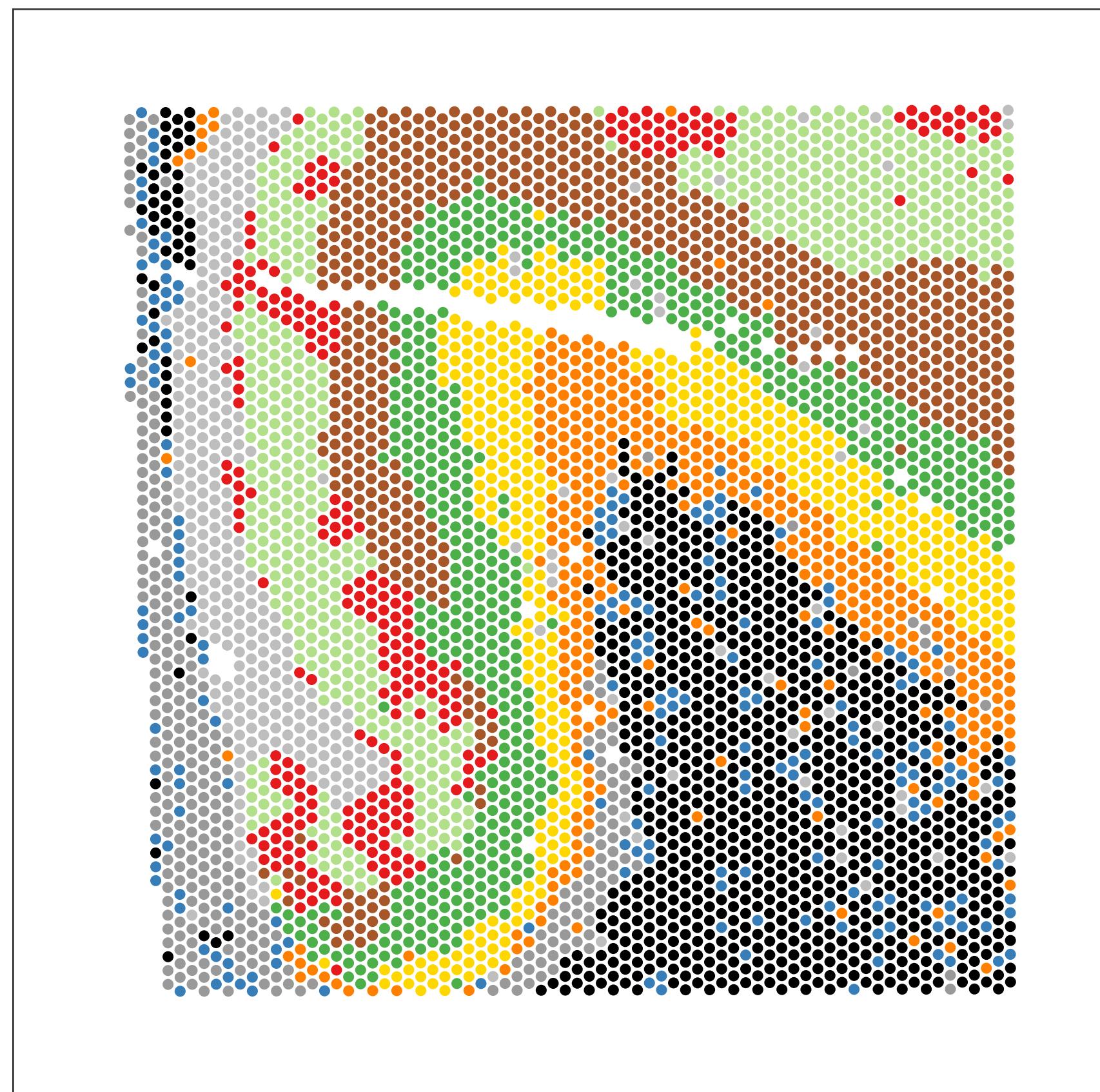
V12J03–002\_C1\_Br6471



V12N28–331\_C1\_Br6471



V12N28–332\_C1\_Br6471

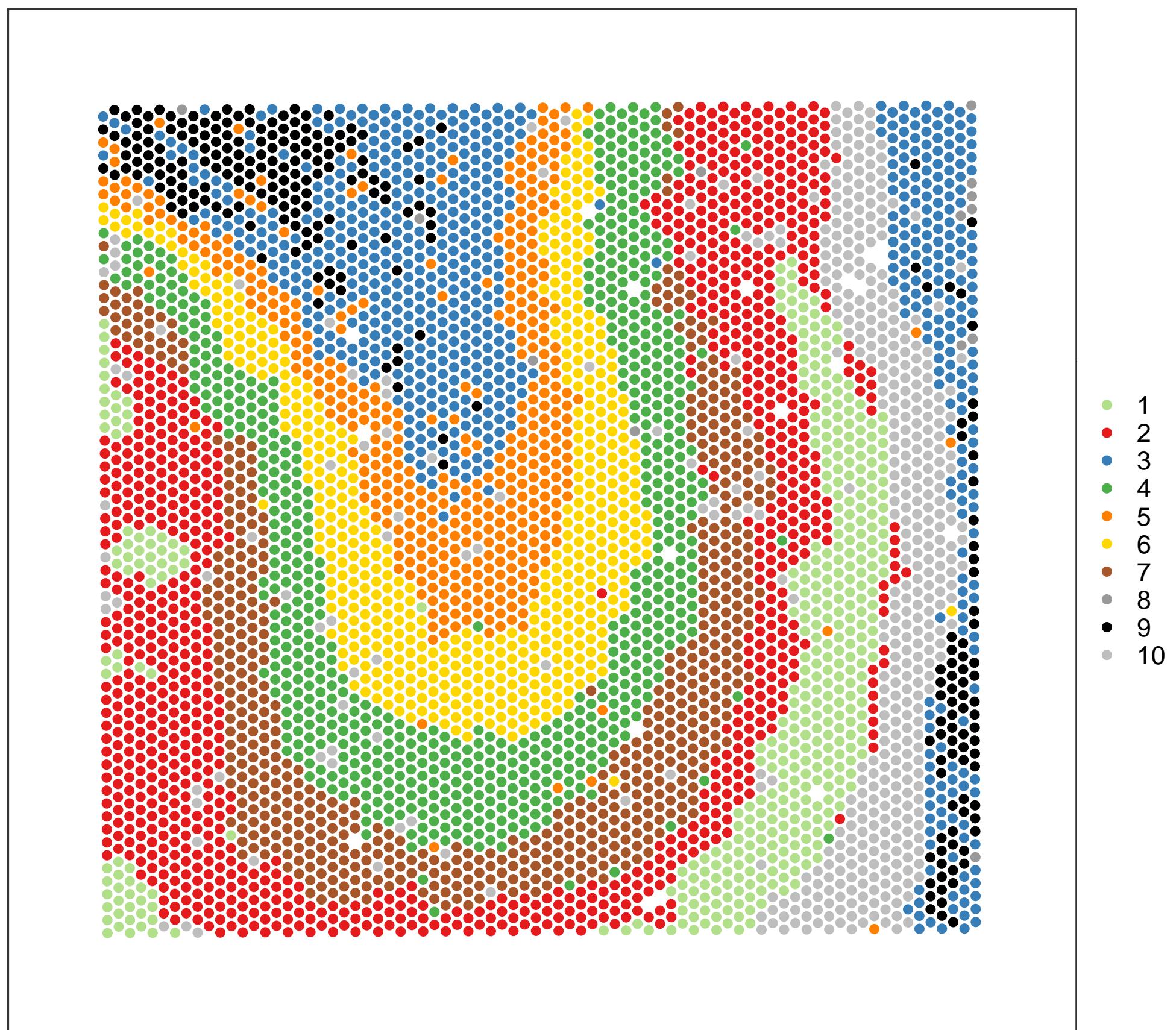


1  
2  
3  
4  
5  
6  
7  
8  
9  
10

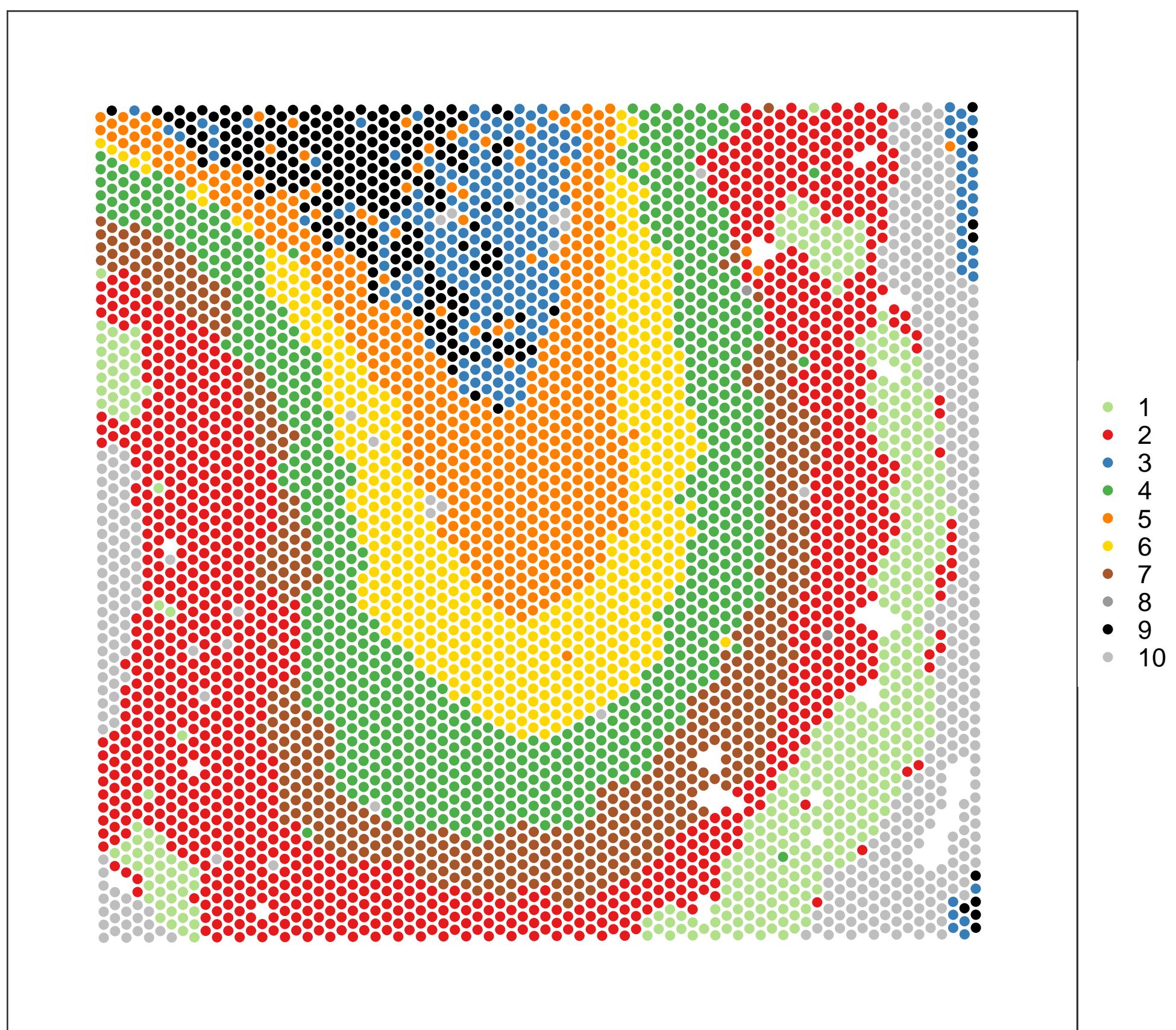
1  
2  
3  
4  
5  
6  
7  
8  
9  
10

1  
2  
3  
4  
5  
6  
7  
8  
9  
10

V12N28–331\_D1\_Br6522

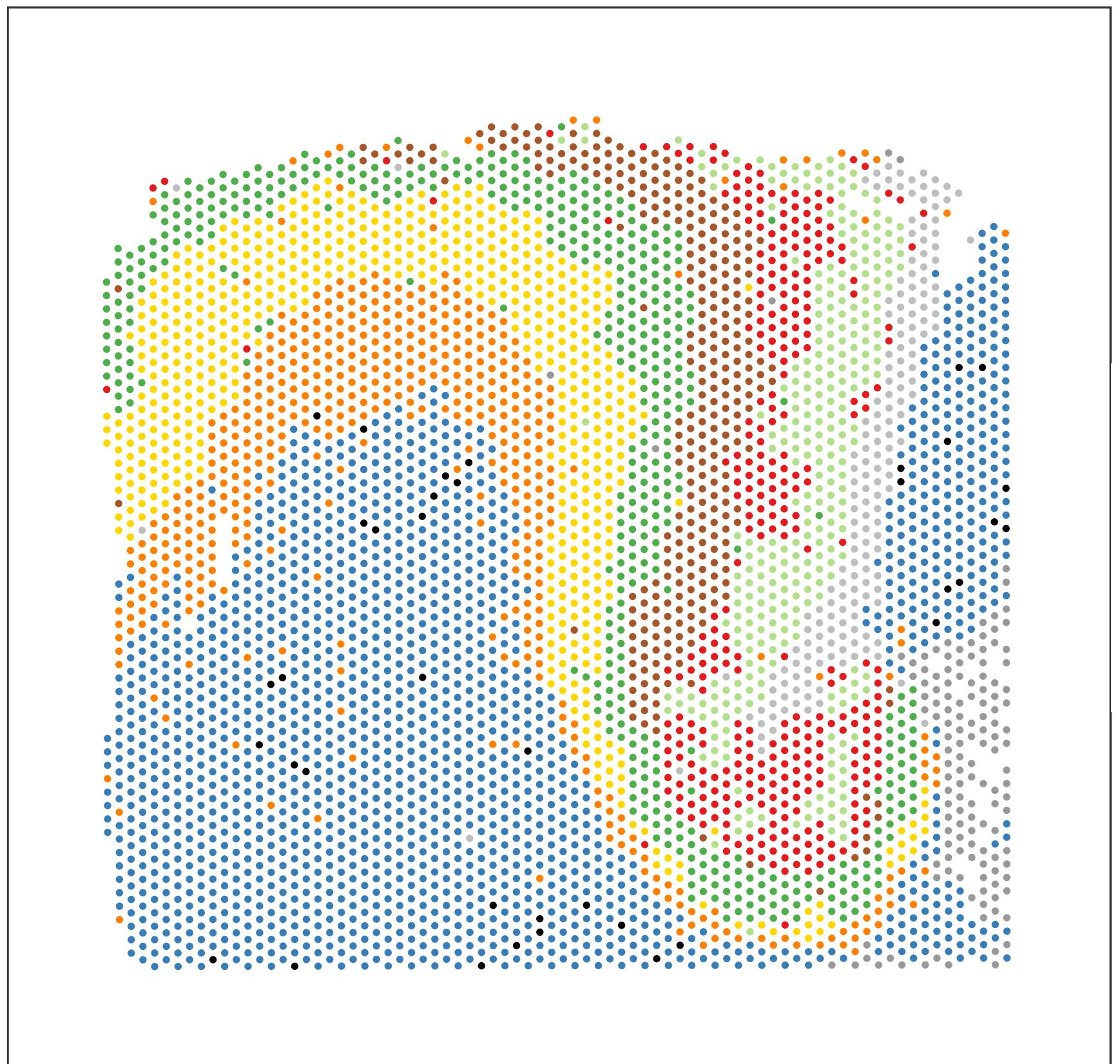


V12N28–332\_D1\_Br6522

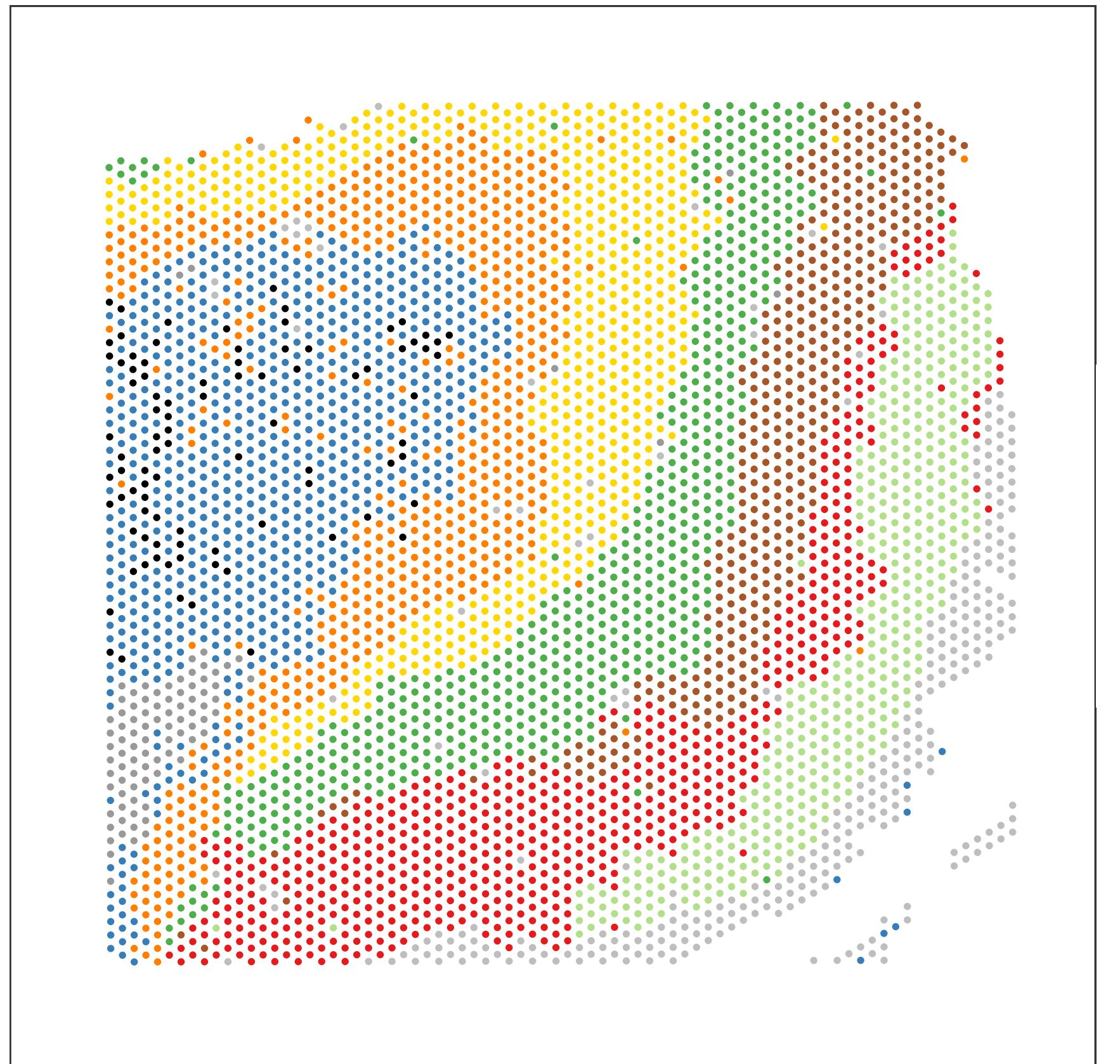


V12N28-334\_C1

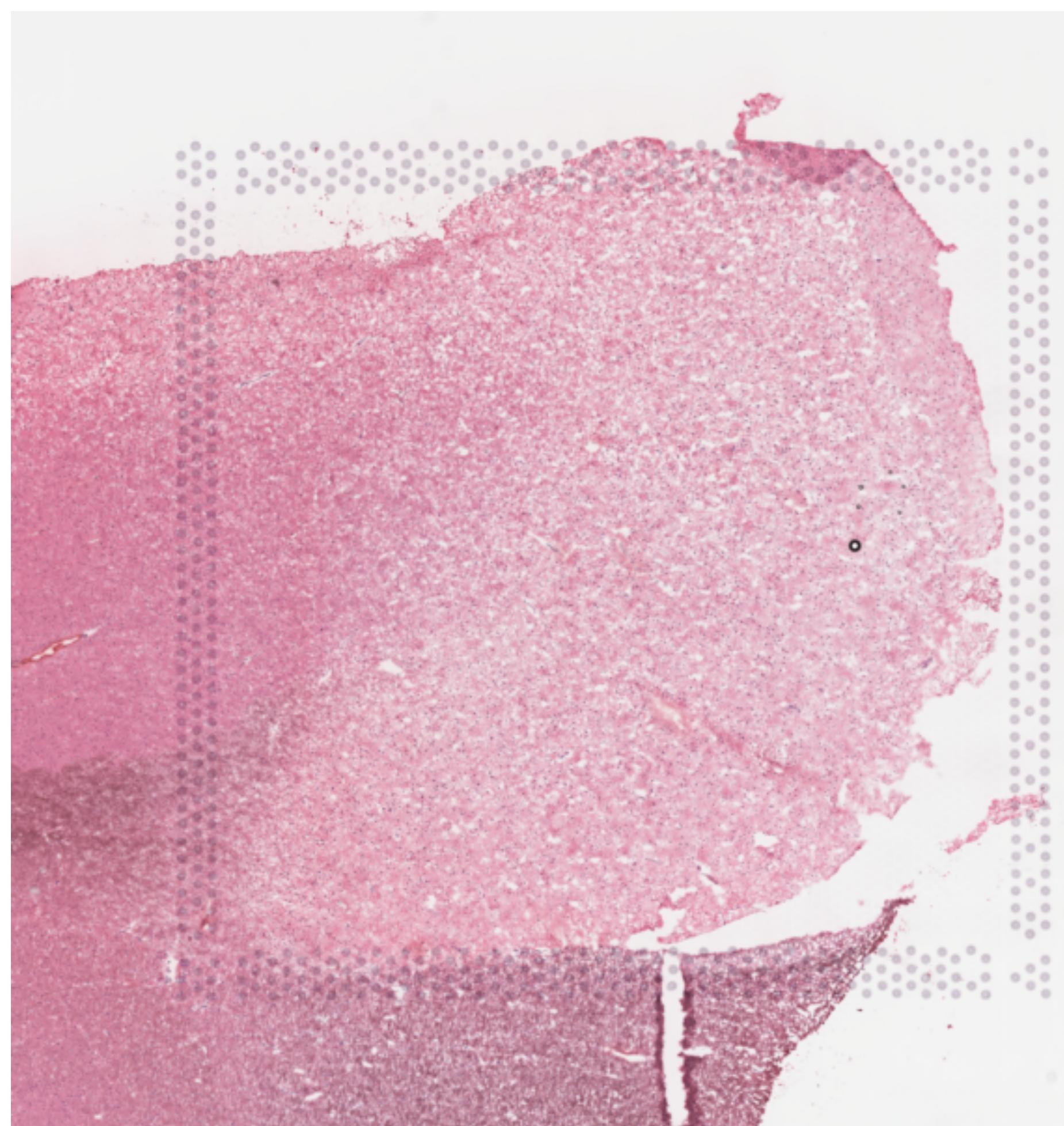
V12N28-334\_C1\_Br2743



V12N28–334\_A1\_Br3942

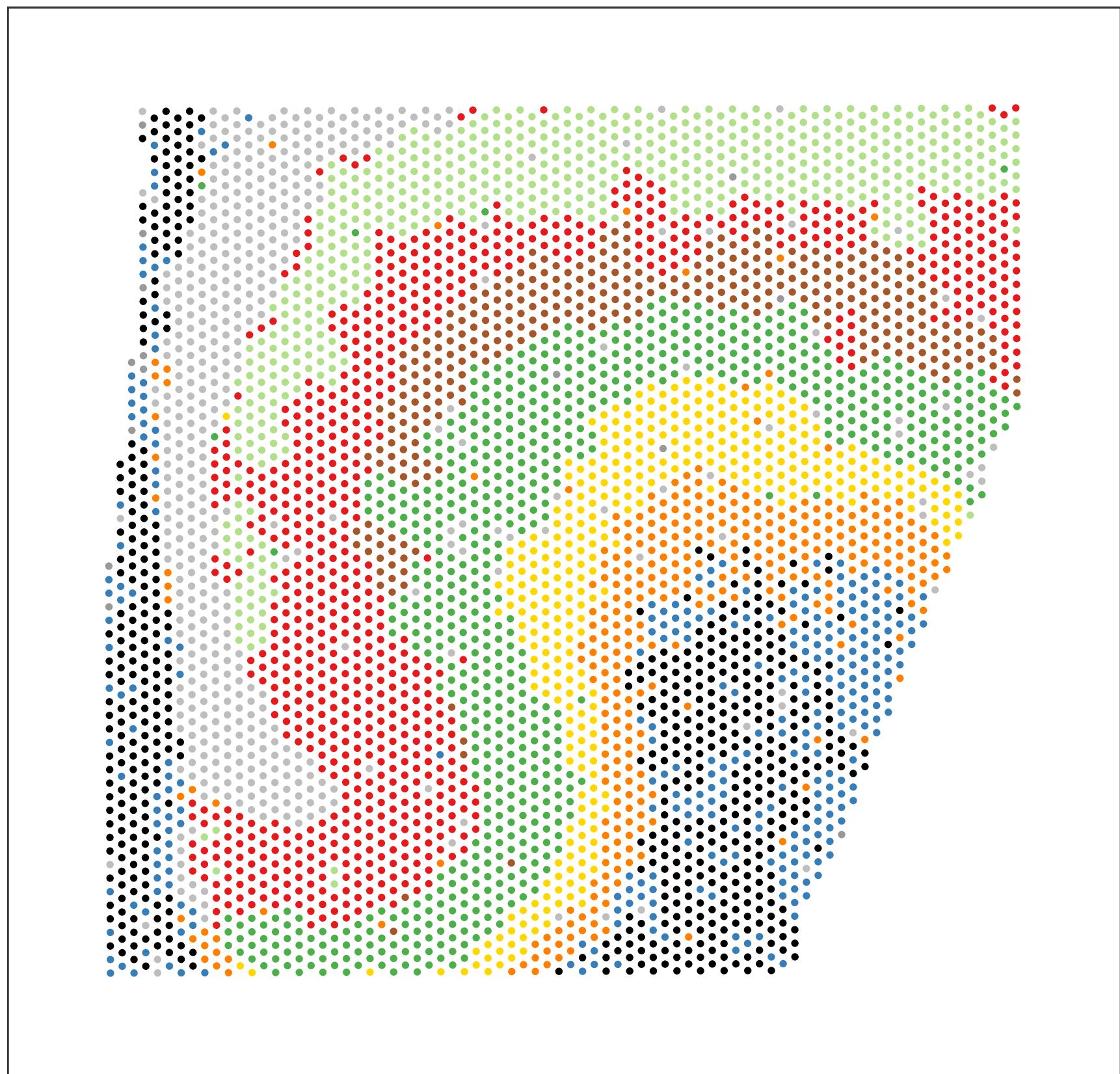


V12N28–334\_A1

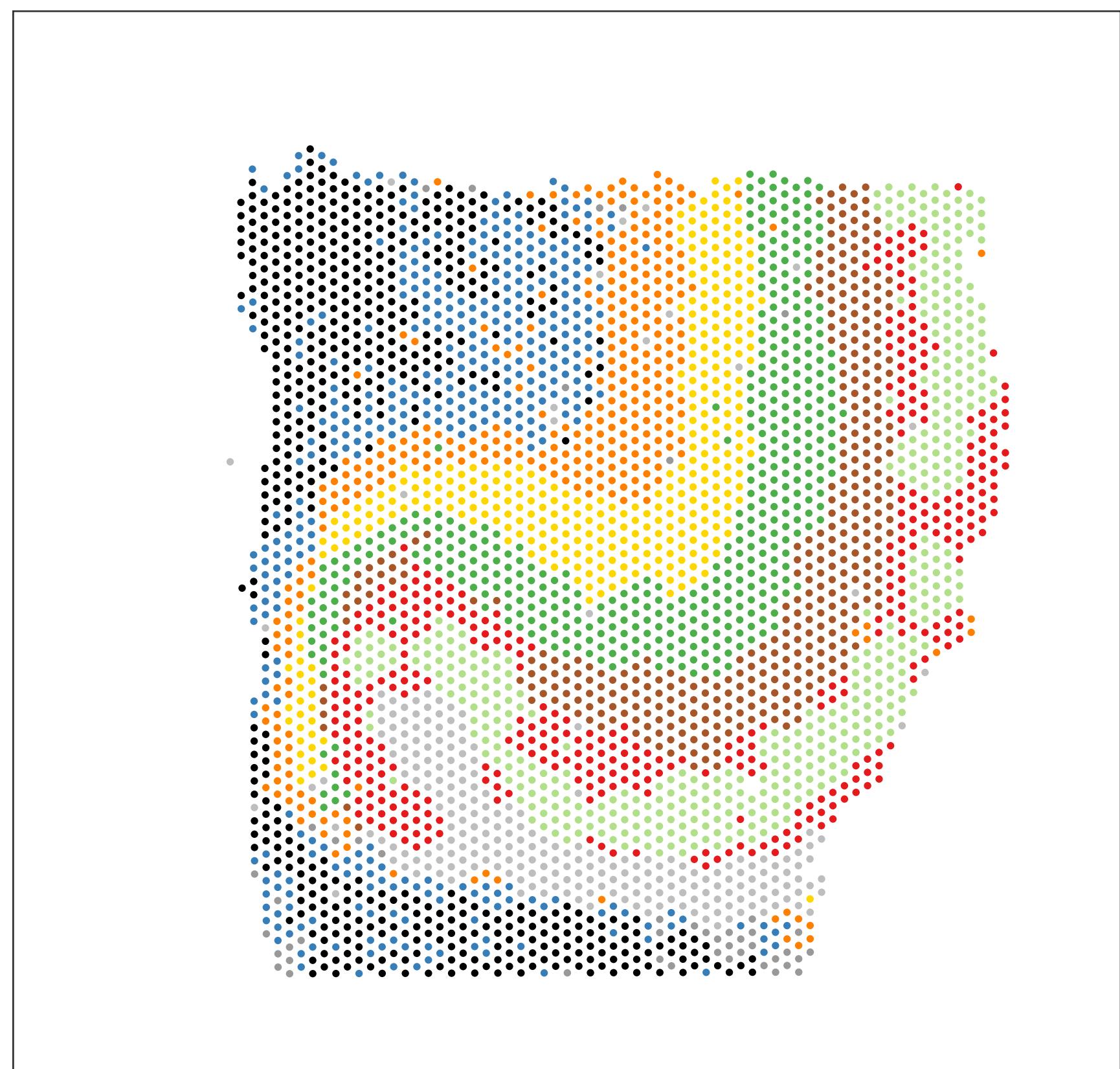


V12N28-334\_D1

V12N28-334\_D1\_Br6423



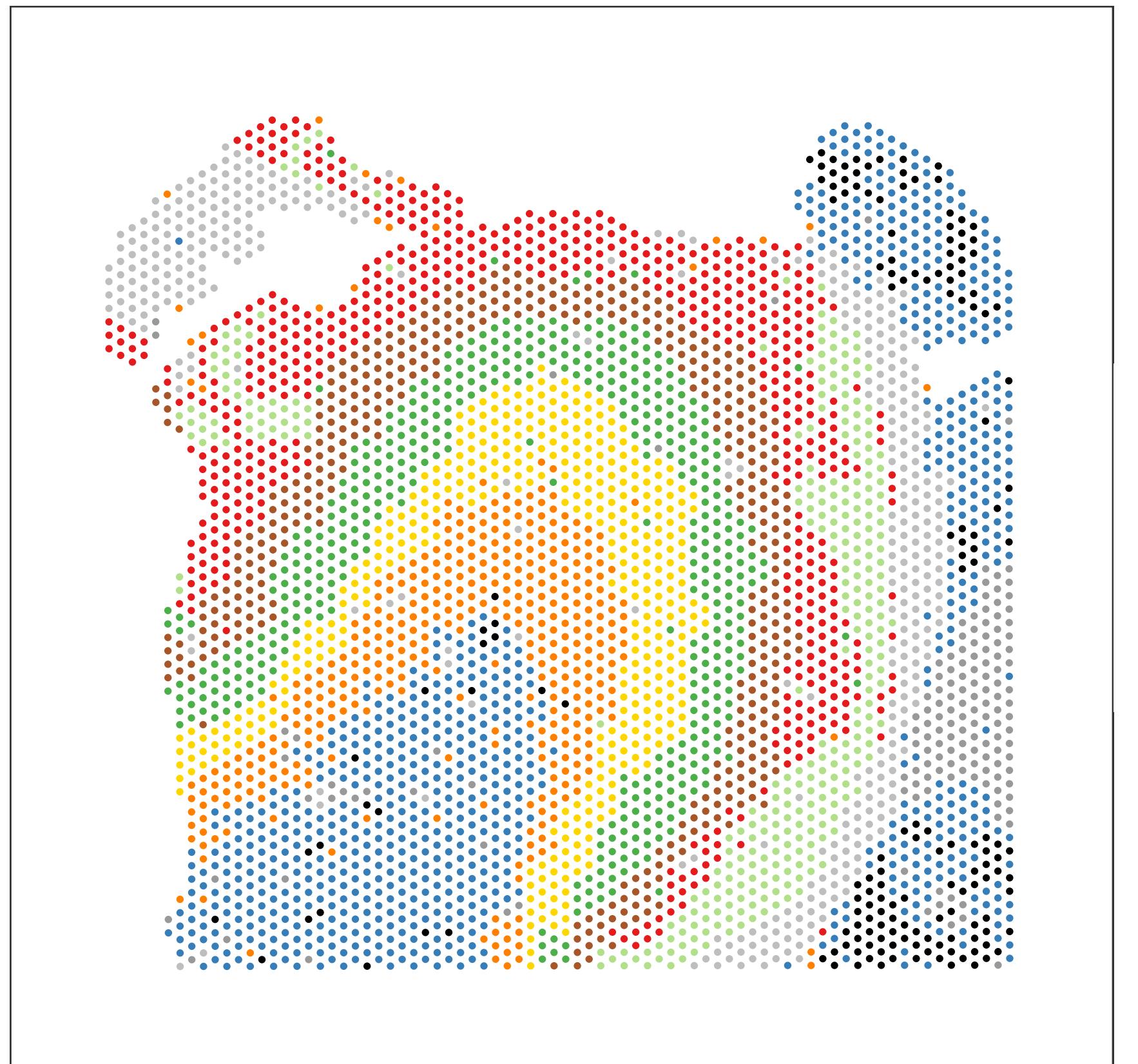
V12Y31–080\_C1\_Br8325



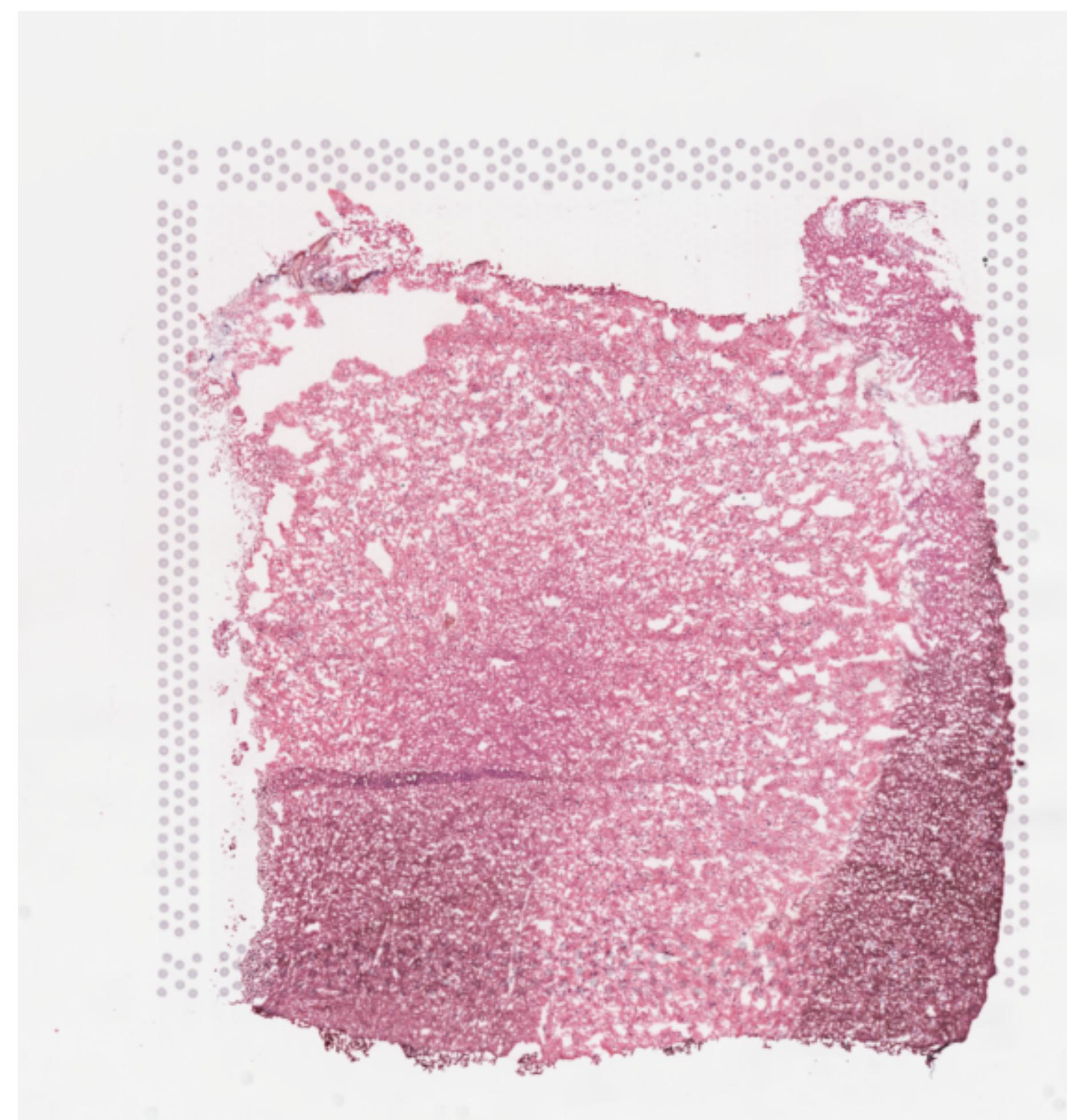
V12Y31–080\_C1



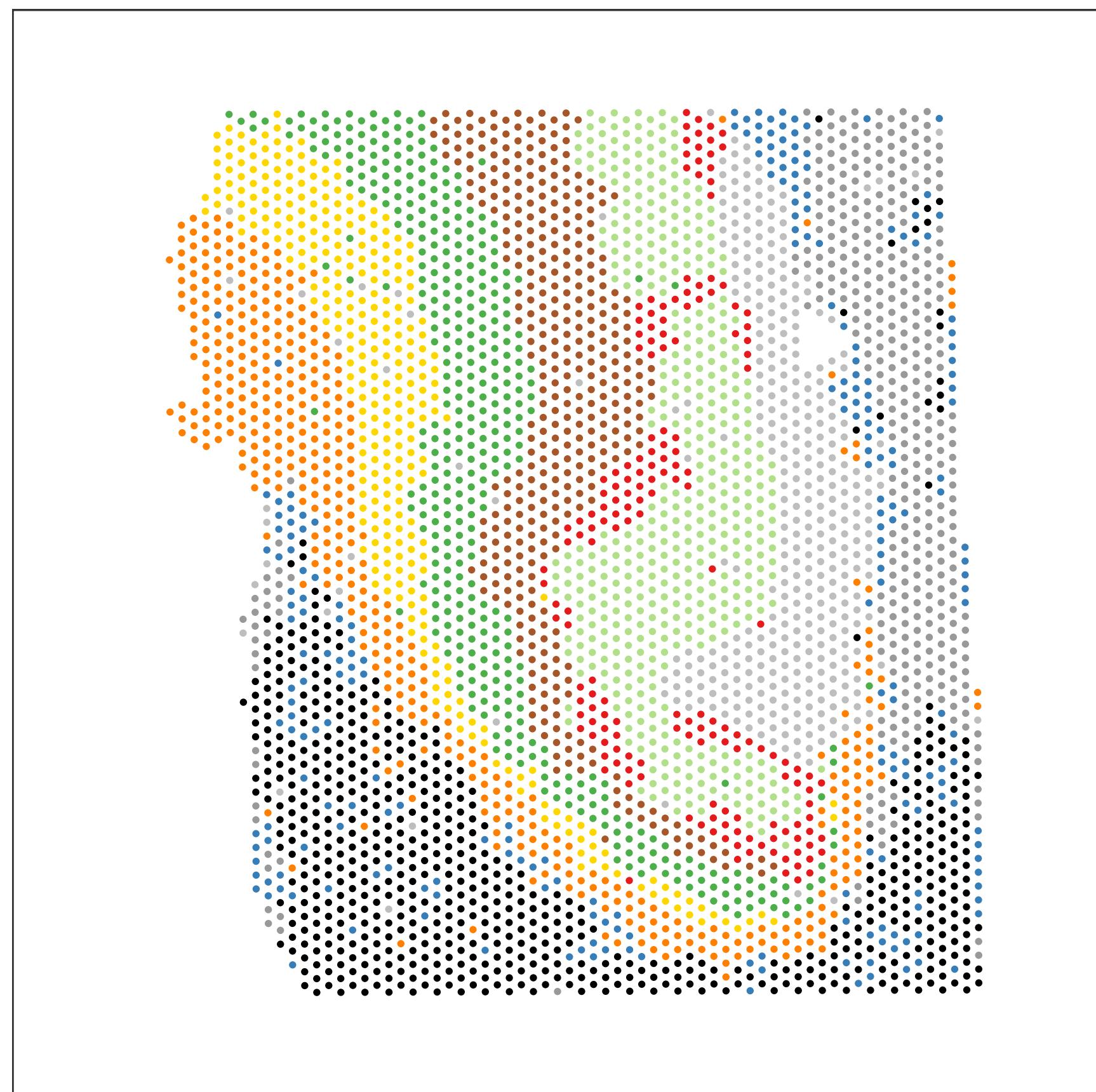
V12N28–334\_B1\_Br8492



V12N28–334\_B1



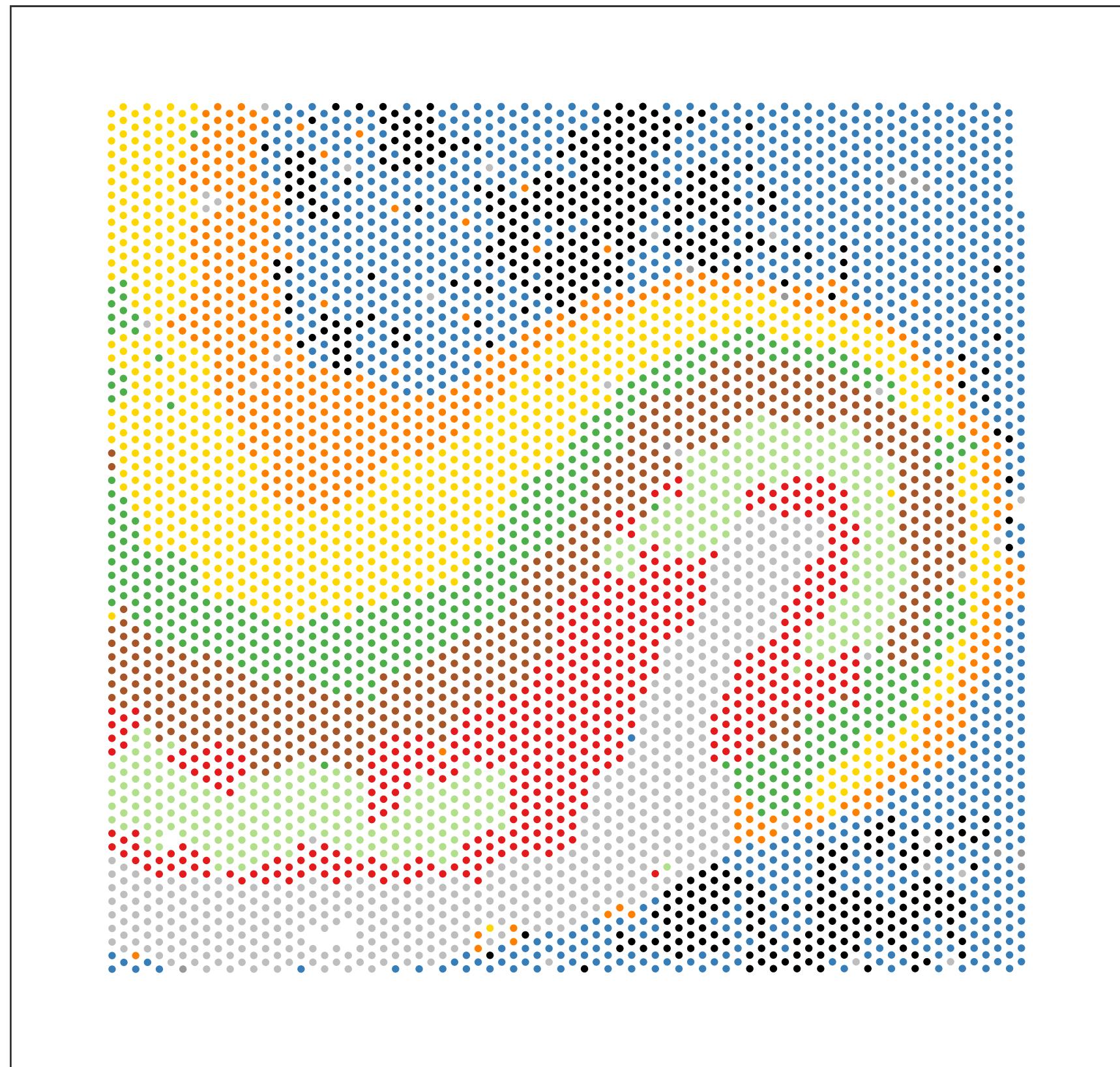
V12Y31-080\_B1\_Br8667



V12Y31-080\_B1



V12J03-002\_A1\_Br2720

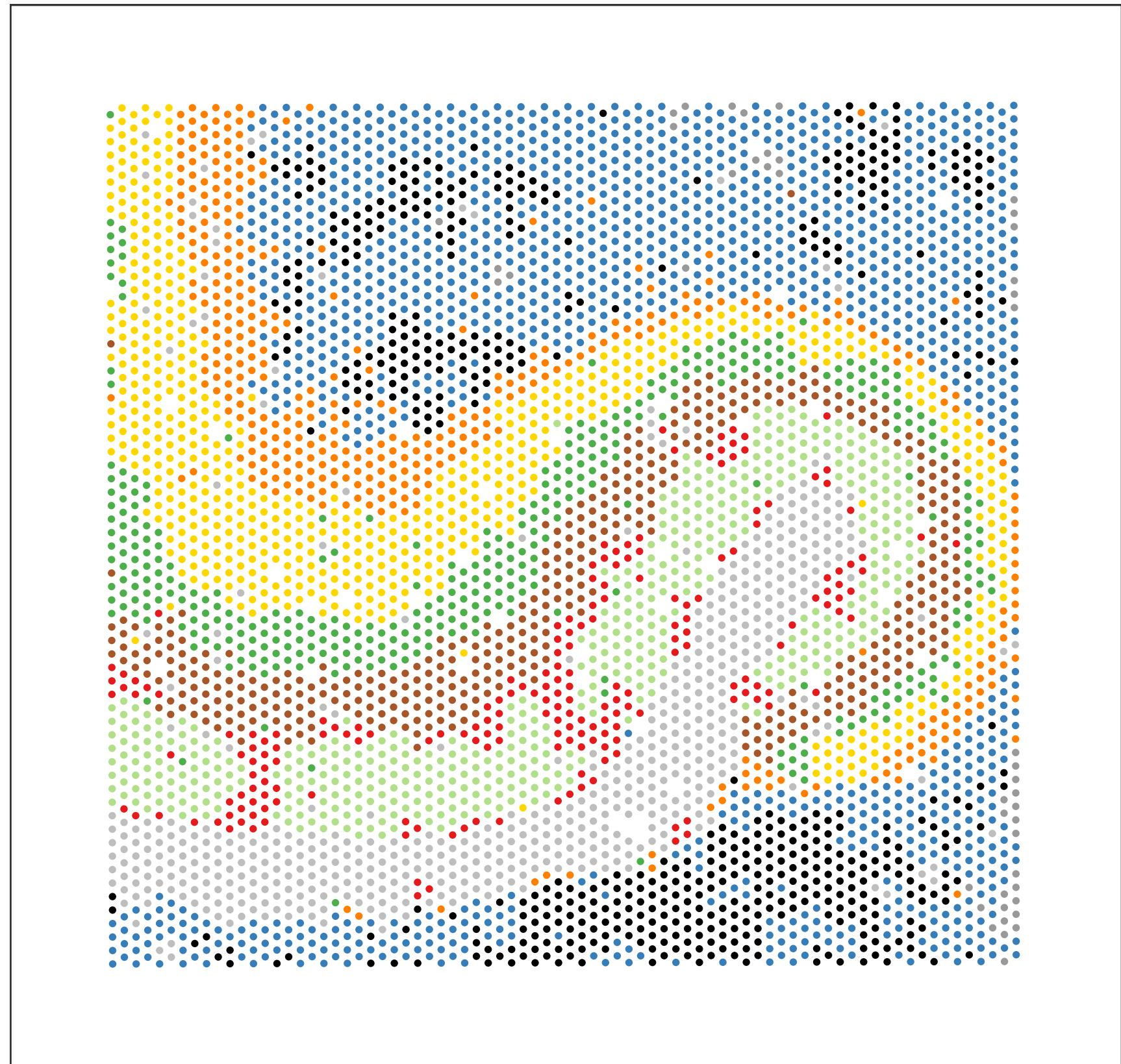


1  
2  
3  
4  
5  
6  
7  
8  
9  
10

V12J03-002\_A1



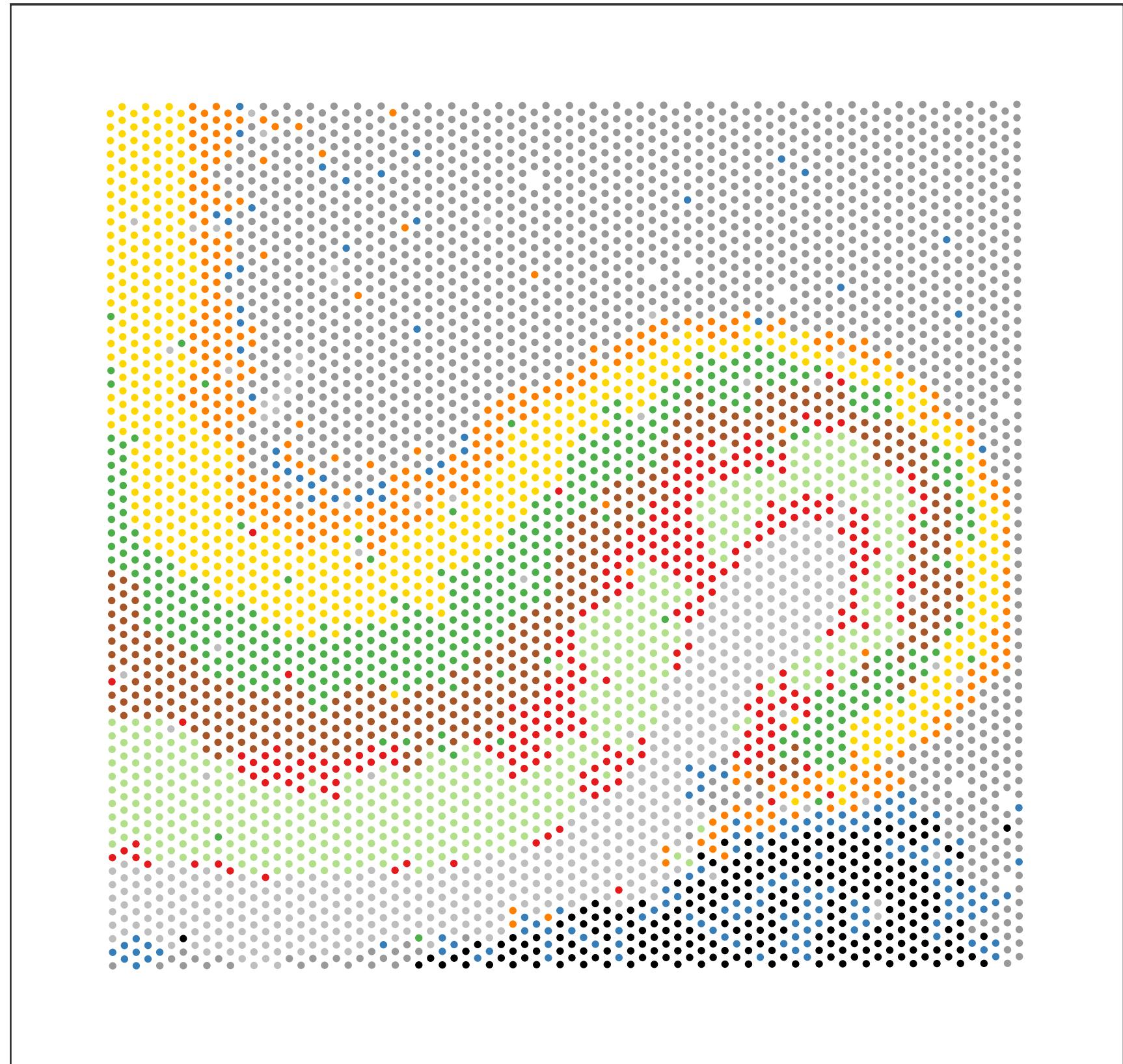
V12N28–331\_A1\_Br2720



V12N28–331\_A1



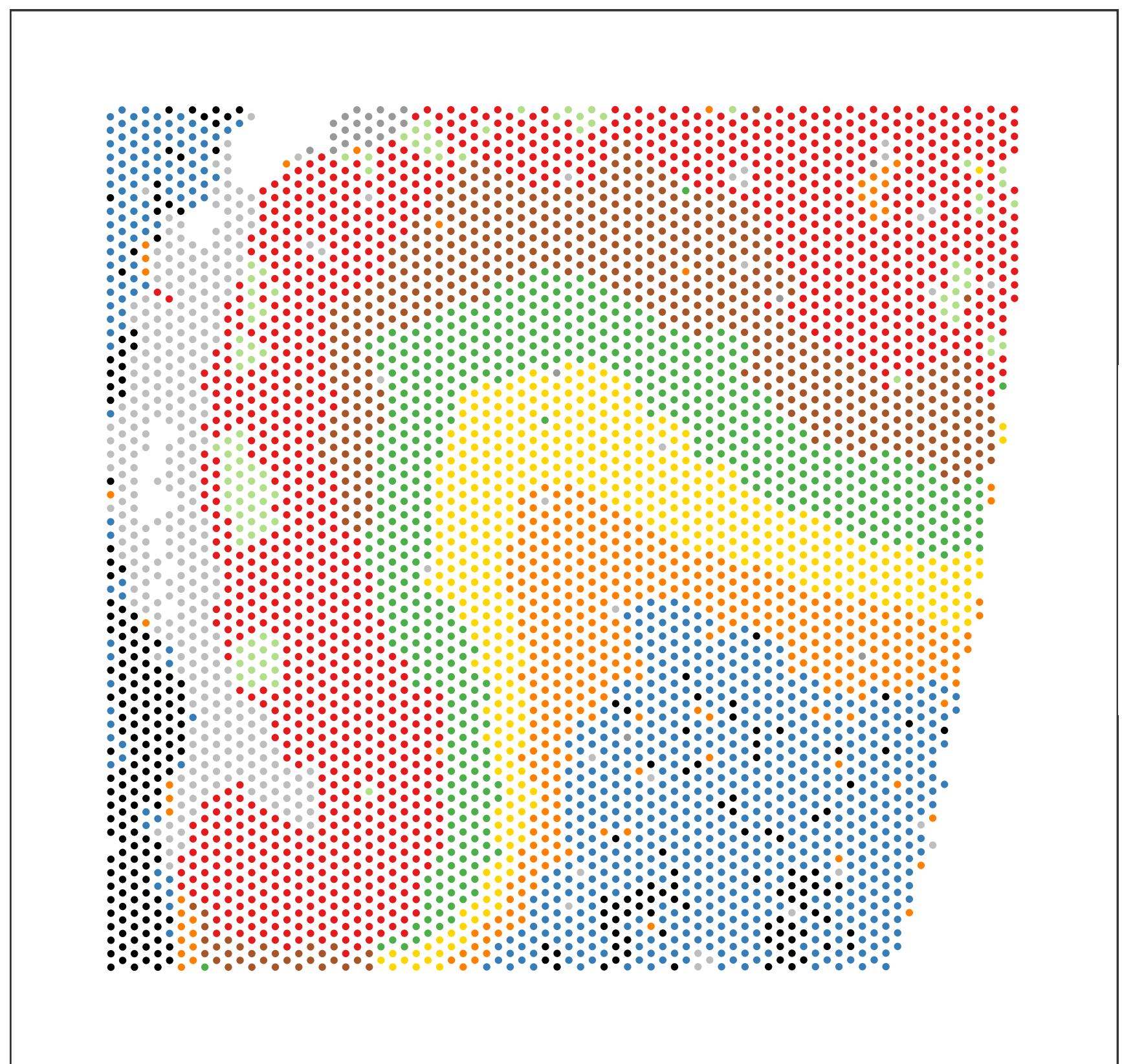
V12N28–332\_A1\_Br2720



V12N28–332\_A1



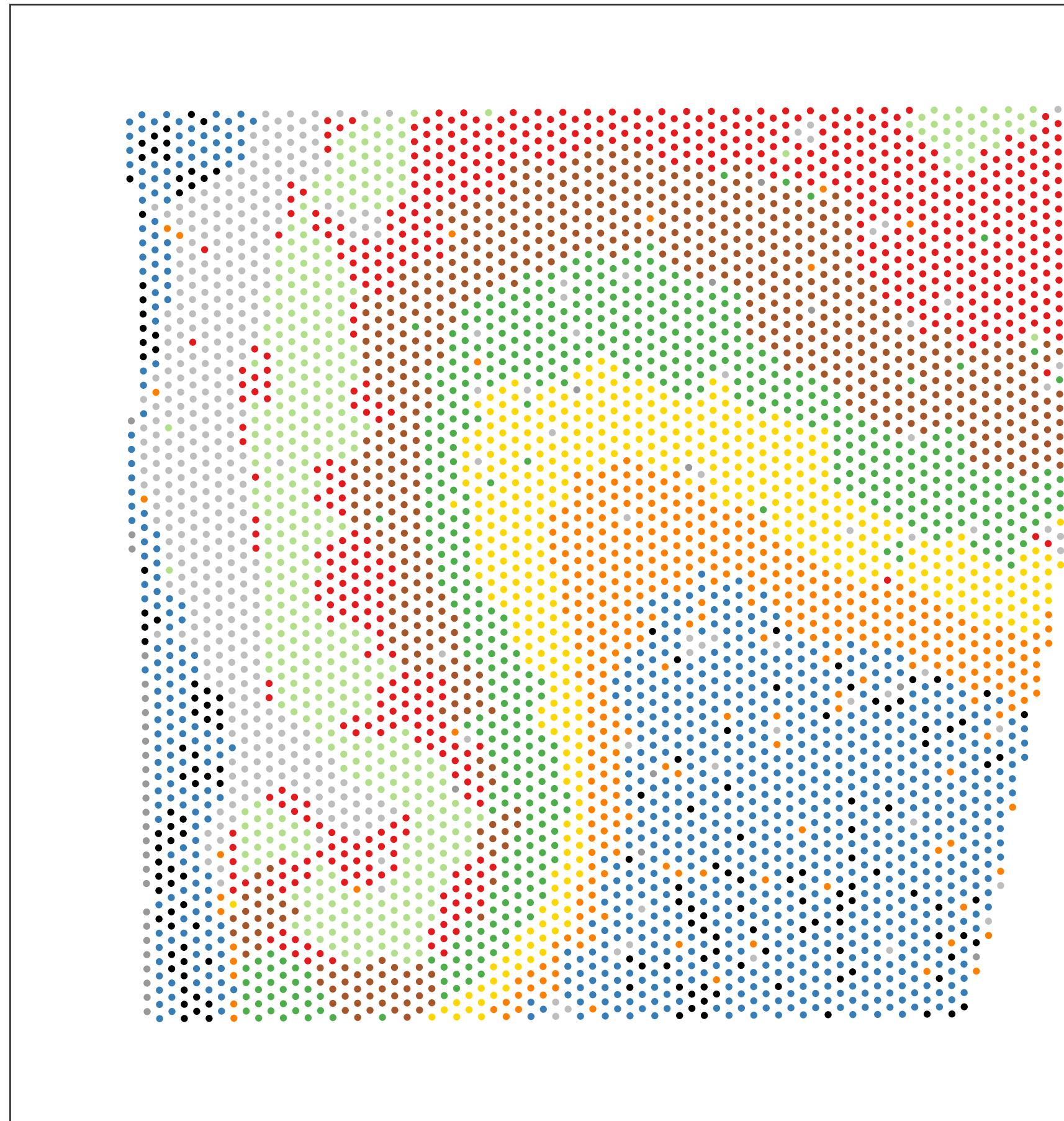
V12J03-002\_B1\_Br6432



V12J03-002\_B1



V12N28–331\_B1\_Br6432



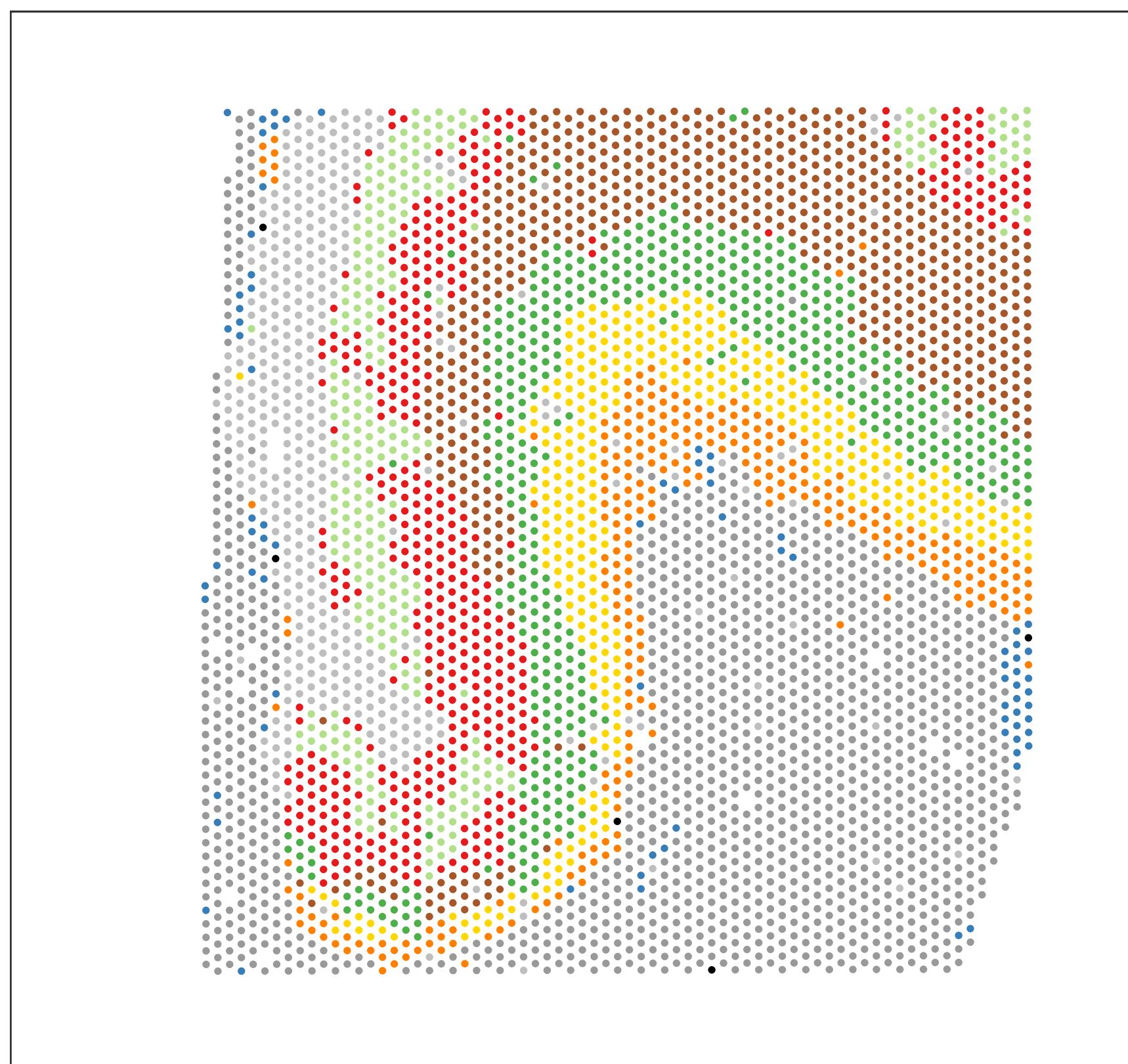
1  
2  
3  
4  
5  
6  
7  
8  
9  
10



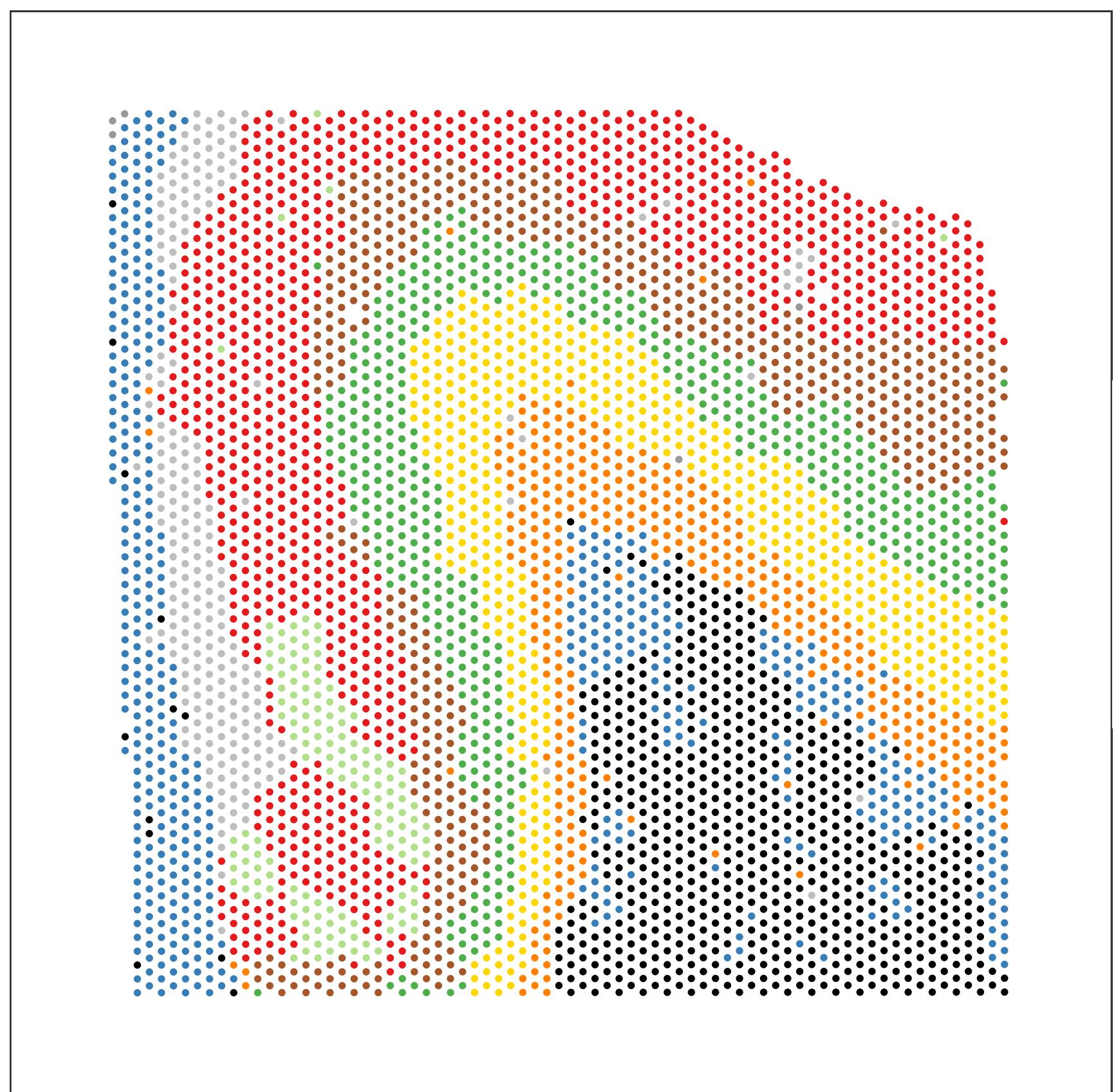
V12N28–331\_B1

V12N28-332\_B1

V12N28-332\_B1\_Br6432



V12J03-002\_C1\_Br6471

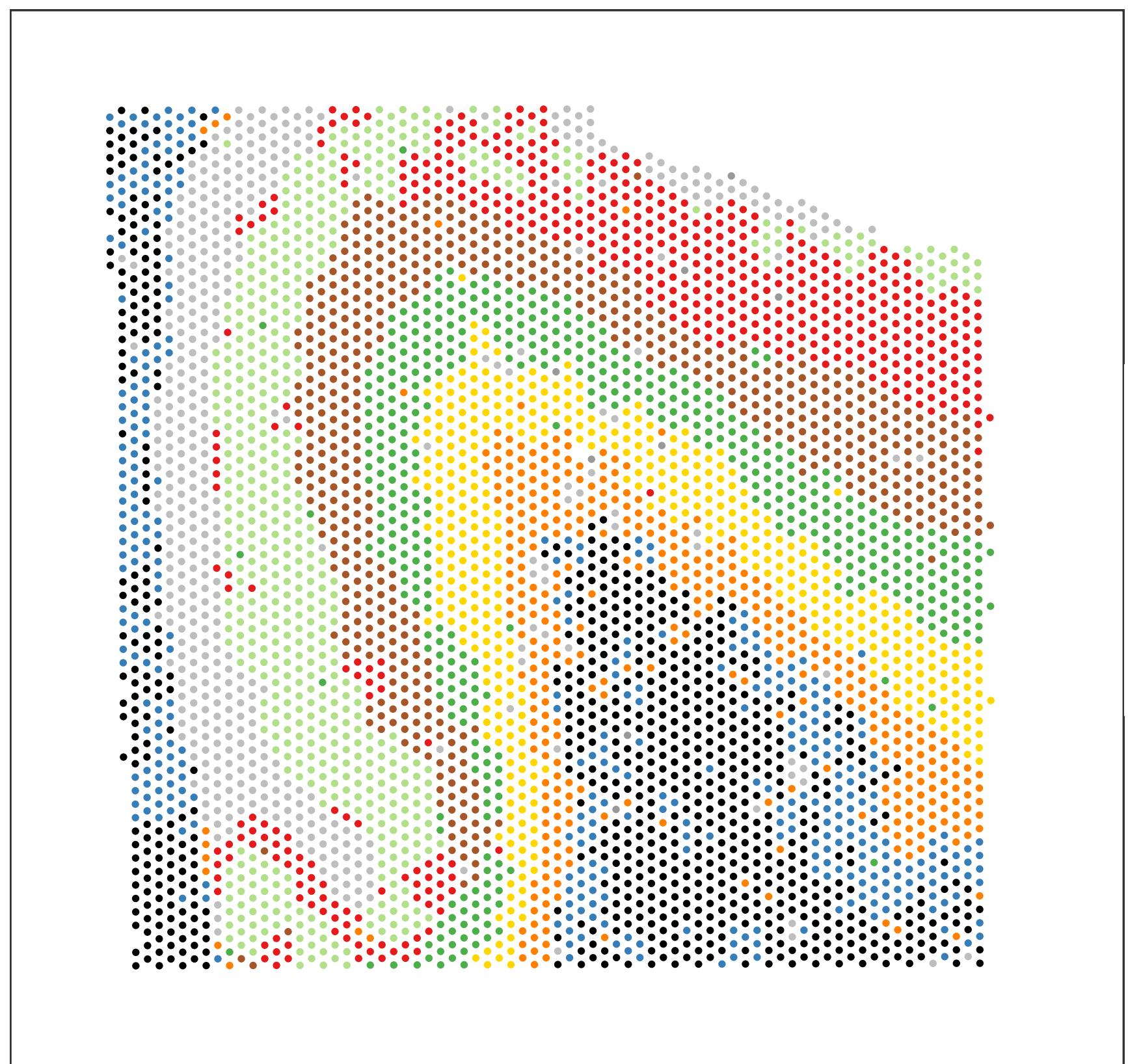


V12J03-002\_C1

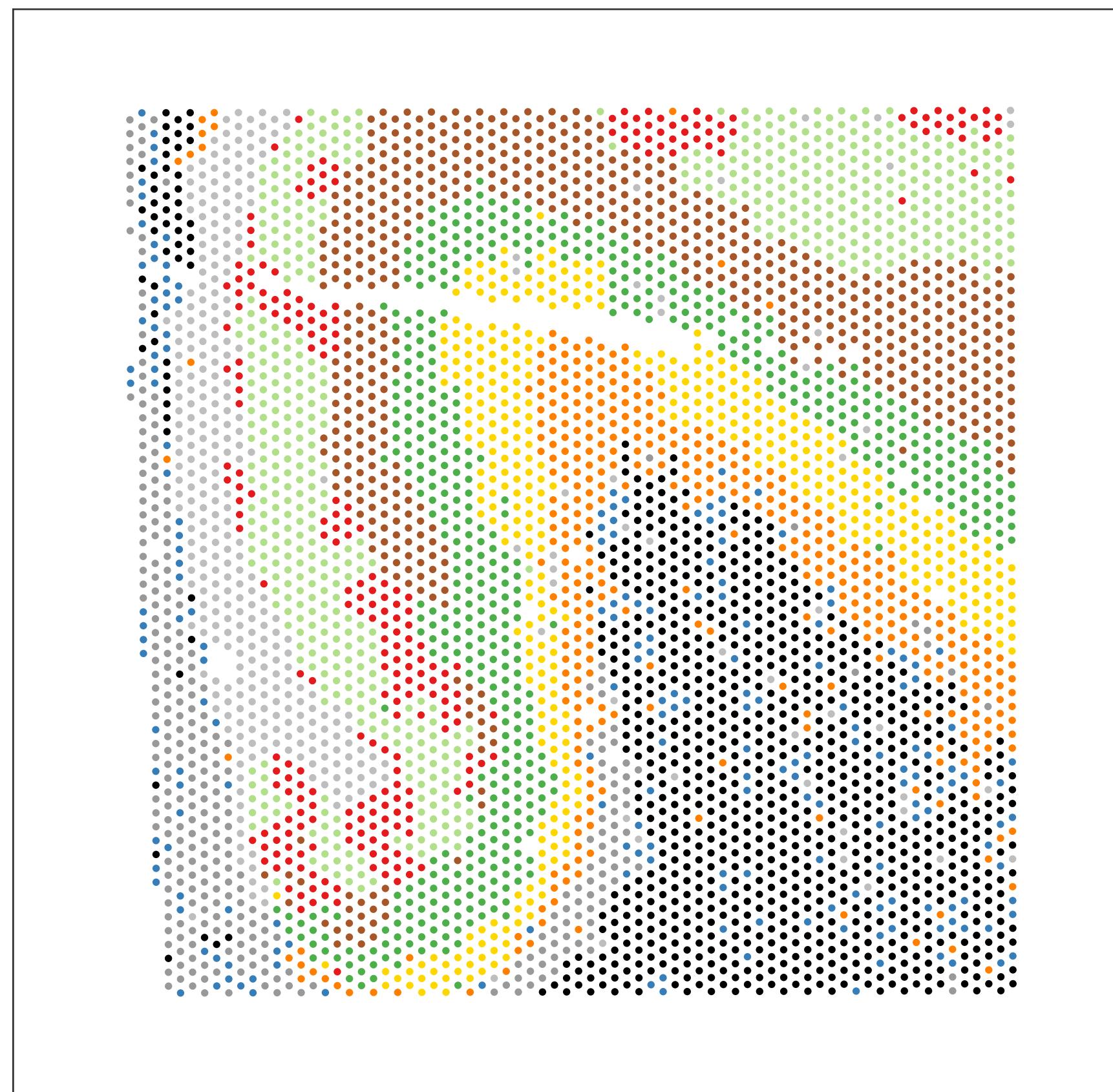


V12N28-331\_C1

V12N28-331\_C1\_Br6471



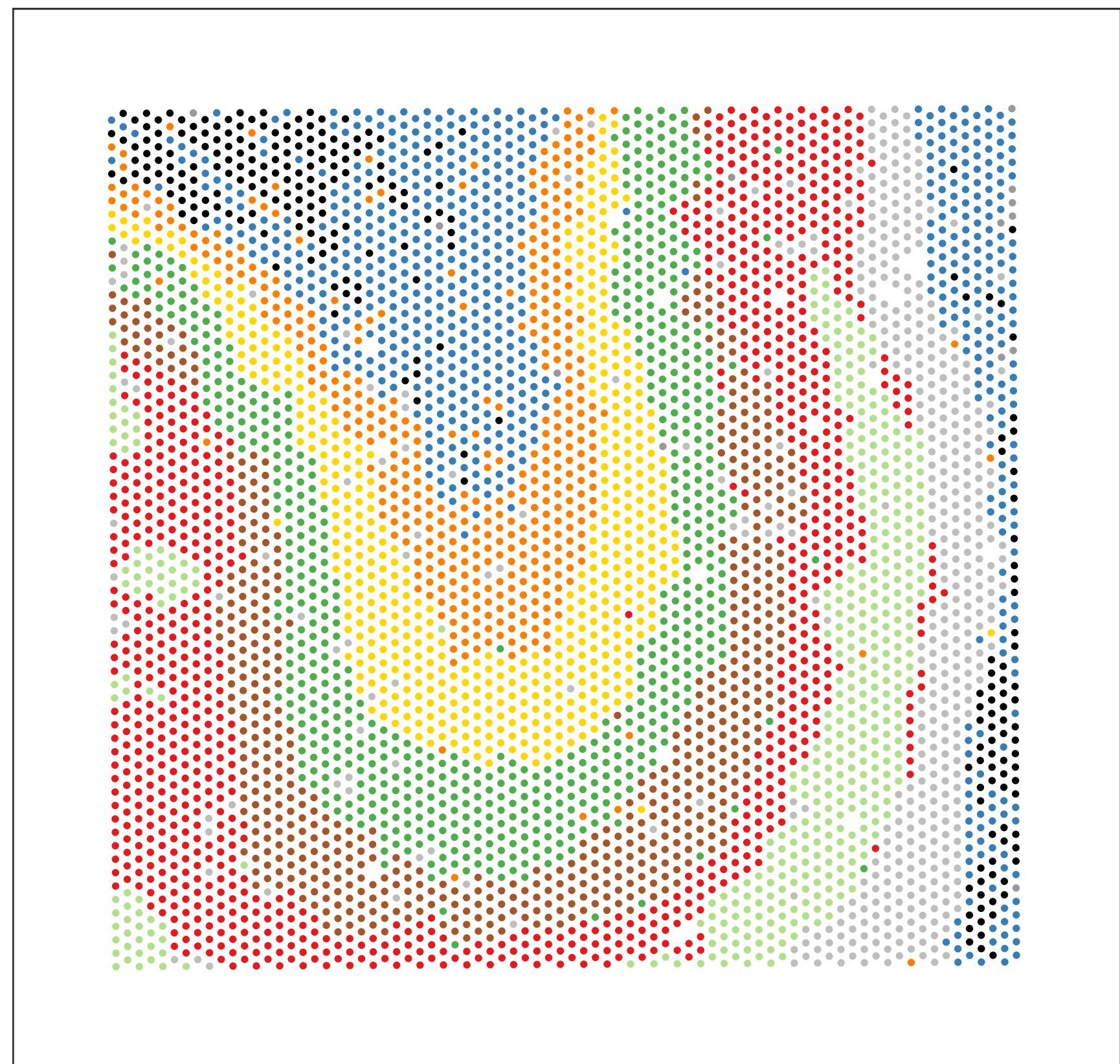
V12N28–332\_C1\_Br6471



V12N28–332\_C1



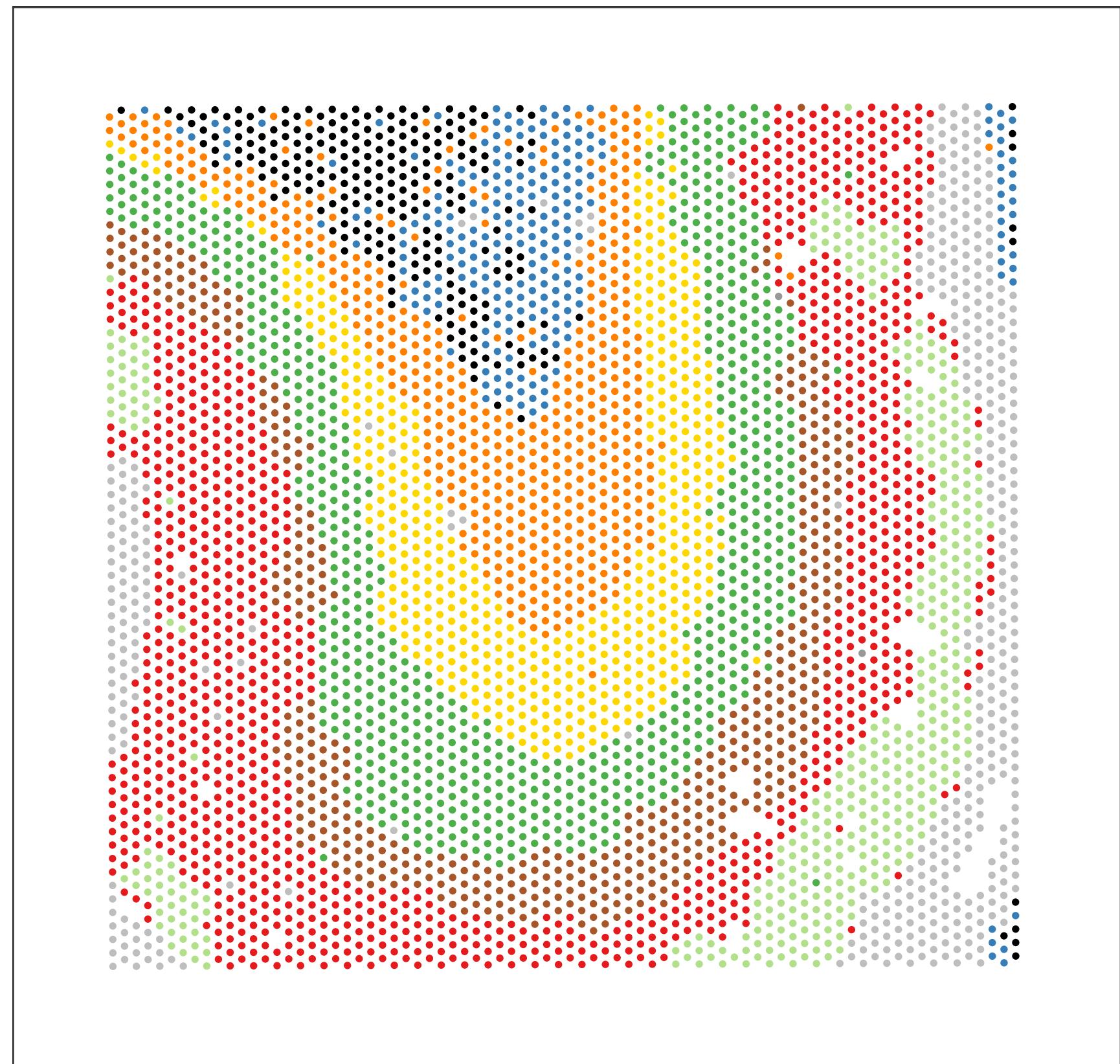
V12N28–331\_D1\_Br6522



V12N28–331\_D1



V12N28–332\_D1\_Br6522



V12N28–332\_D1

