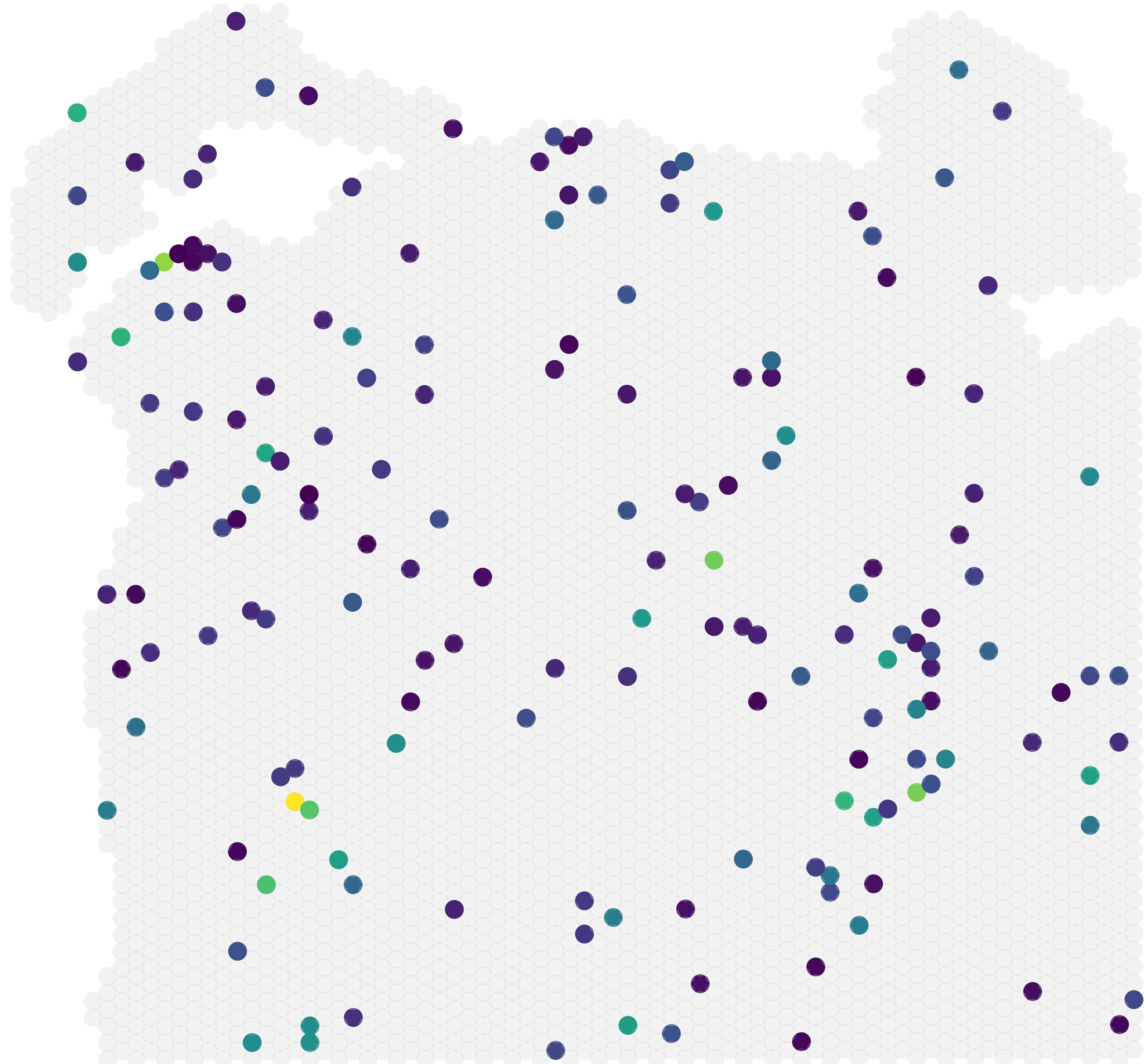
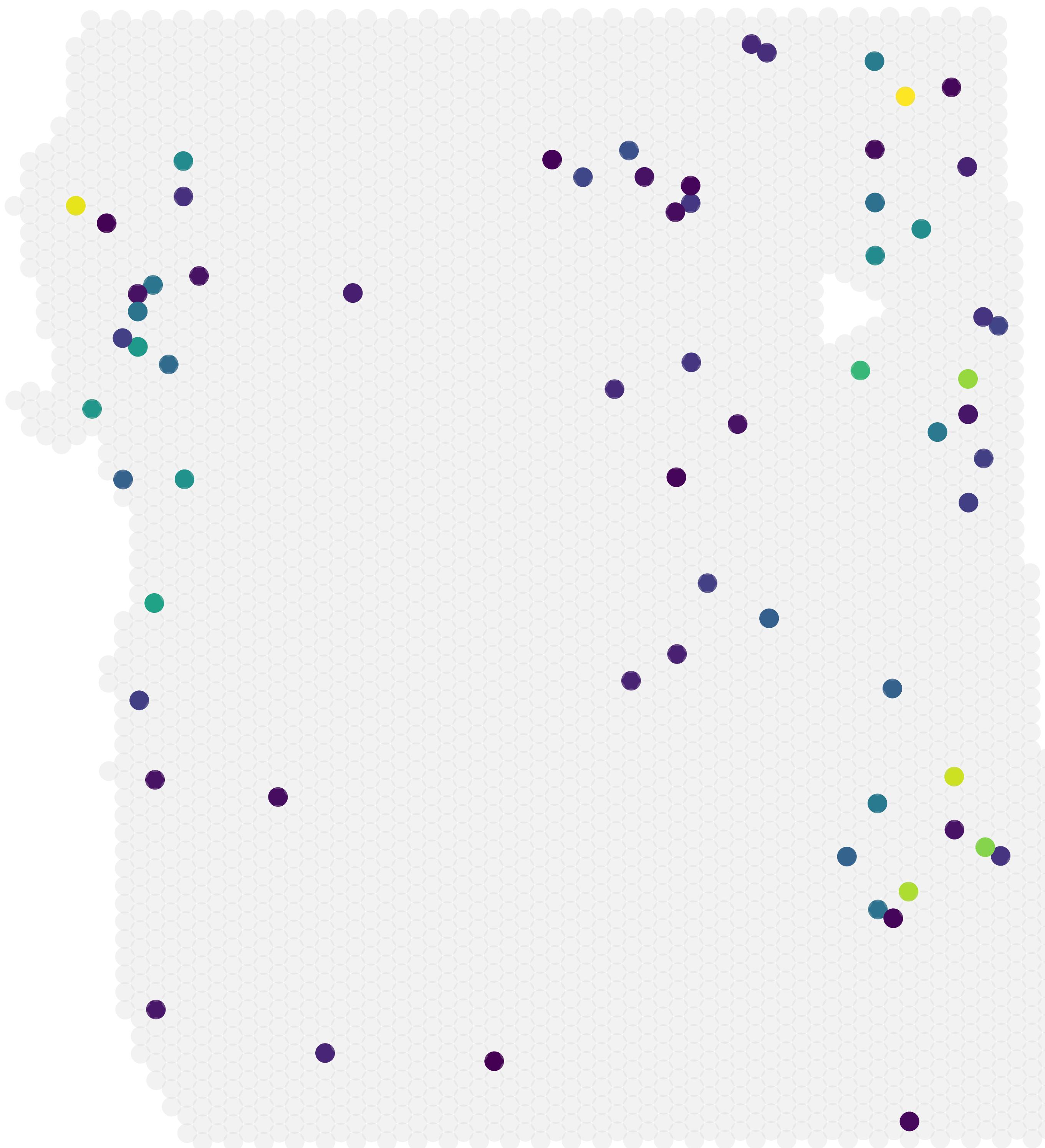


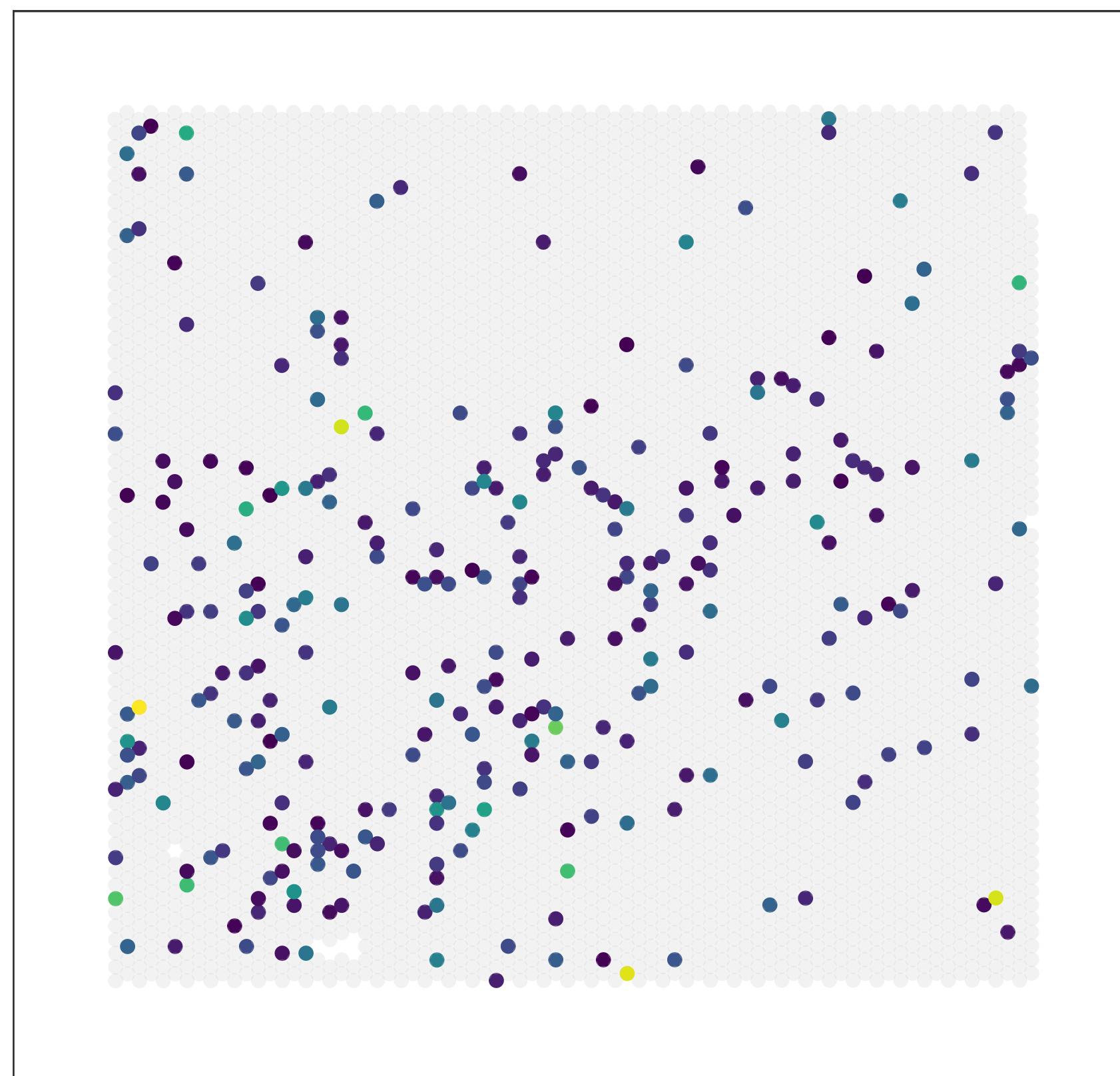
min > 0  
100  
75  
50  
25



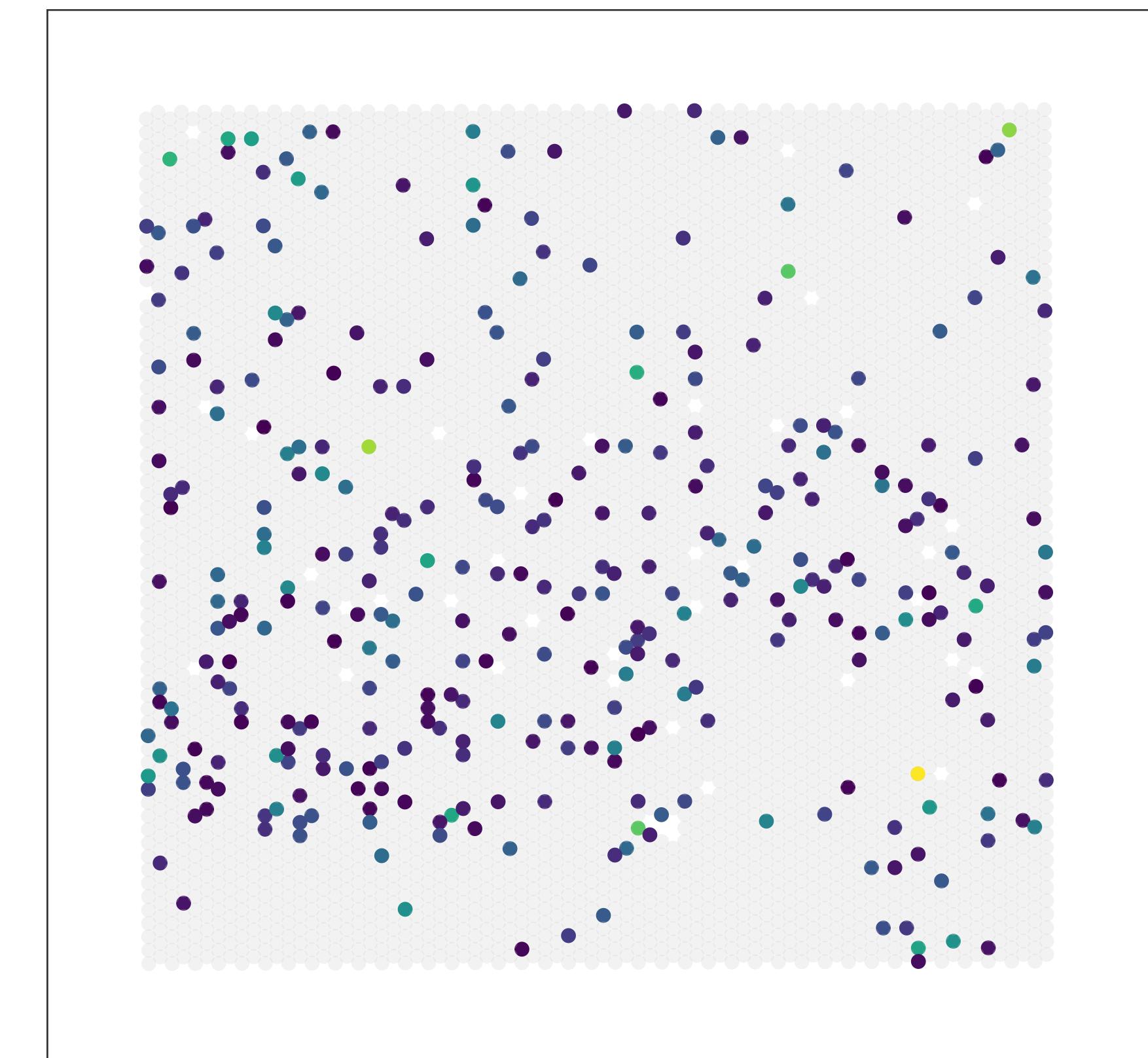
min > 0  
150  
100  
50



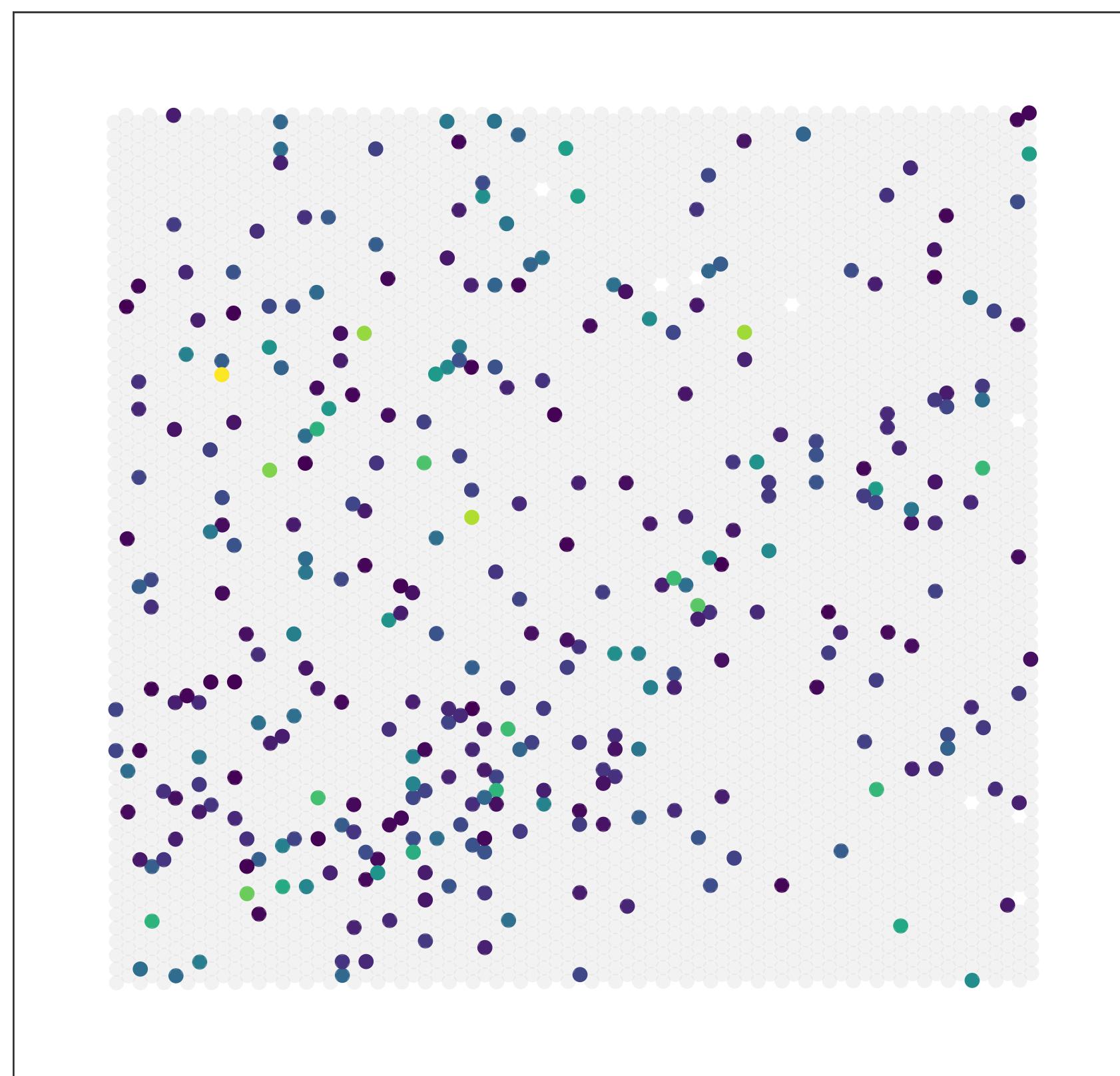
V12J03–002\_A1 NMF\_61



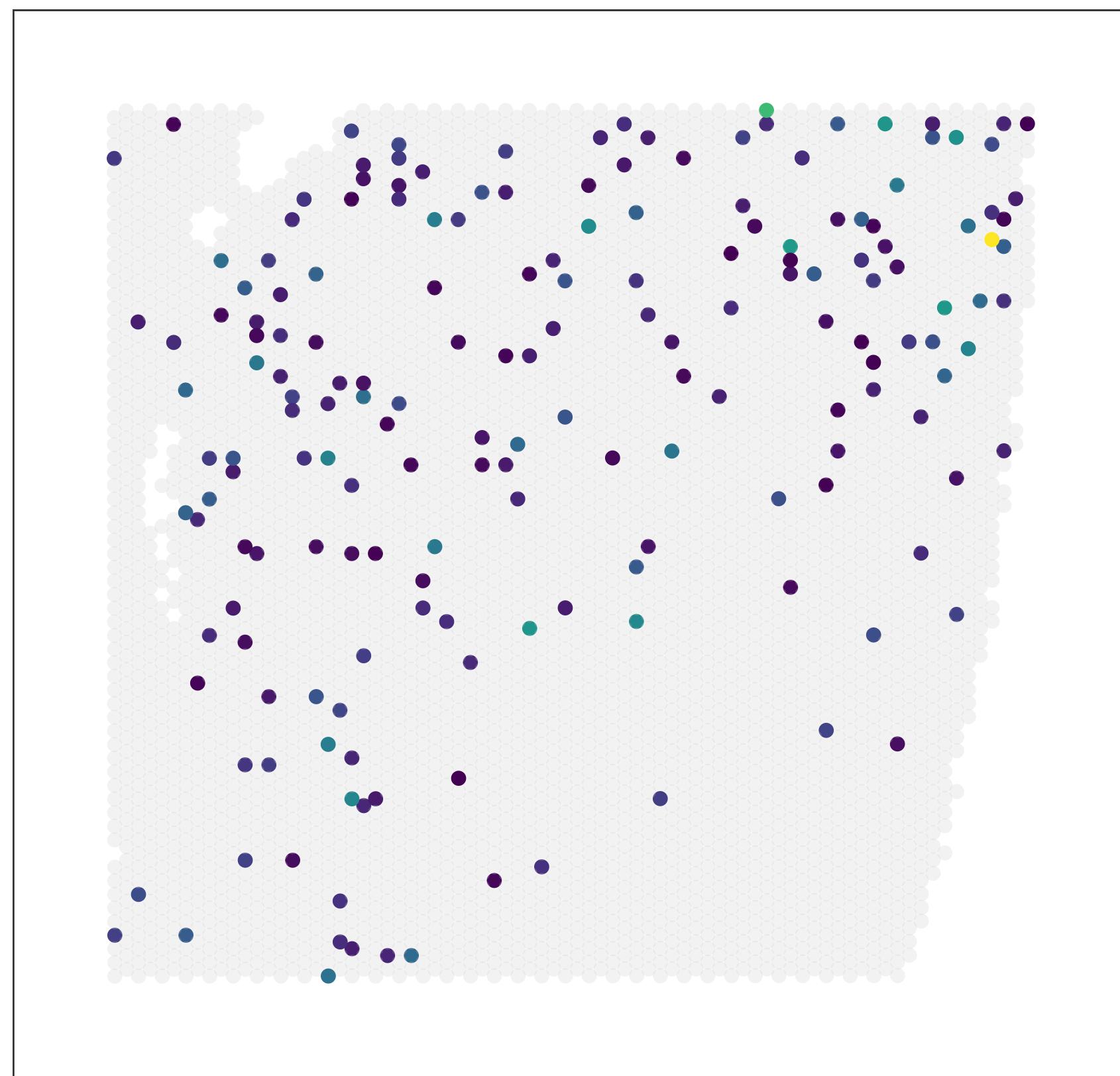
V12N28–331\_A1 NMF\_61



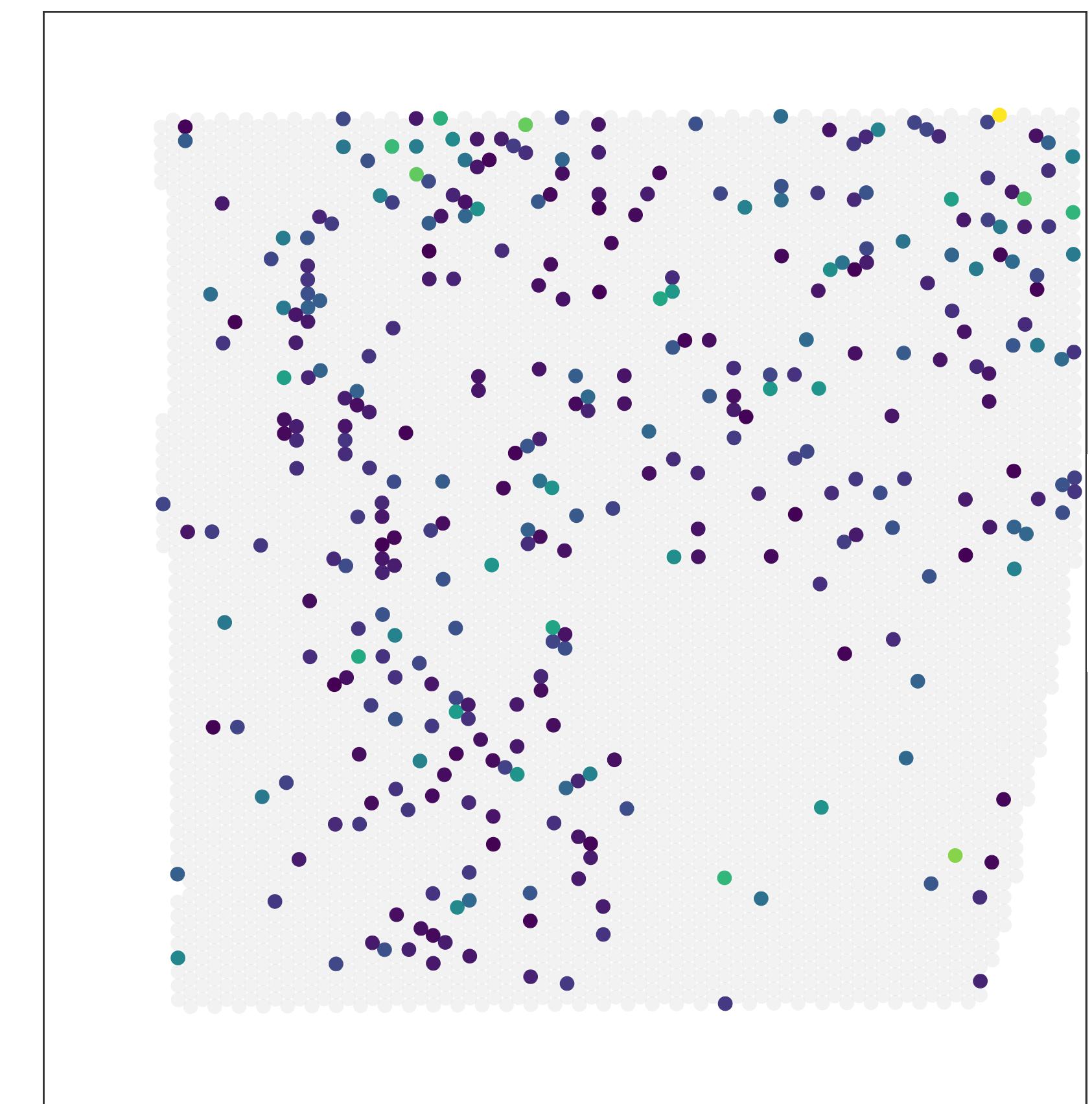
V12N28–332\_A1 NMF\_61



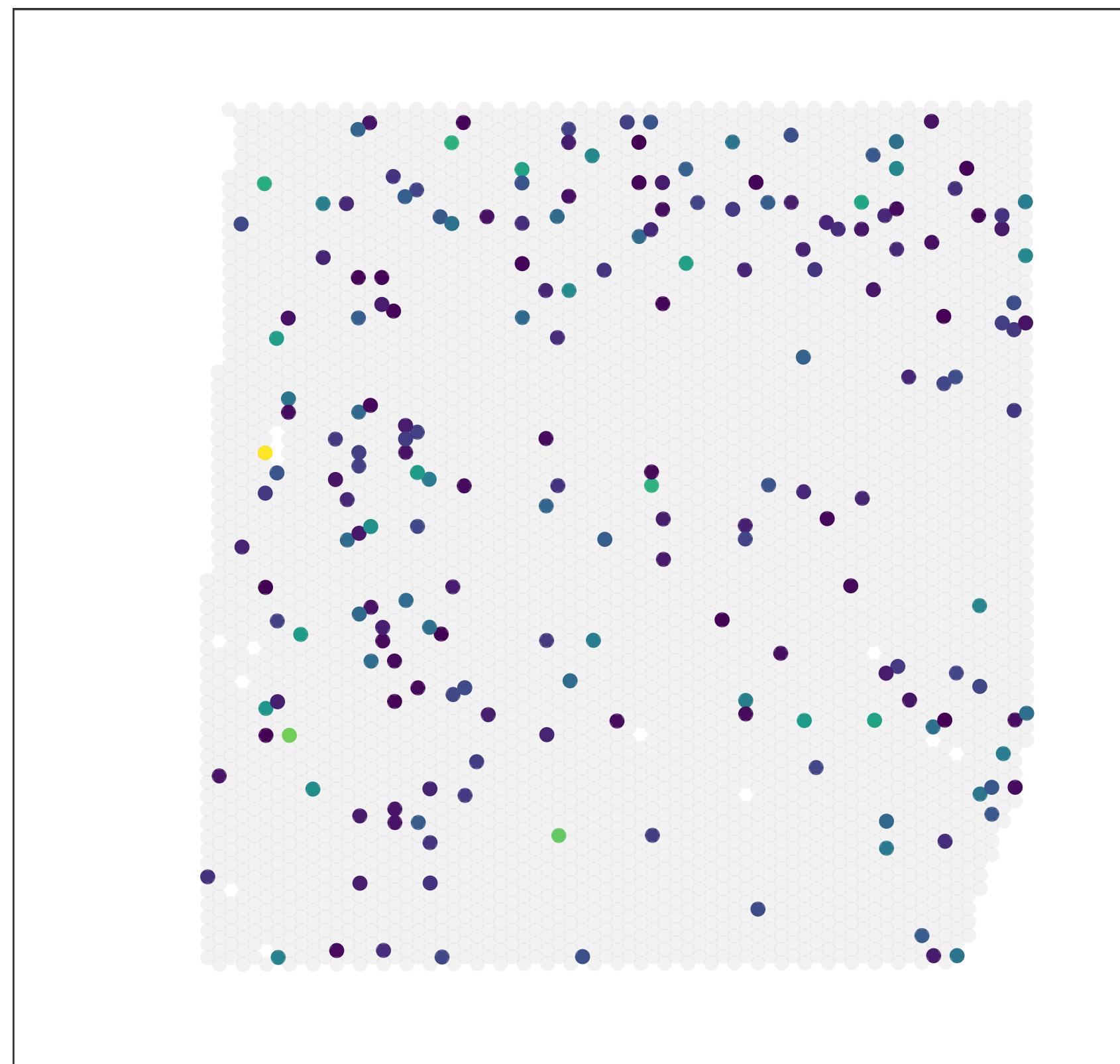
V12J03–002\_B1 NMF\_61



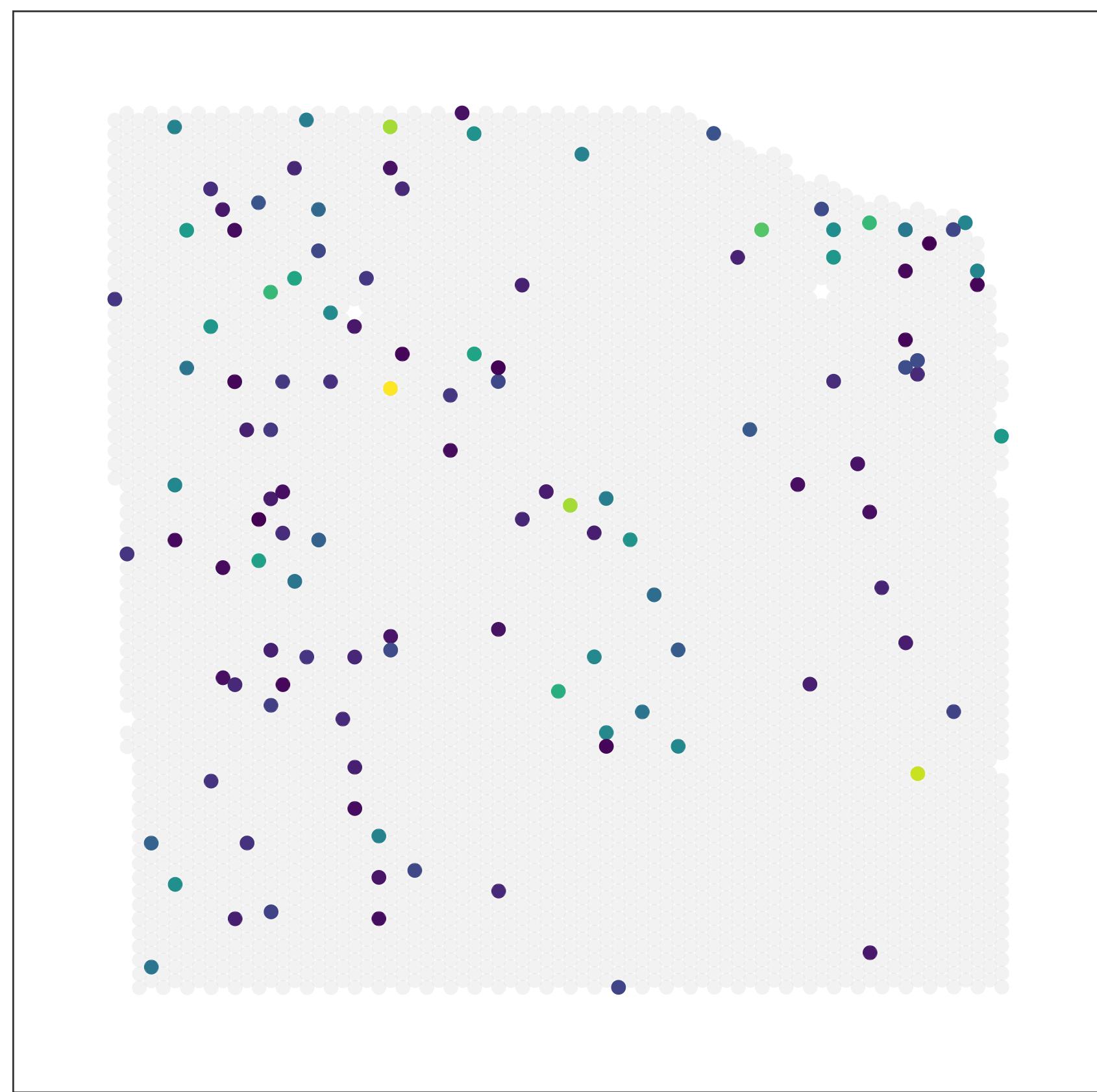
V12N28–331\_B1 NMF\_61



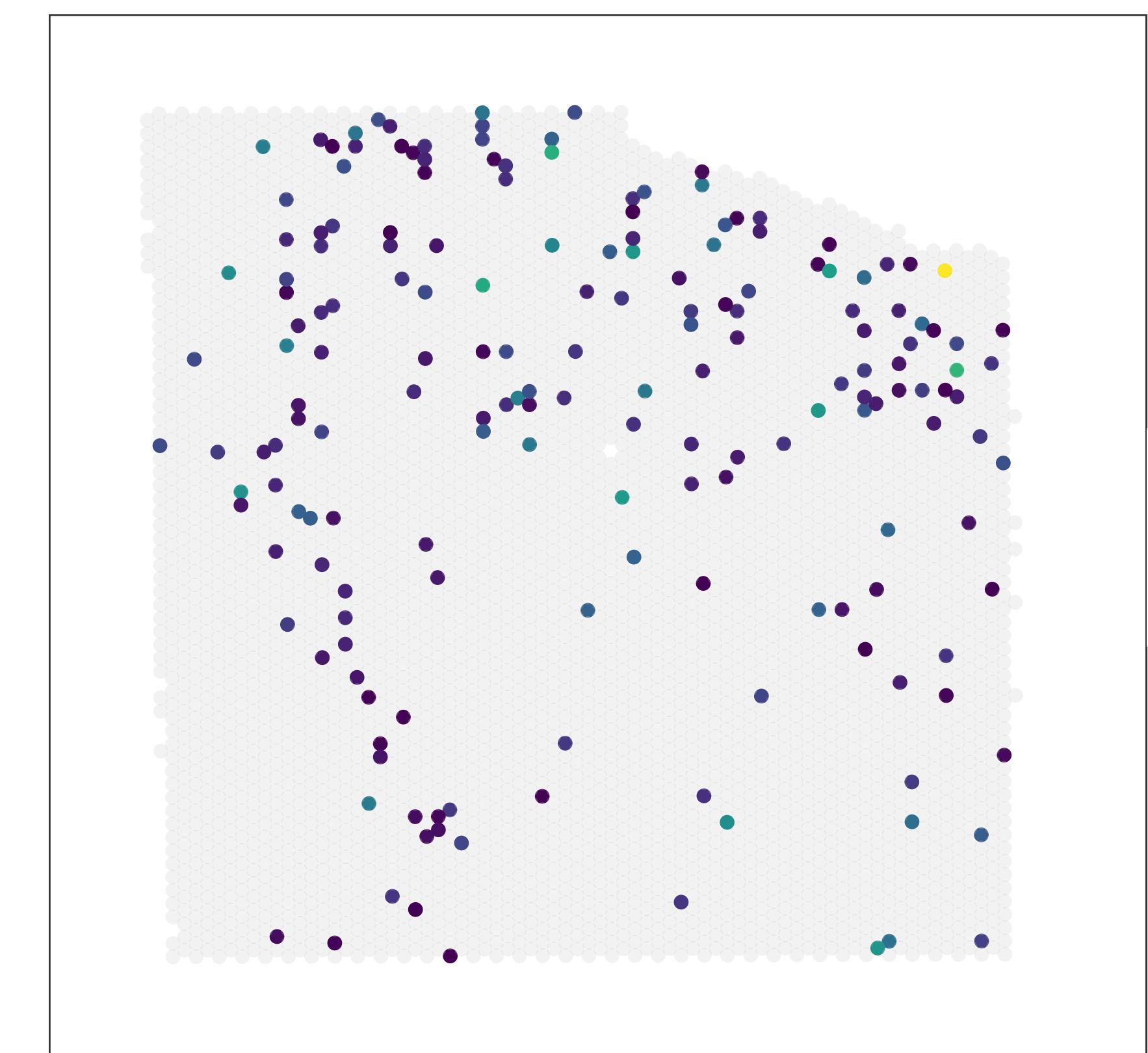
V12N28–332\_B1 NMF\_61



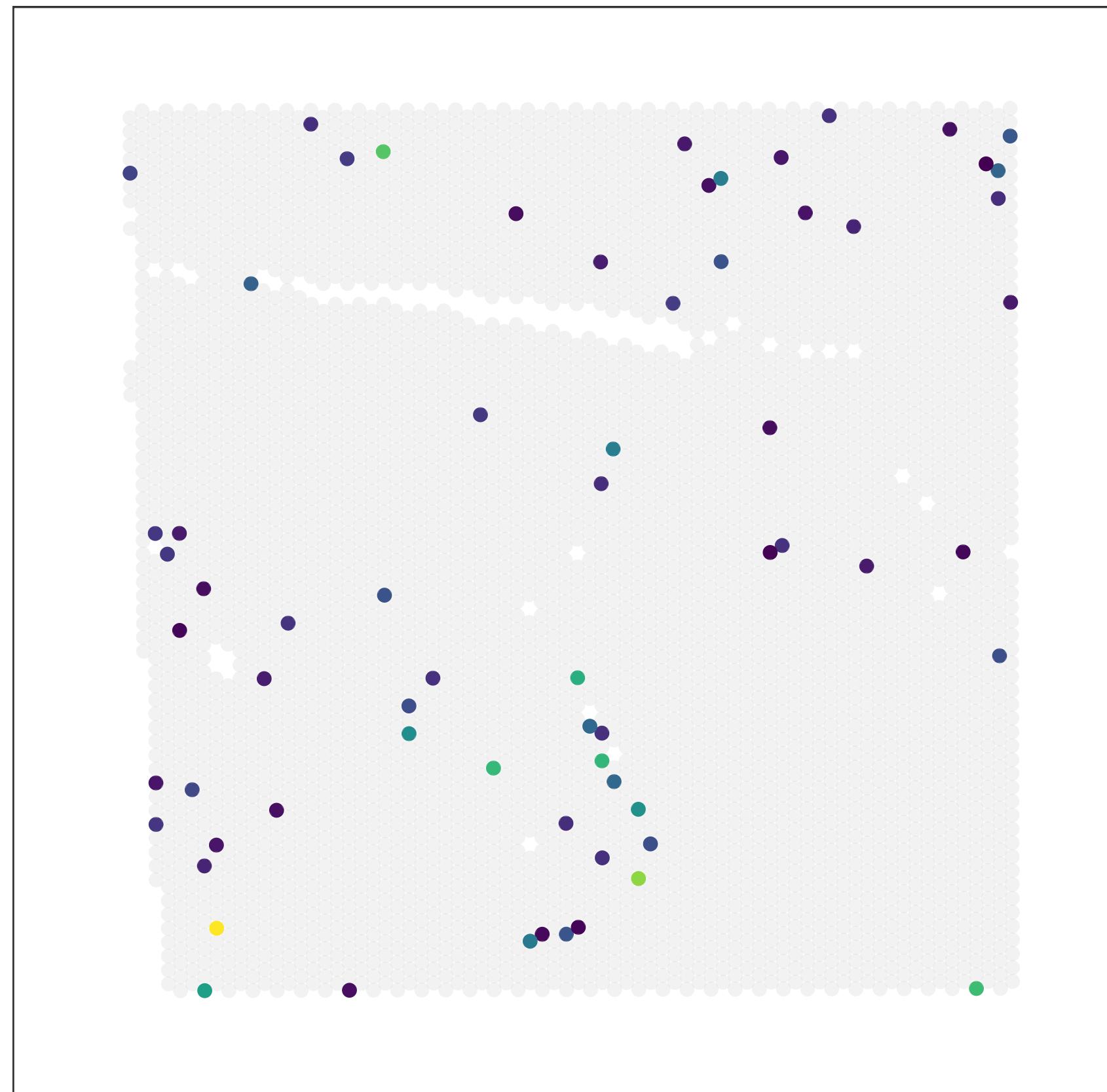
V12J03–002\_C1 NMF\_61



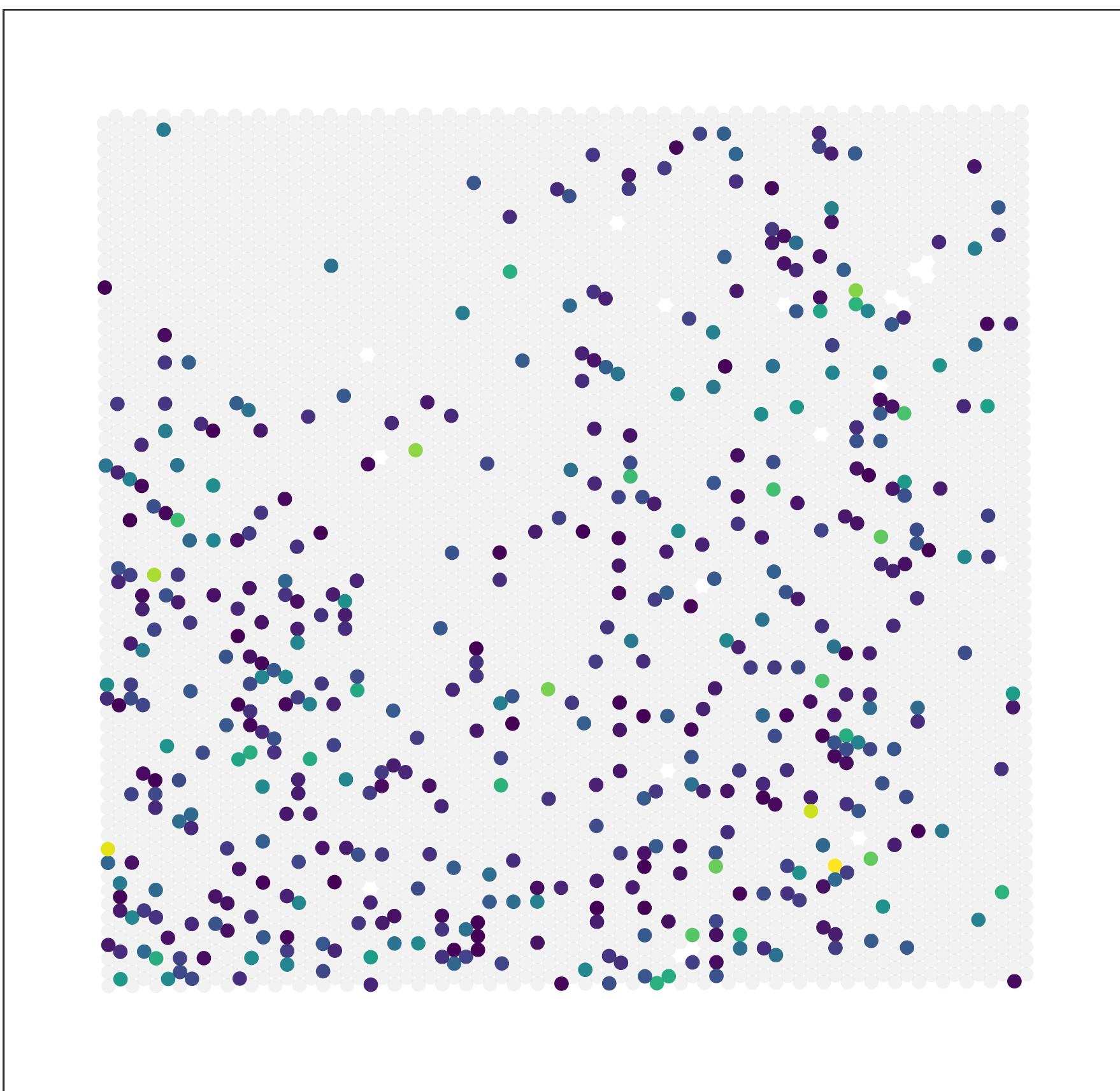
V12N28–331\_C1 NMF\_61



V12N28–332\_C1 NMF\_61



V12N28-331\_D1 NMF\_61



V12N28-332\_D1 NMF\_61

