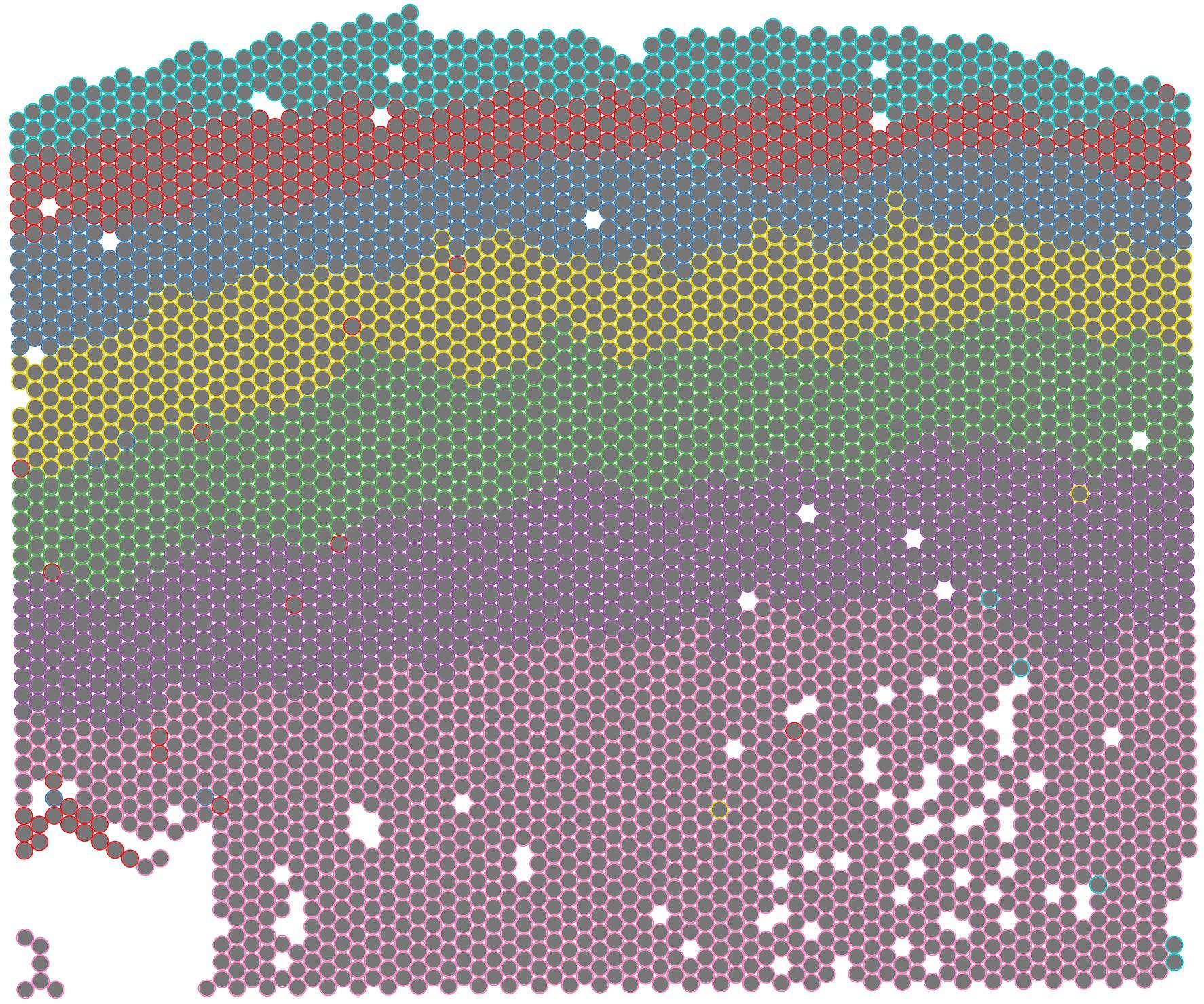
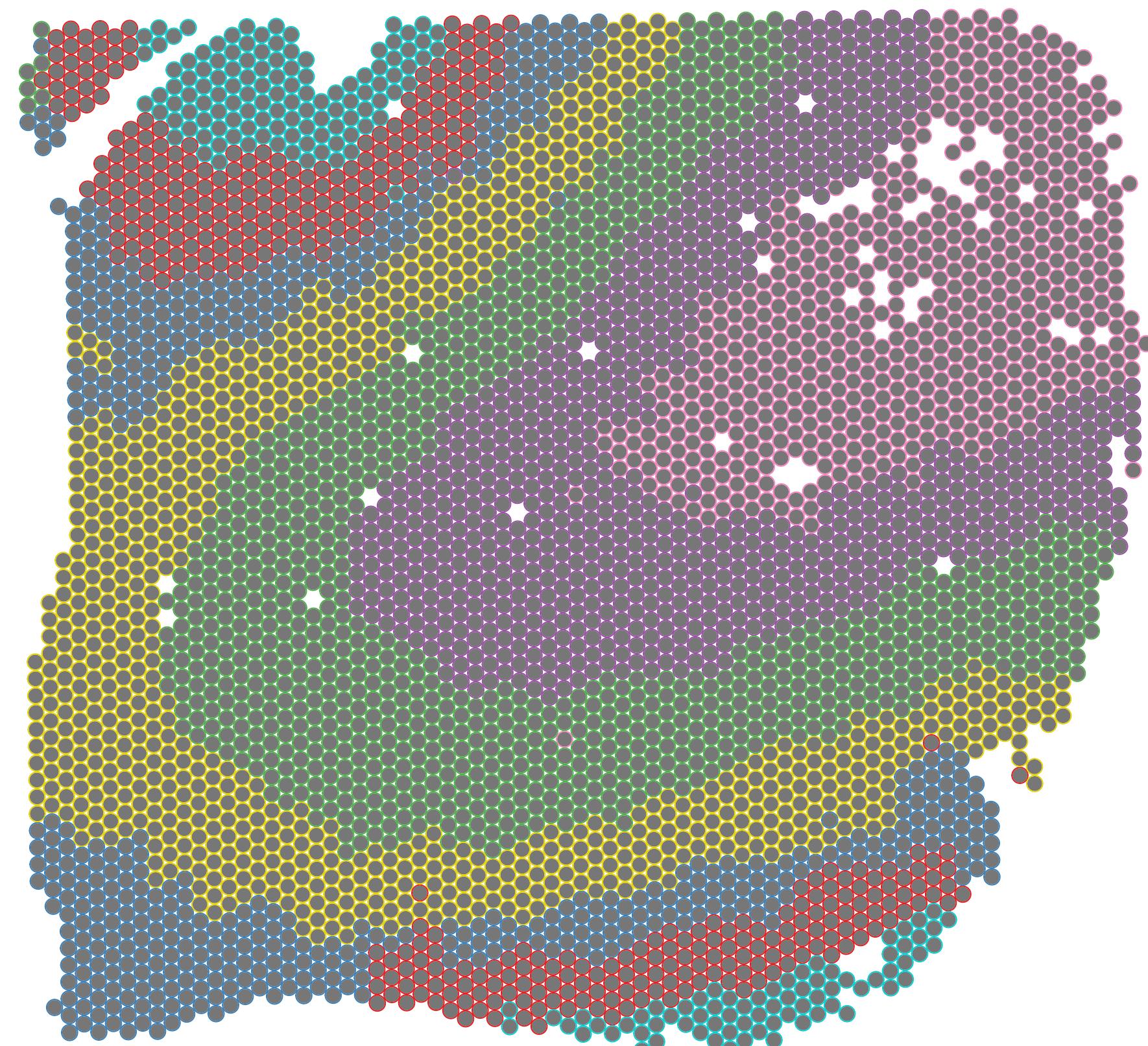


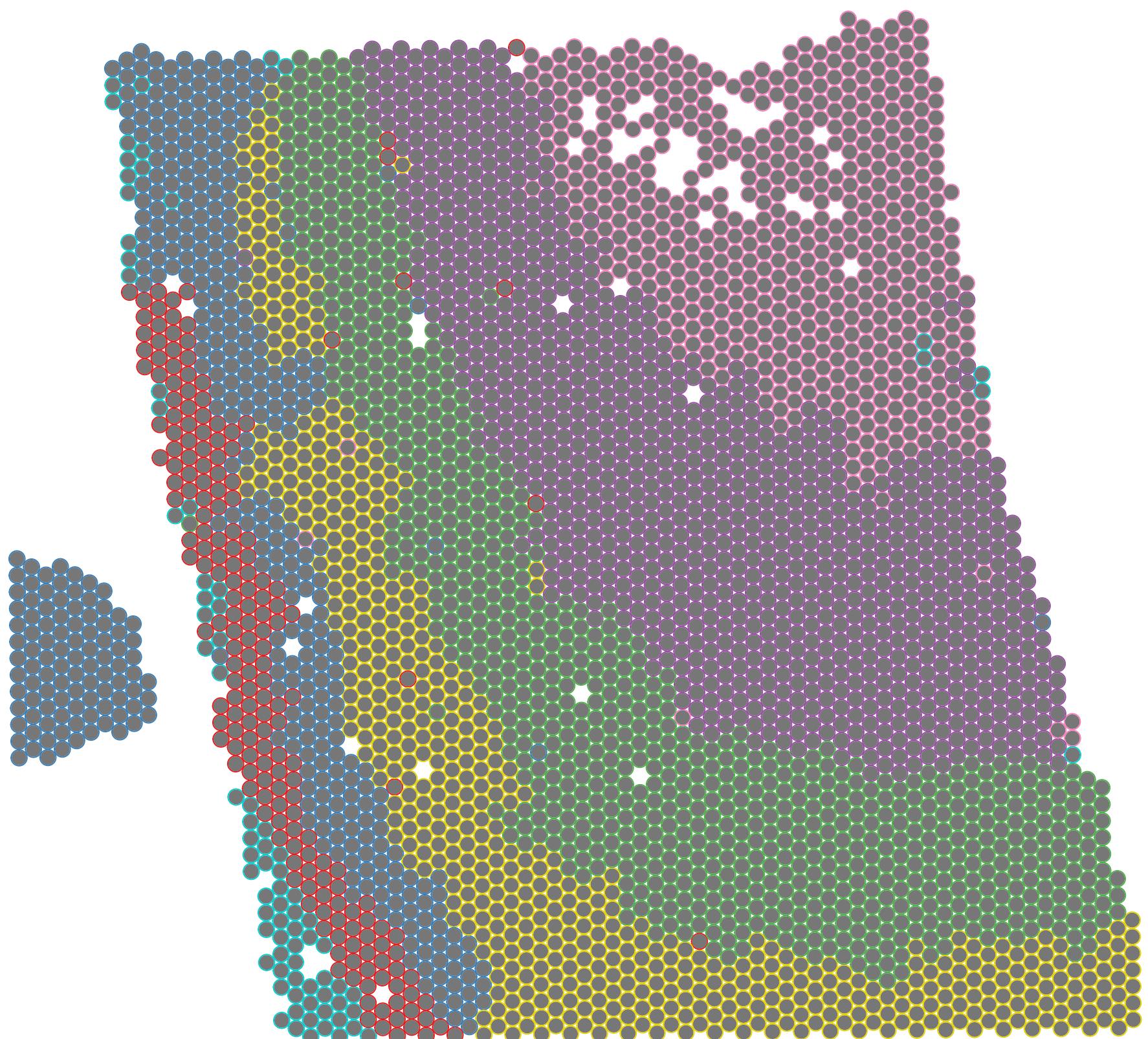
Sample Br2743_ant



Sample Br2743_mid



Sample Br2743_post



layer
 L1
 L2
 L3
 L4
 L5
 L6
 WM

nmf38
 0

layer
 L1
 L2
 L3
 L4
 L5
 L6
 WM

nmf38
 0

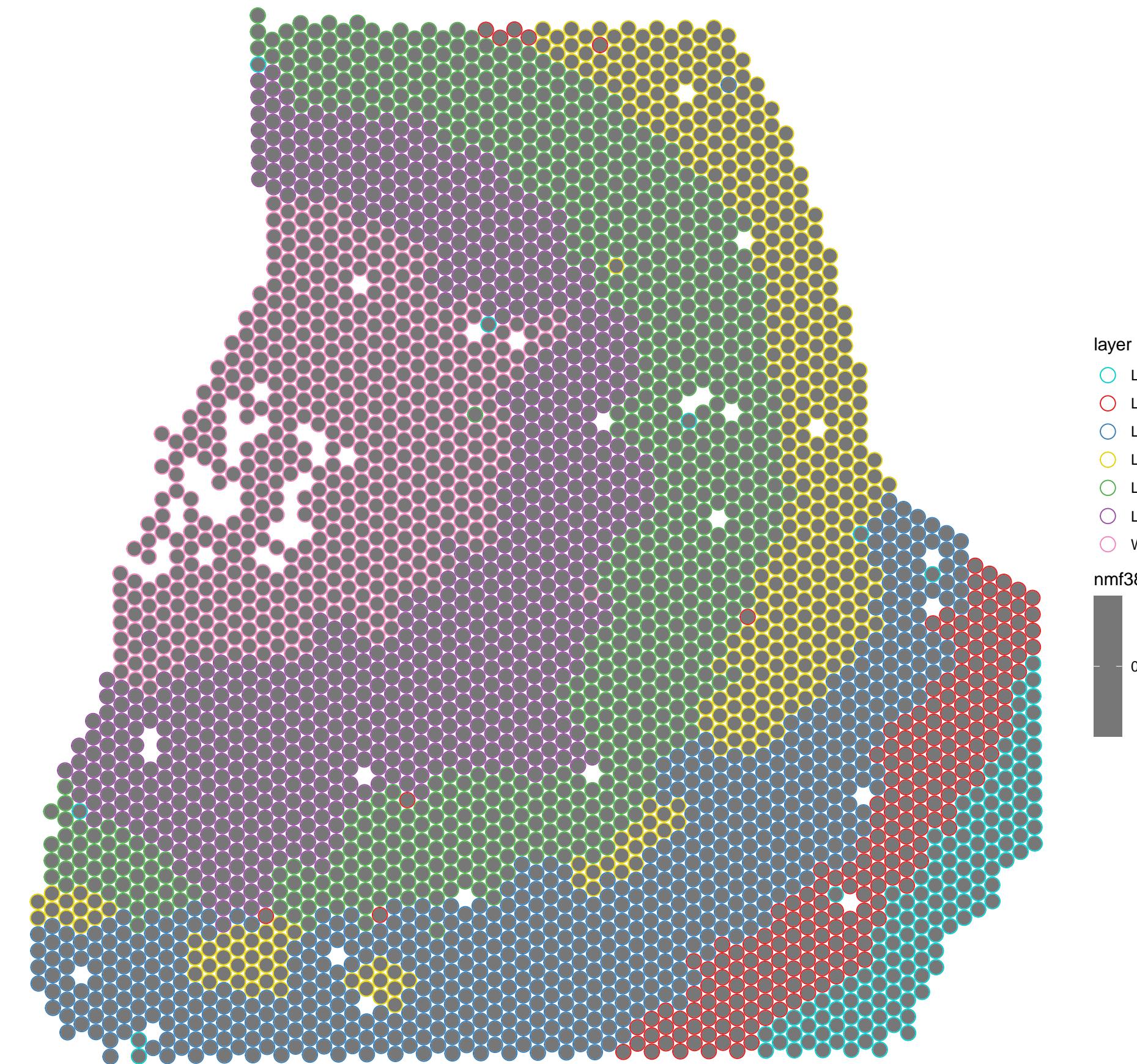
layer
 L1
 L2
 L3
 L4
 L5
 L6
 WM

nmf38
 0

Sample Br3942_ant



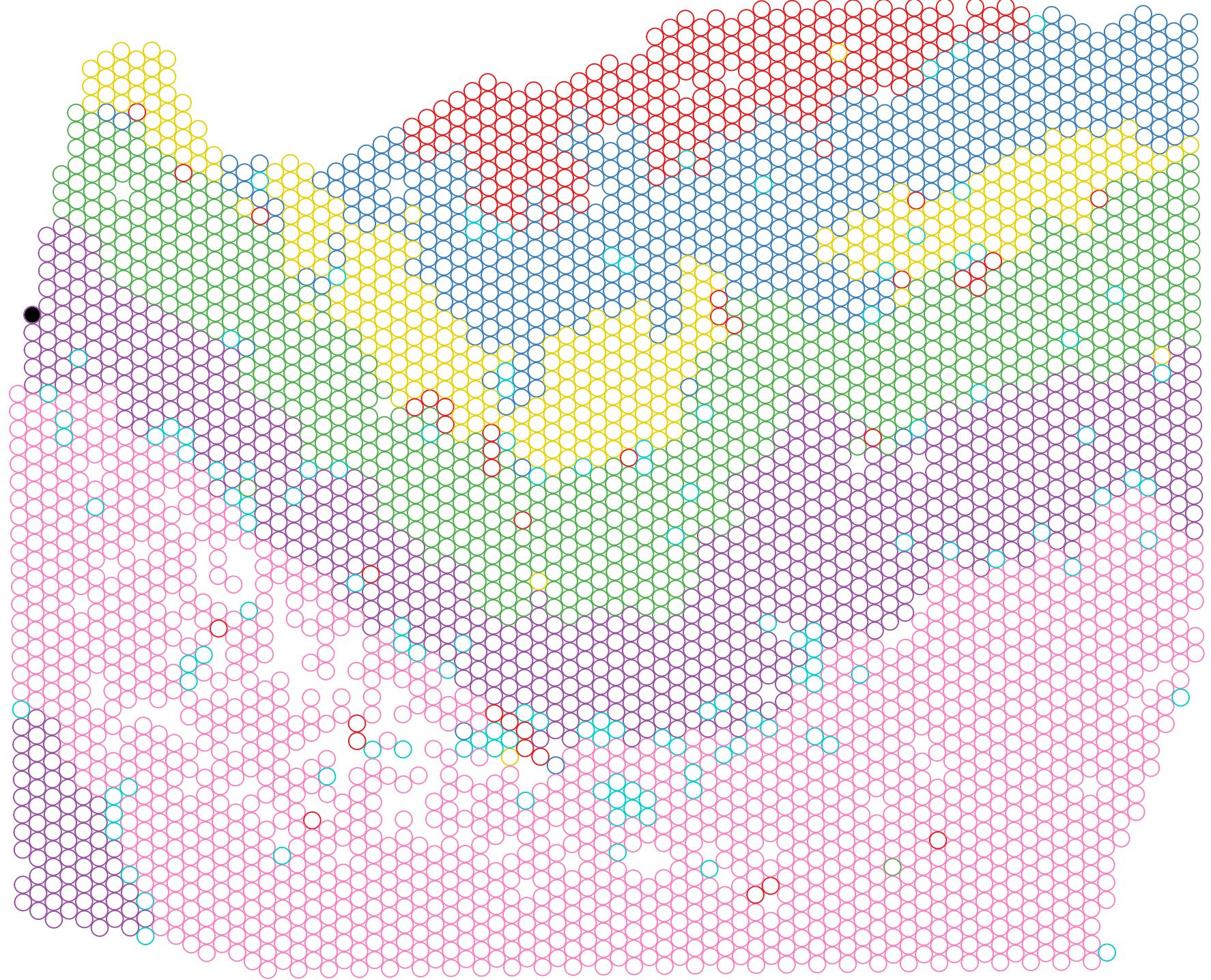
Sample Br3942_mid



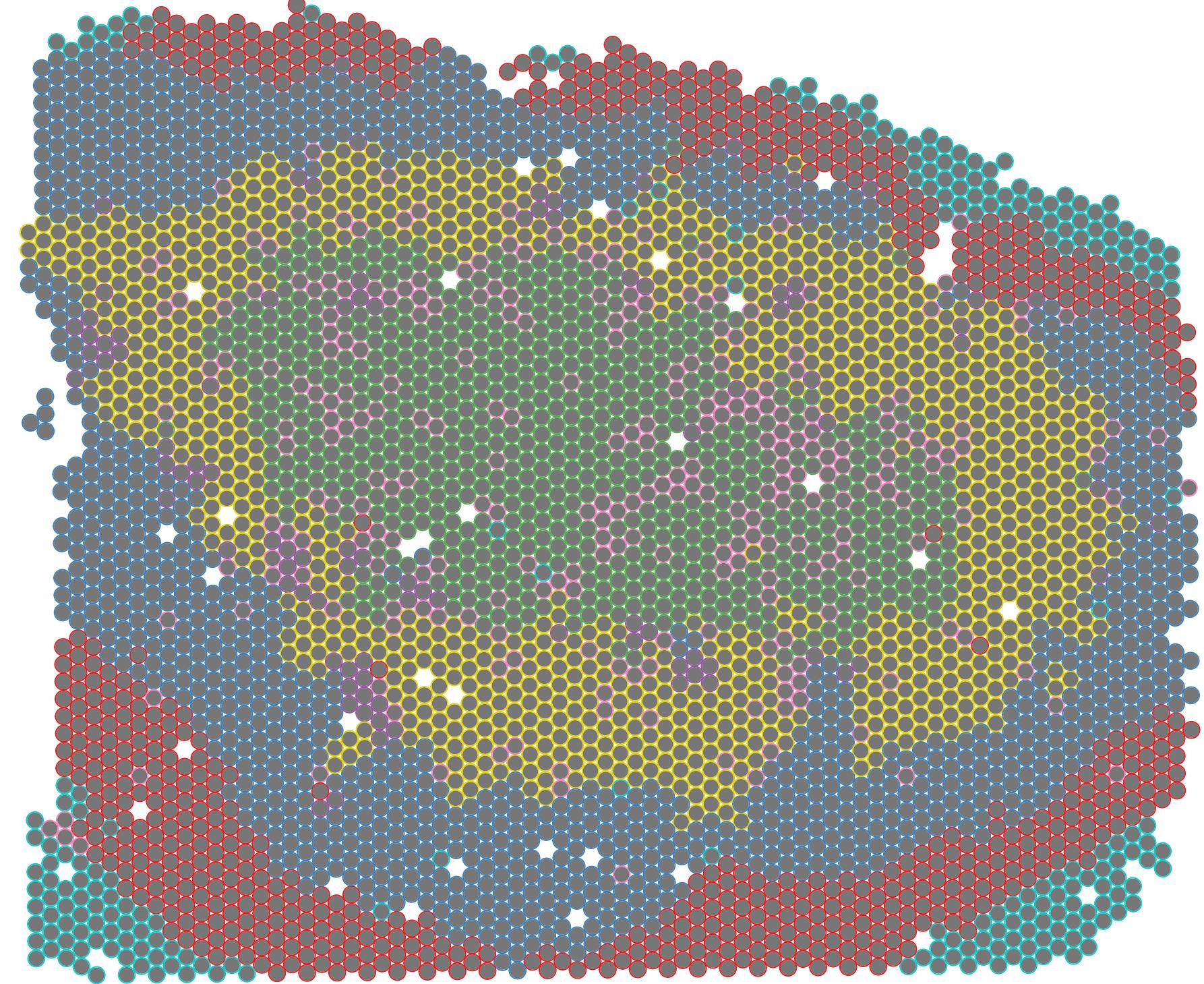
Sample Br3942_post



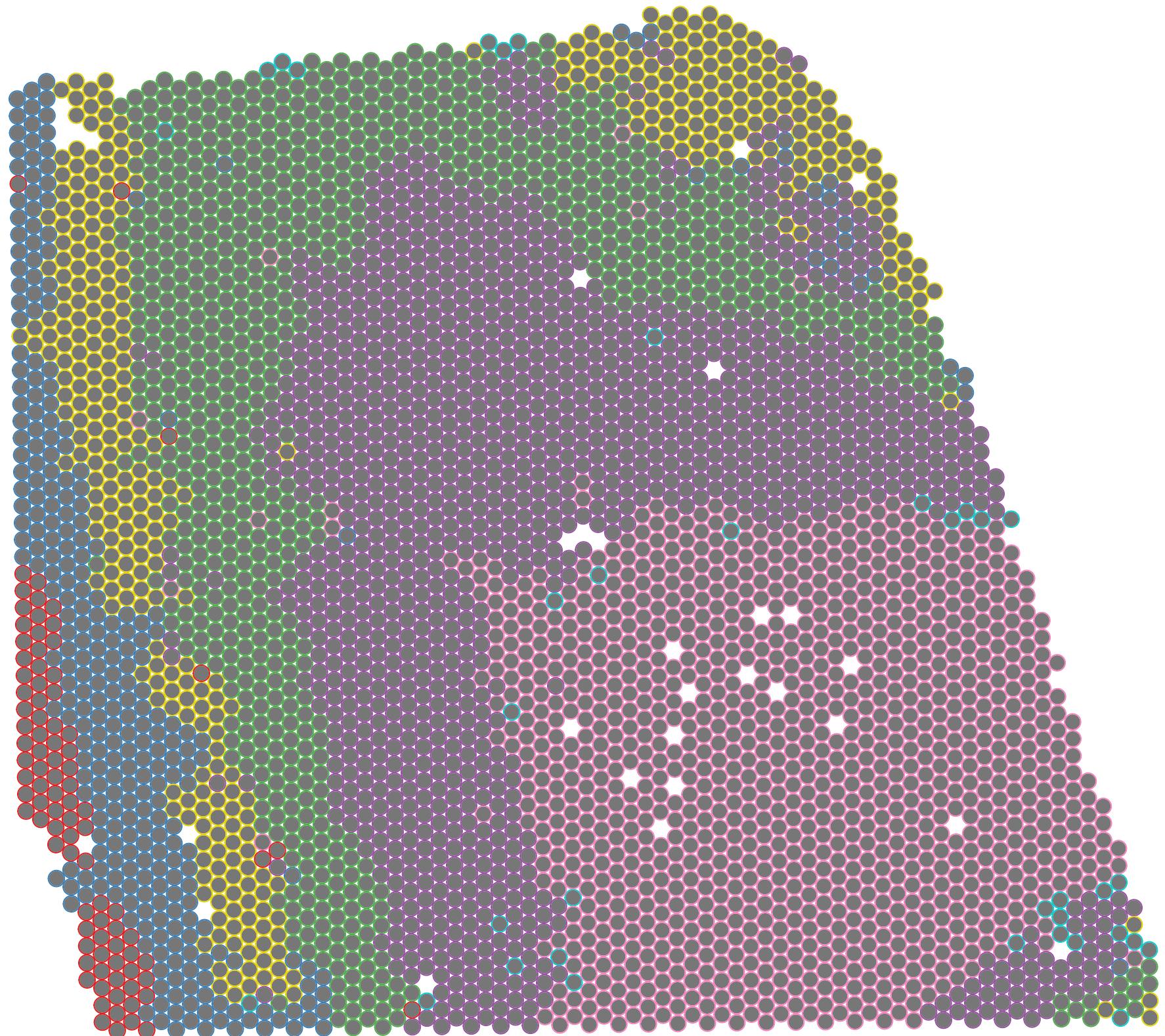
Sample Br6423_ant



Sample Br6423_mid



Sample Br6423_post



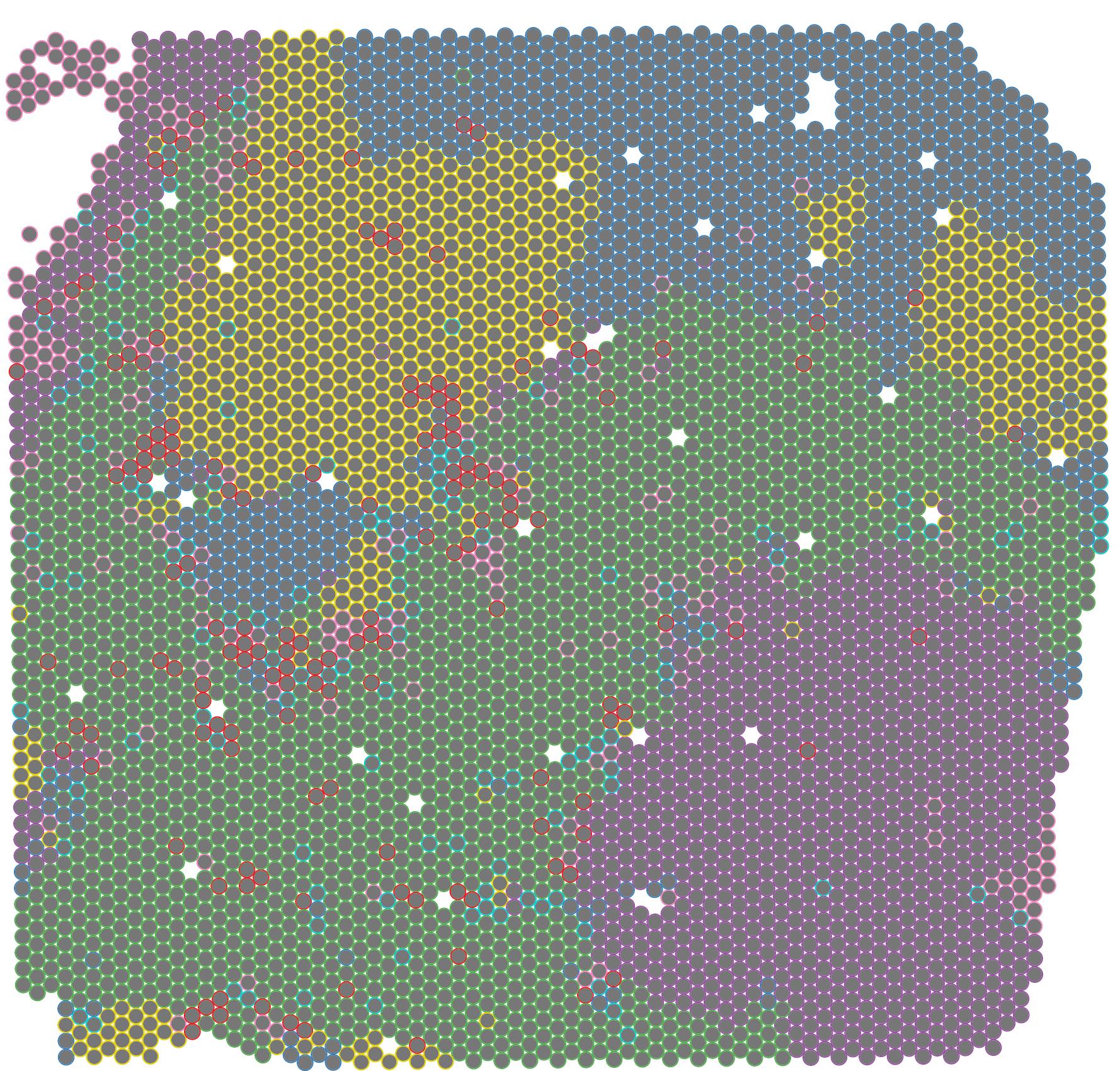
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

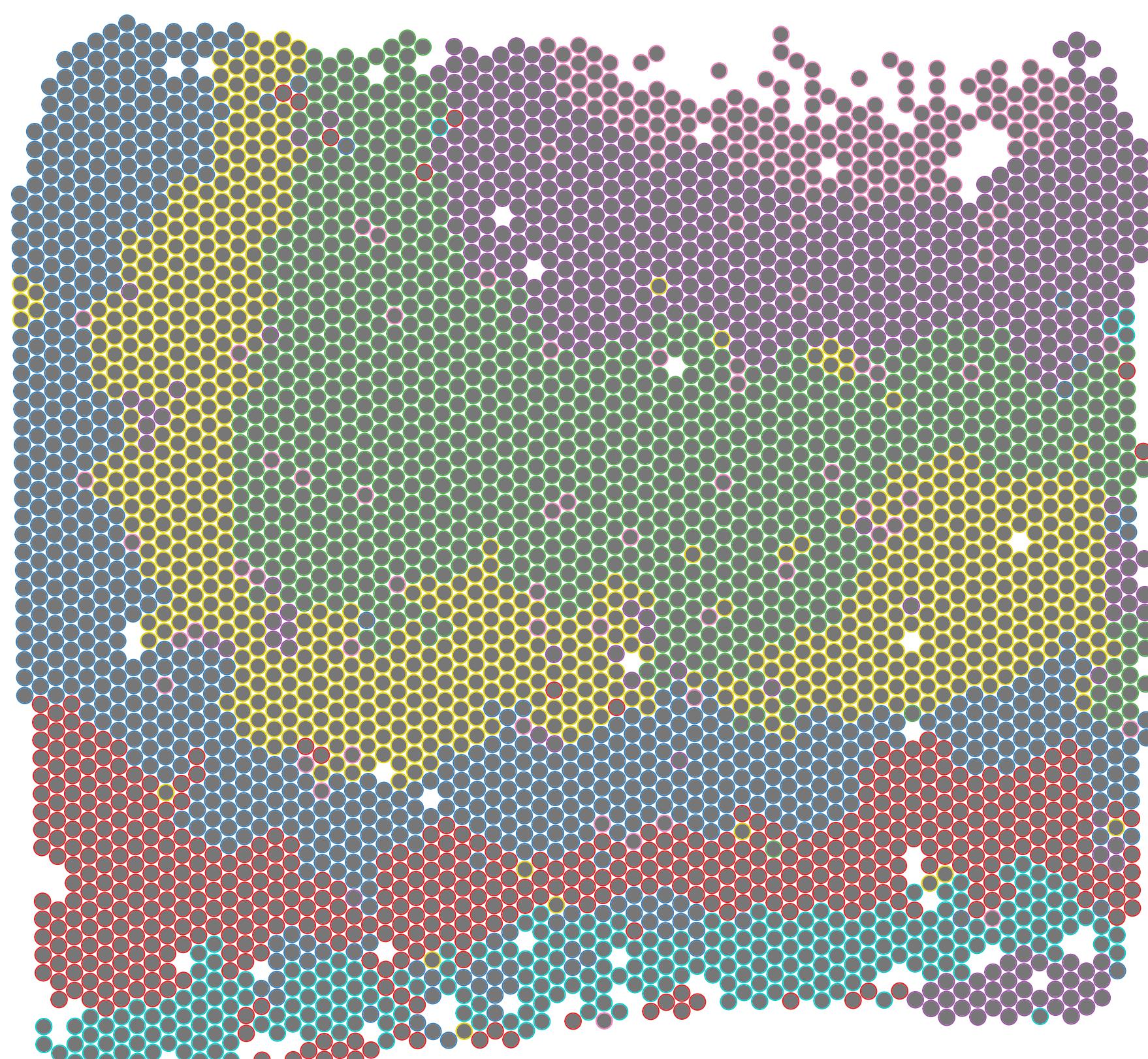
nmf38

0

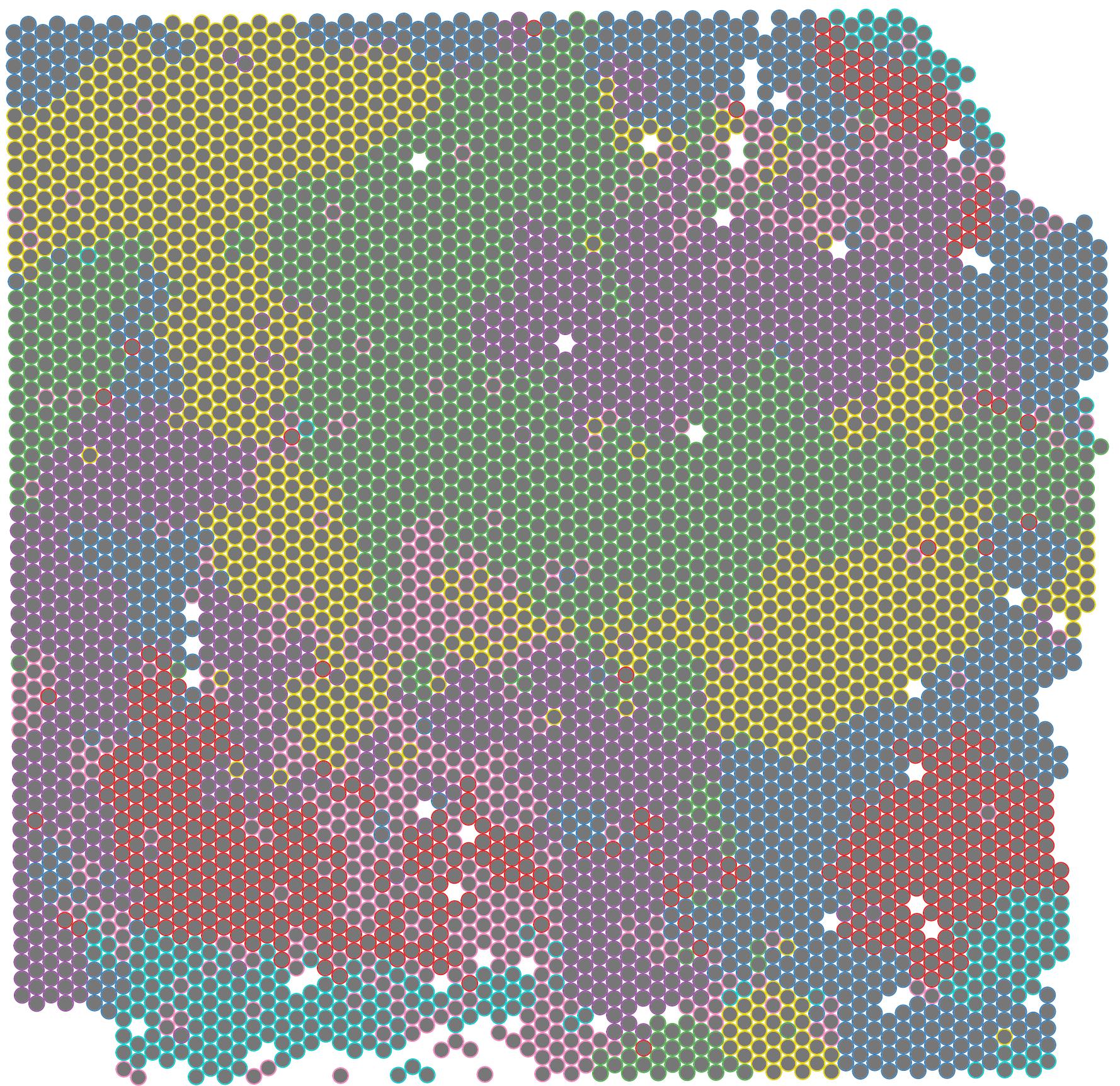
Sample Br8492_ant



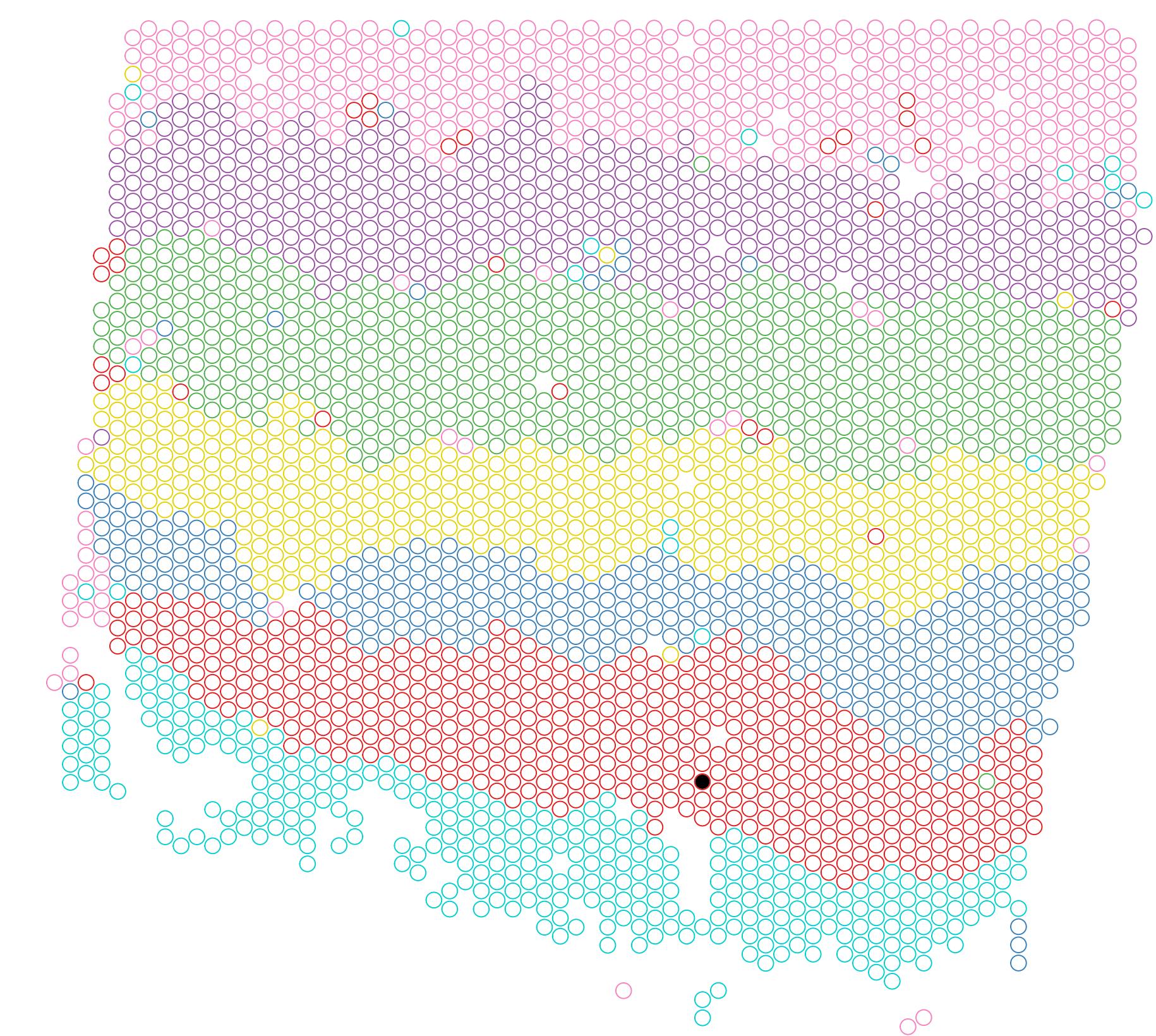
Sample Br8492_mid



Sample Br8492_post



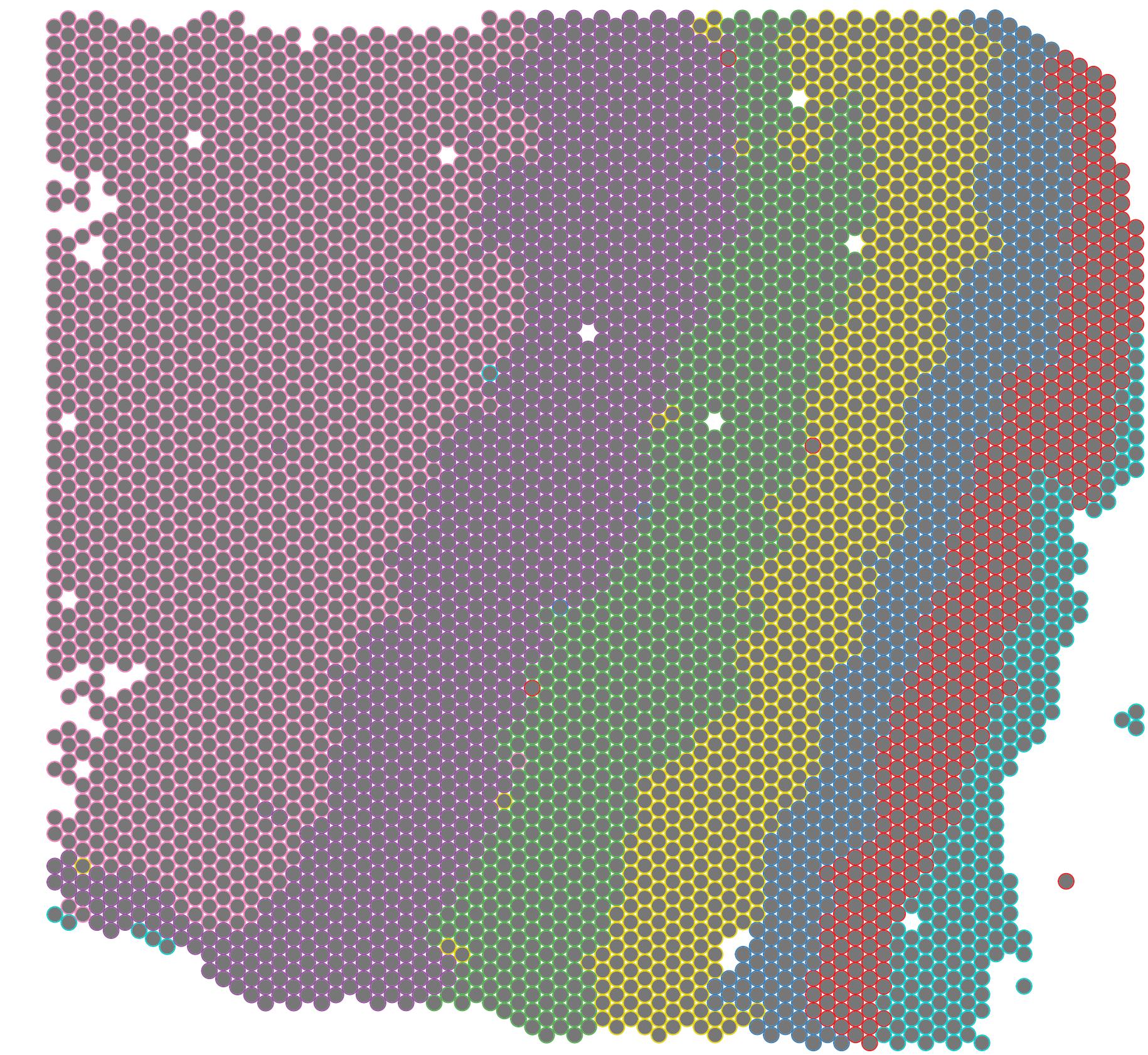
Sample Br2720_ant



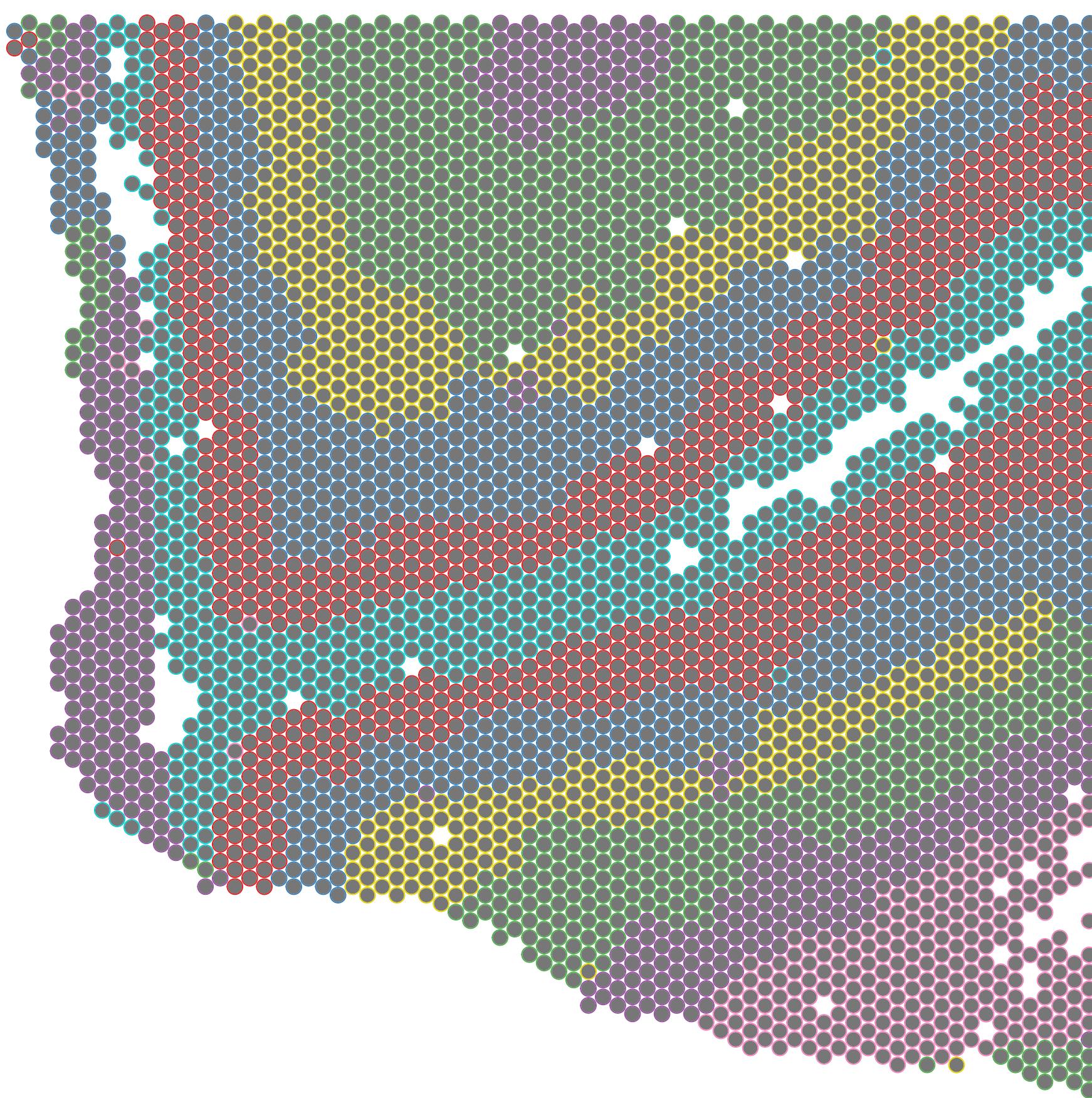
Sample Br2720_mid



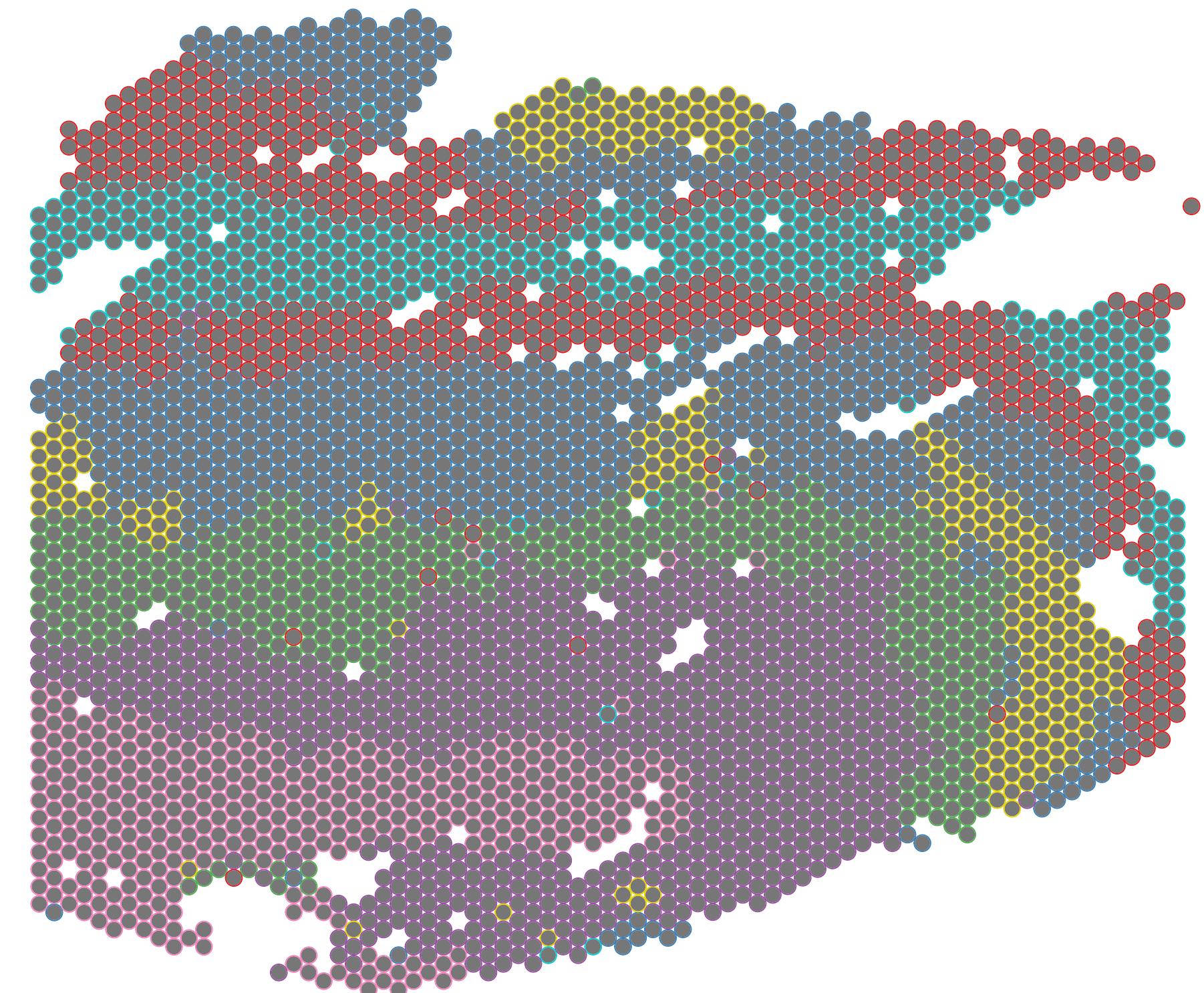
Sample Br2720_post



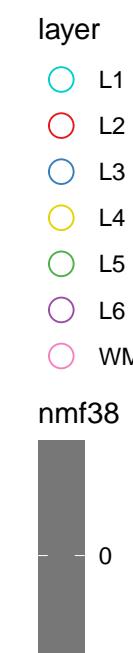
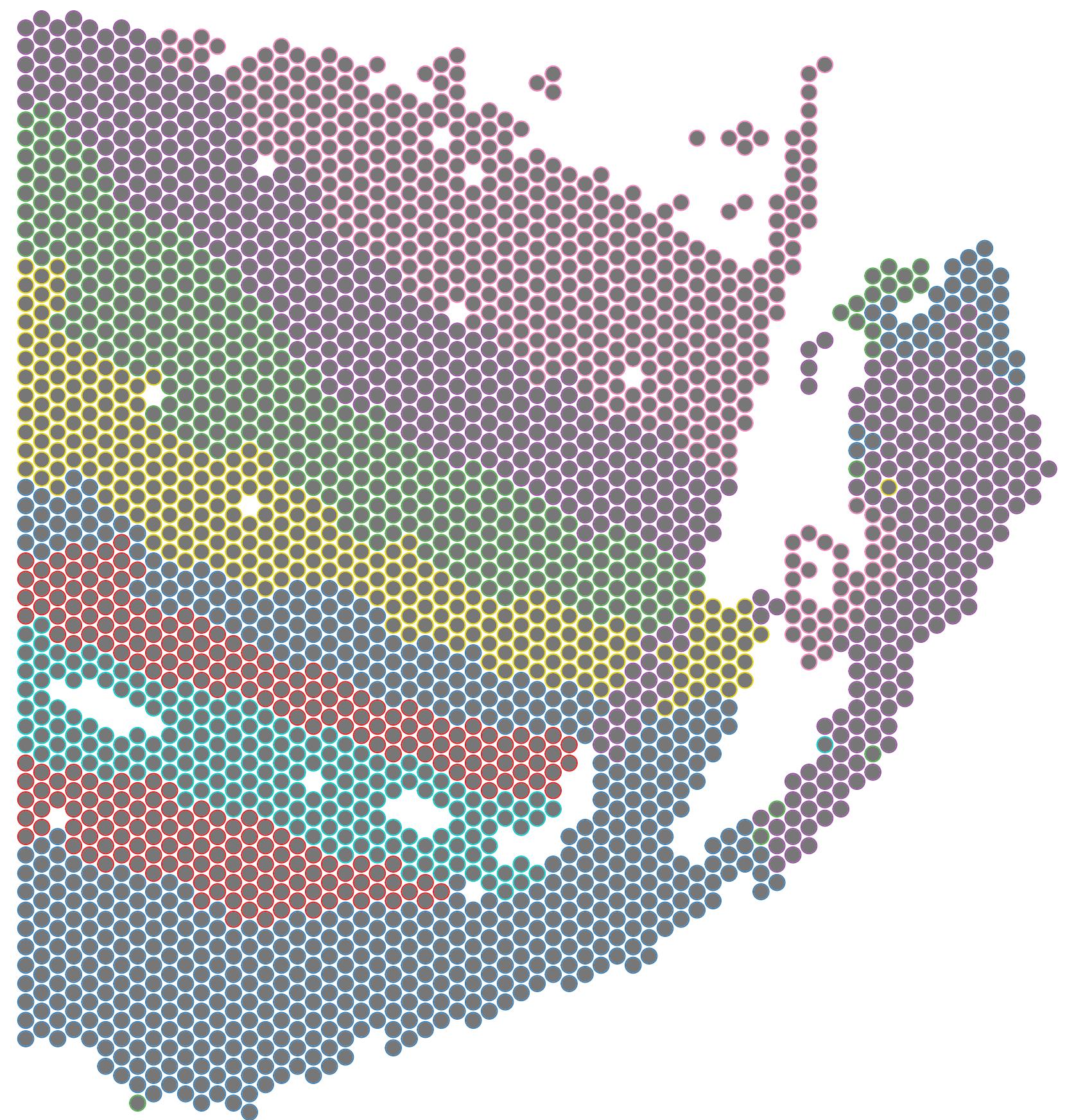
Sample Br6432_ant



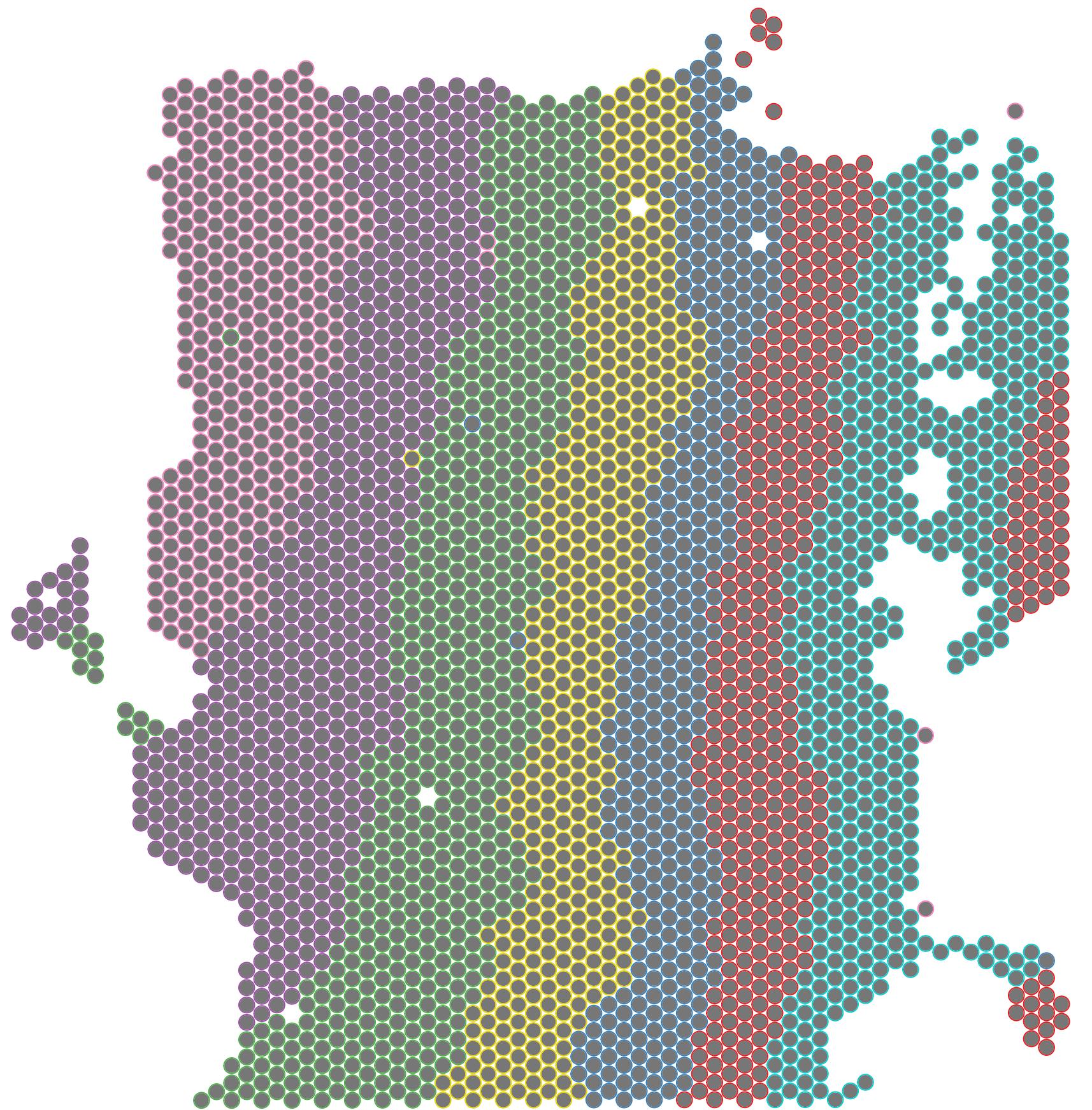
Sample Br6432_mid



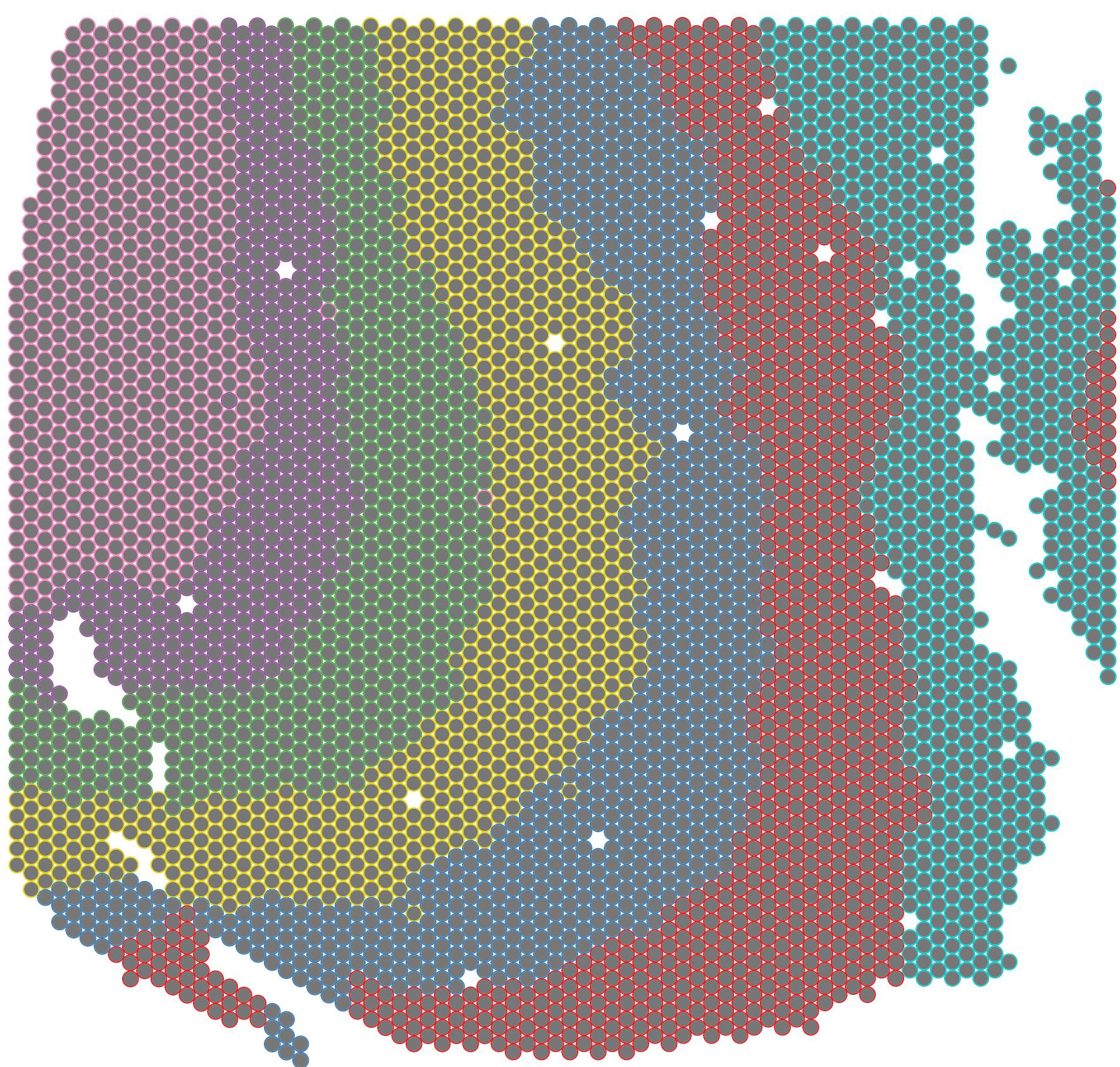
Sample Br6432_post



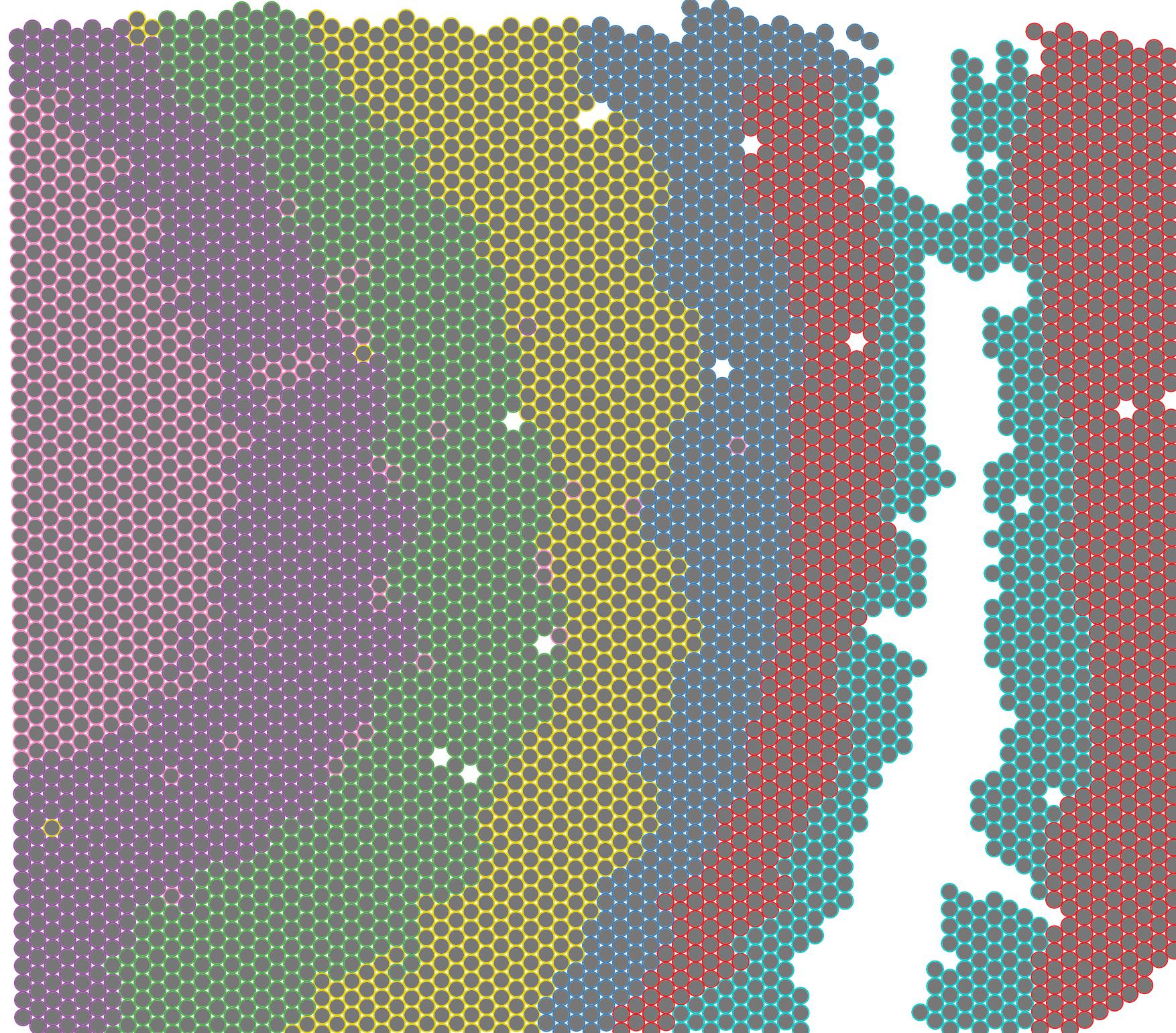
Sample Br6471_ant



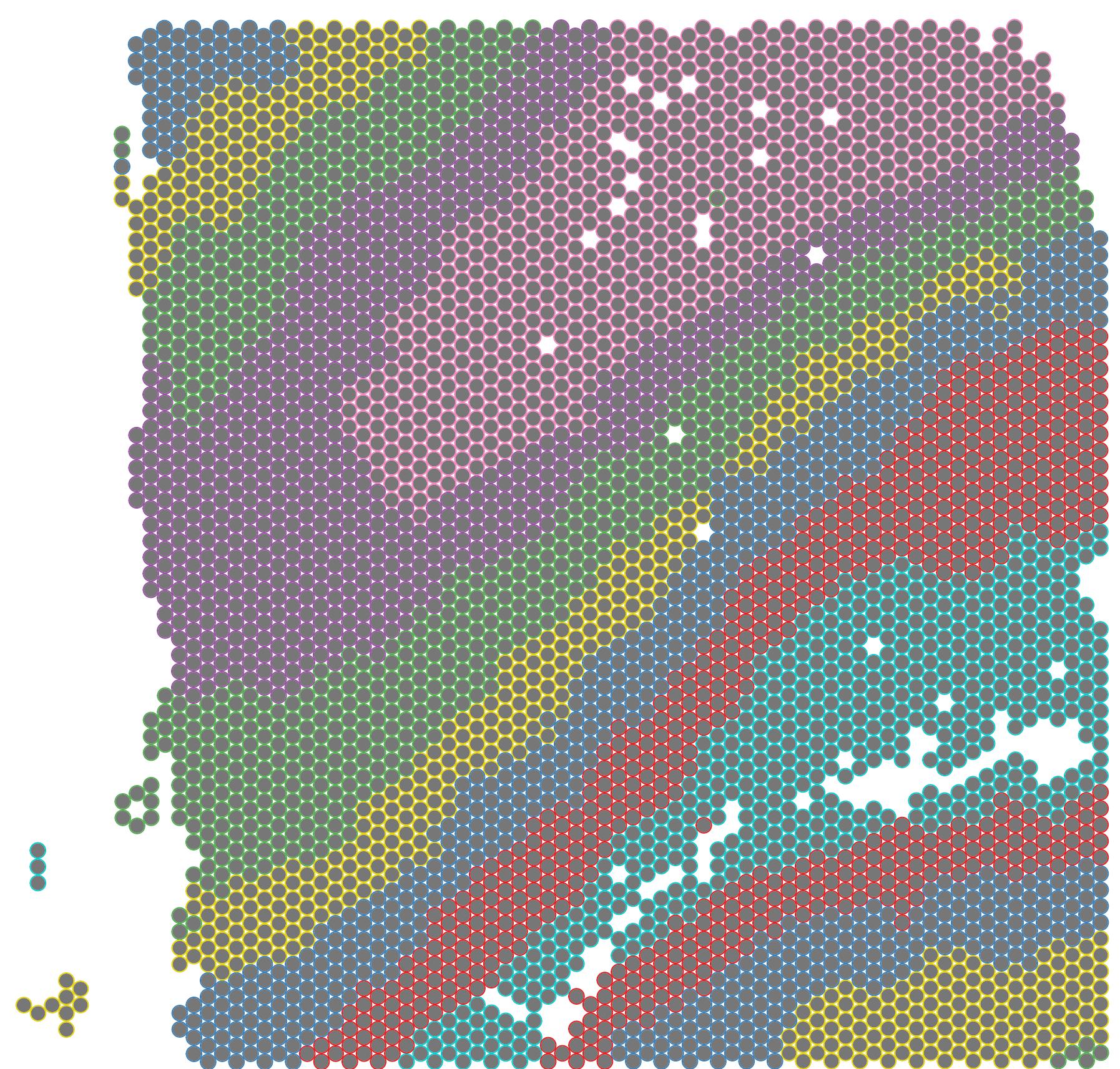
Sample Br6471_mid



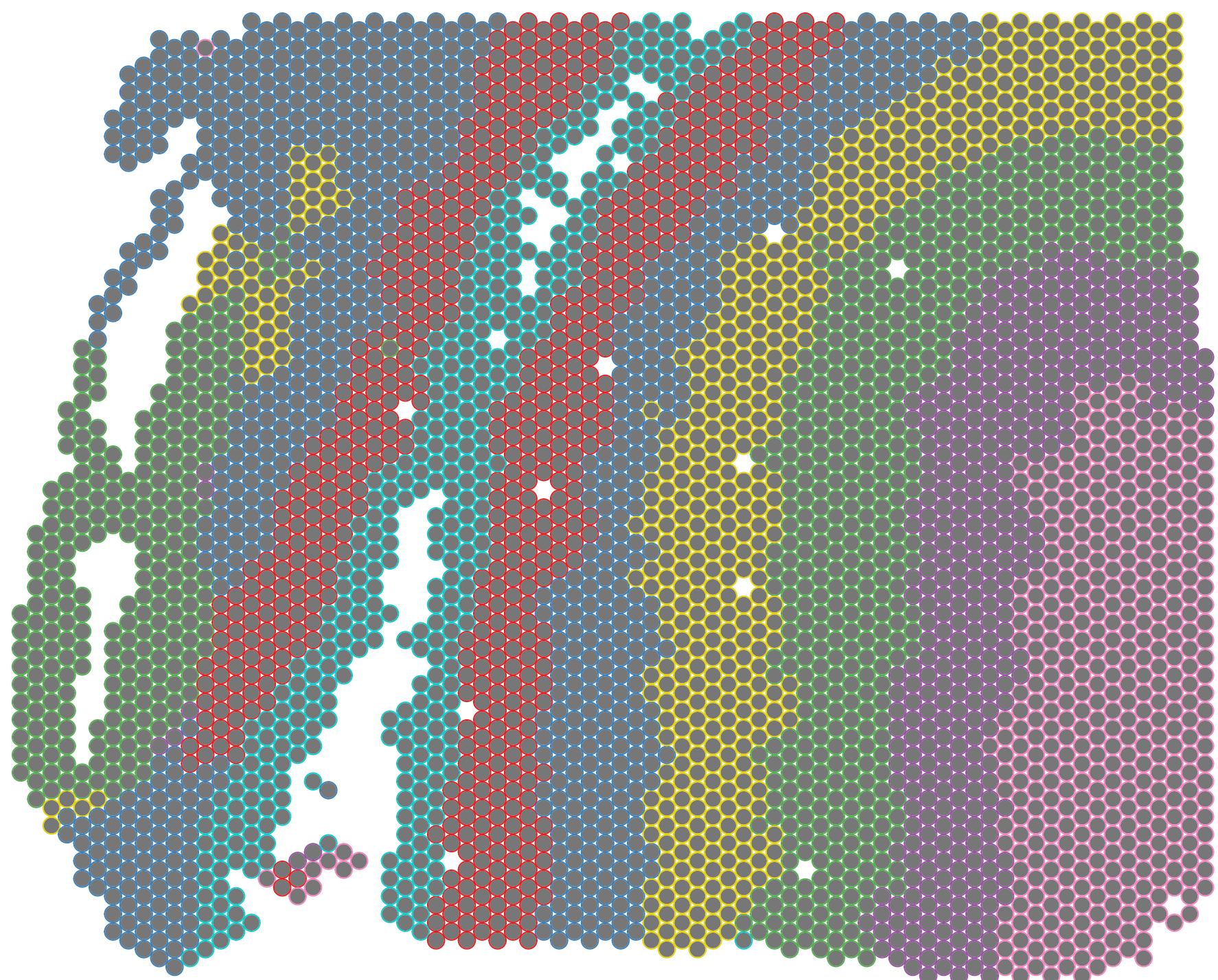
Sample Br6471_post



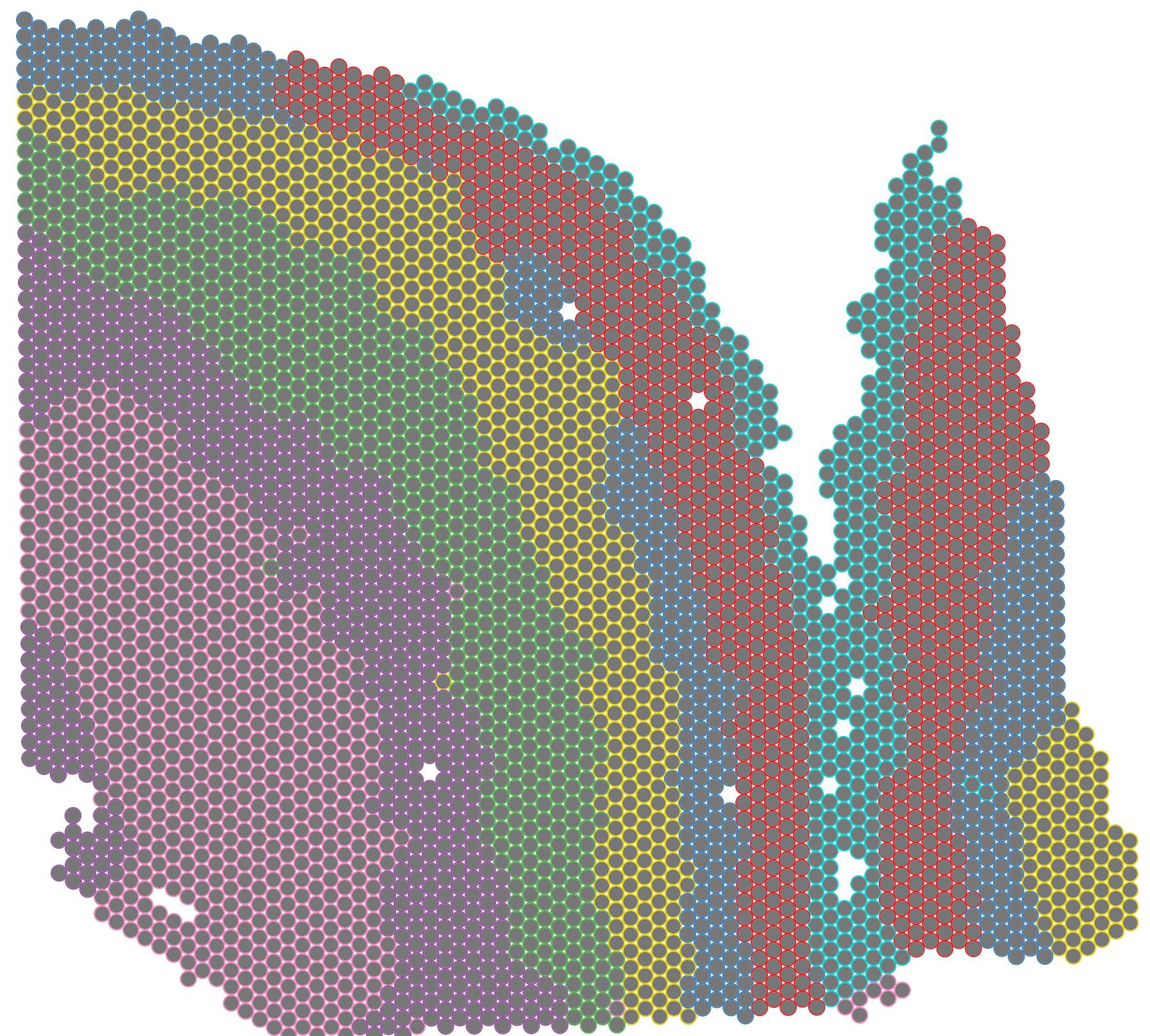
Sample Br6522_ant



Sample Br6522_mid



Sample Br6522_post



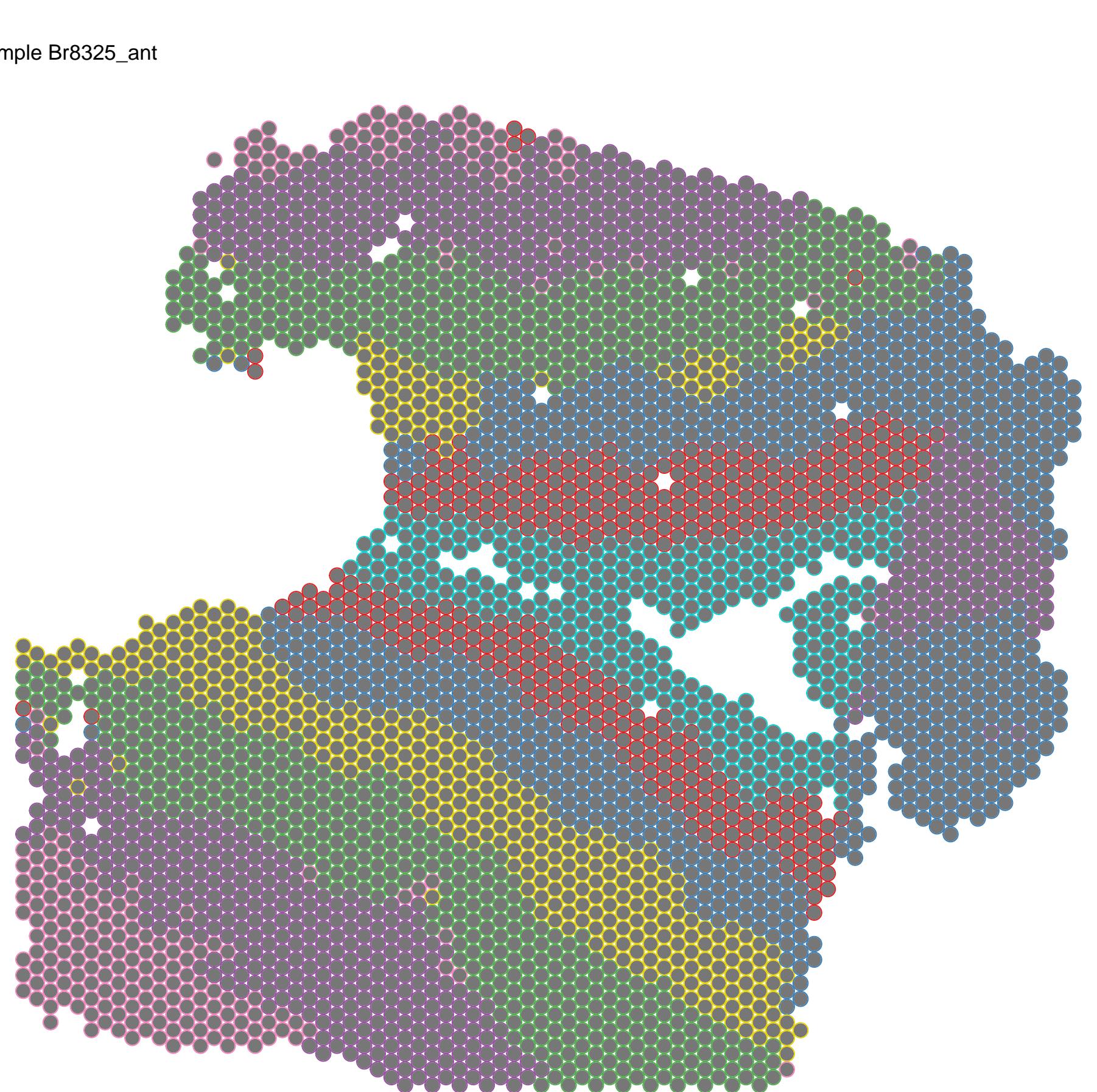
layer

L1
L2
L3
L4
L5
L6
WM

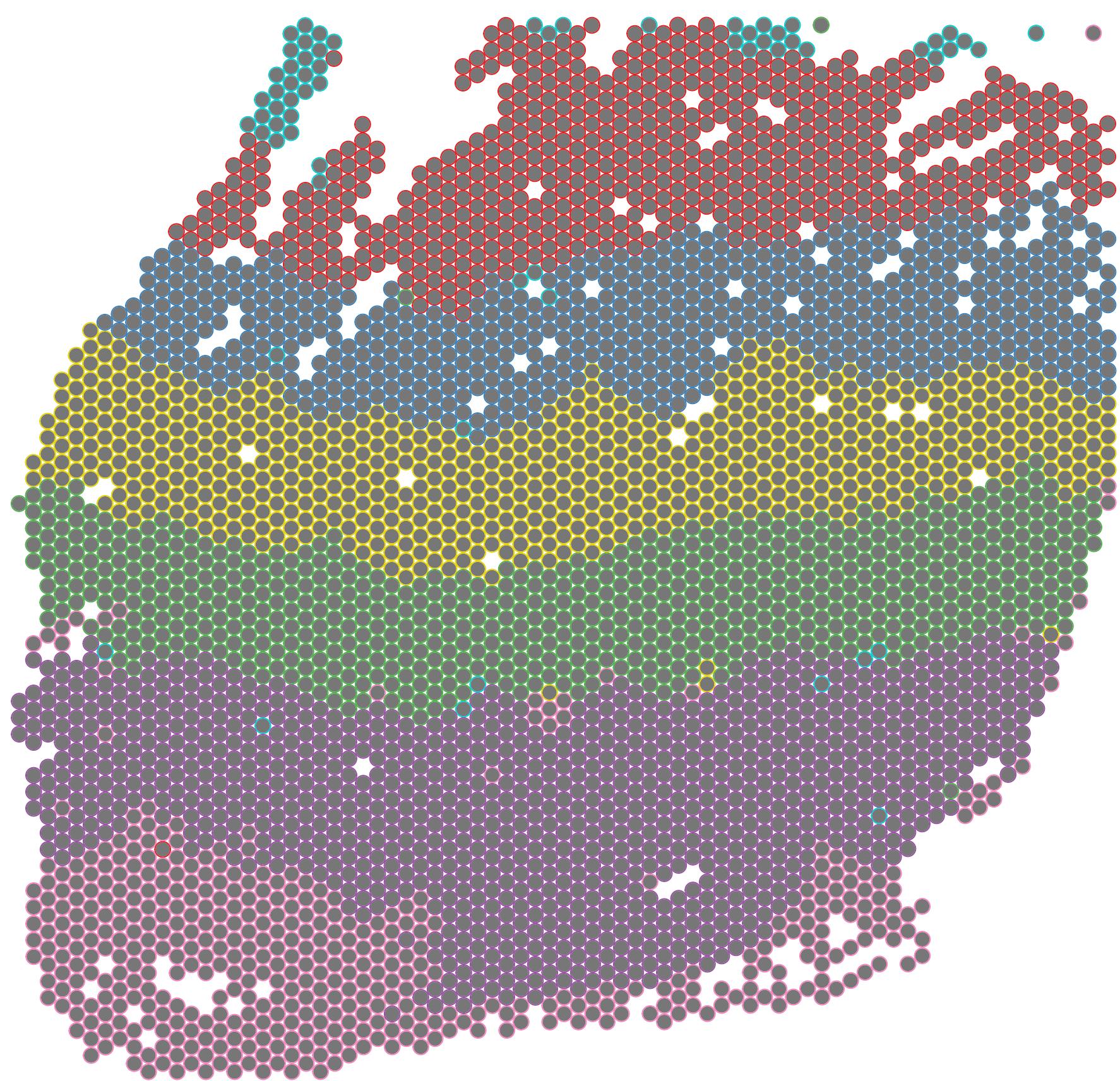
nmf38

0

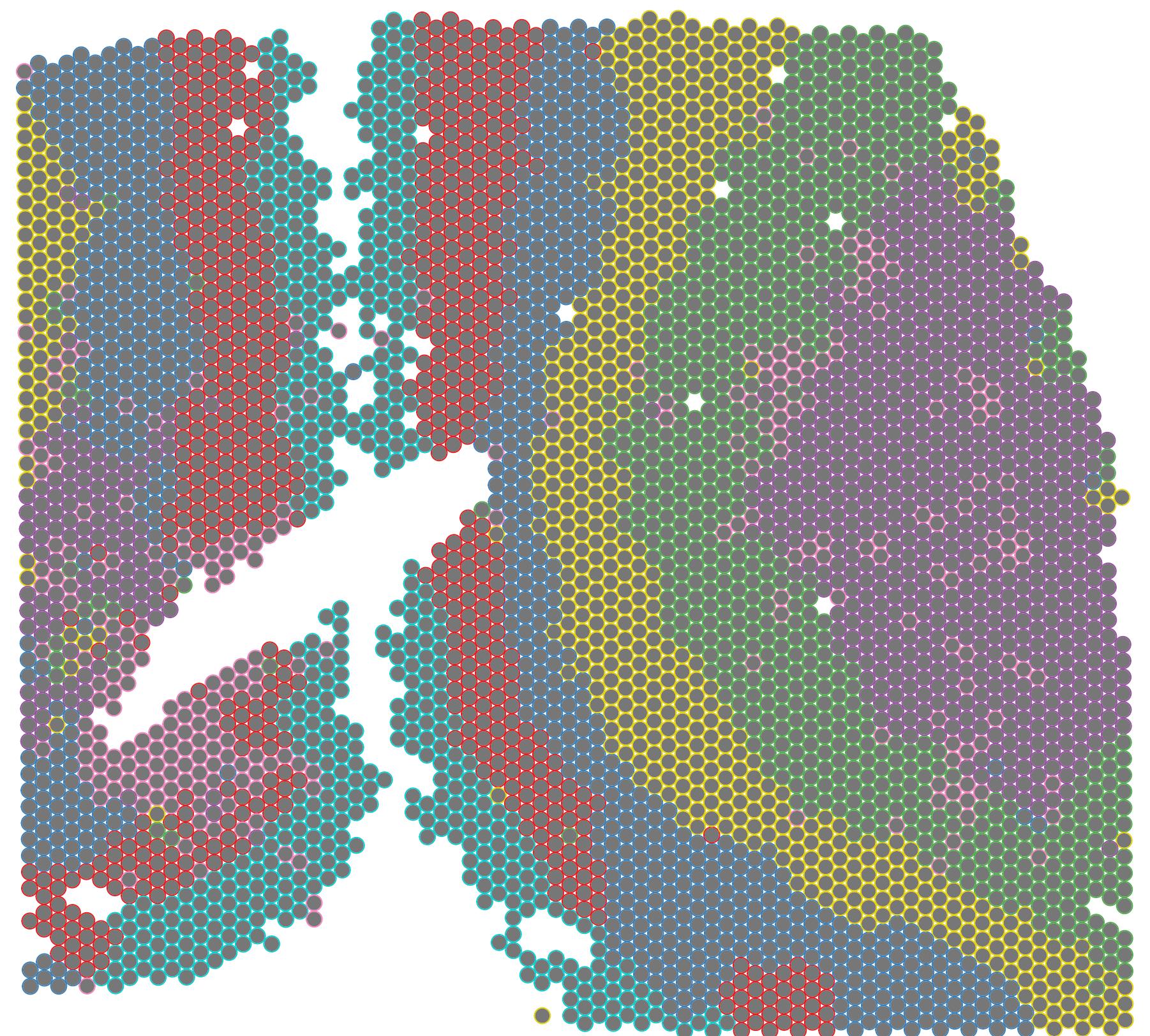
Sample Br8325_ant



Sample Br8325_mid



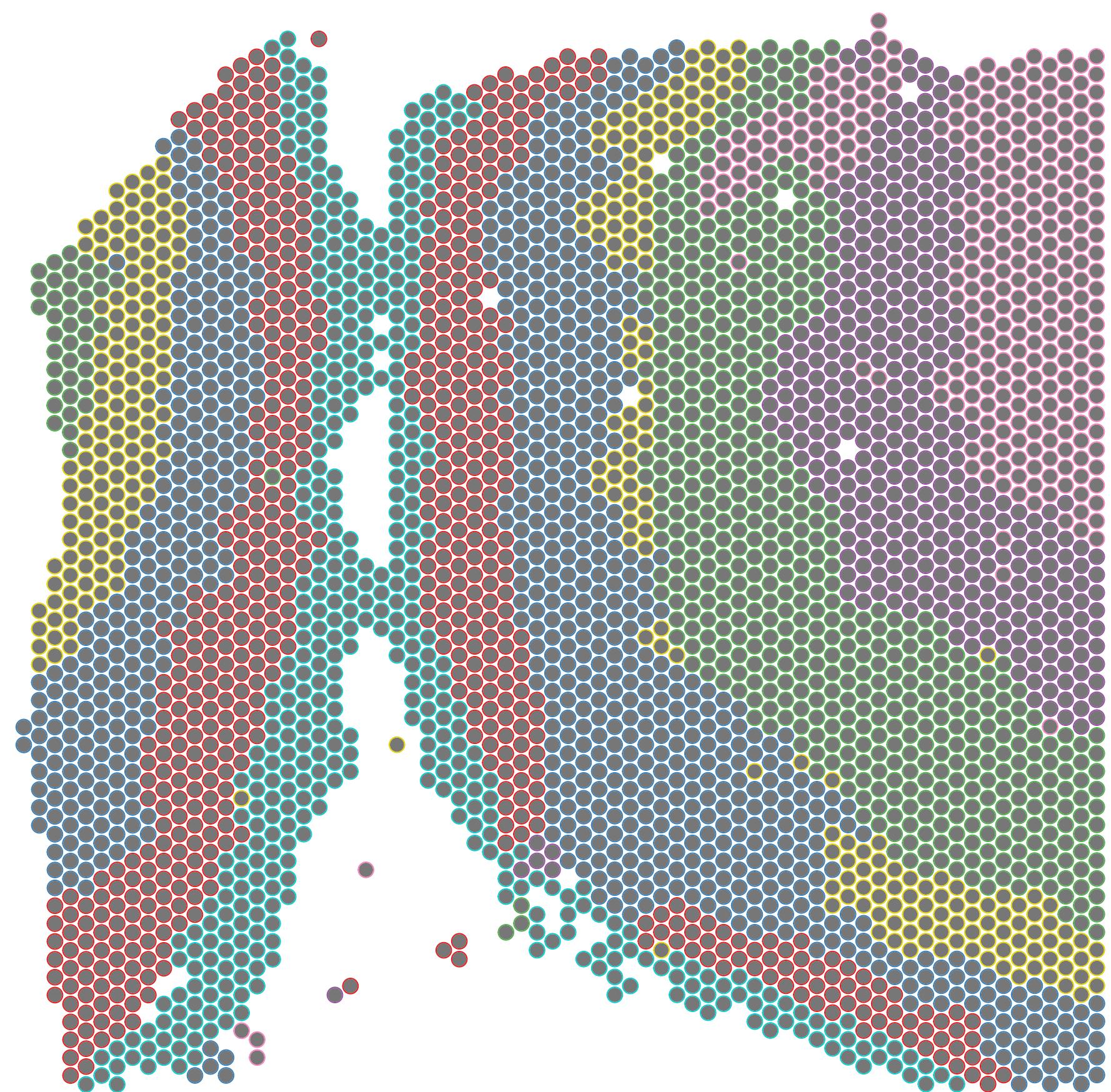
Sample Br8325_post



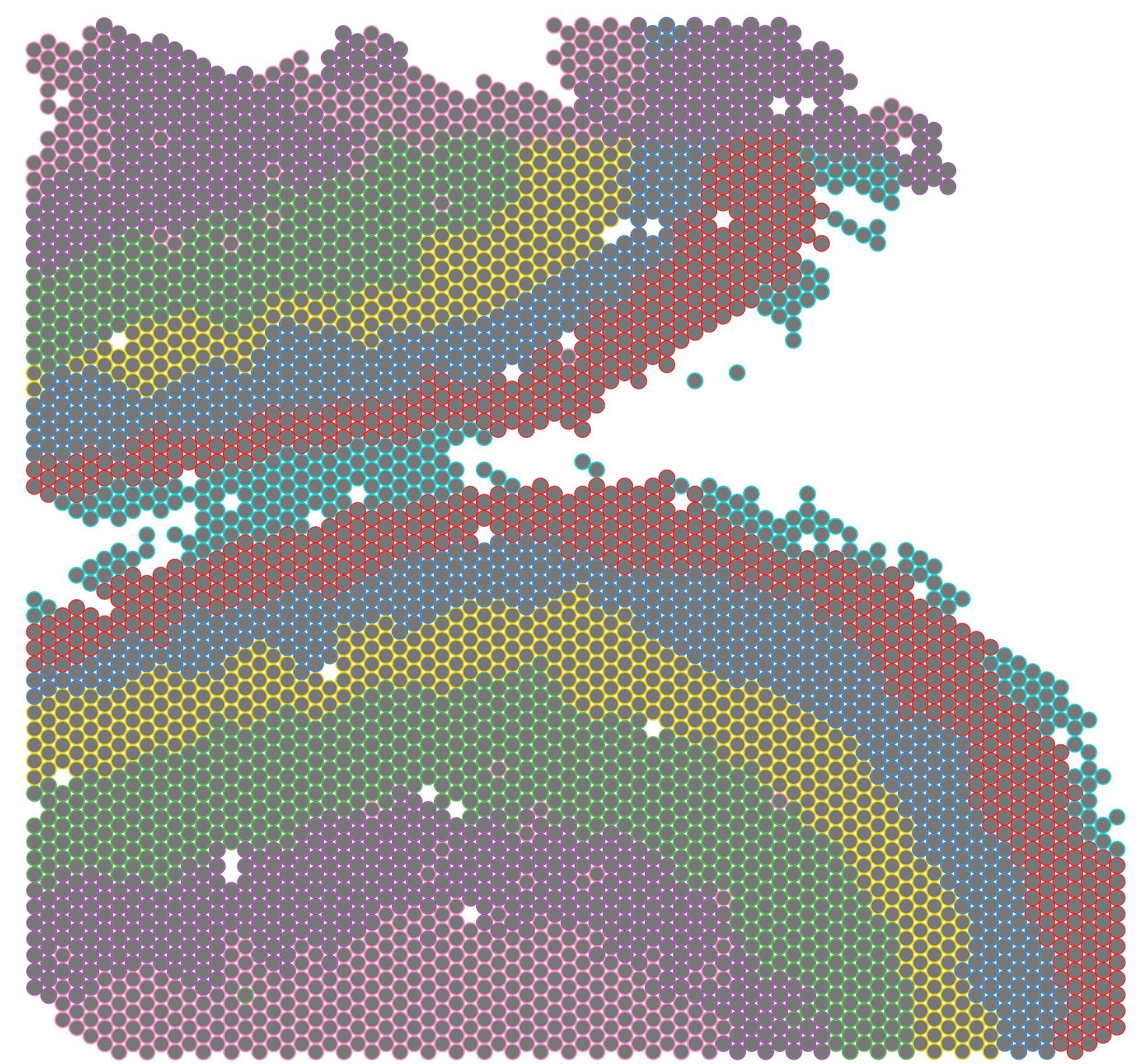
layer
 L1
 L2
 L3
 L4
 L5
 L6
 WM
 nmf38
 0

layer
 L1
 L2
 L3
 L4
 L5
 L6
 WM
 nmf38
 0

Sample Br8667_ant



Sample Br8667_mid



Sample Br8667_post

