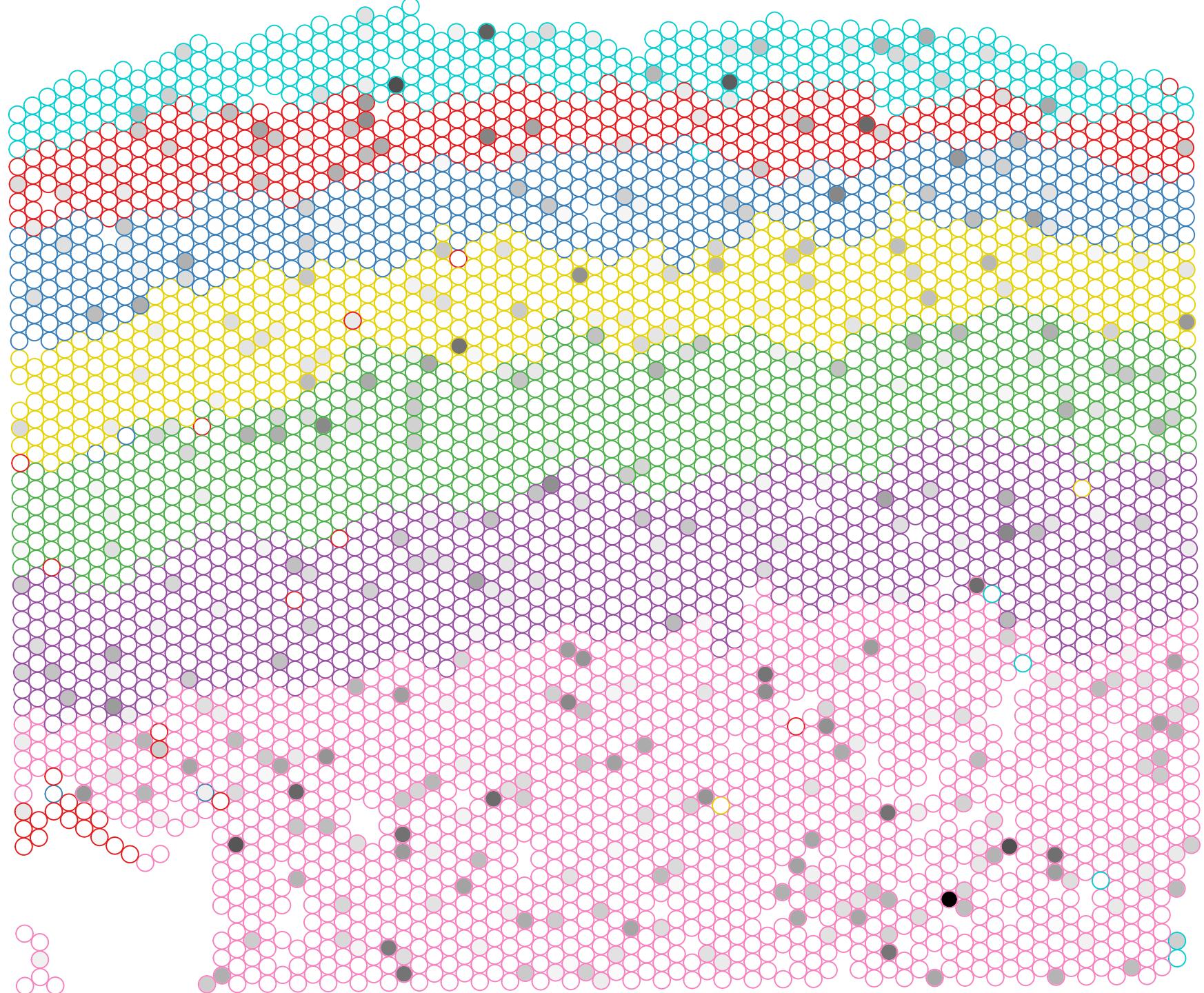
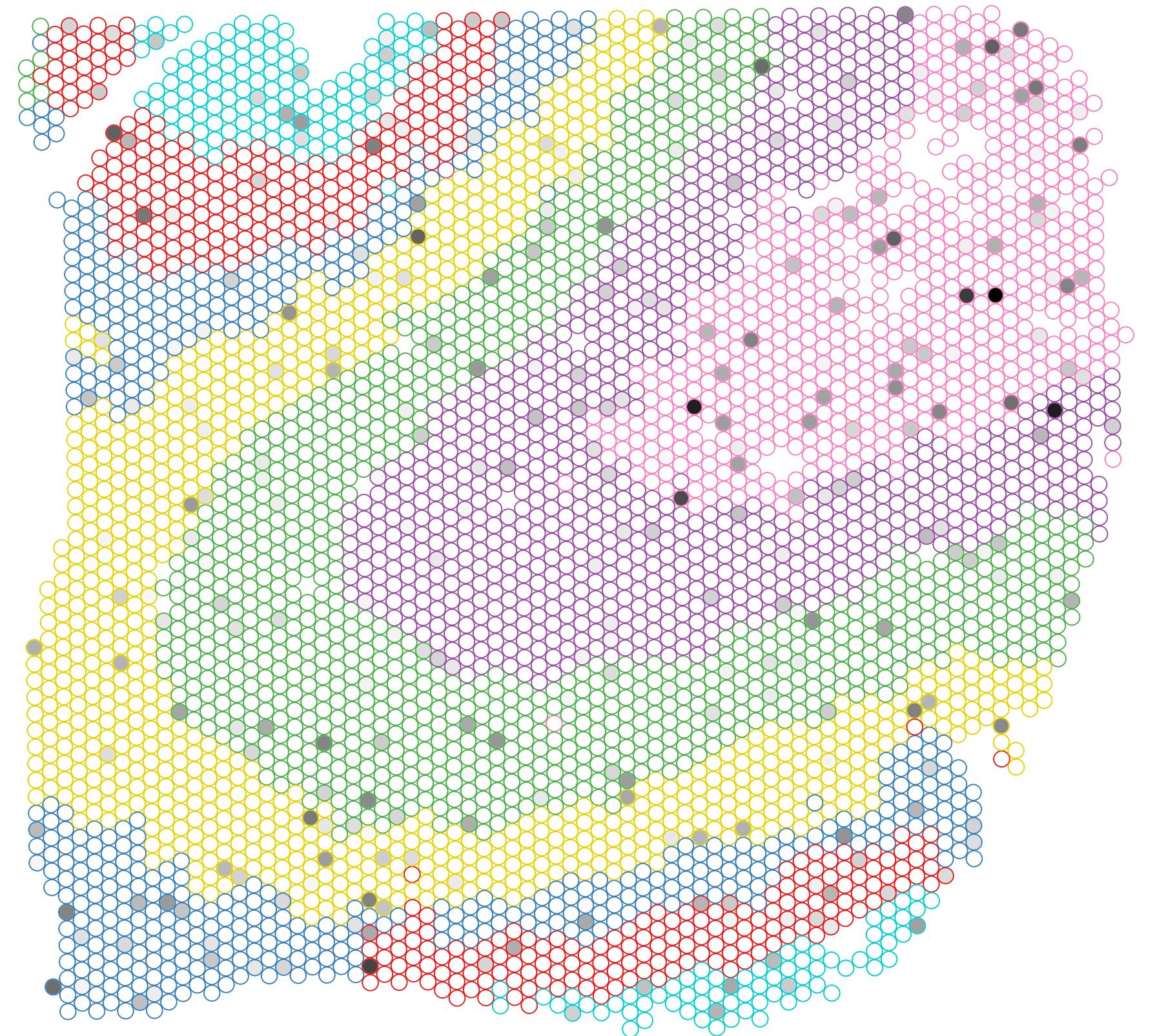


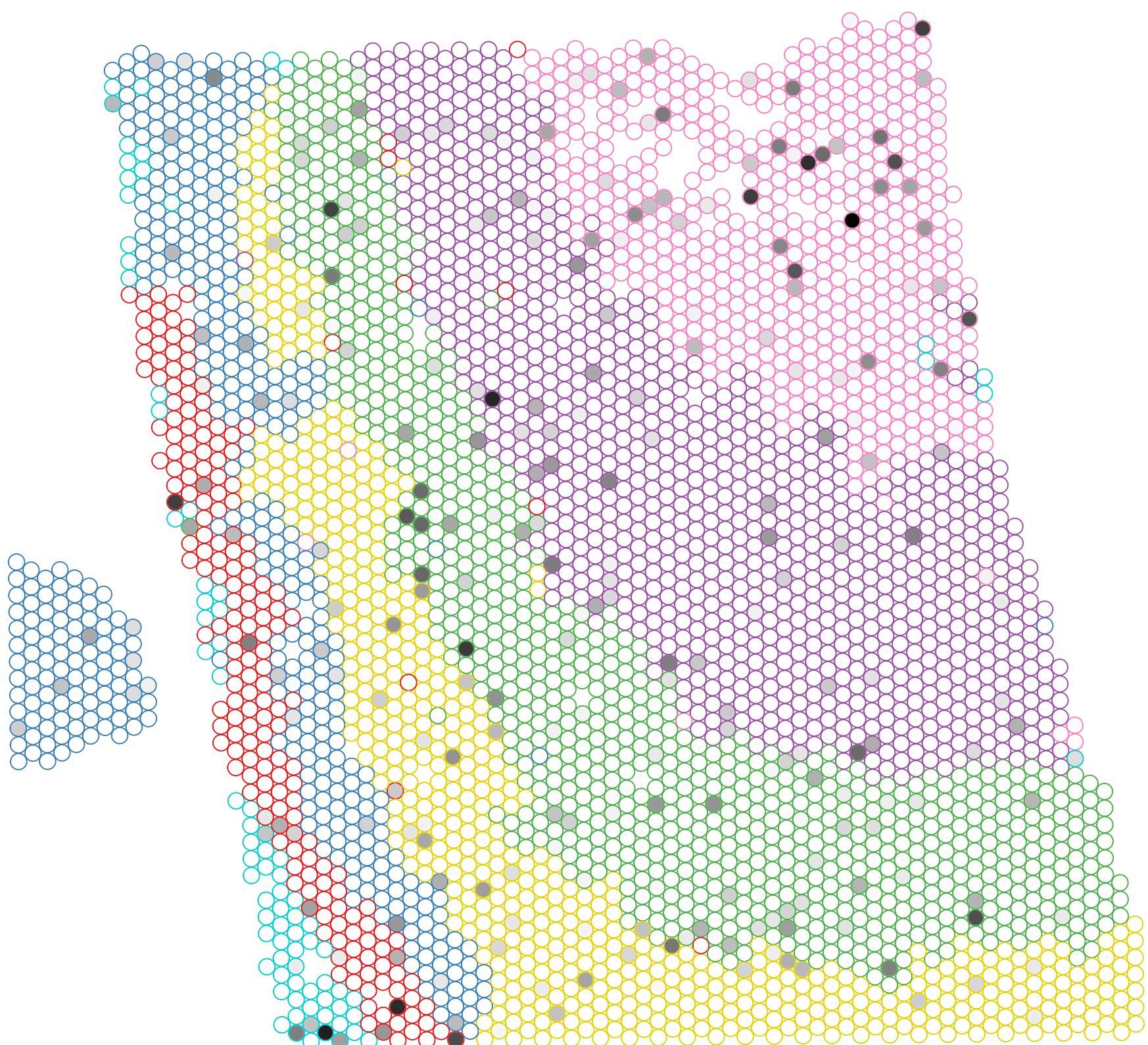
Sample Br2743_ant



Sample Br2743_mid



Sample Br2743_post



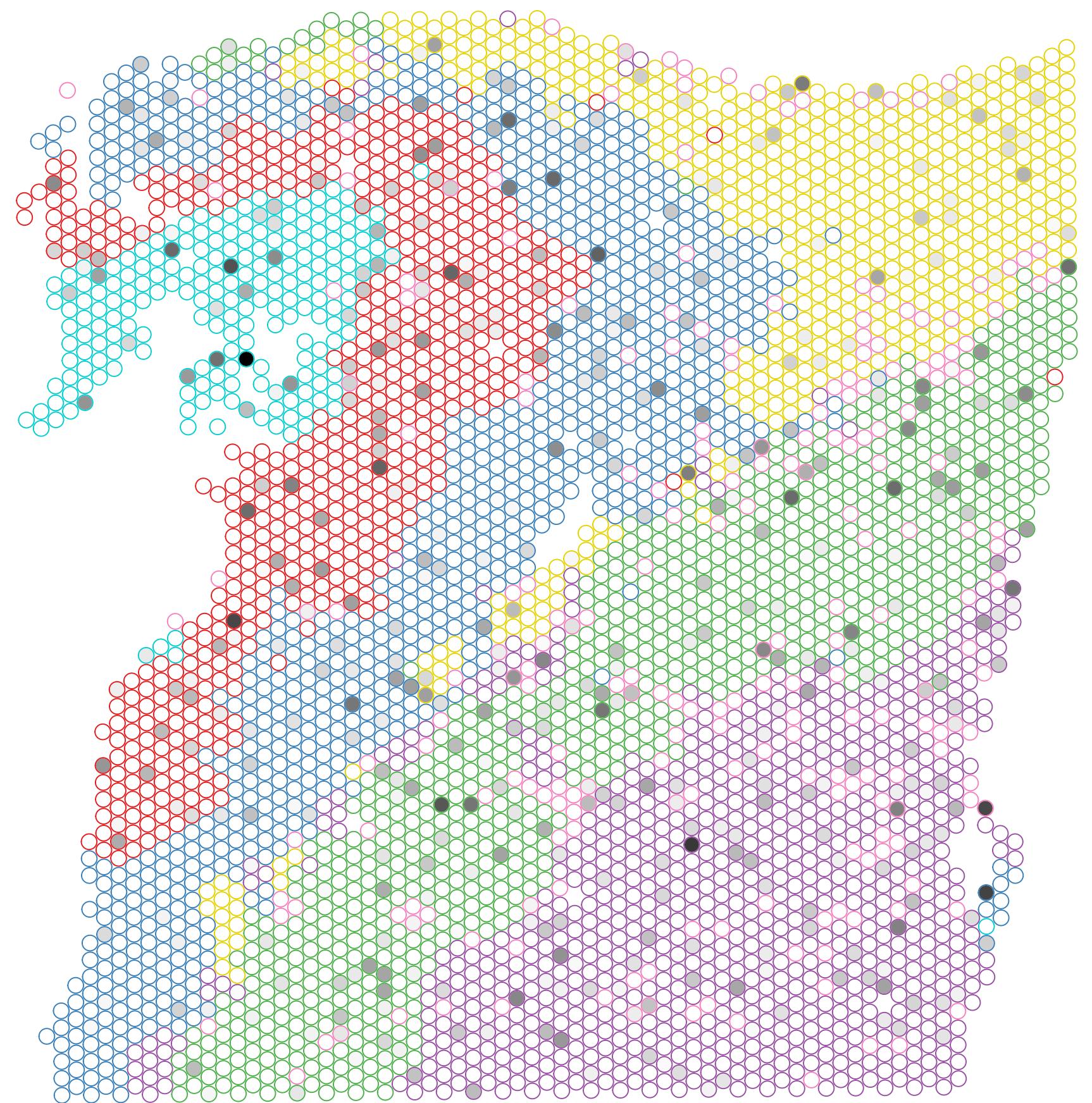
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

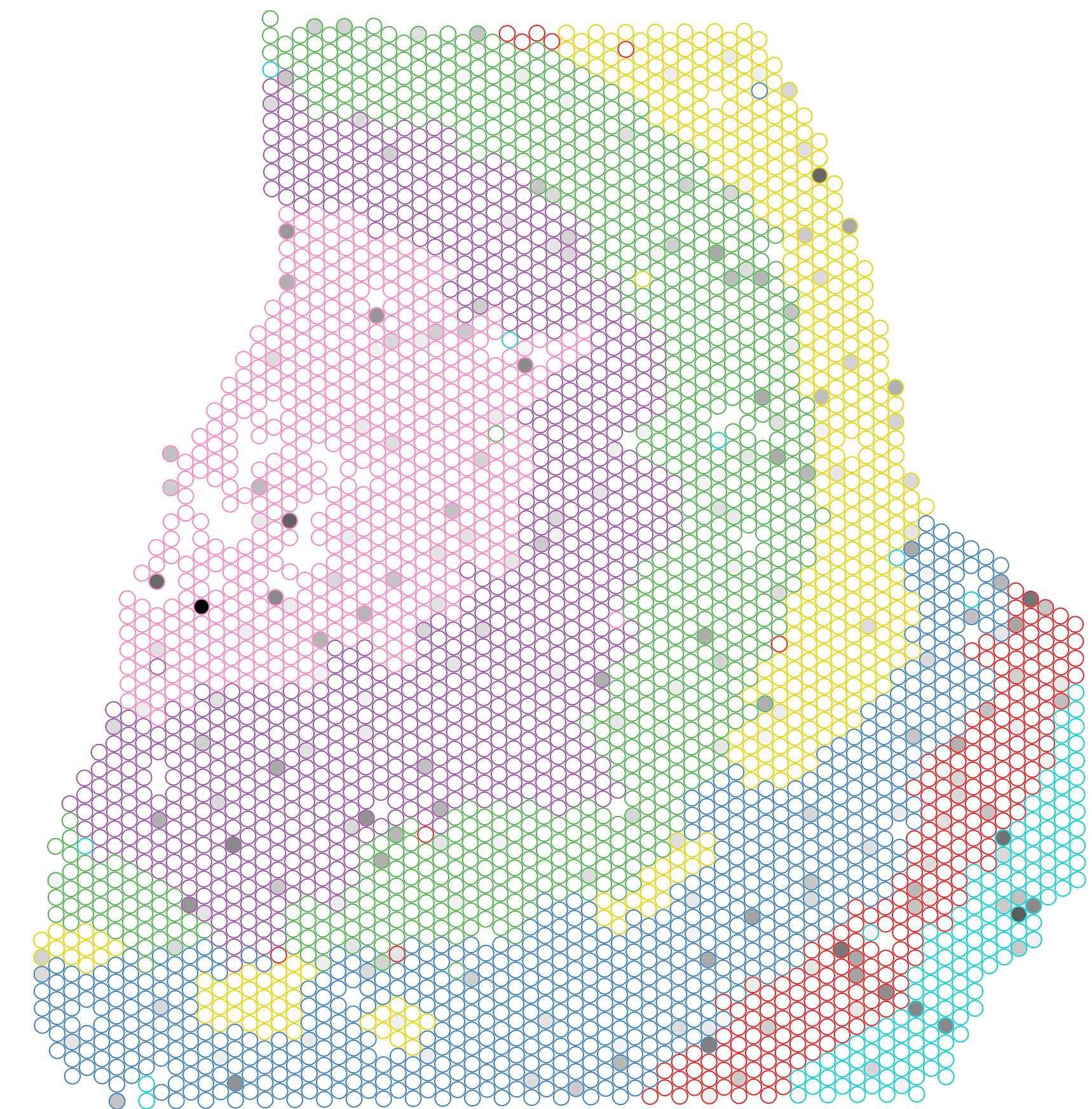
nmf64

- 3e-04
- 2e-04
- 1e-04
- 0e+00

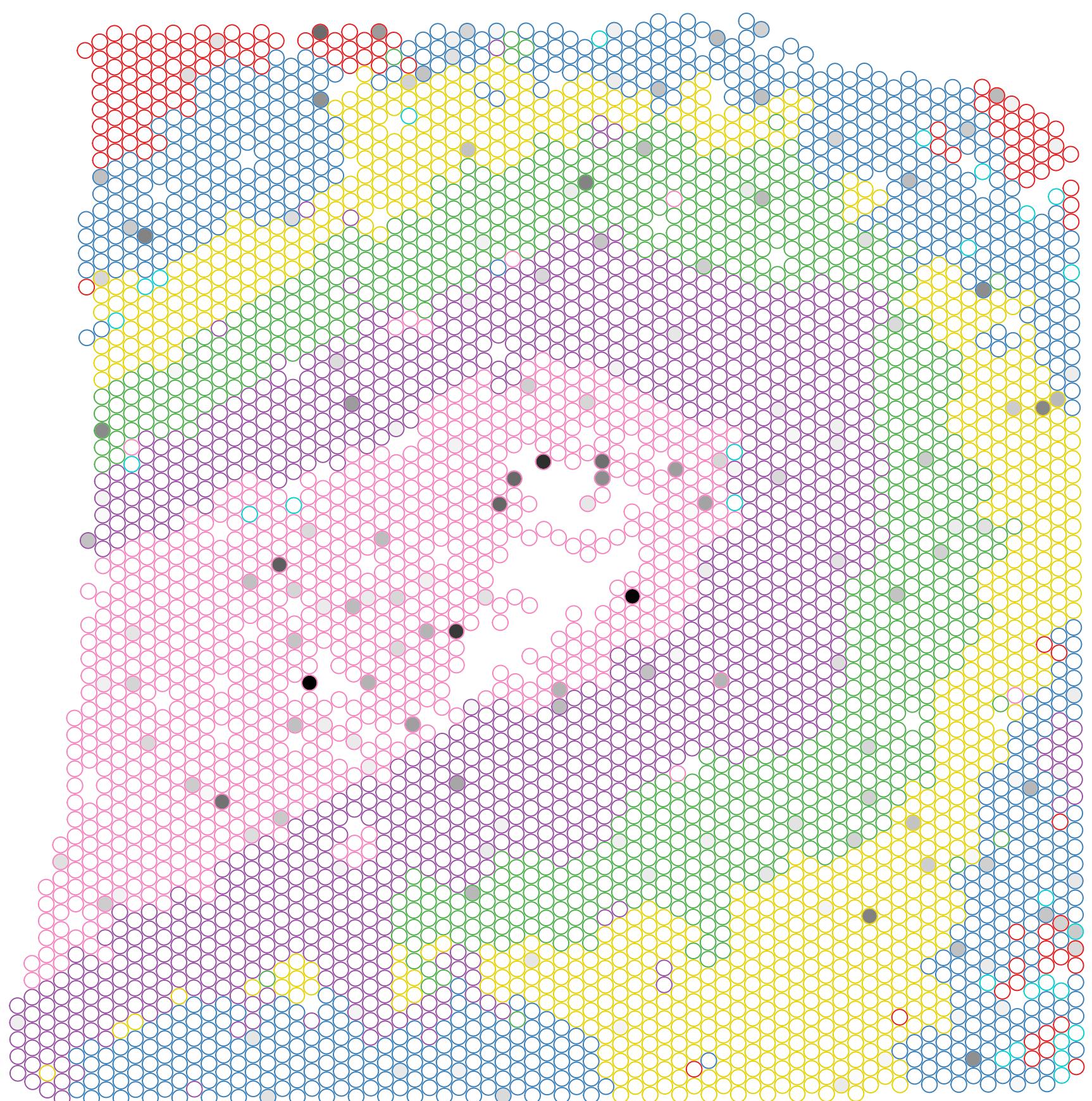
Sample Br3942_ant



Sample Br3942_mid



Sample Br3942_post

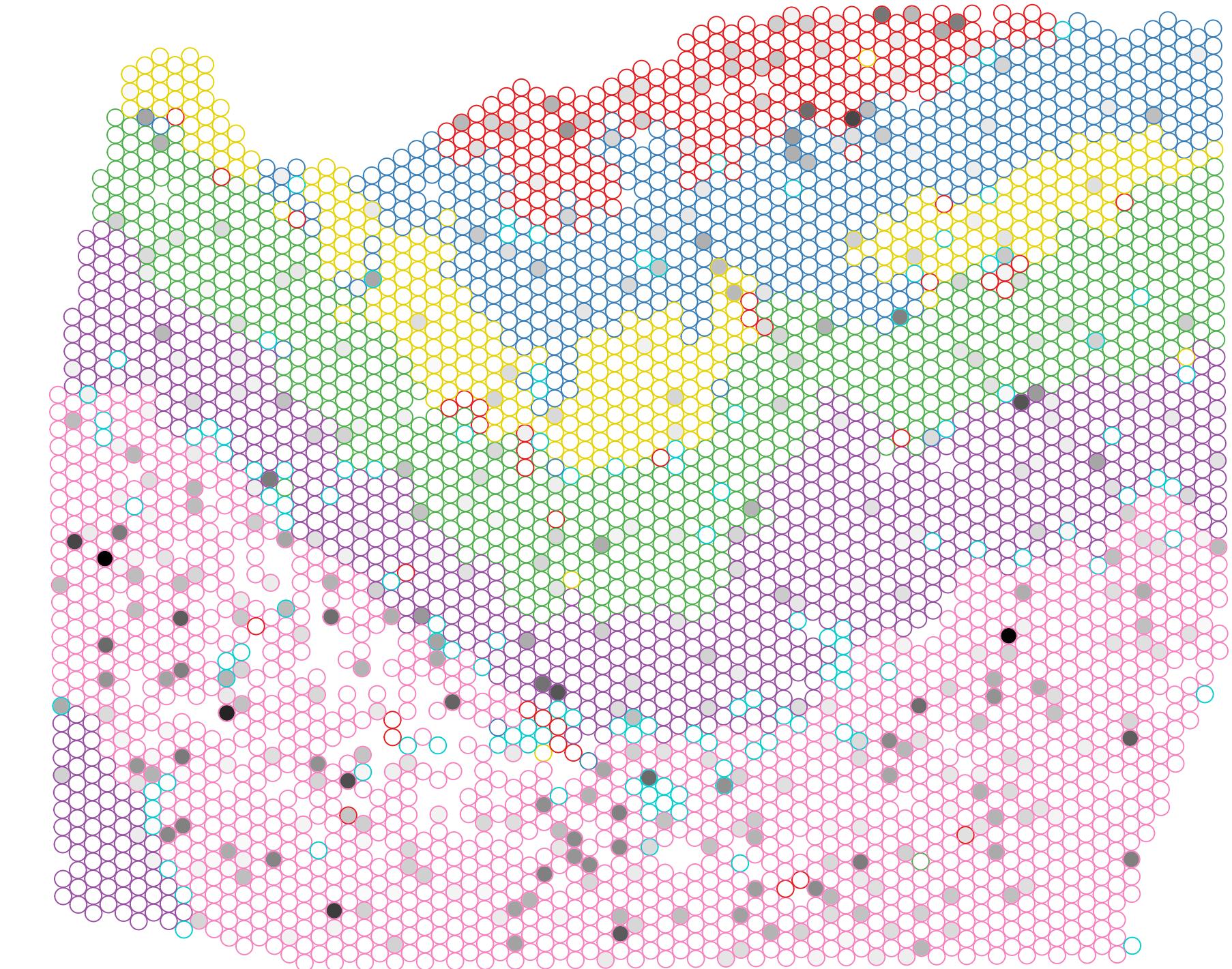


nmf64
layer
L1
L2
L3
L4
L5
L6
WM

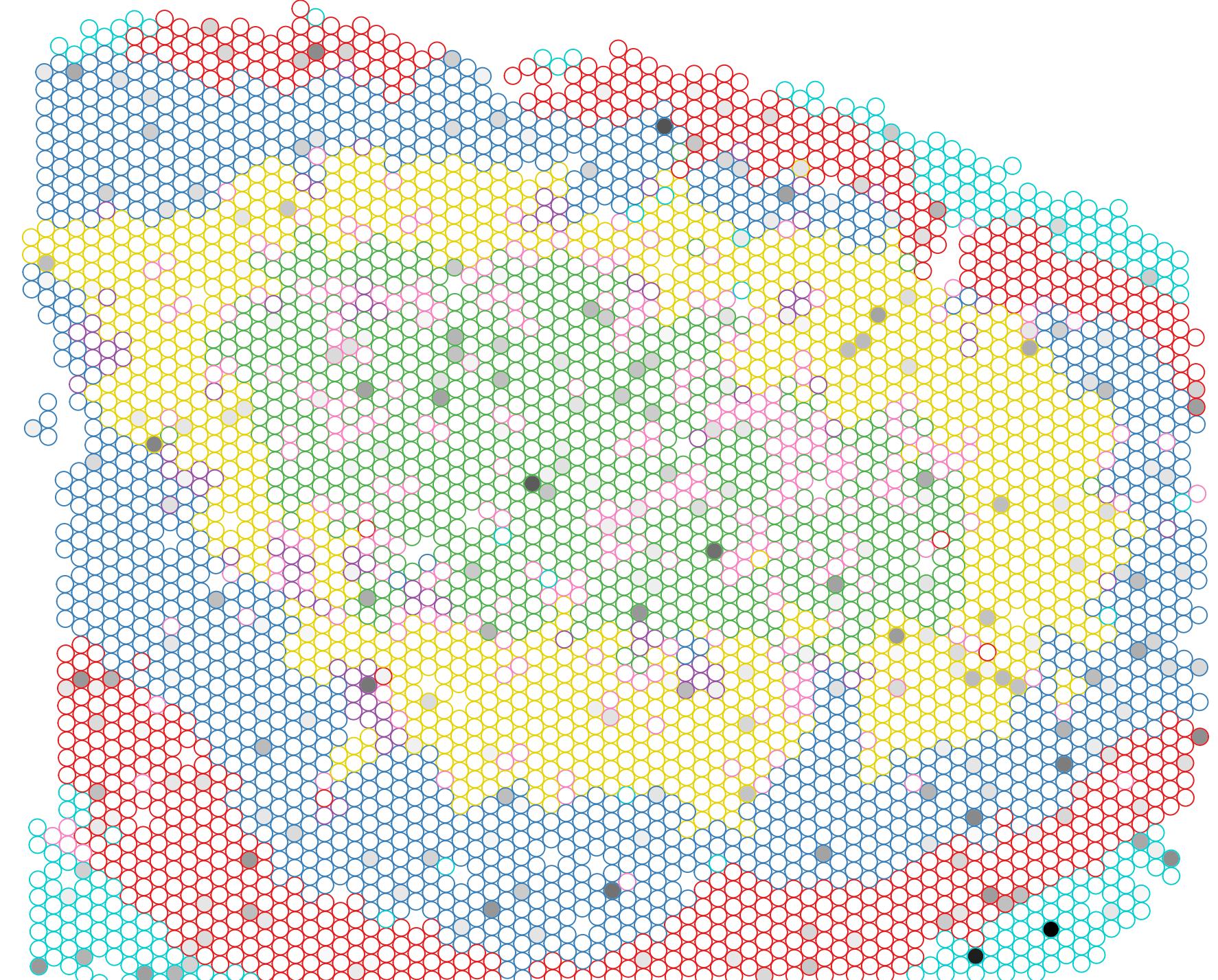
nmf64
layer
L1
L2
L3
L4
L5
L6
WM

nmf64
layer
L1
L2
L3
L4
L5
L6
WM

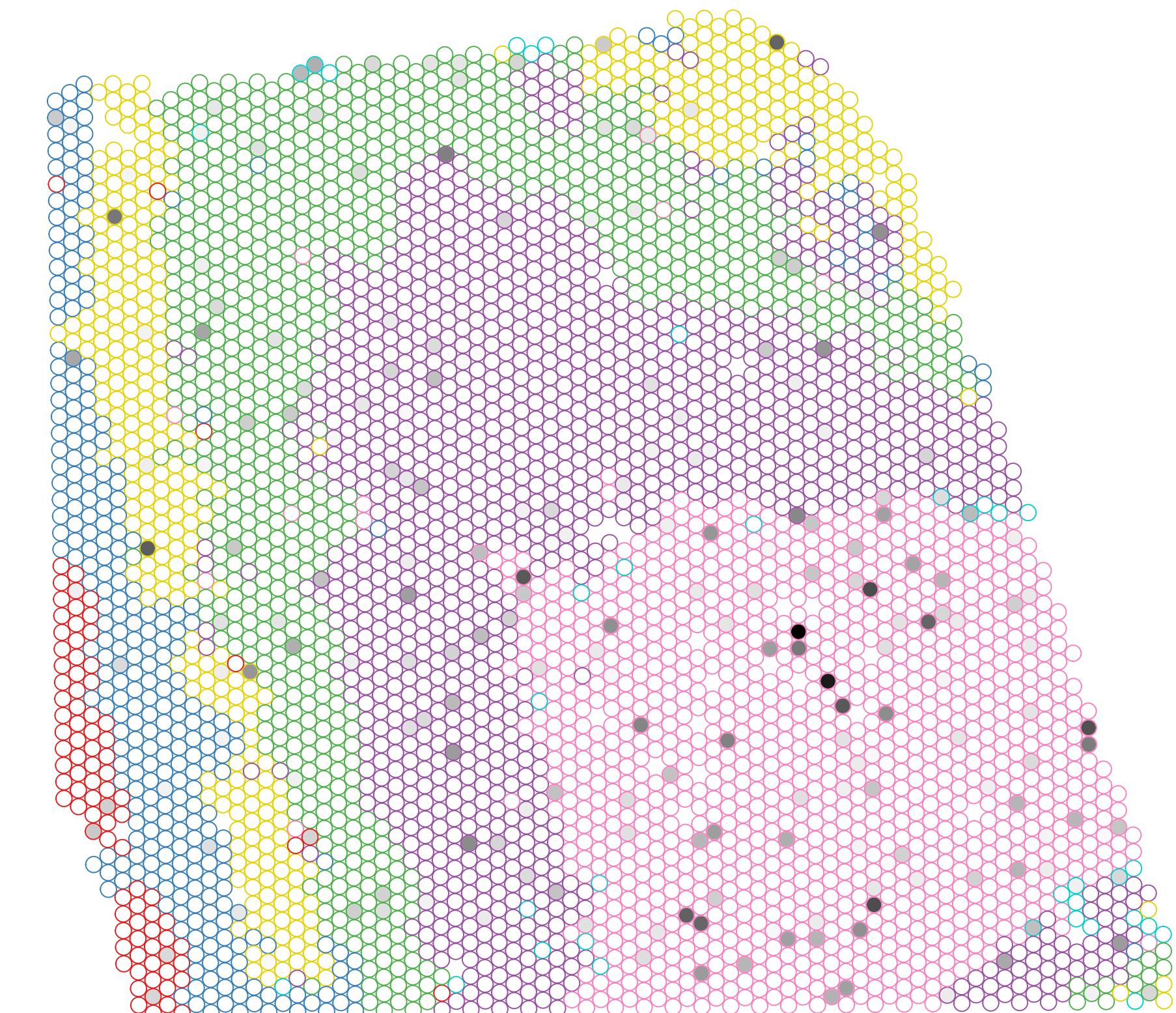
Sample Br6423_ant



Sample Br6423_mid



Sample Br6423_post



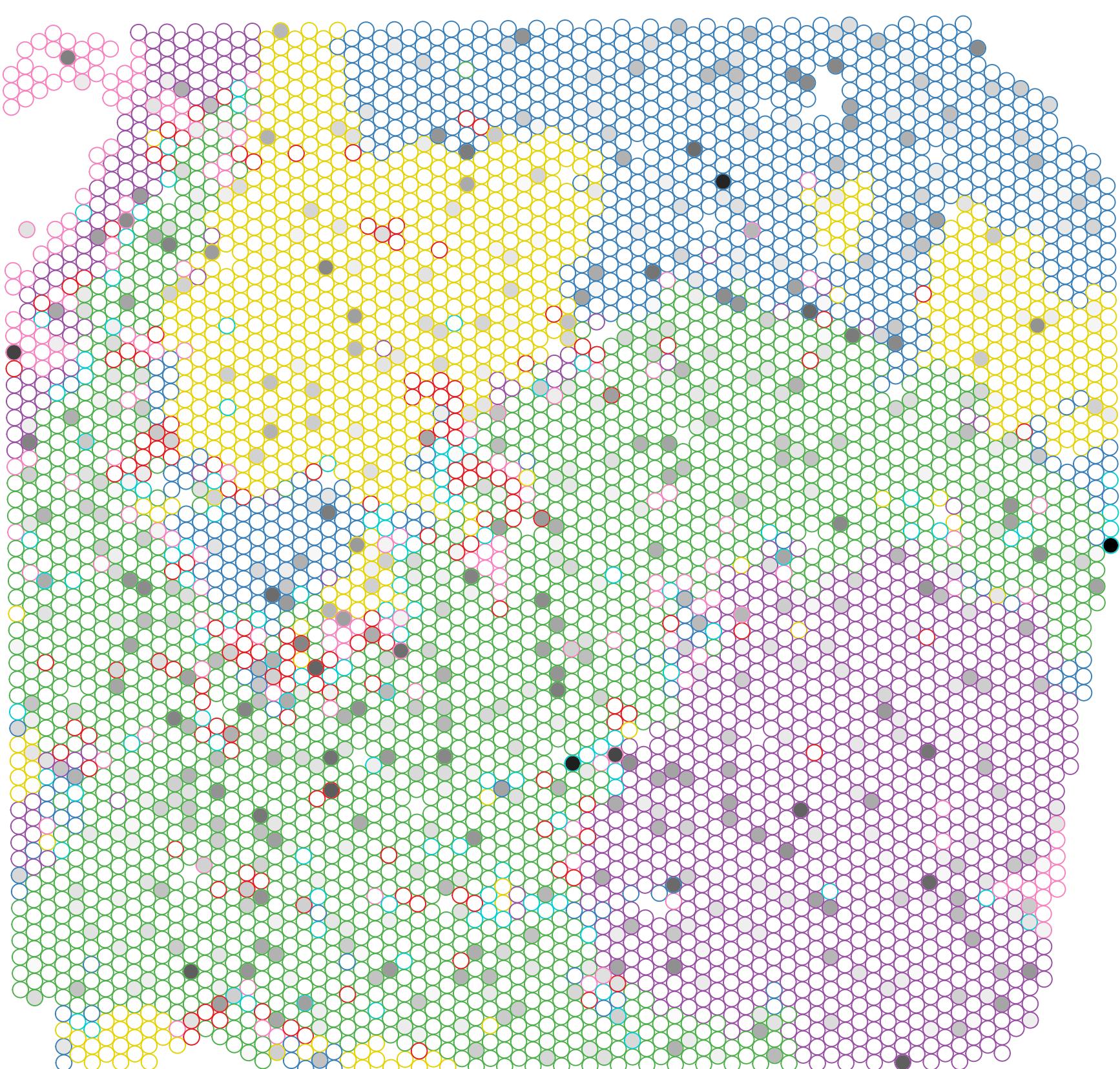
nmf64

5e-04
4e-04
3e-04
2e-04
1e-04
0e+00

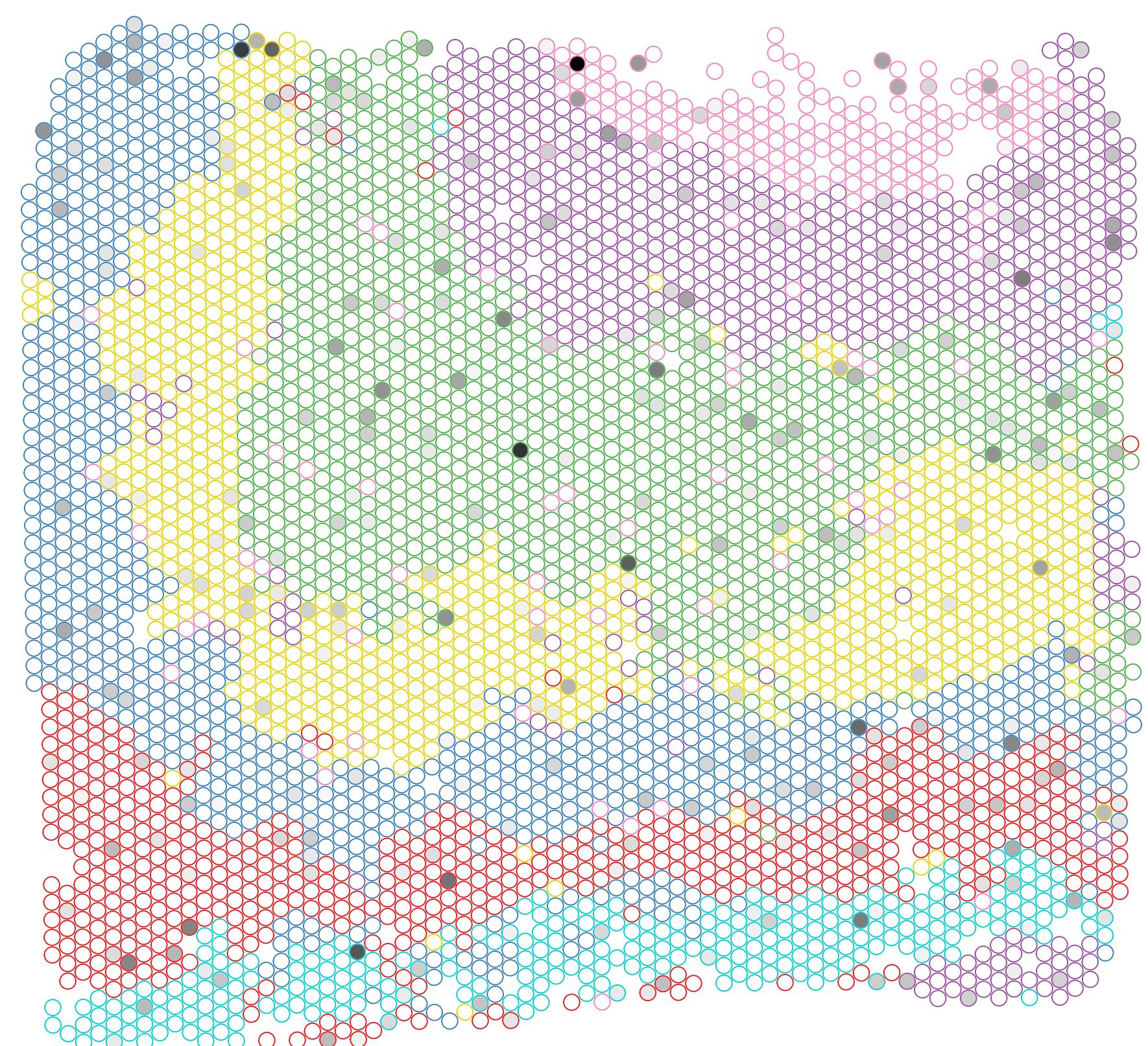
layer

L1
L2
L3
L4
L5
L6
WM

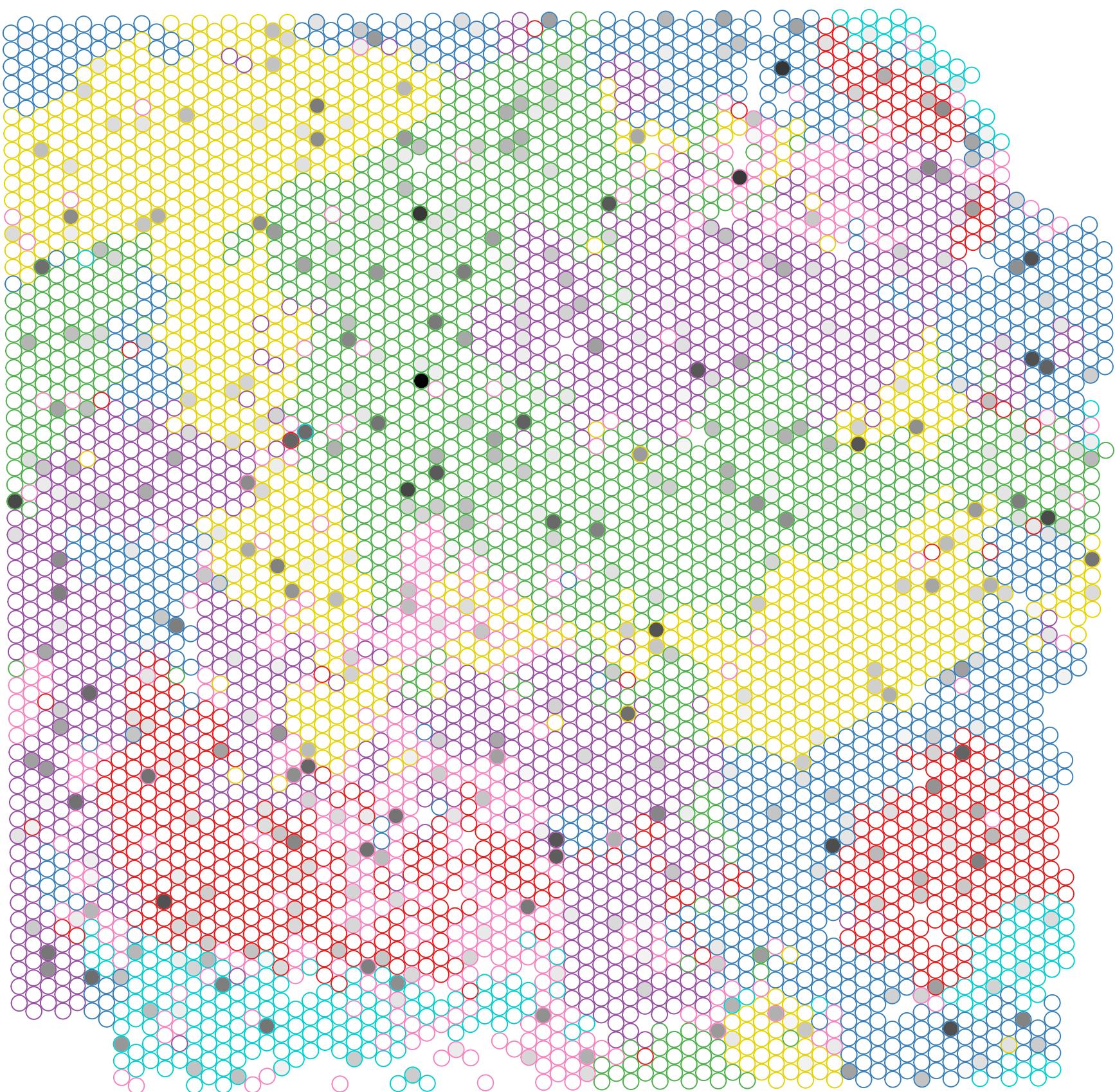
Sample Br8492_ant



Sample Br8492_mid



Sample Br8492_post



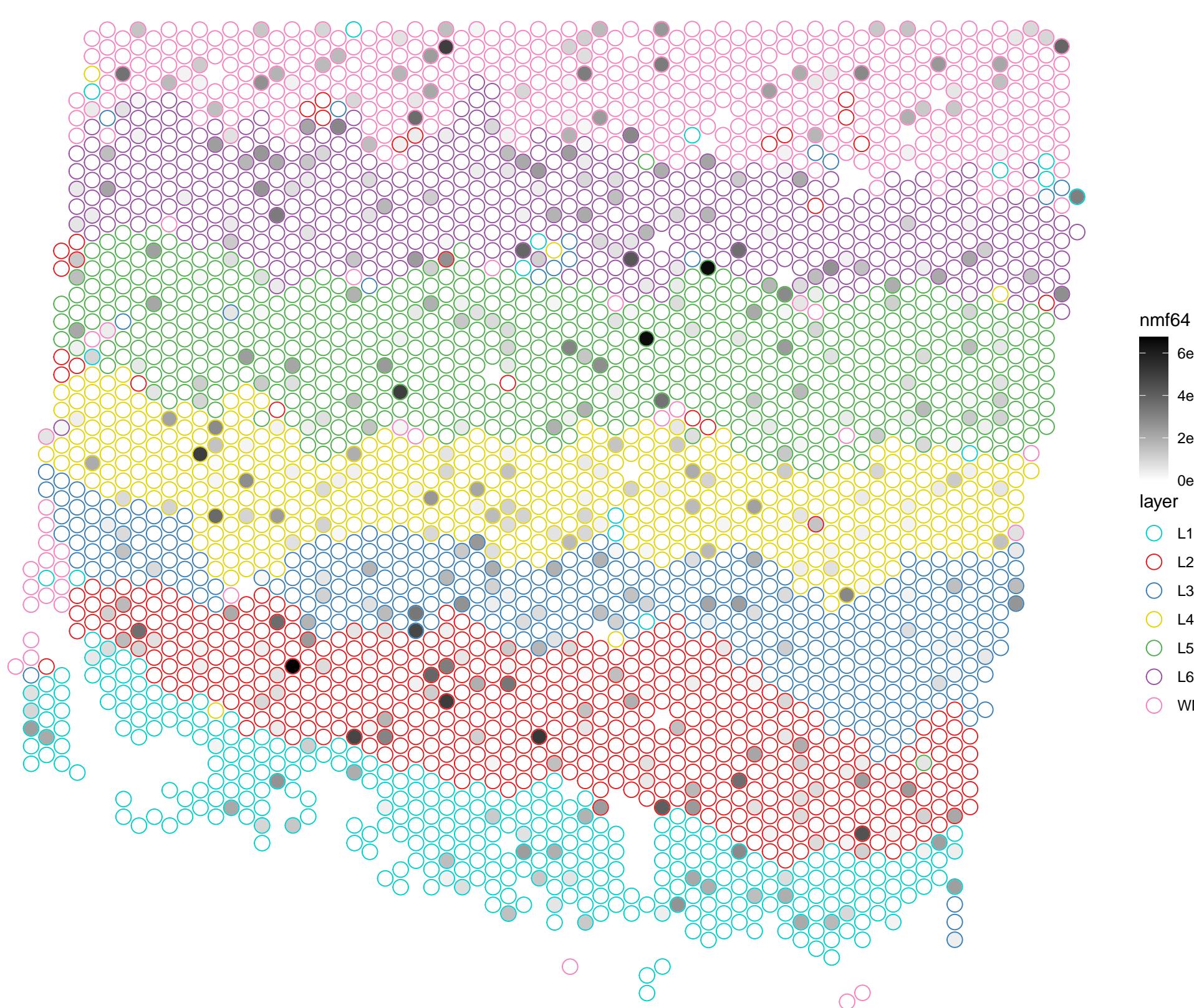
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

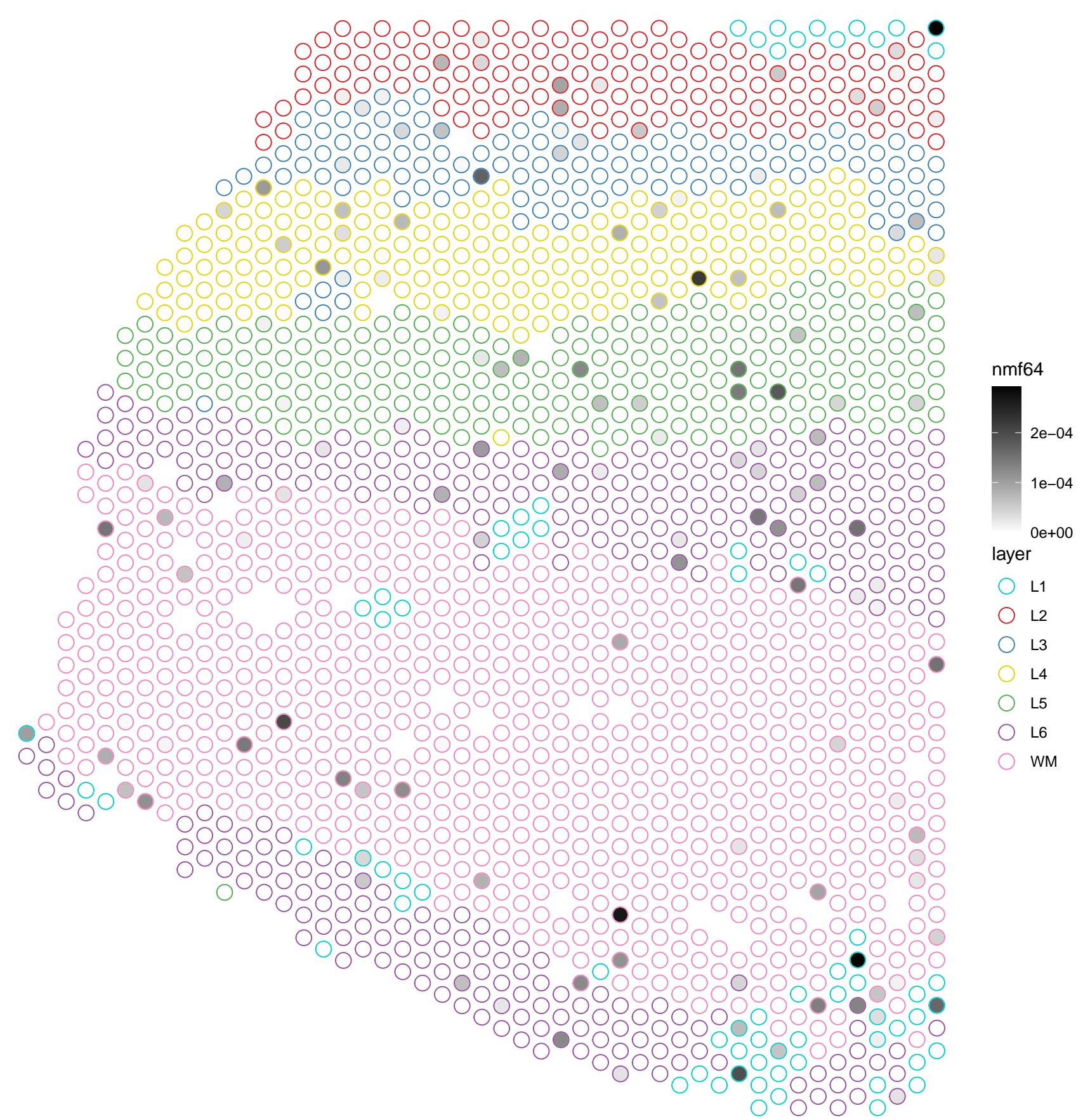
nmf64

- 4e-04
- 3e-04
- 2e-04
- 1e-04
- 0e+00

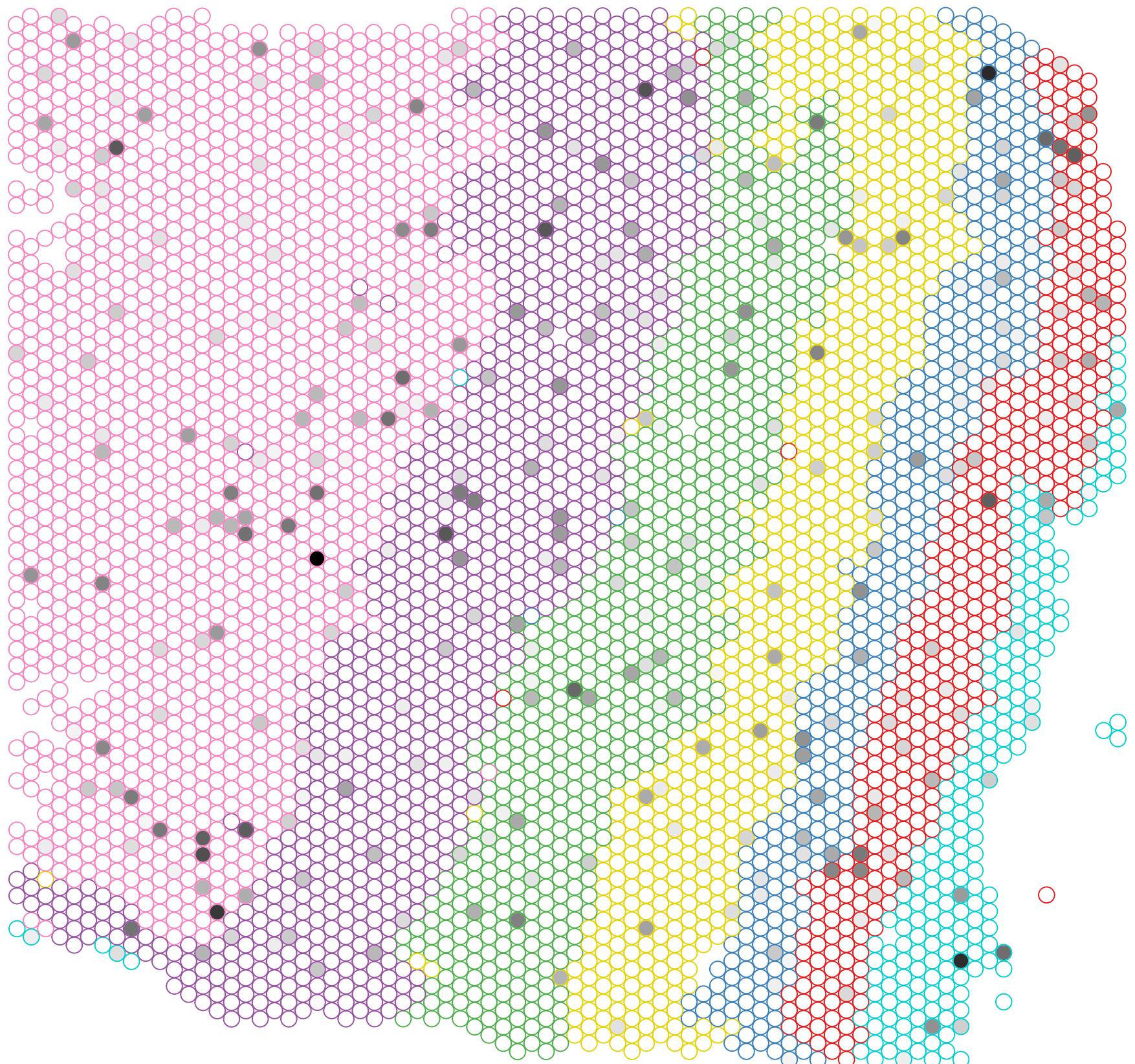
Sample Br2720_ant



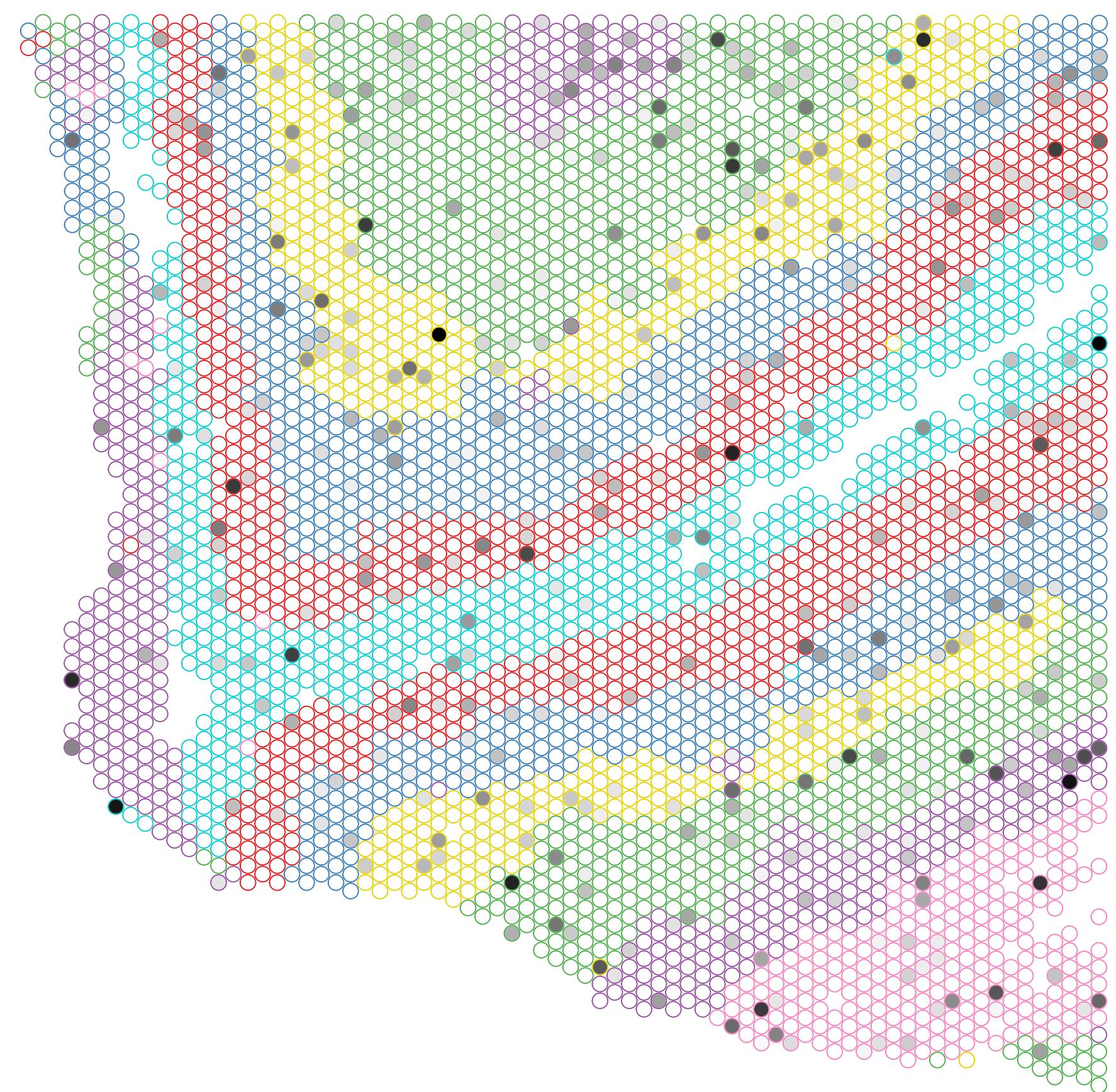
Sample Br2720_mid



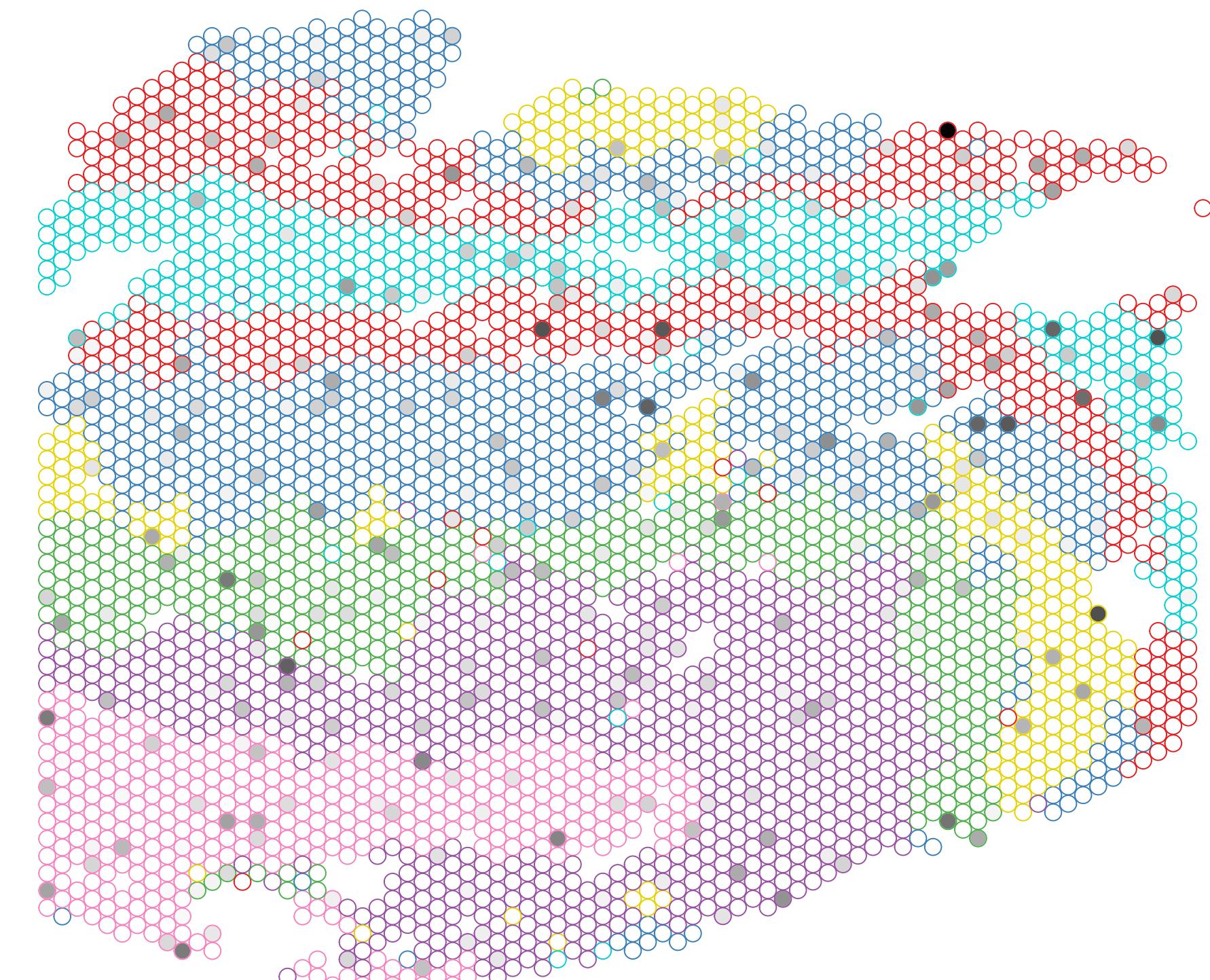
Sample Br2720_post



Sample Br6432_ant

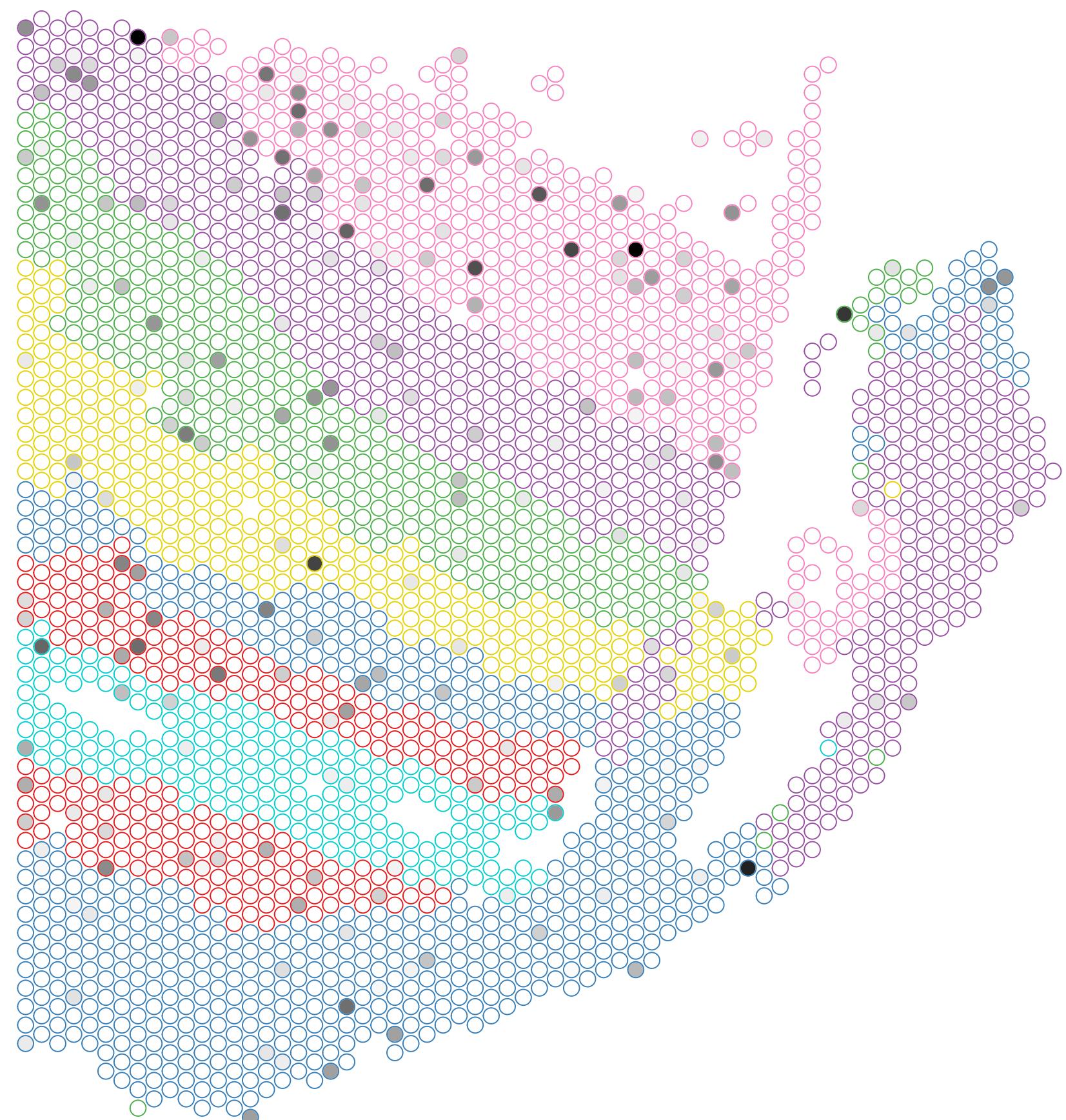


Sample Br6432_mid



layer
L1
L2
L3
L4
L5
L6
WM
nmf64
4e-04
3e-04
2e-04
1e-04
0e+00

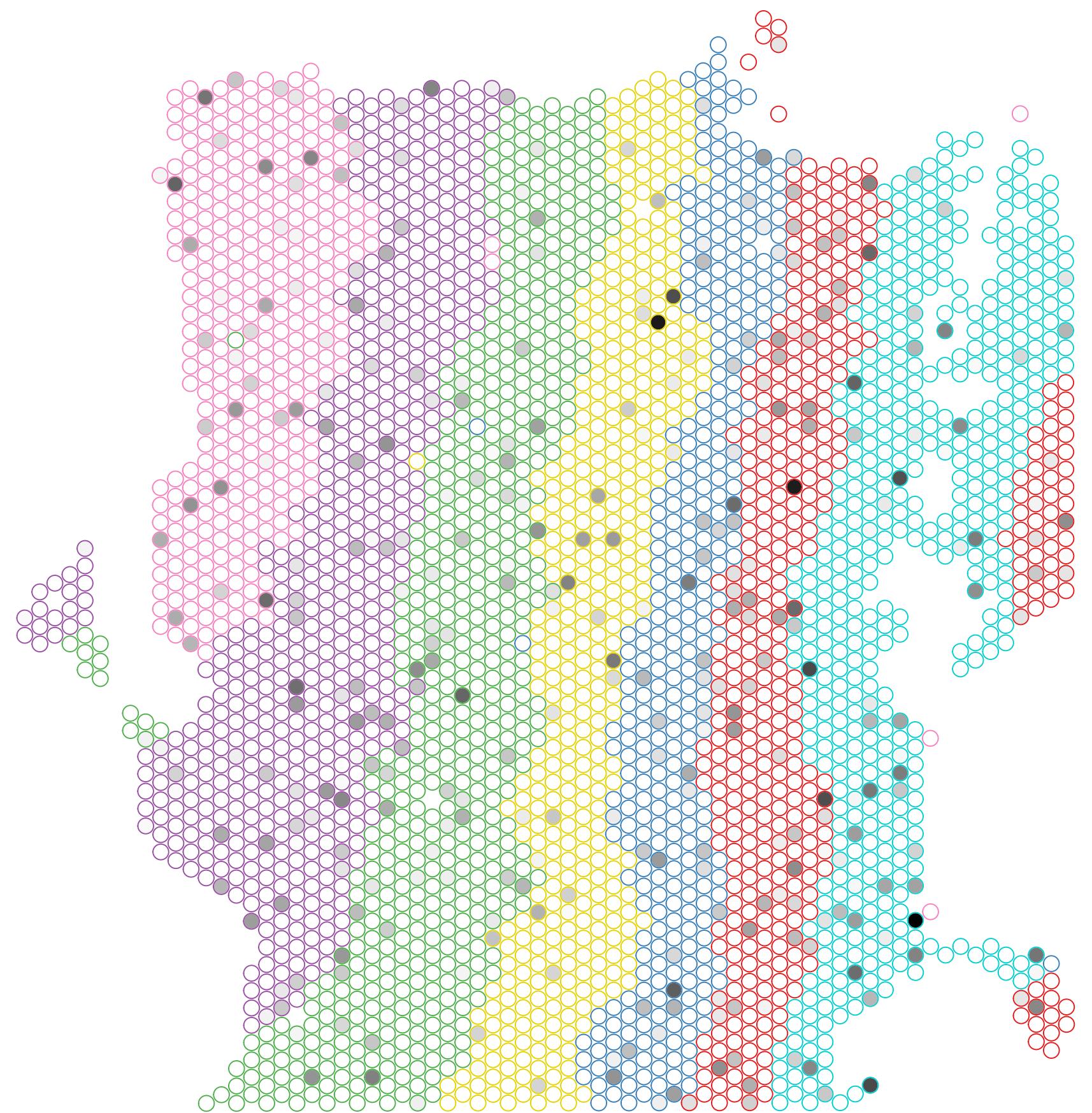
Sample Br6432_post



nmf64
3e-04
2e-04
1e-04
0e+00
layer
L1
L2
L3
L4
L5
L6
WM

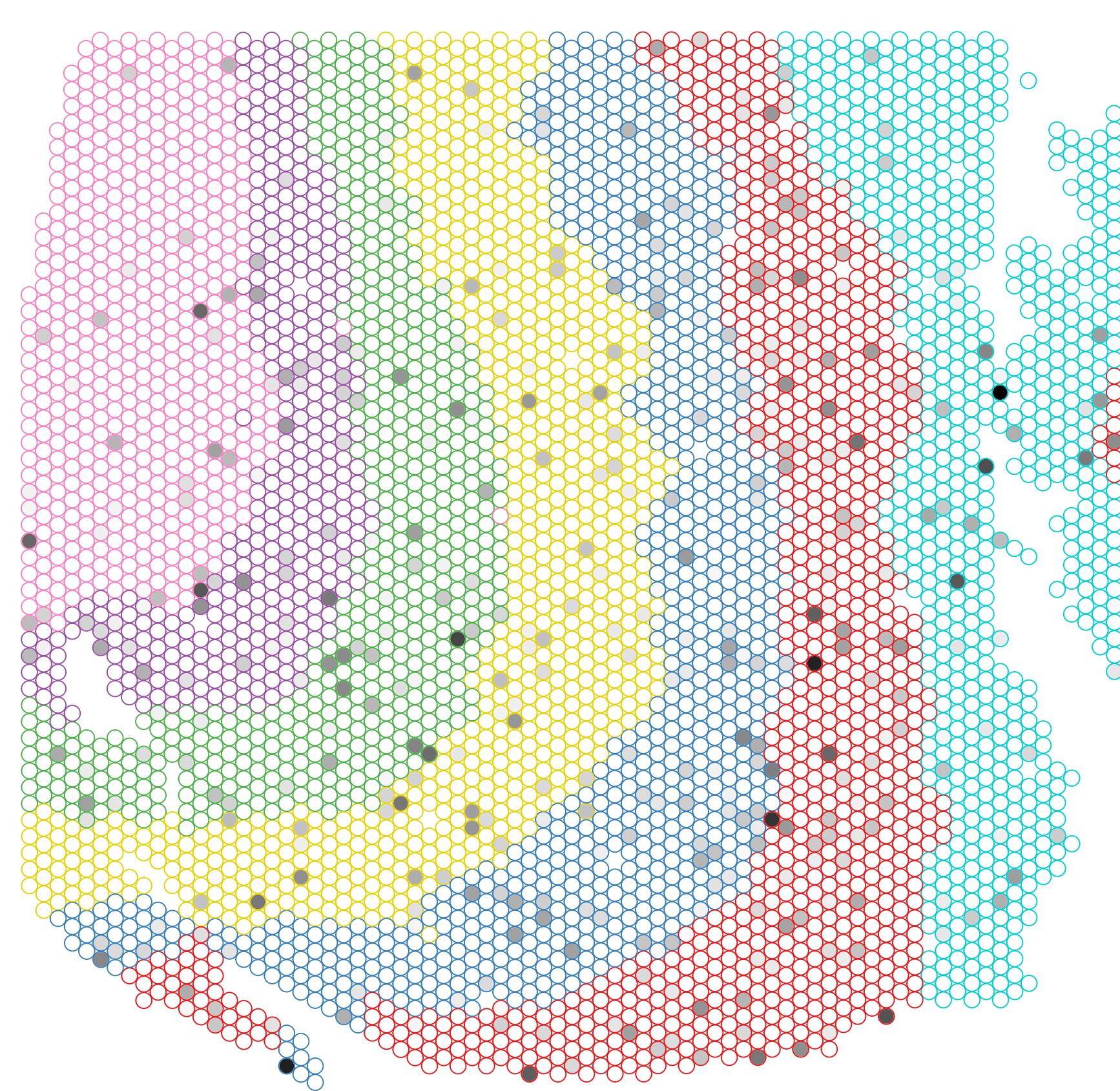
Sample Br6471_ant

Sample Br6471_mid

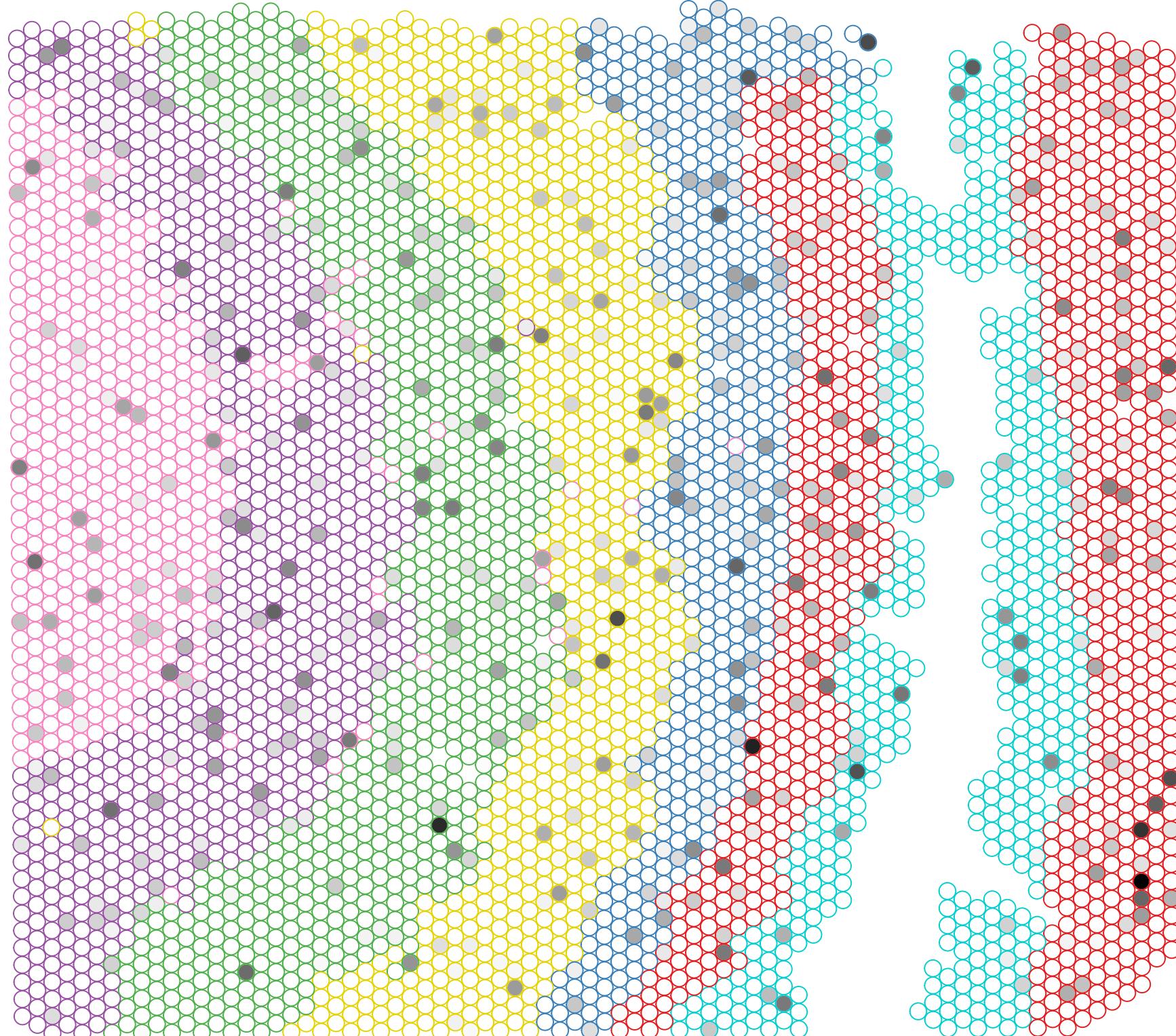


nmf64
layer
L1
L2
L3
L4
L5
L6
WM

nmf64
layer
L1
L2
L3
L4
L5
L6
WM

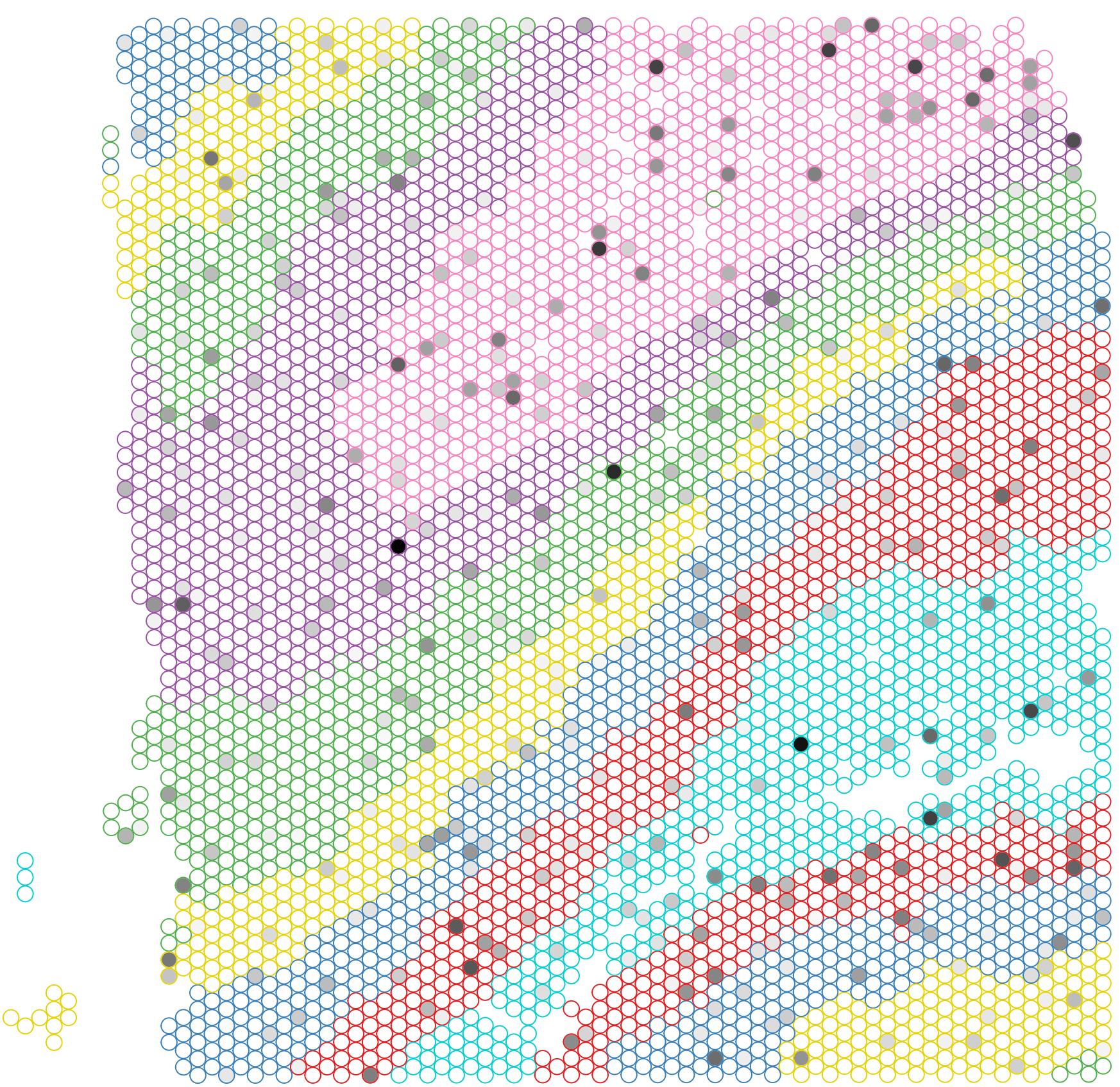


Sample Br6471_post

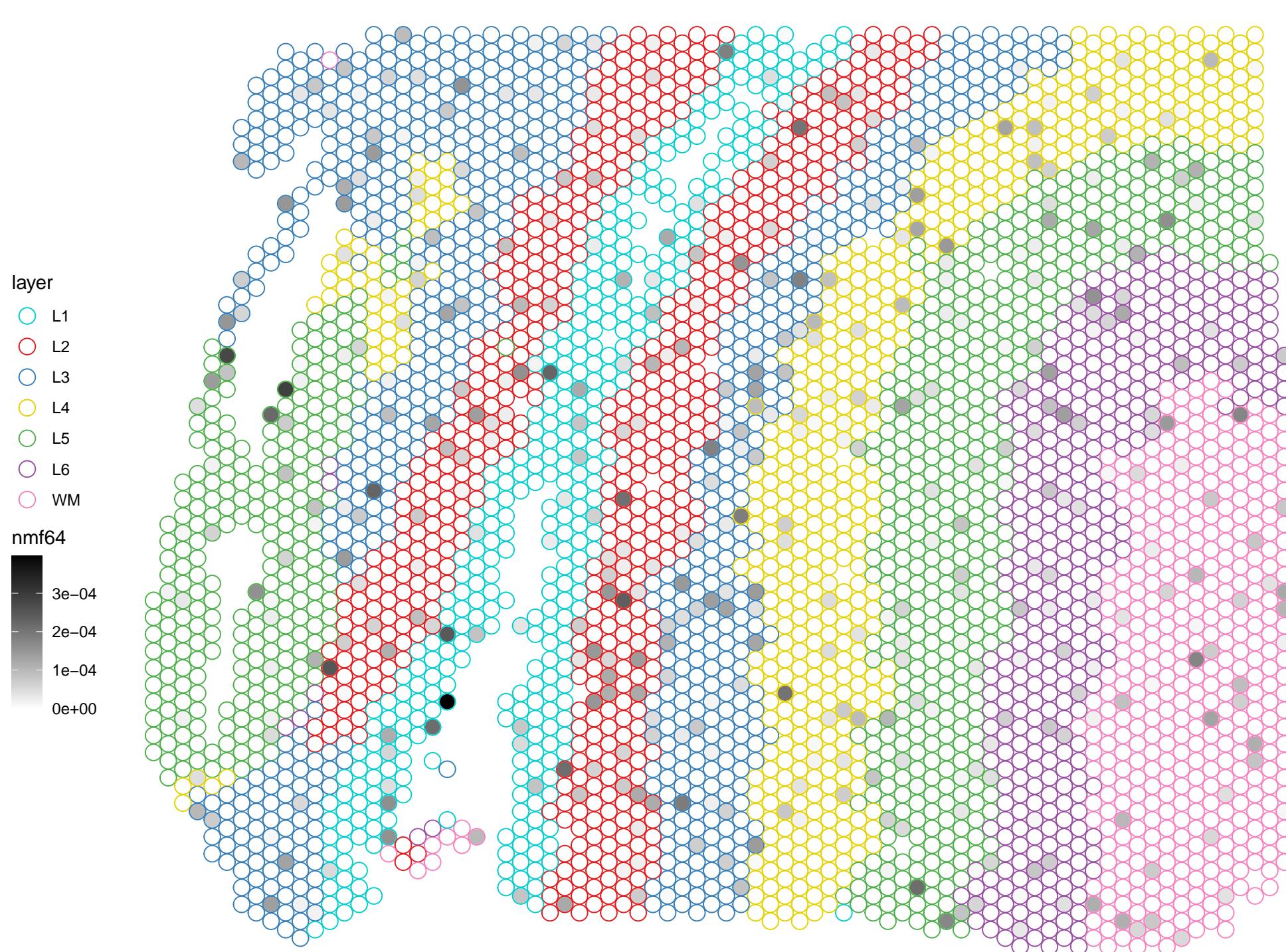


nmf64
layer
L1
L2
L3
L4
L5
L6
WM

Sample Br6522_ant



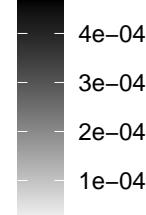
Sample Br6522_mid



Sample Br6522_post



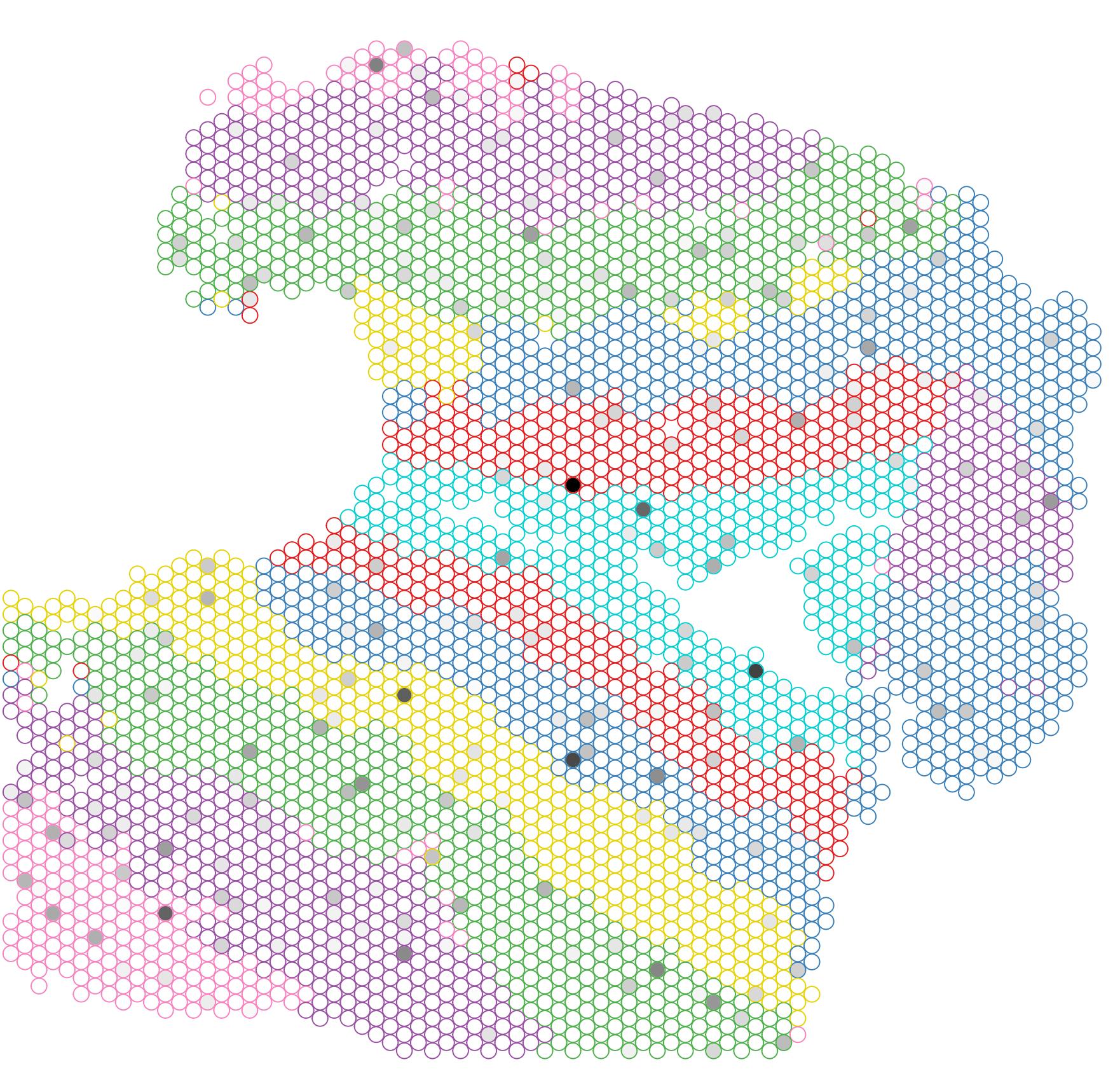
nmf64



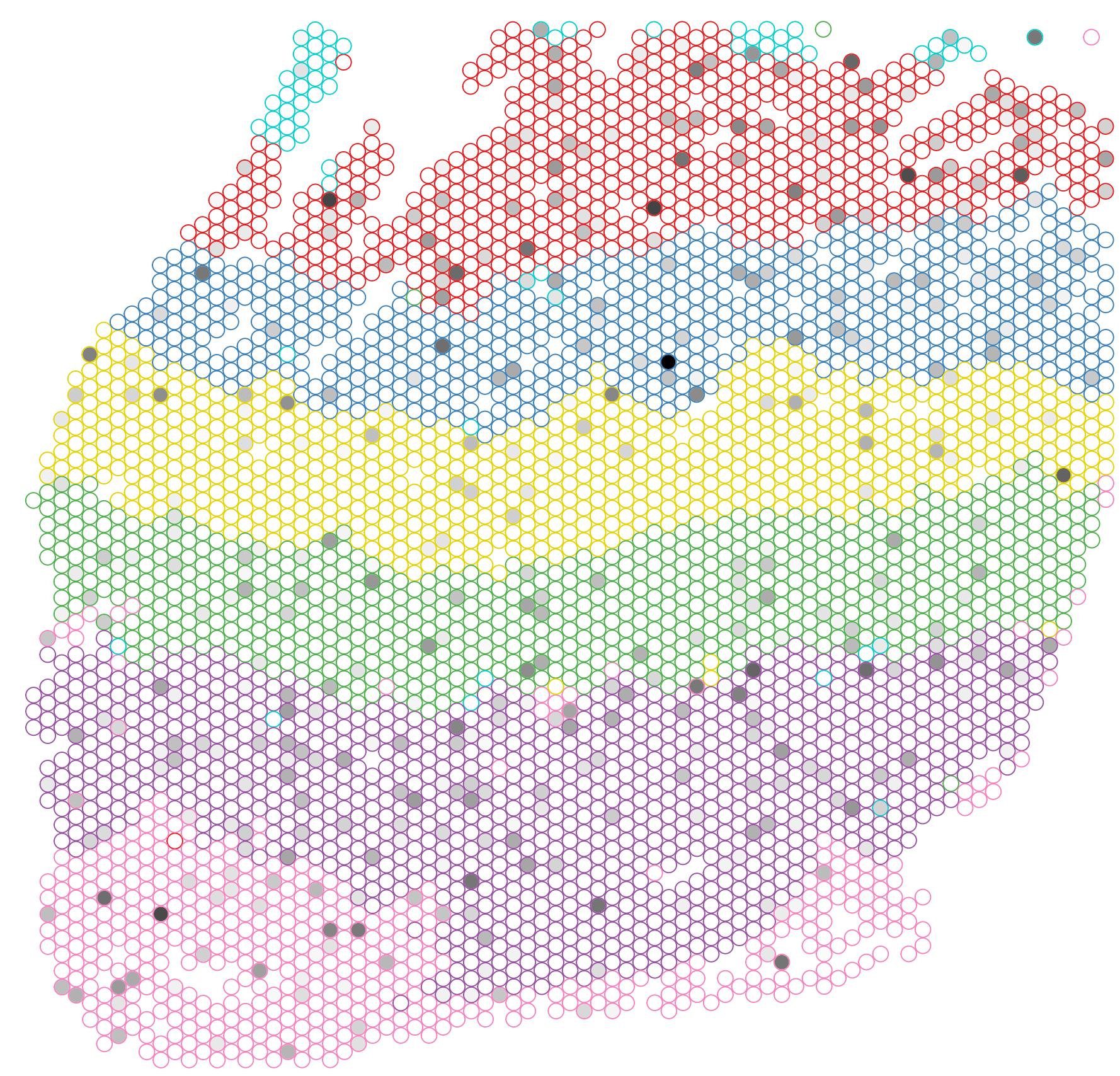
layer



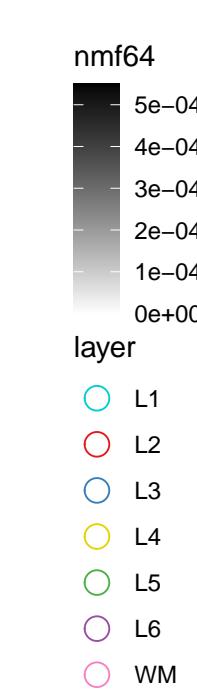
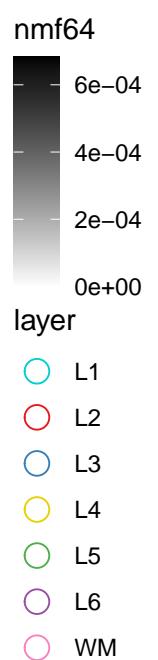
Sample Br8325_ant



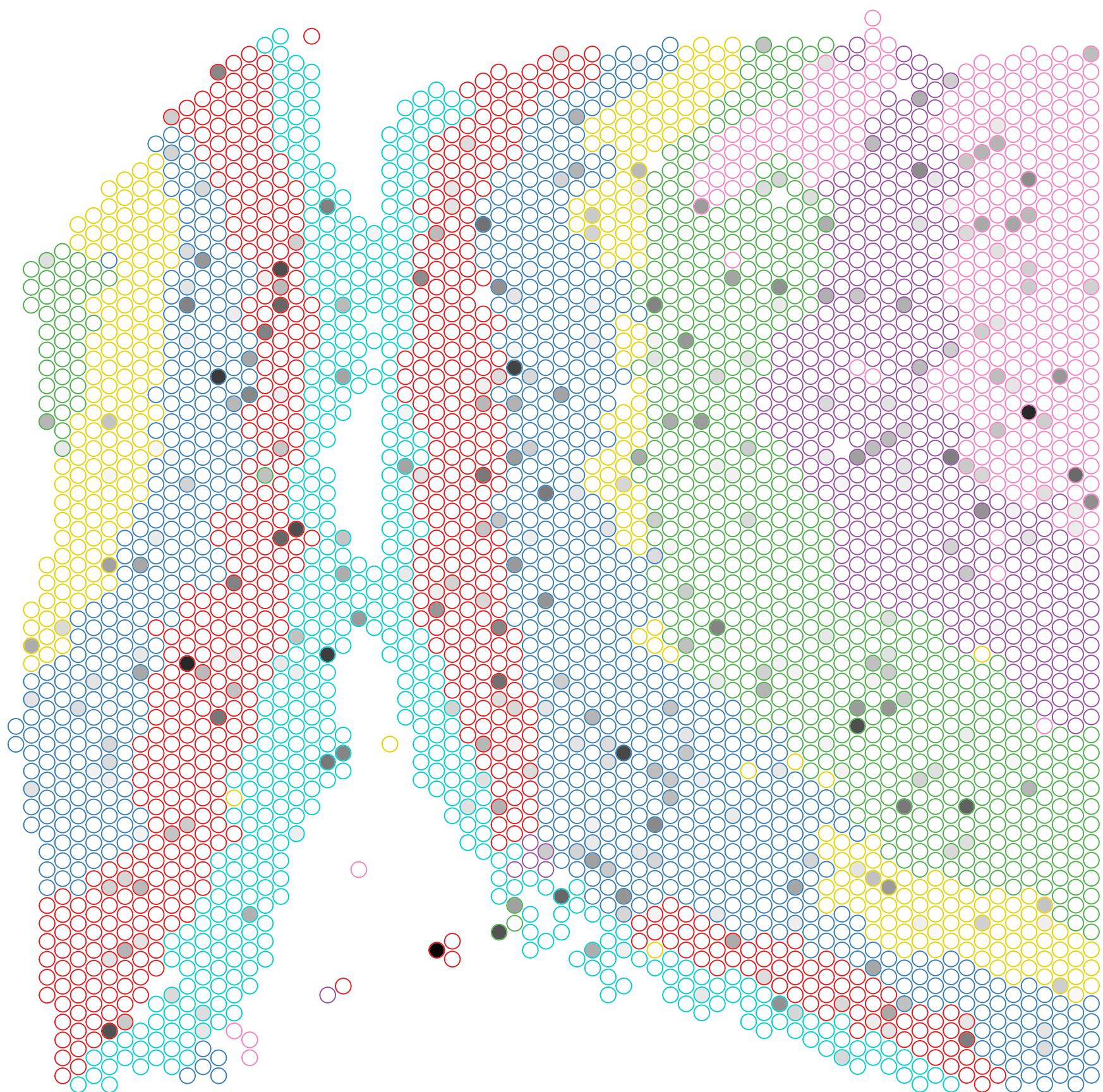
Sample Br8325_mid



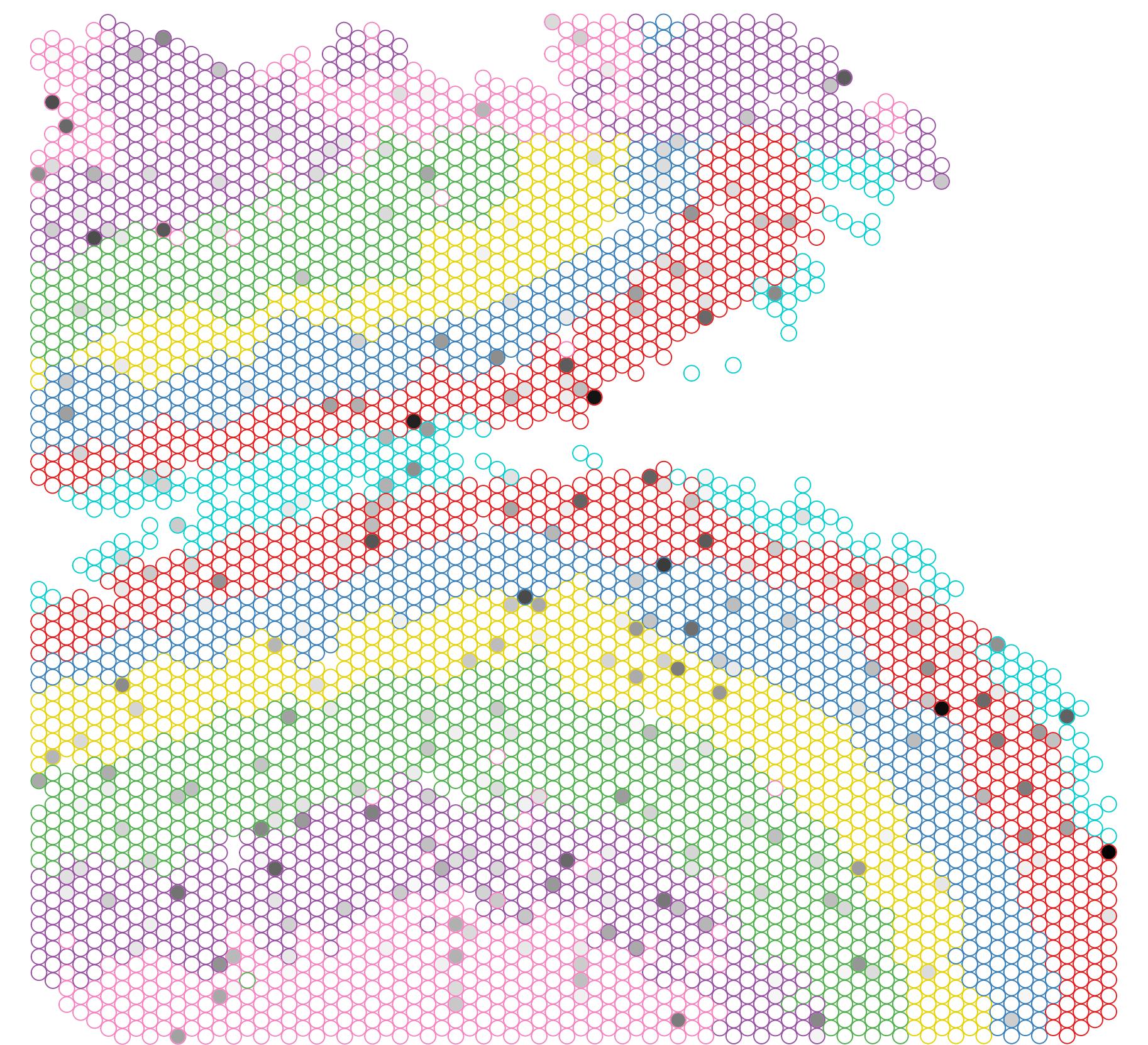
Sample Br8325_post



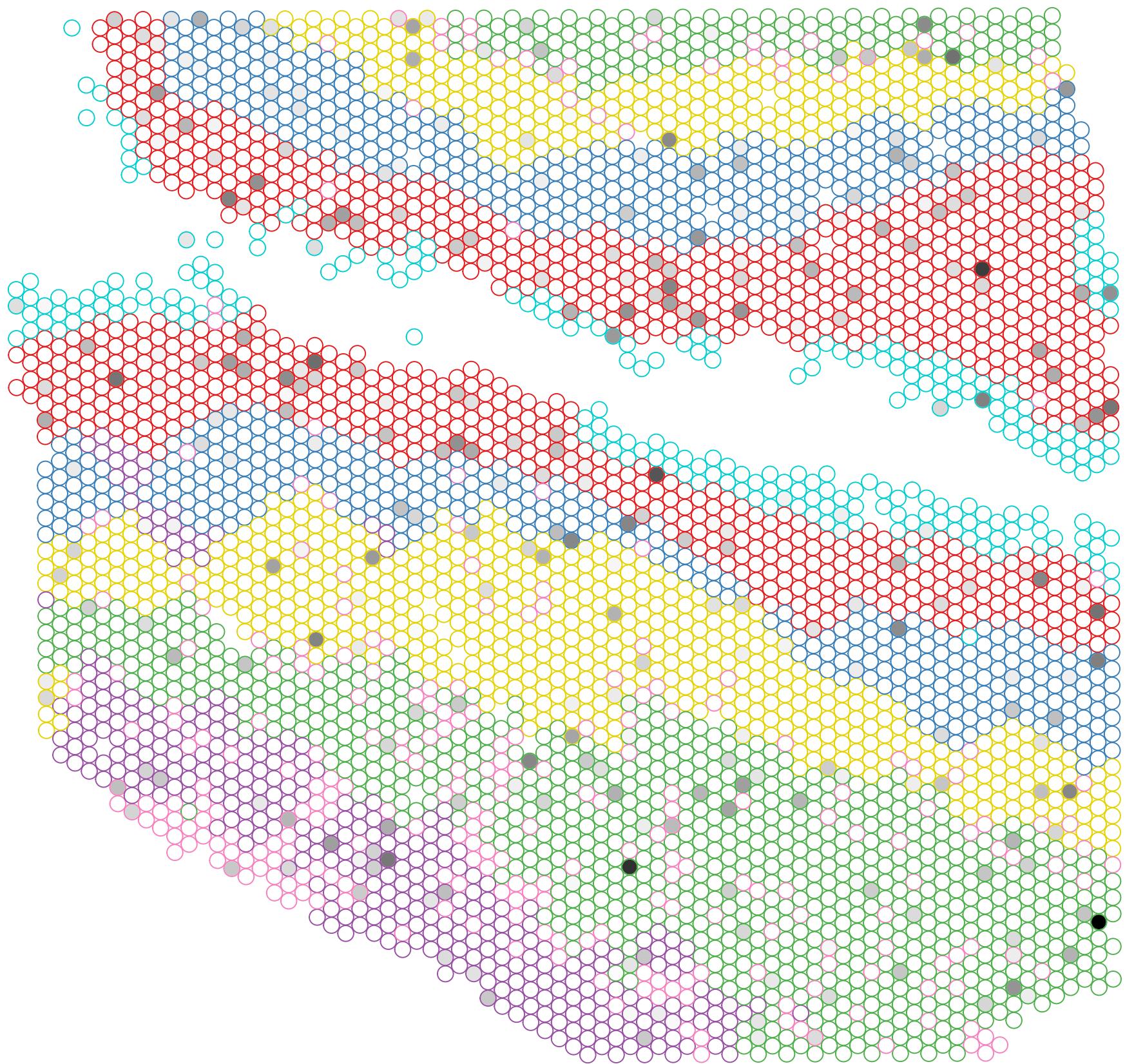
Sample Br8667_ant



Sample Br8667_mid



Sample Br8667_post



nmf64

4e-04
3e-04
2e-04
1e-04
0e+00

layer

L1
L2
L3
L4
L5
L6
WM