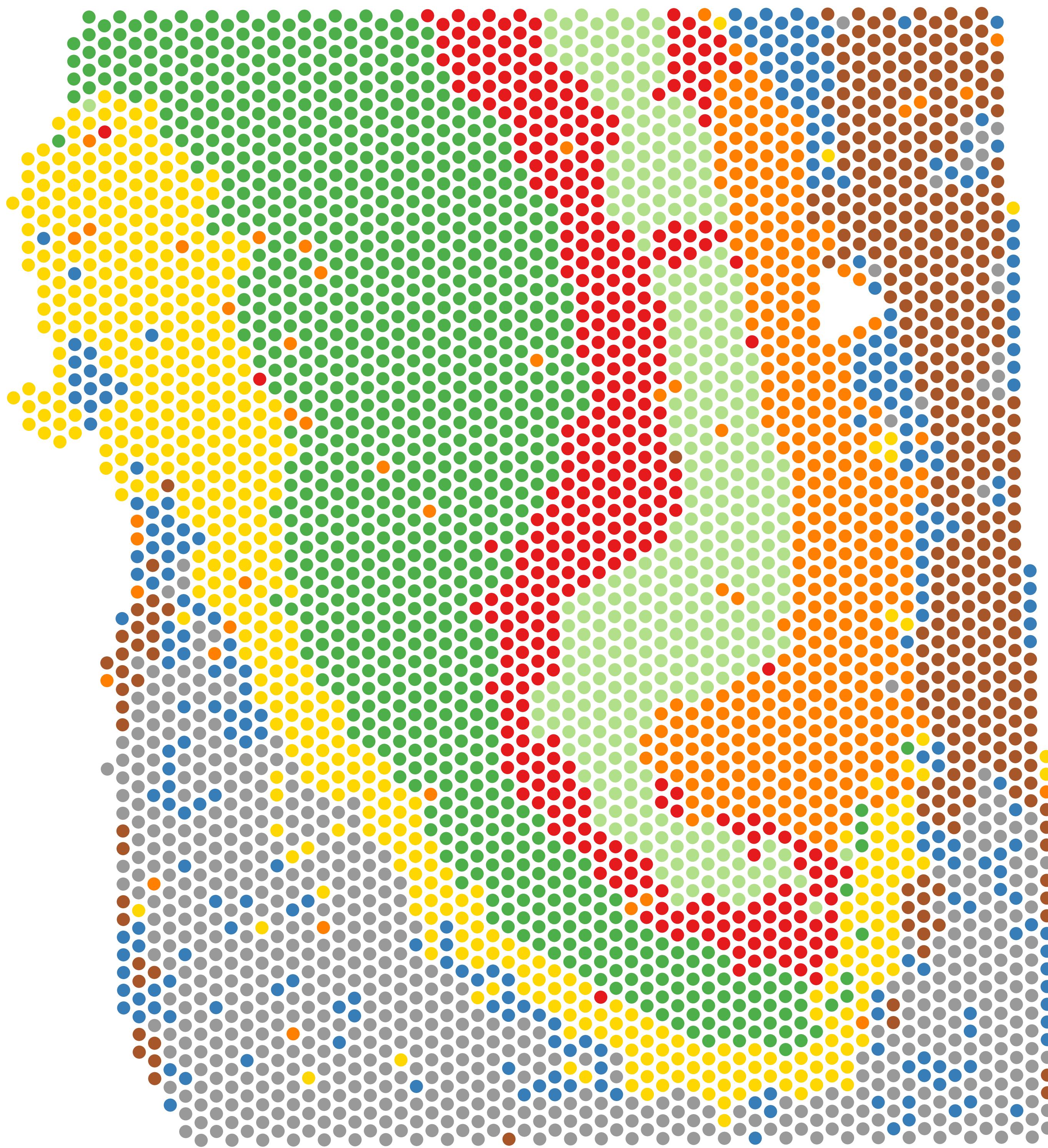
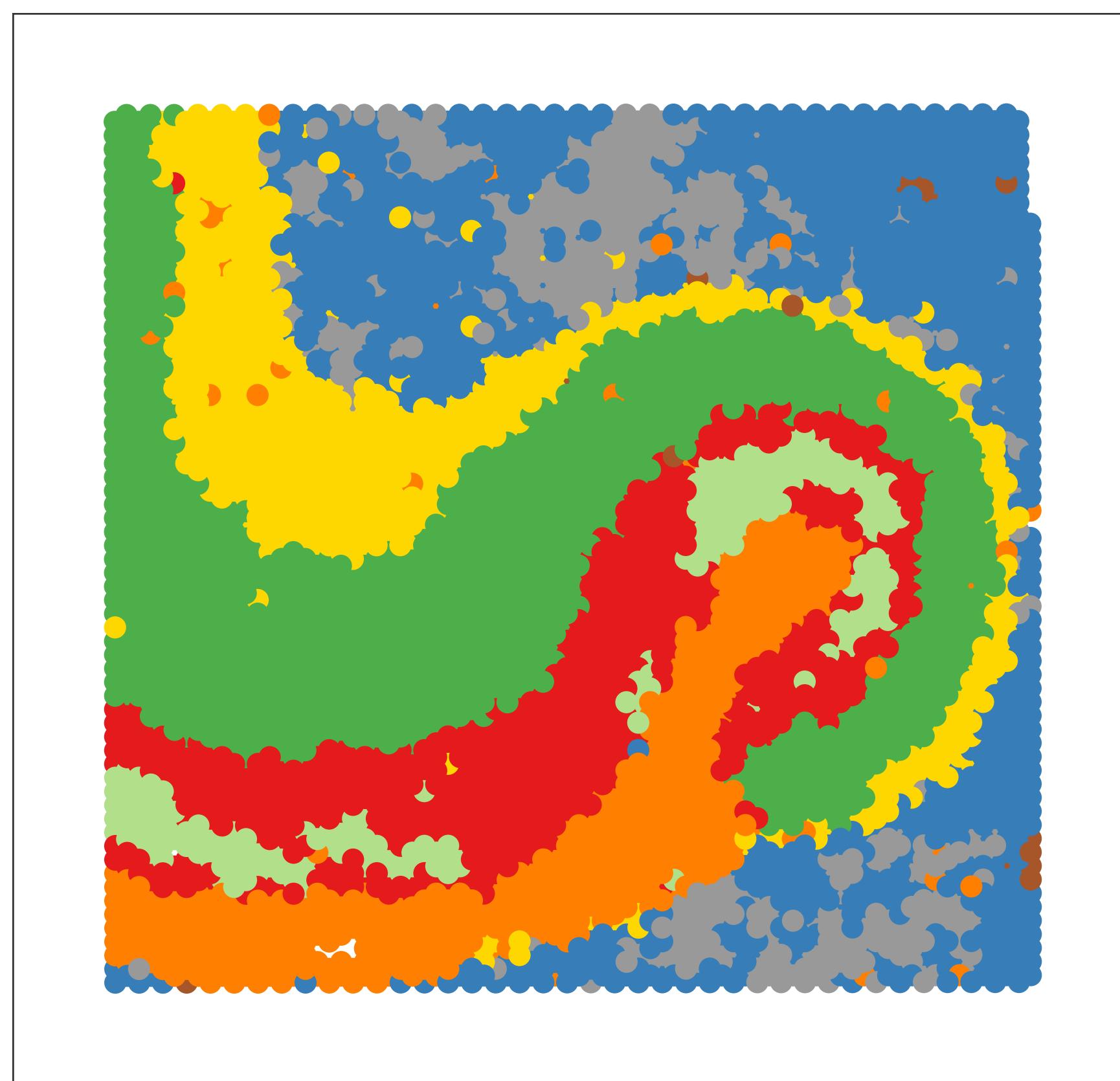


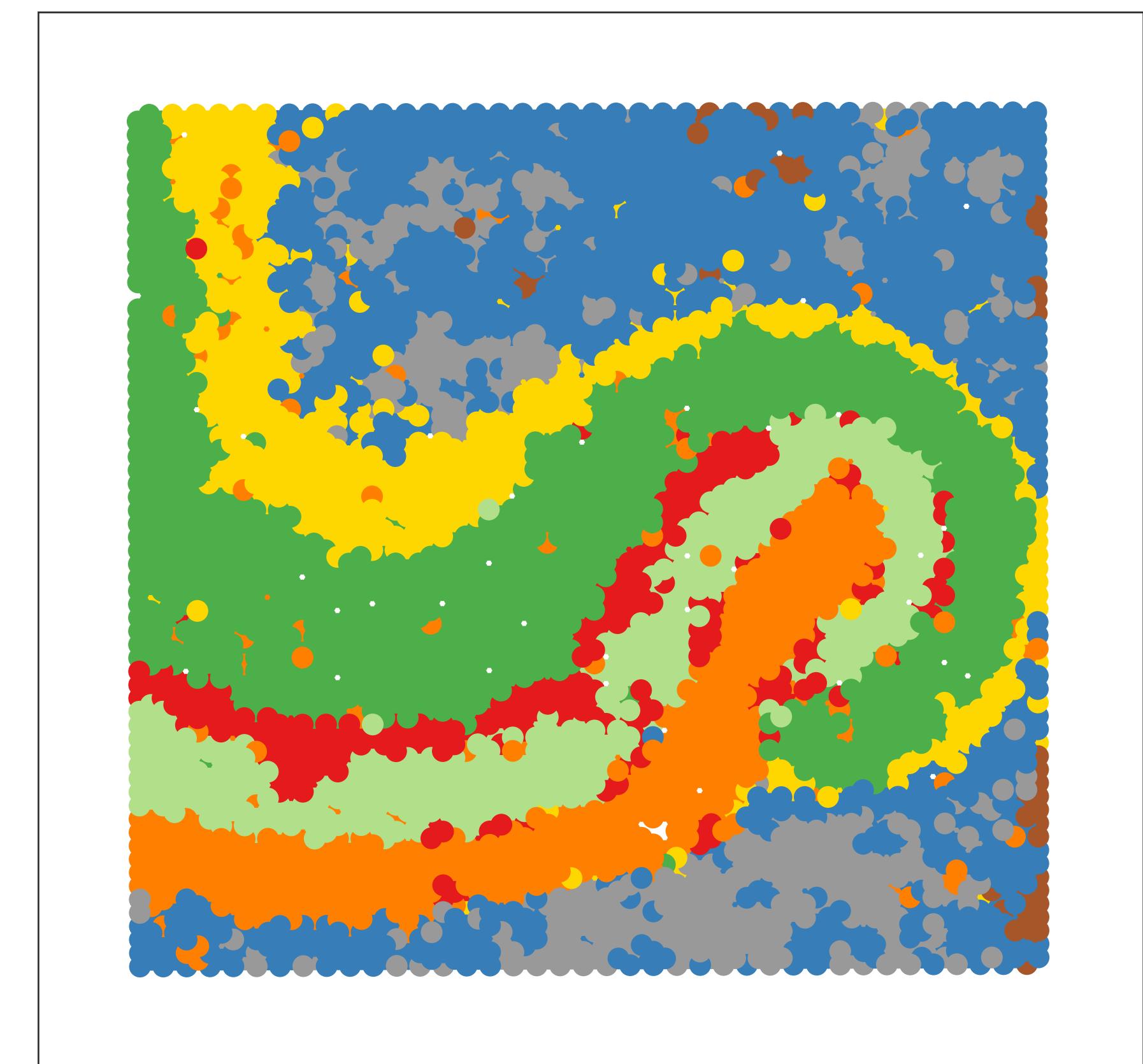
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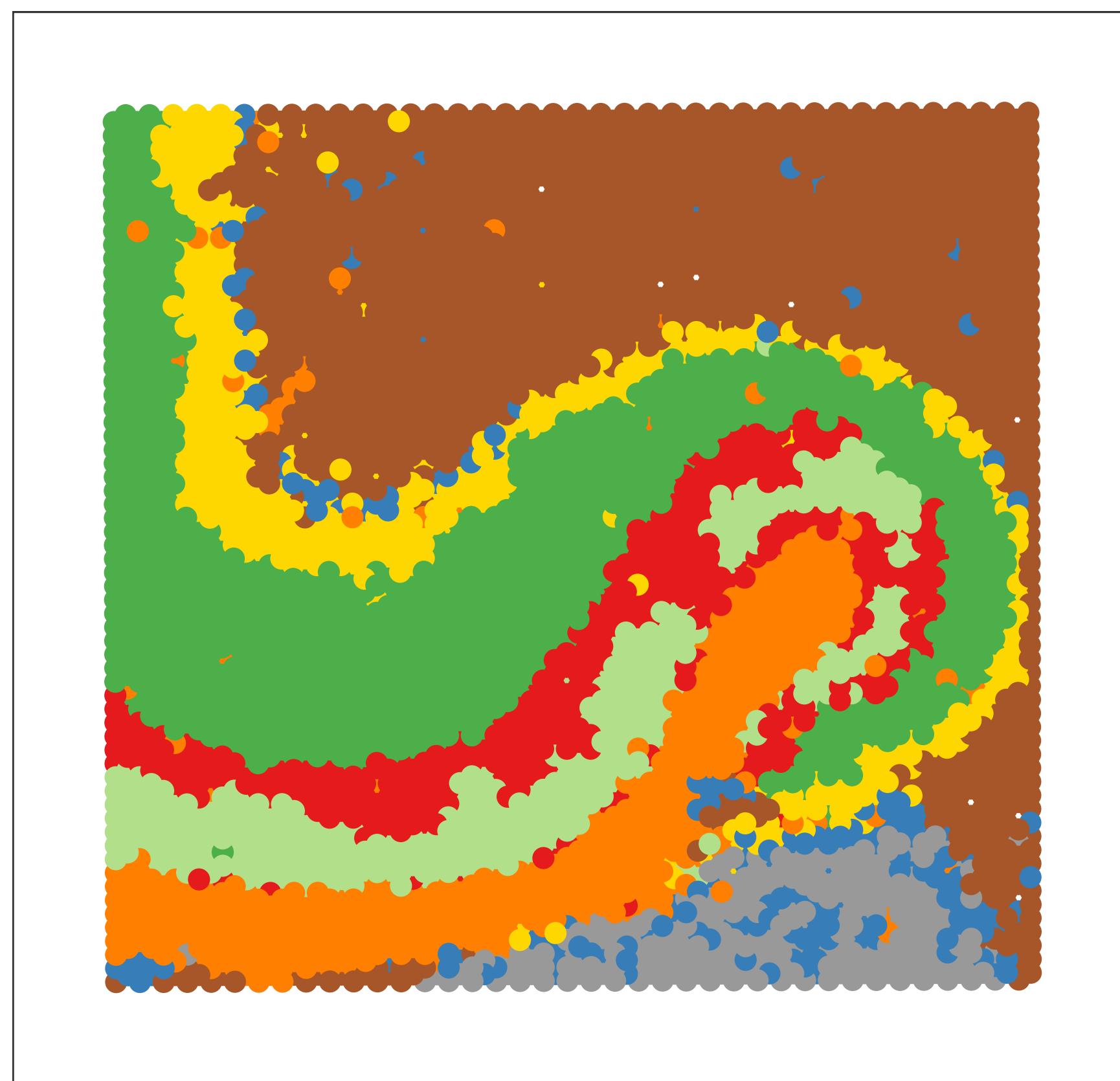
V12J03–002_A1_Br2720



V12N28–331_A1_Br2720



V12N28–332_A1_Br2720

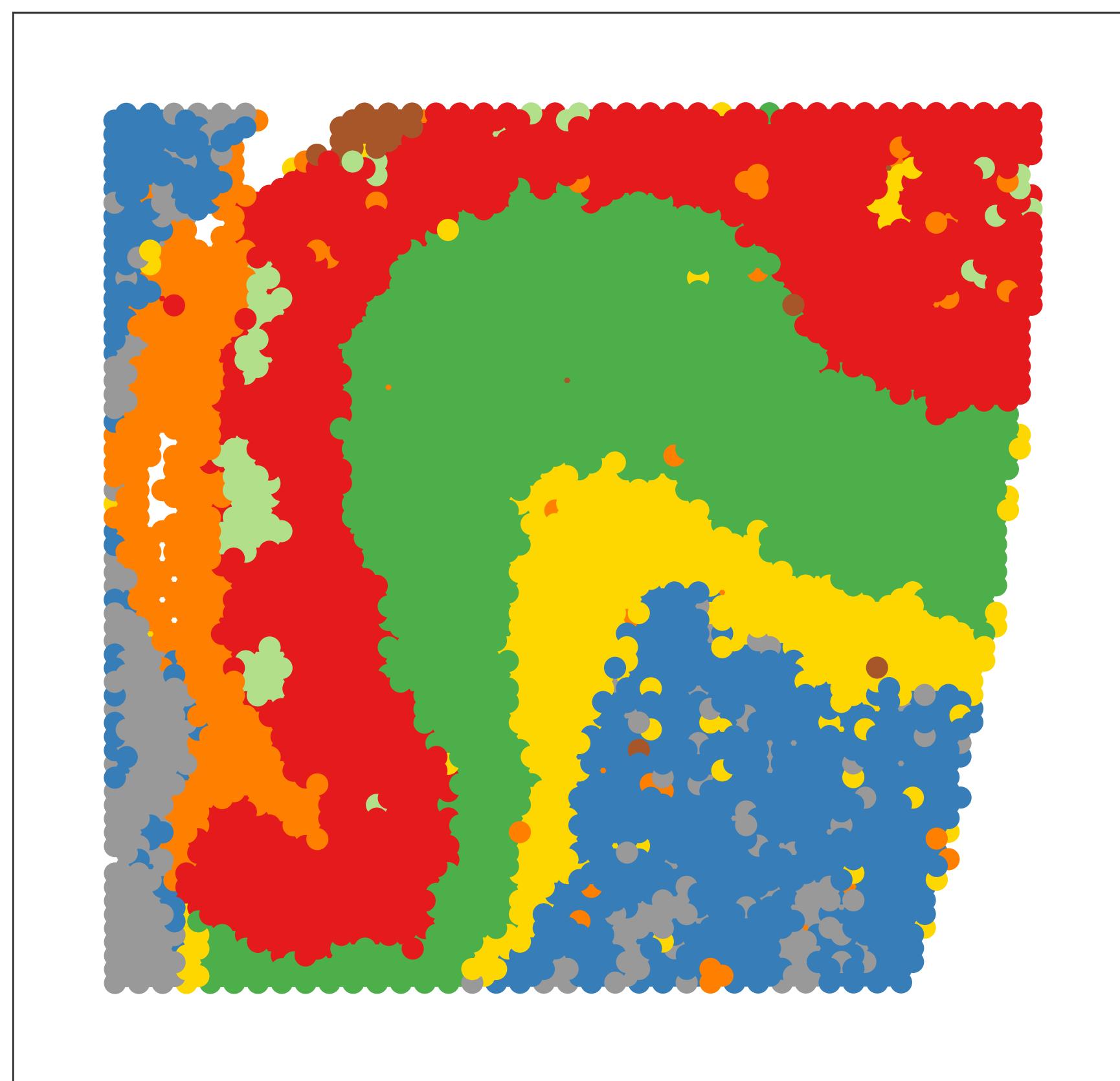


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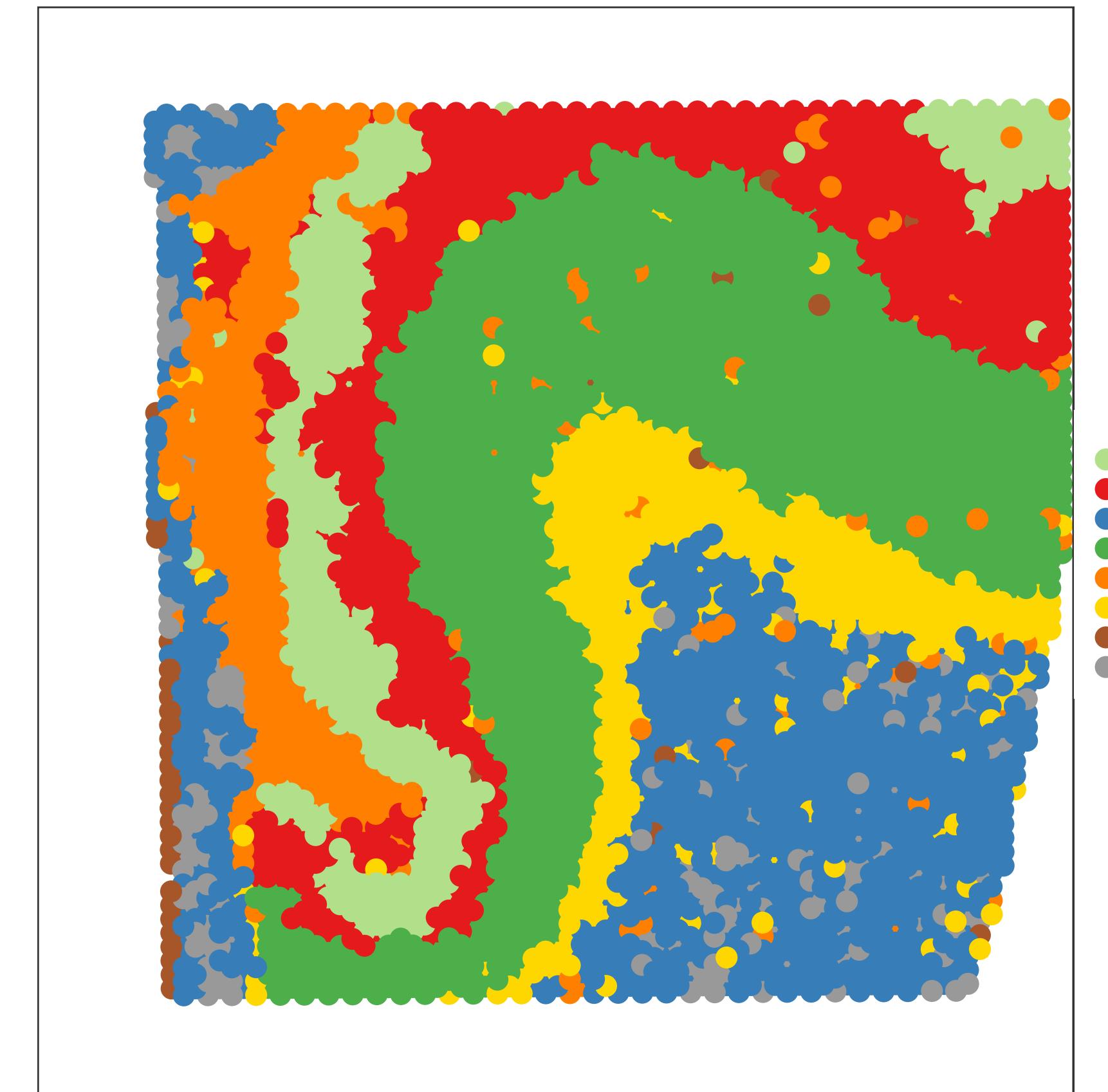
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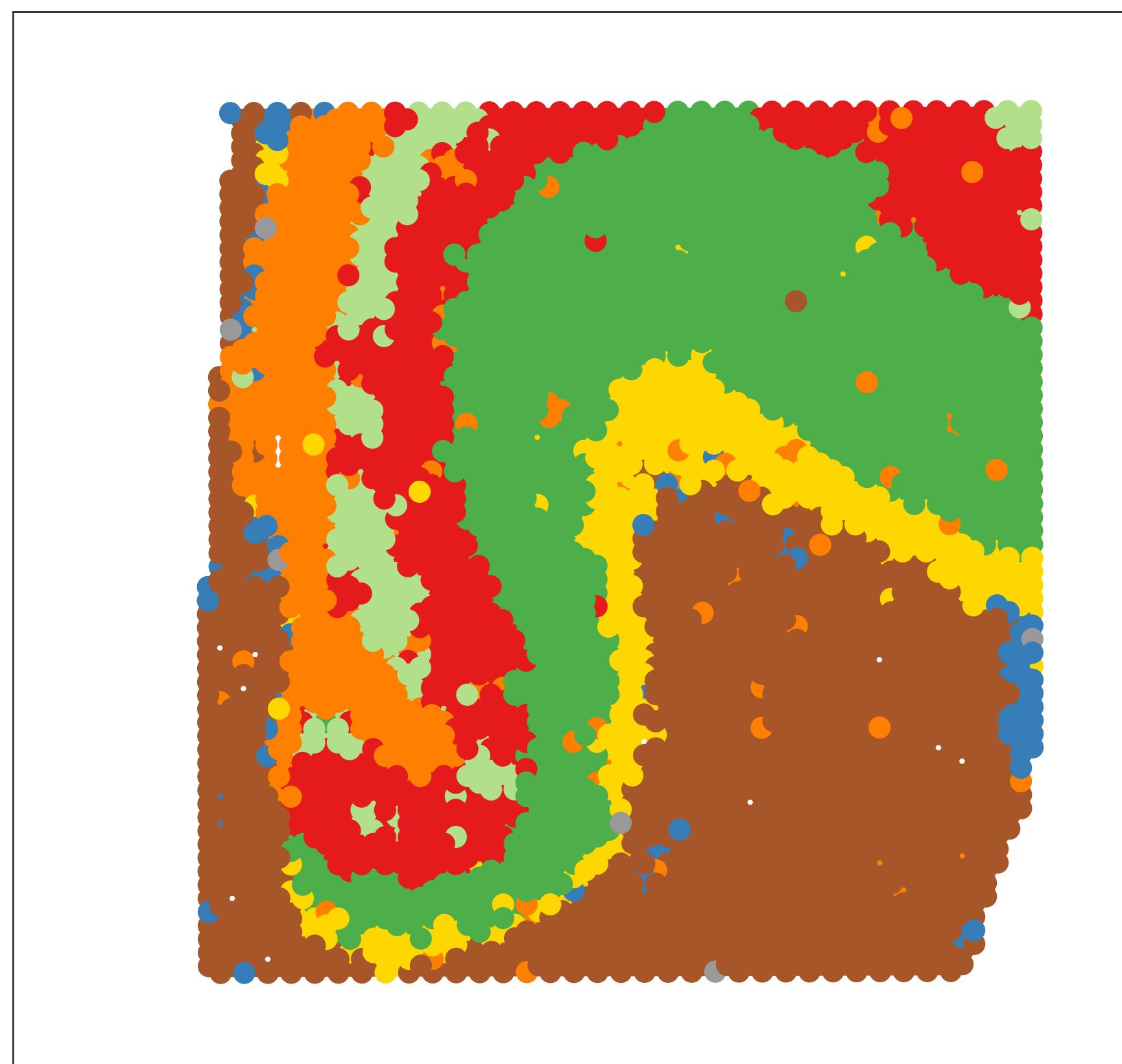
V12J03-002_B1_Br6432



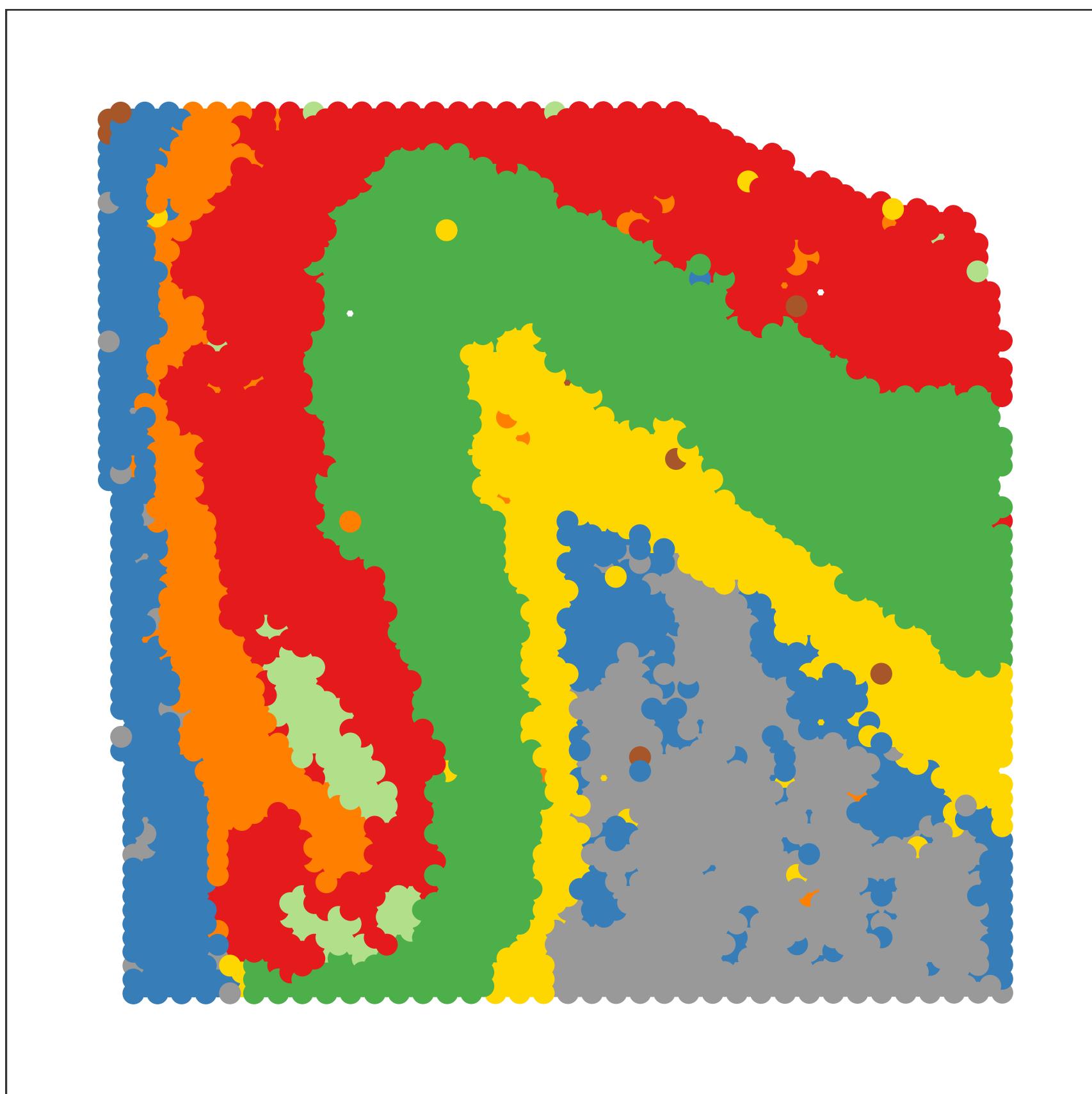
V12N28-331_B1_Br6432



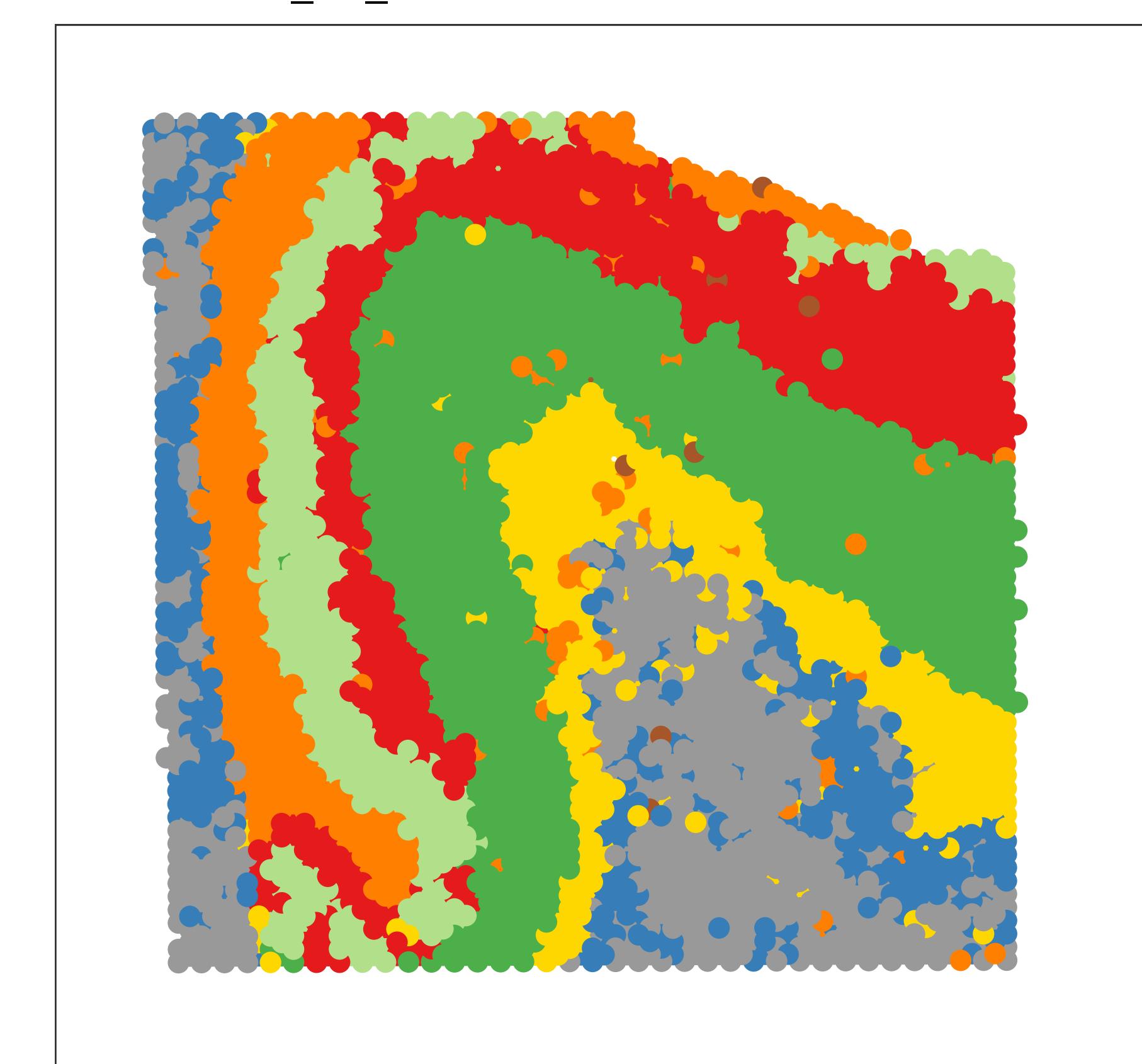
V12N28-332_B1_Br6432



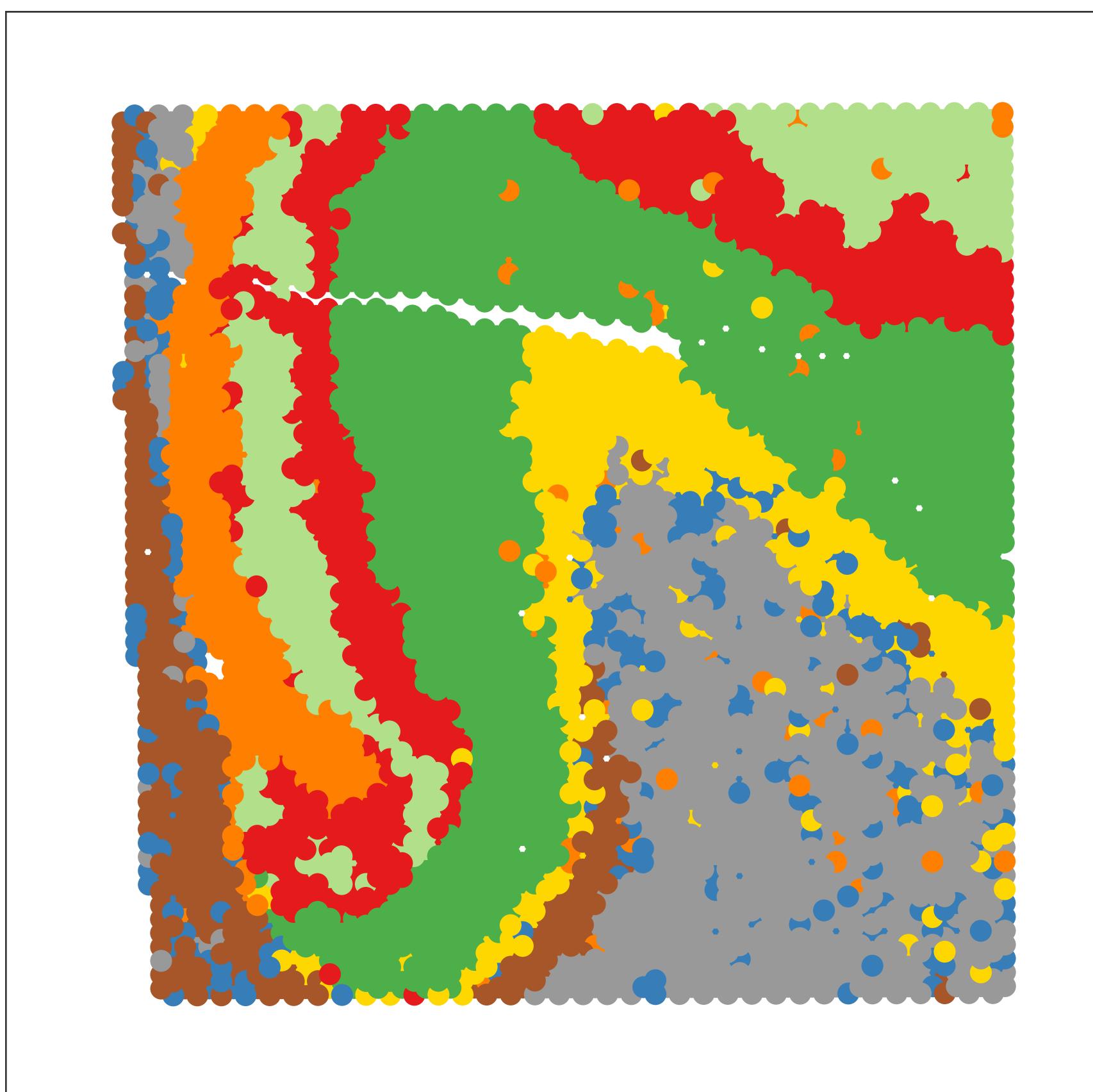
V12J03-002_C1_Br6471



V12N28-331_C1_Br6471



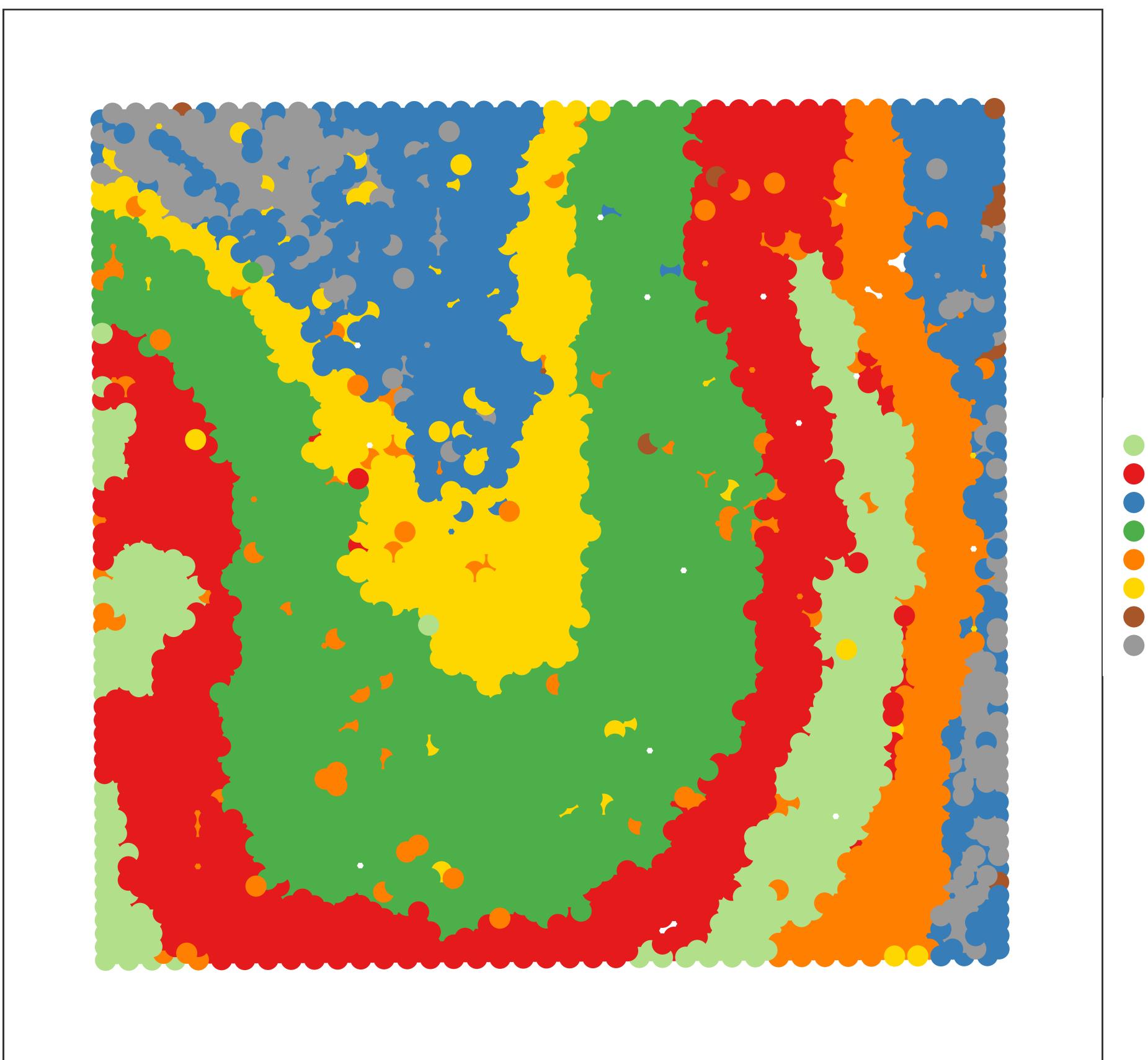
V12N28-332_C1_Br6471



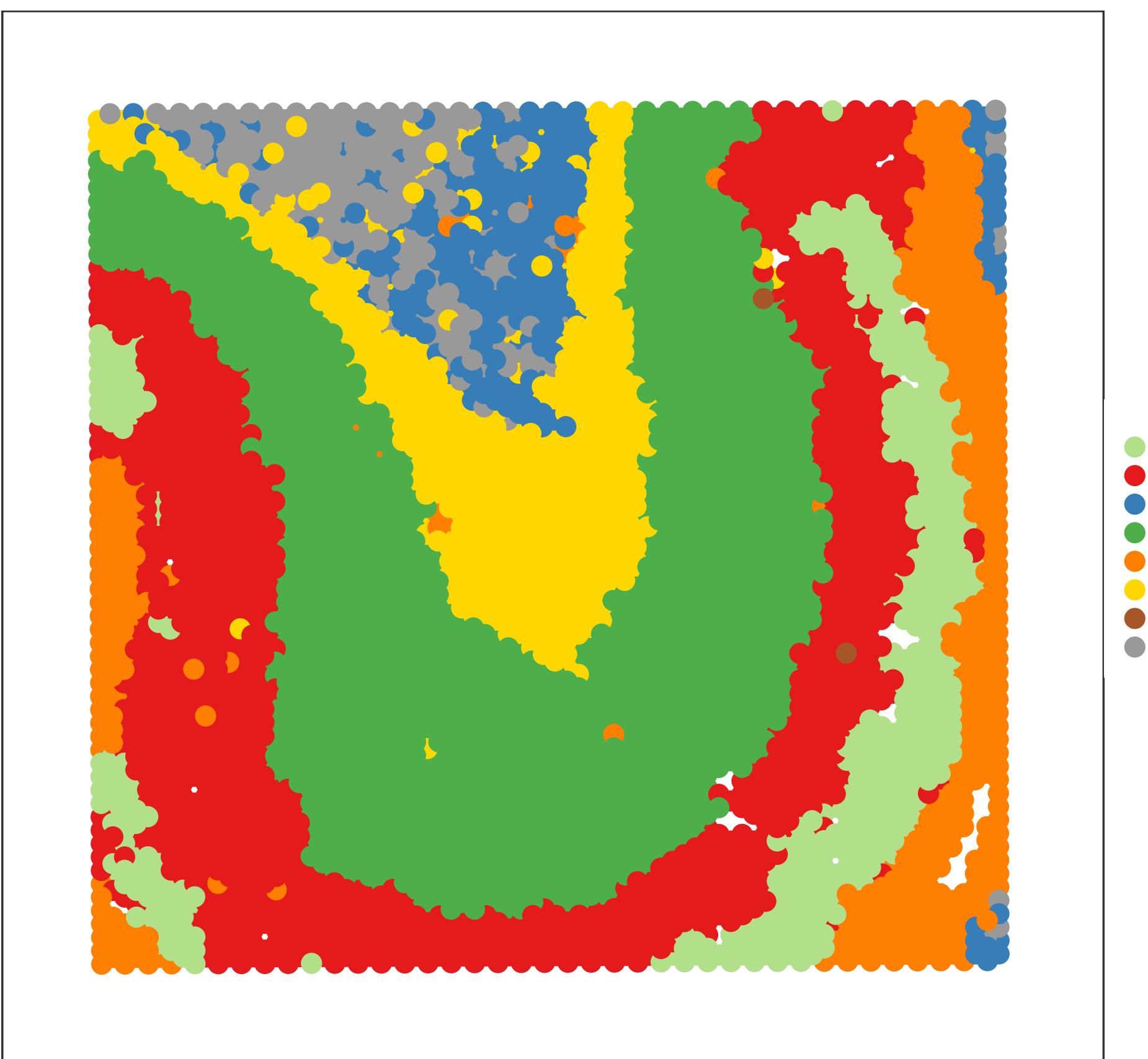
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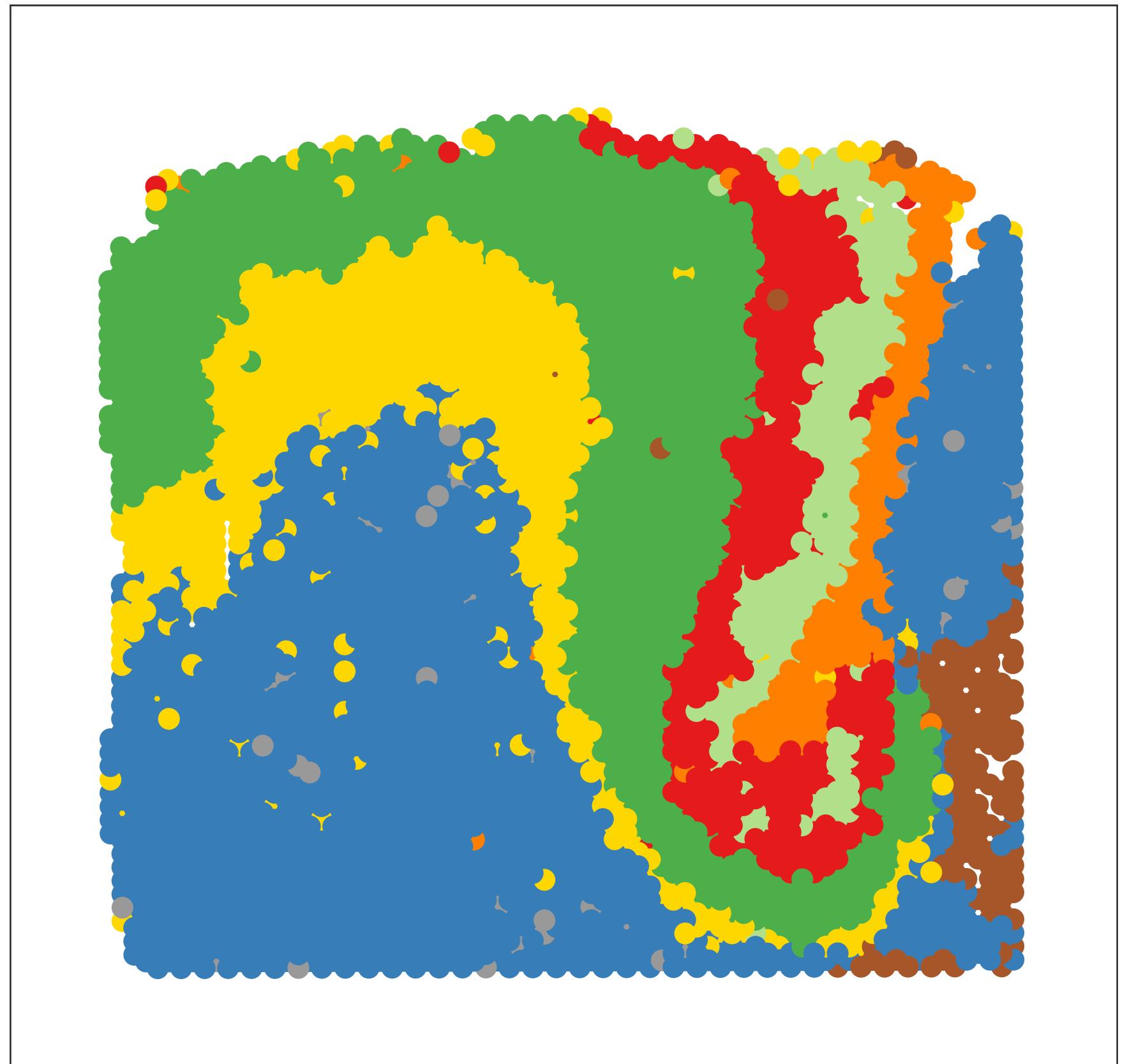
V12N28–331_D1_Br6522



V12N28–332_D1_Br6522



V12N28–334_C1_Br2743

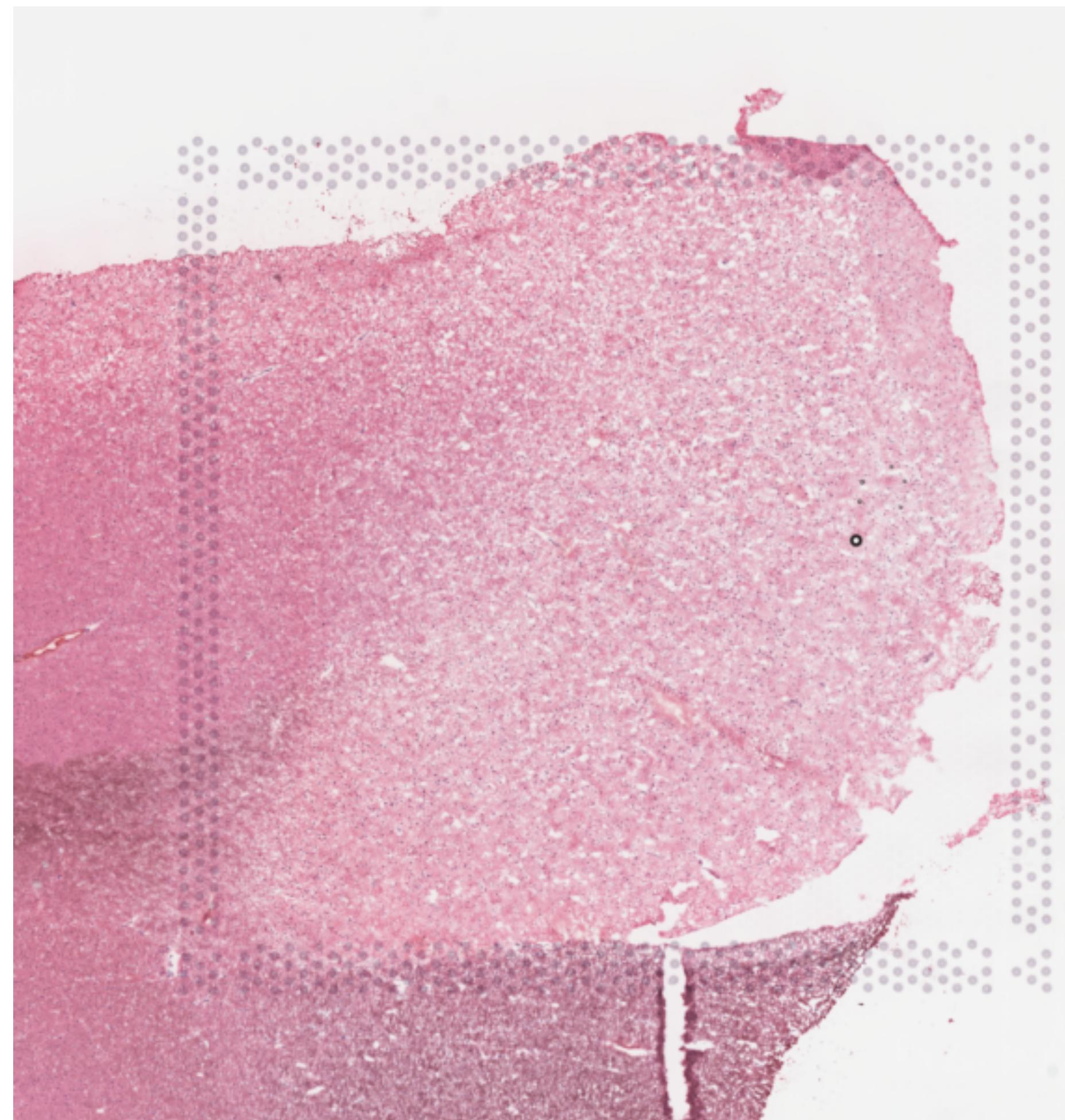
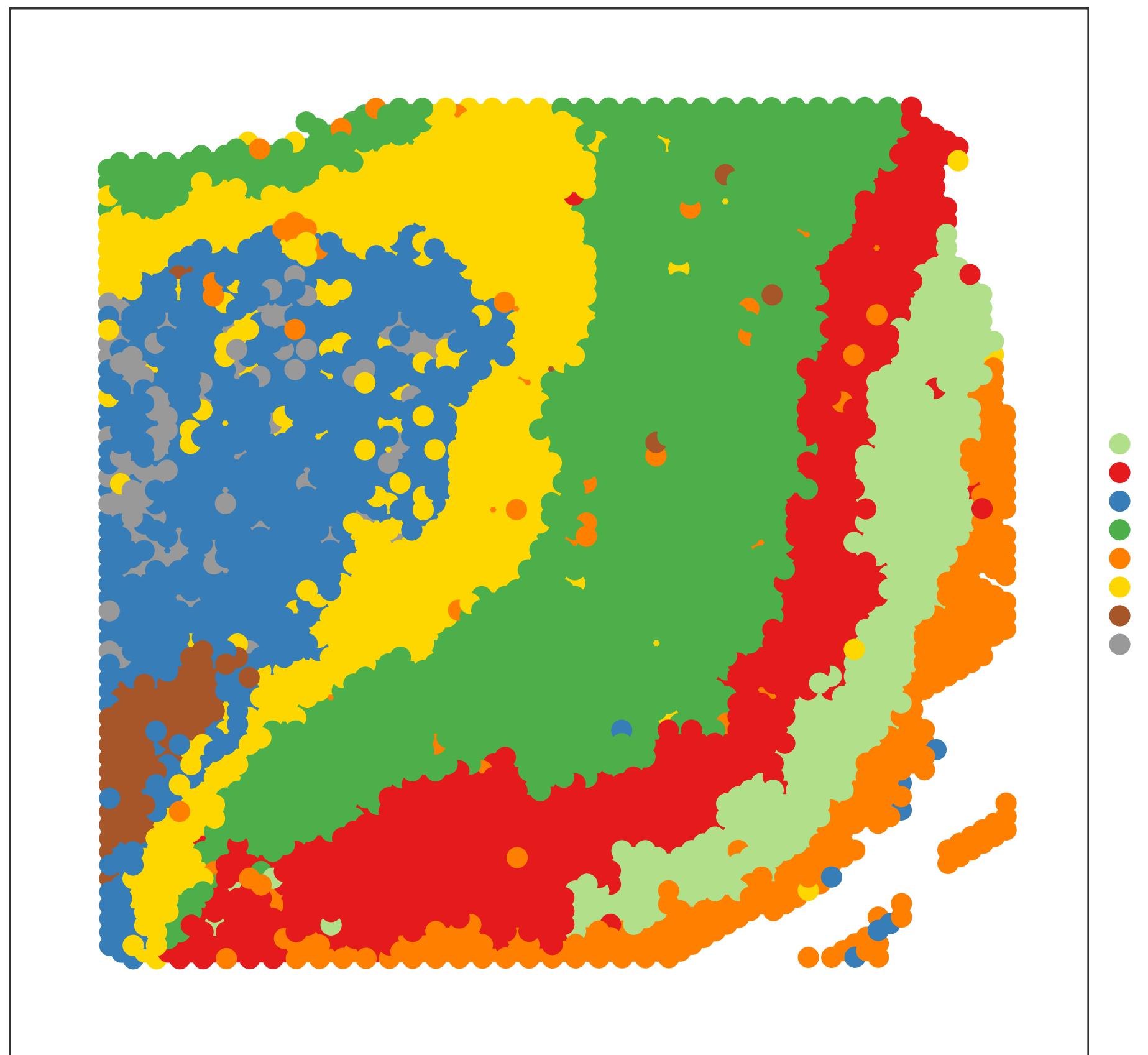


V12N28–334_C1



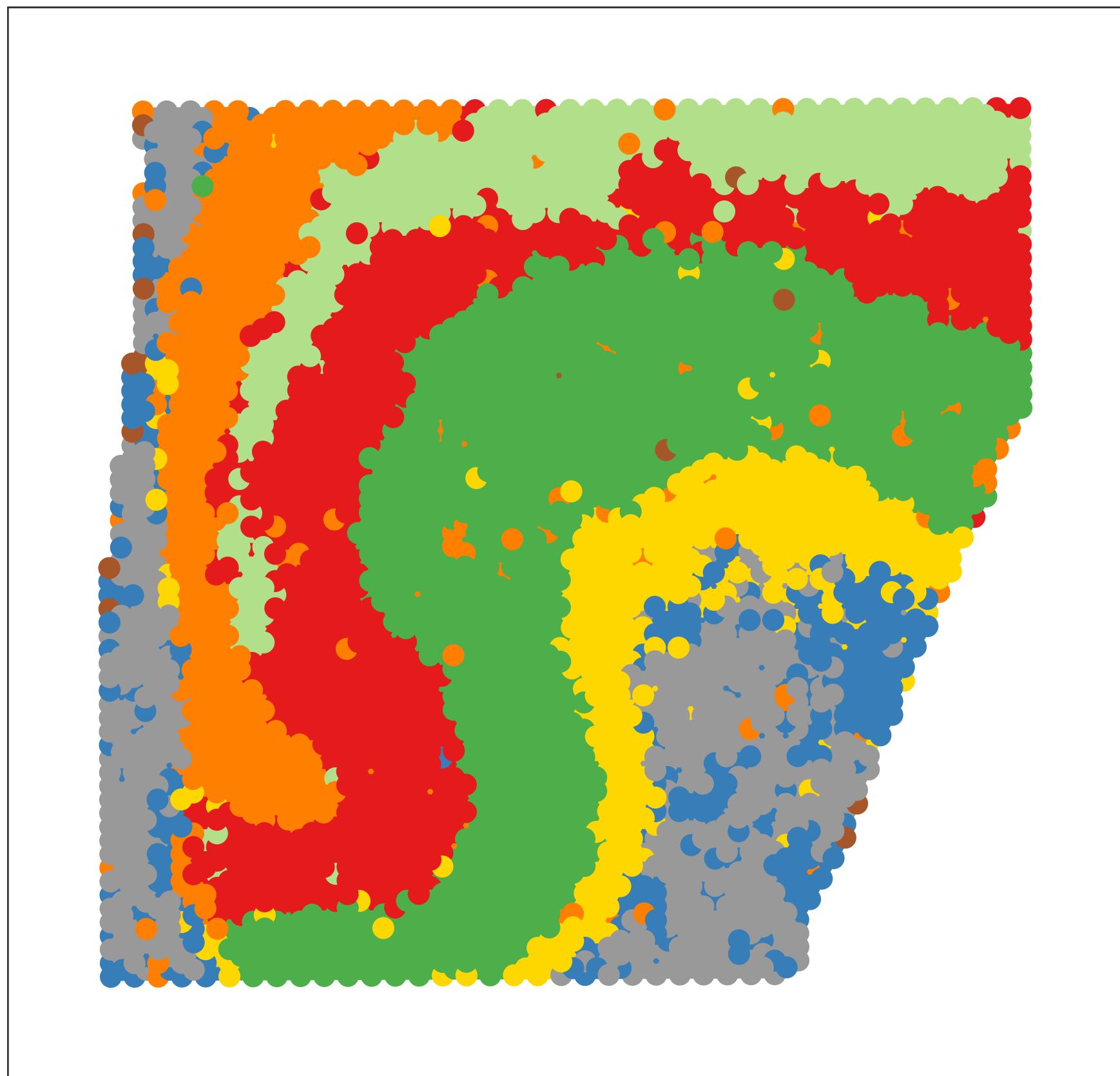
V12N28-334_A1

V12N28-334_A1_Br3942

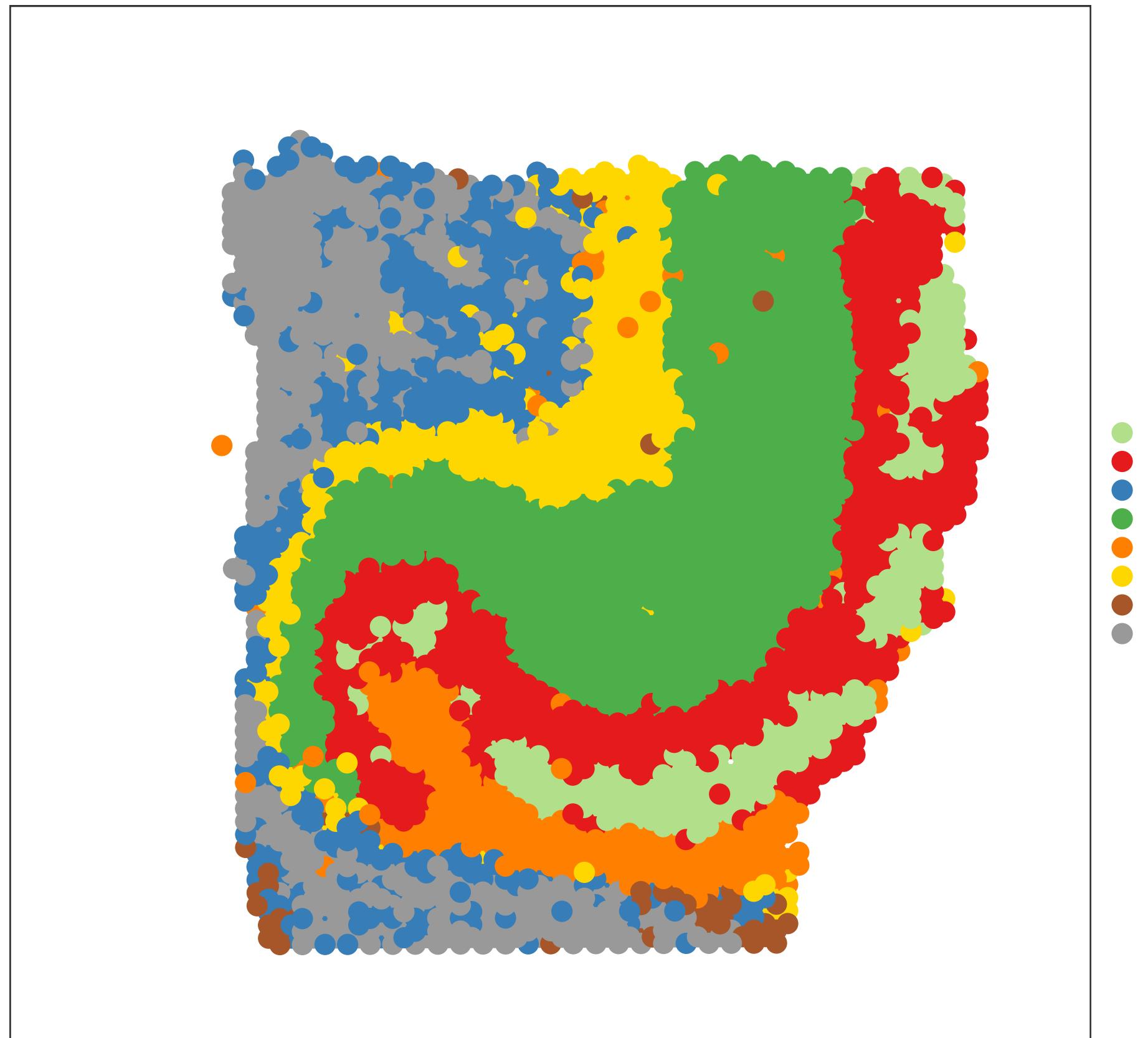


V12N28-334_D1

V12N28-334_D1_Br6423



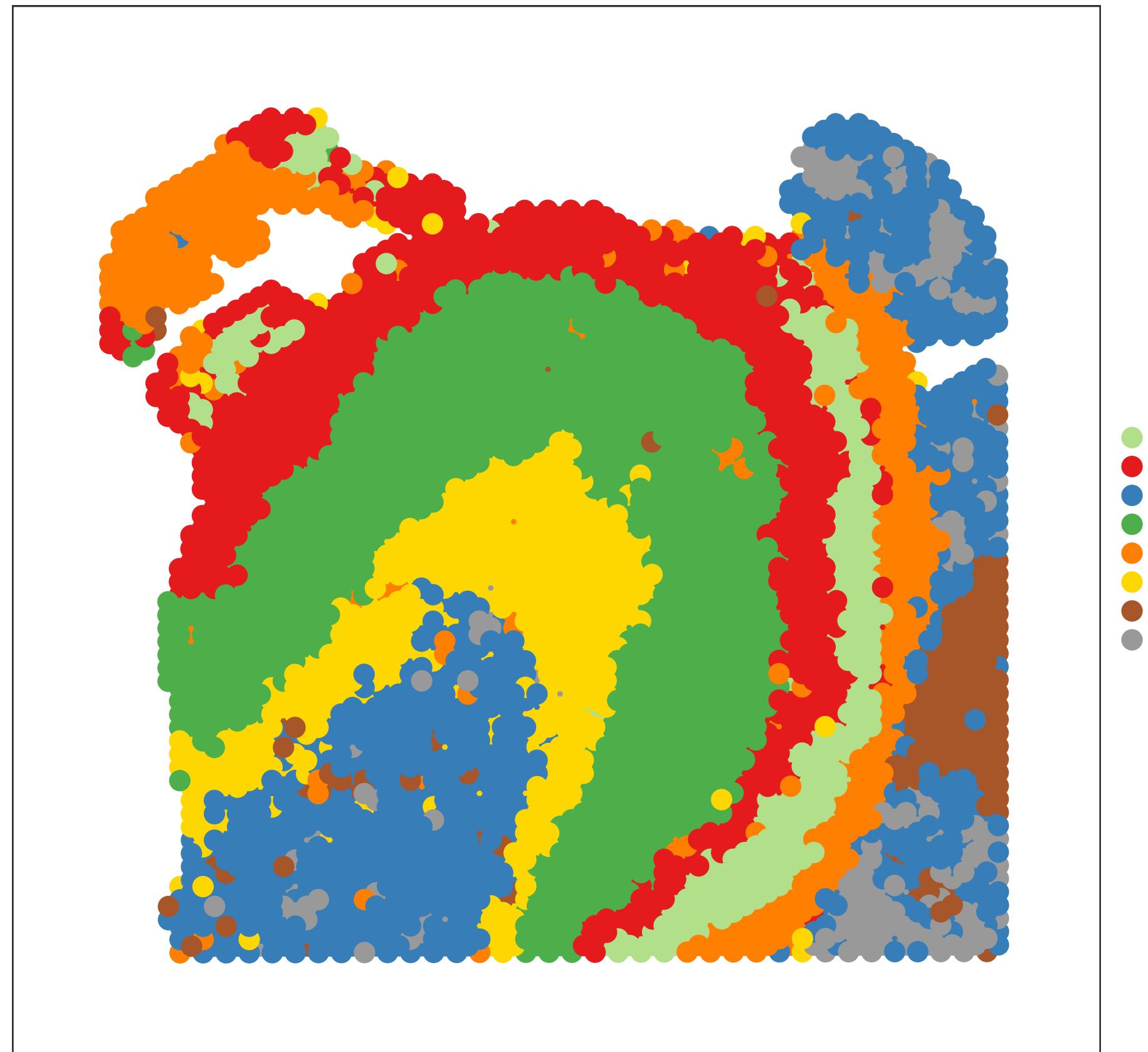
V12Y31–080_C1_Br8325



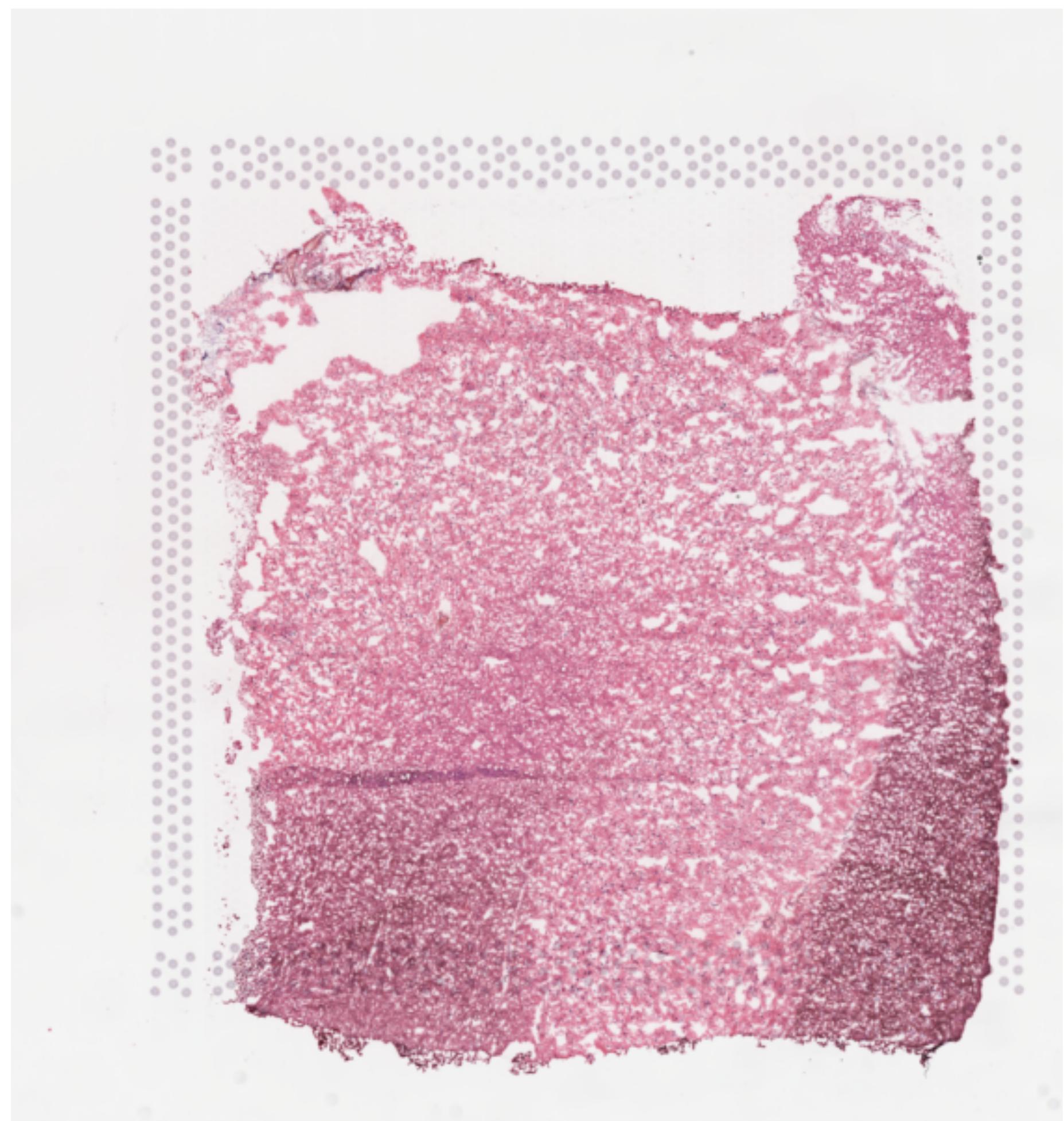
V12Y31–080_C1



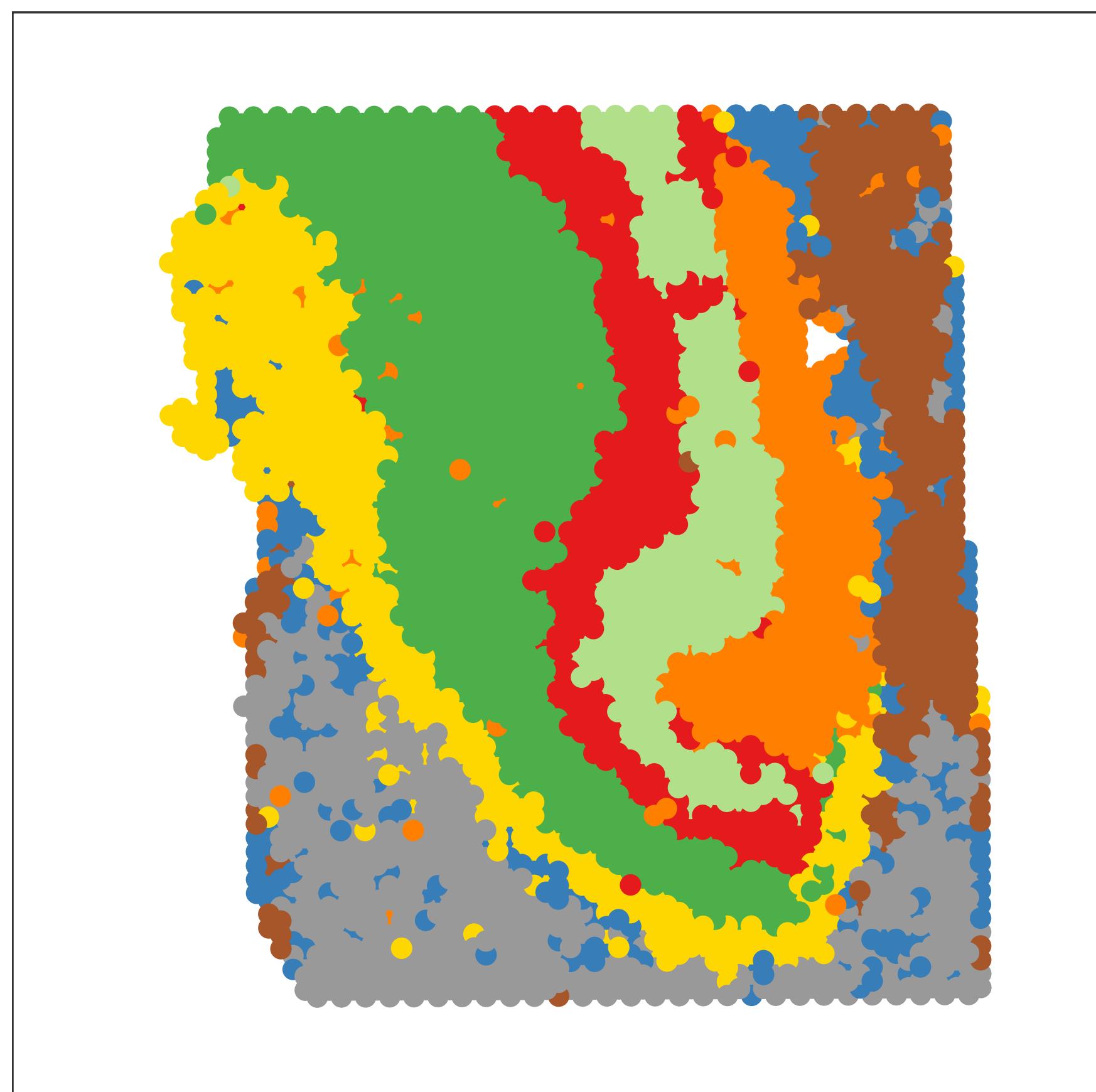
V12N28–334_B1_Br8492



V12N28–334_B1



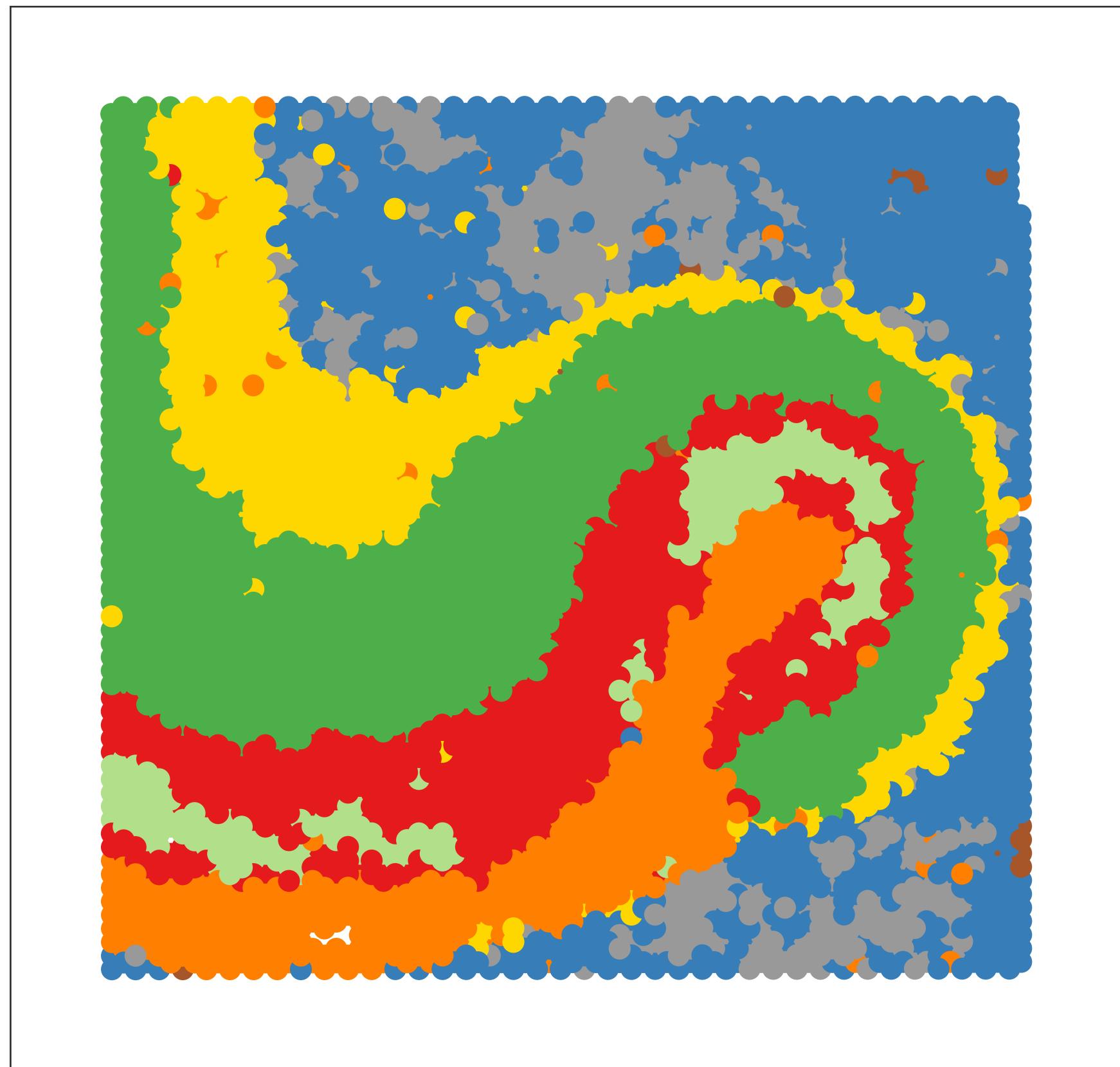
V12Y31–080_B1_Br8667



V12Y31–080_B1



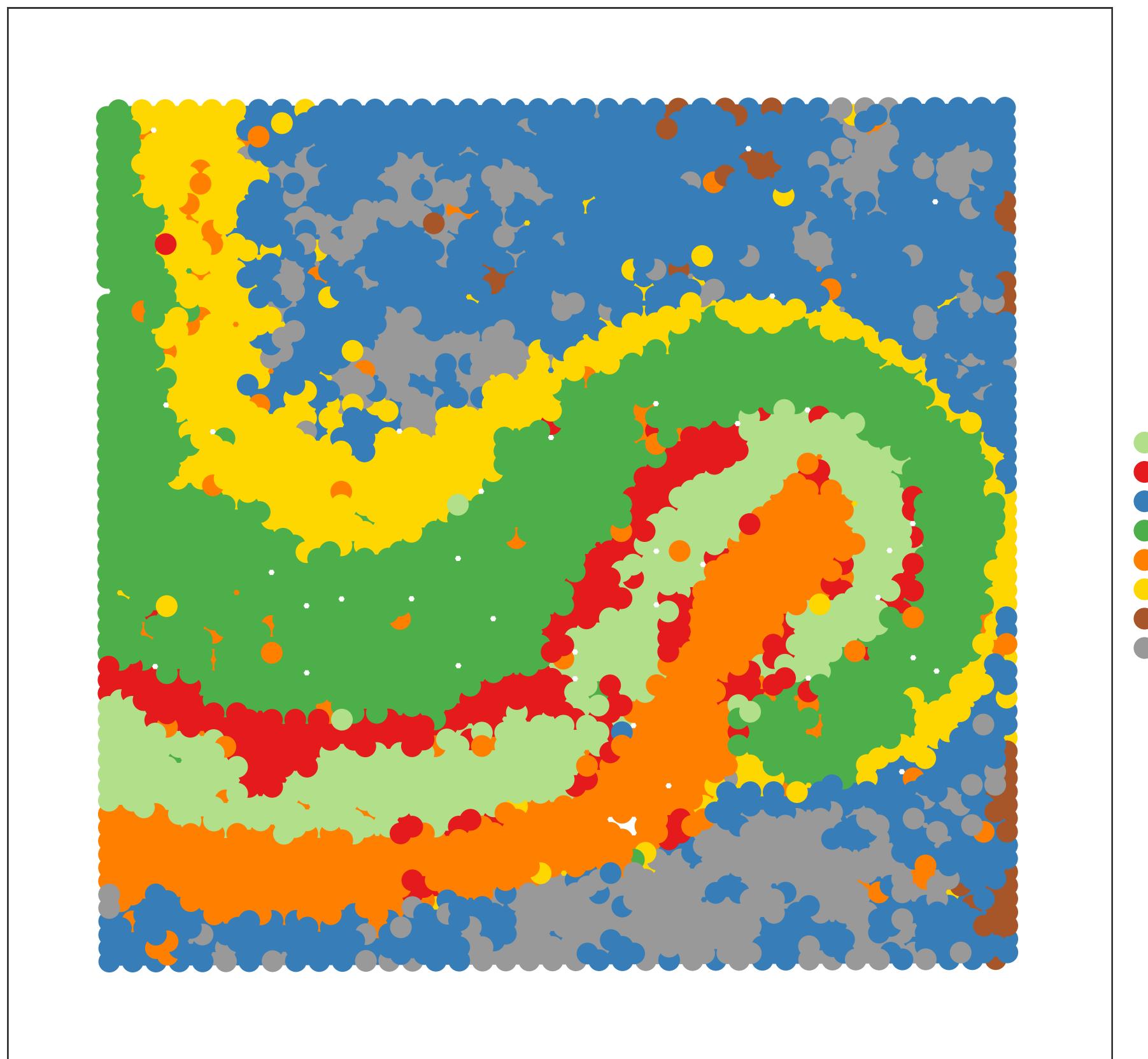
V12J03-002_A1_Br2720



V12J03-002_A1



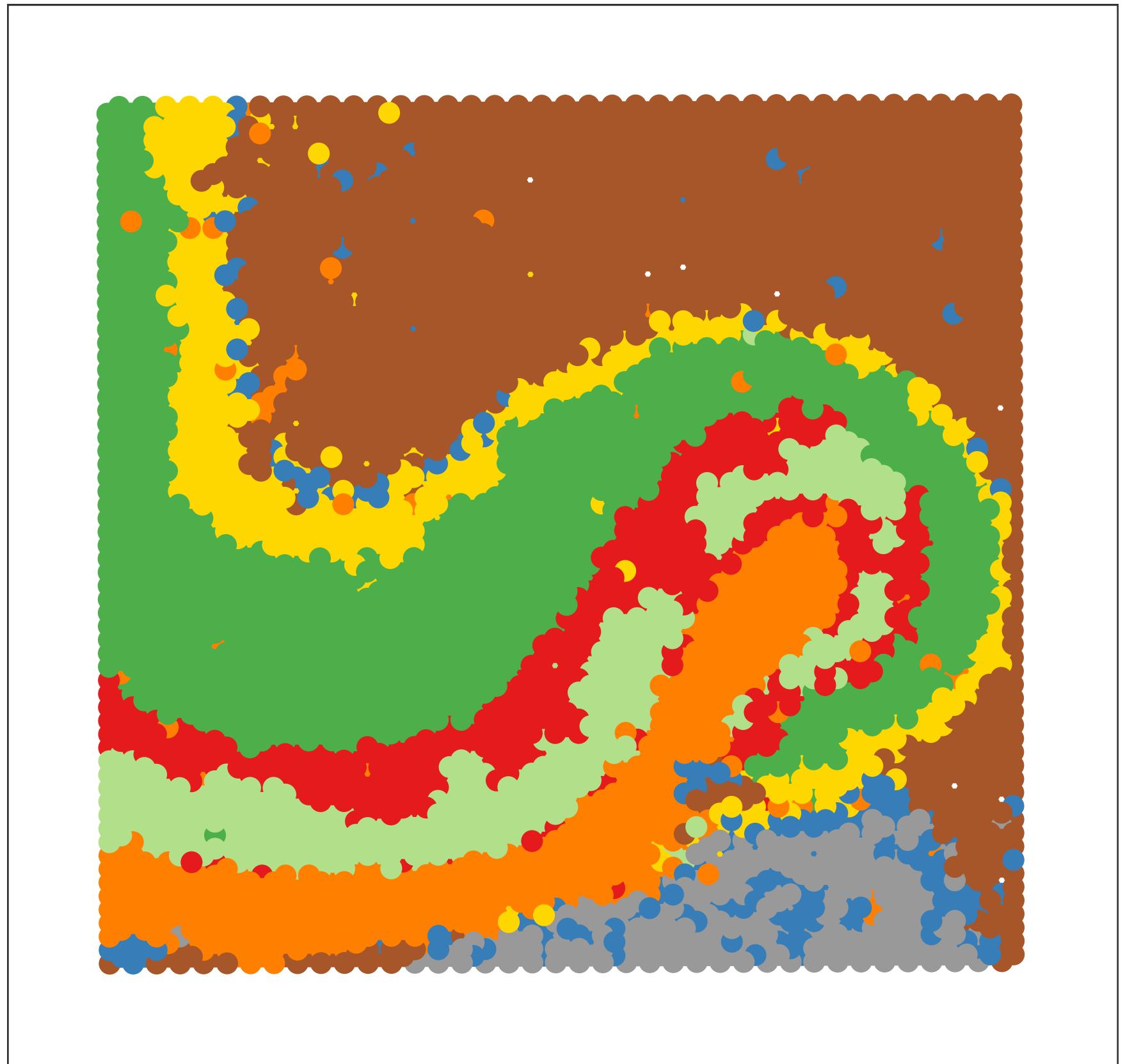
V12N28–331_A1_Br2720



V12N28–331_A1



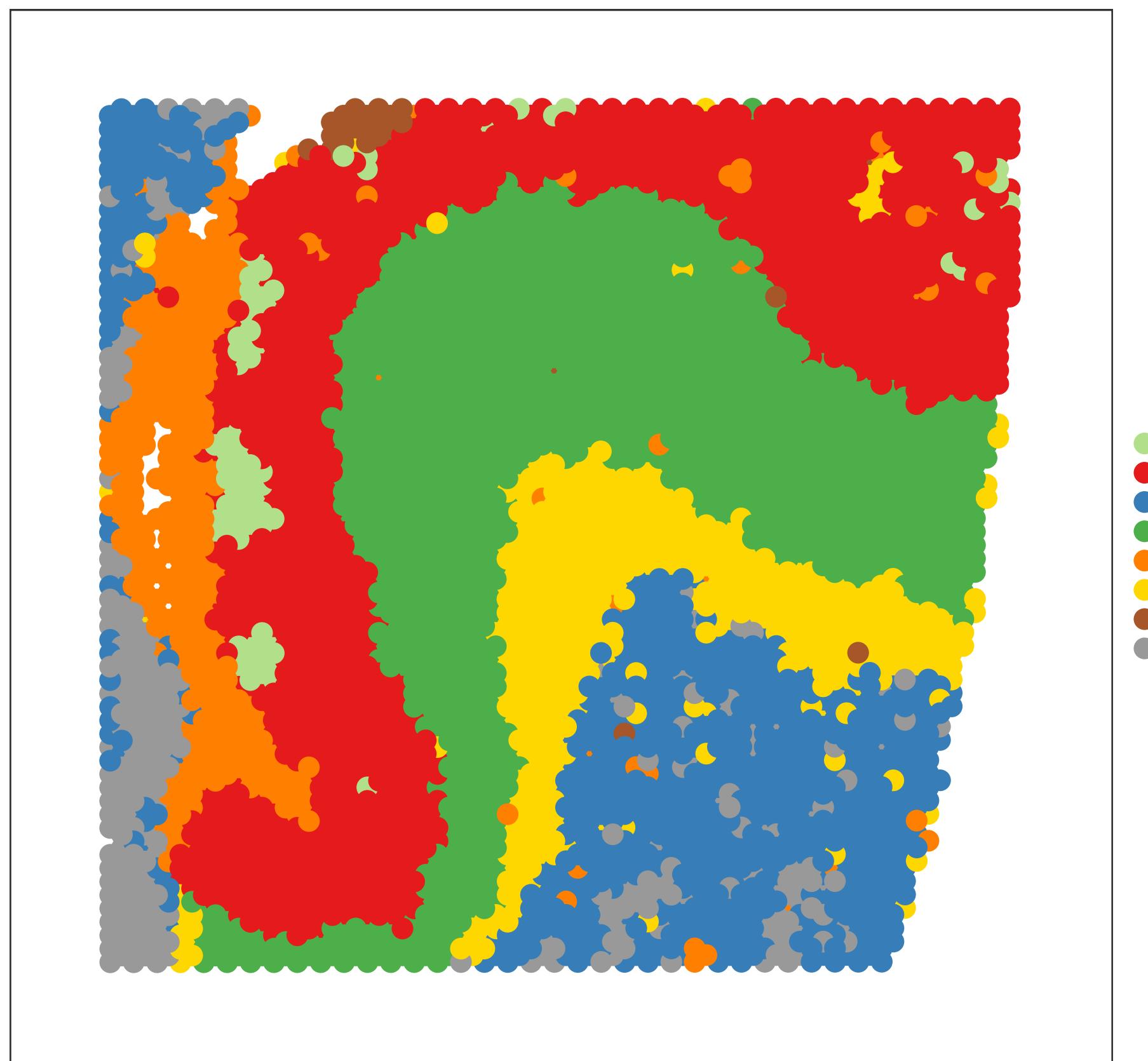
V12N28-332_A1_Br2720



V12N28-332_A1



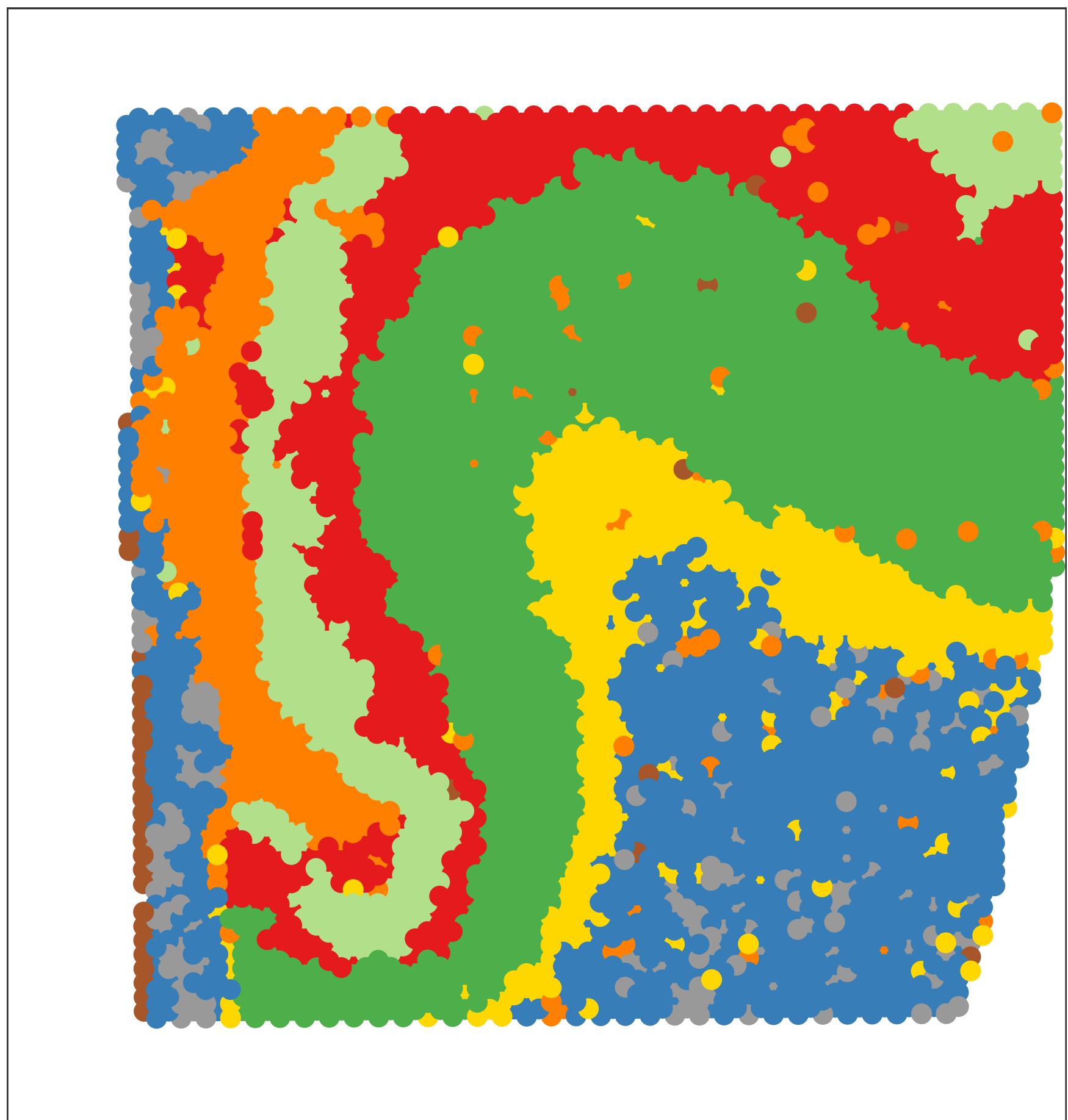
V12J03-002_B1_Br6432



V12J03-002_B1



V12N28–331_B1_Br6432

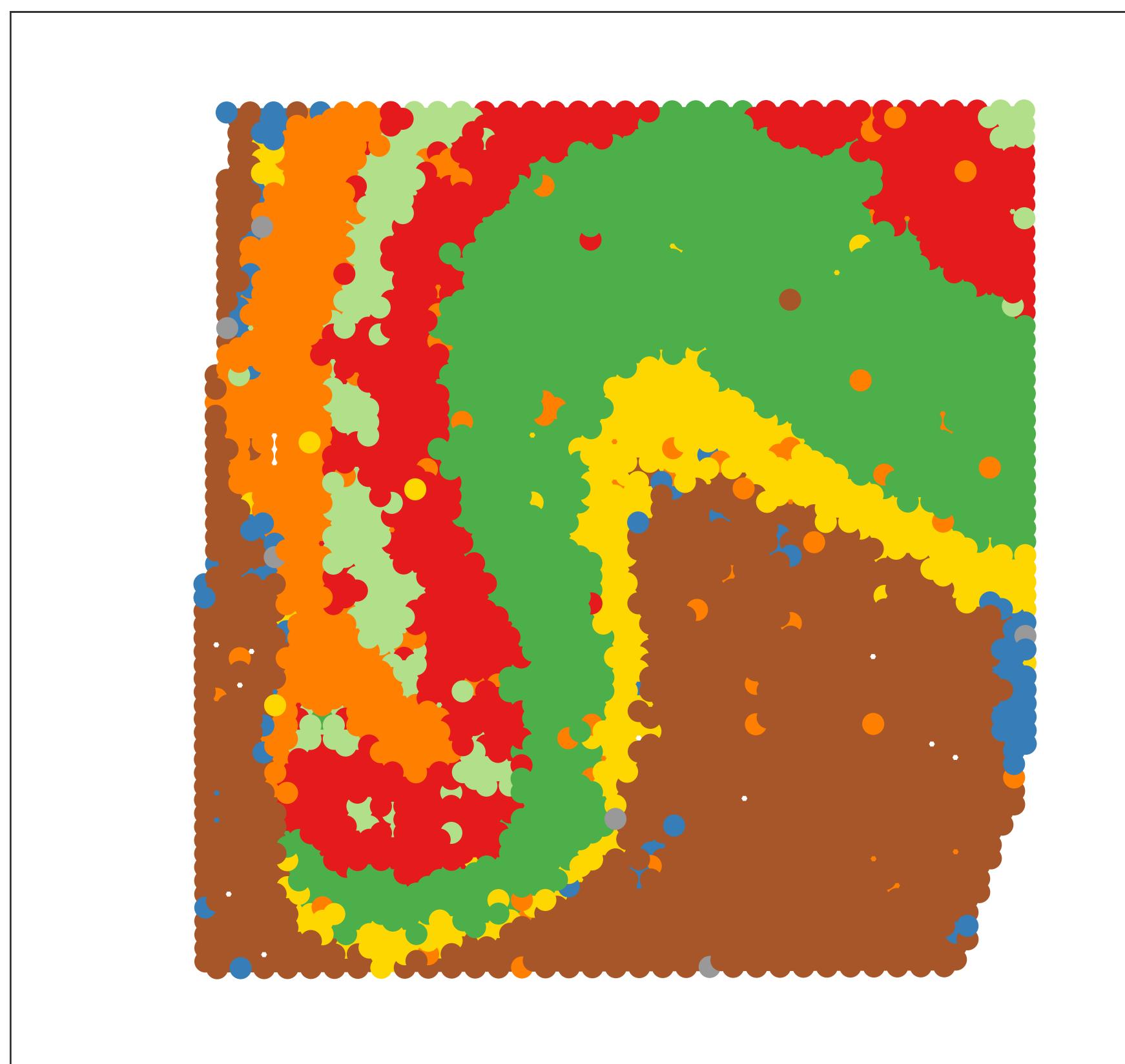


V12N28–331_B1

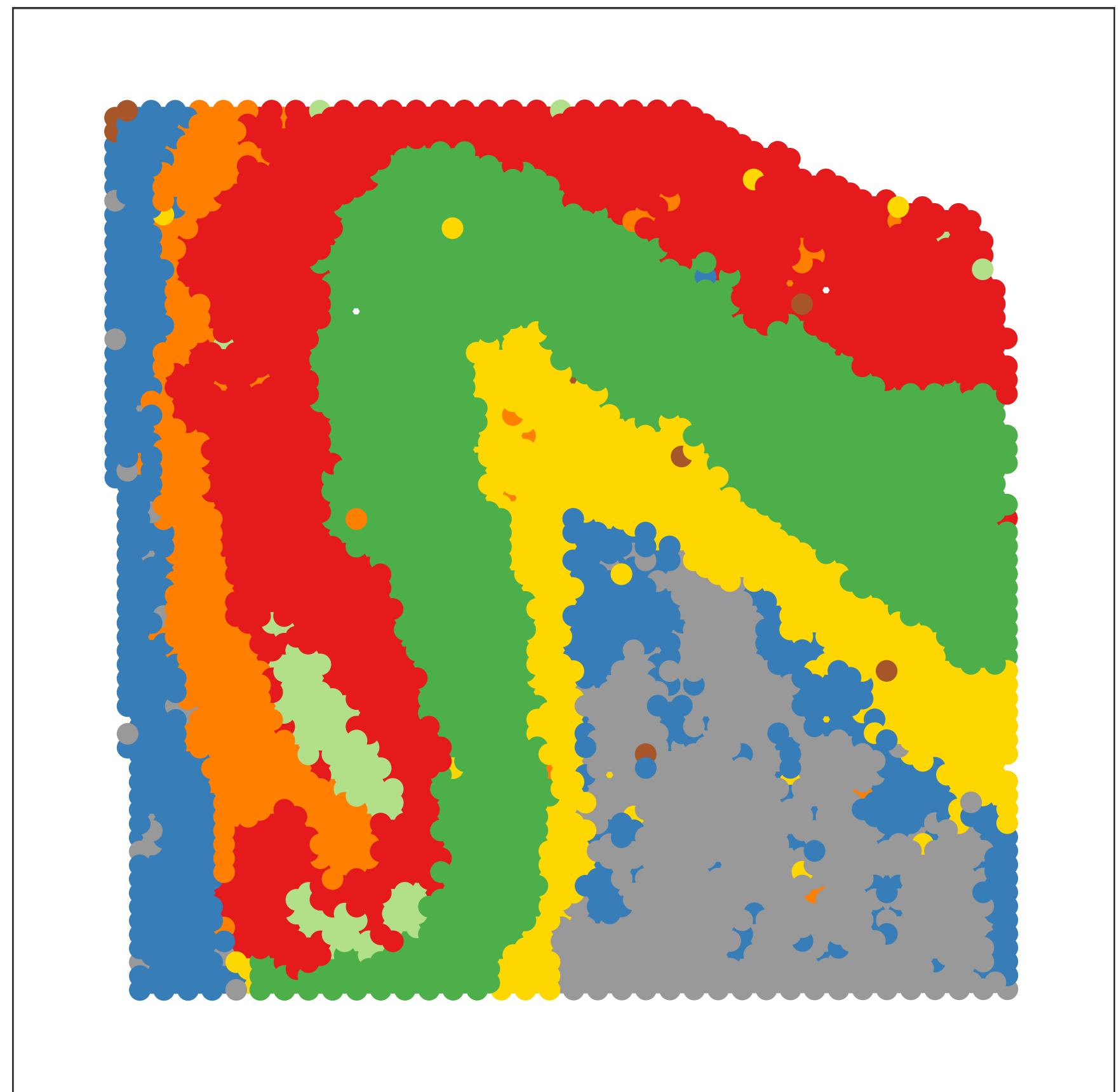


V12N28-332_B1

V12N28-332_B1_Br6432



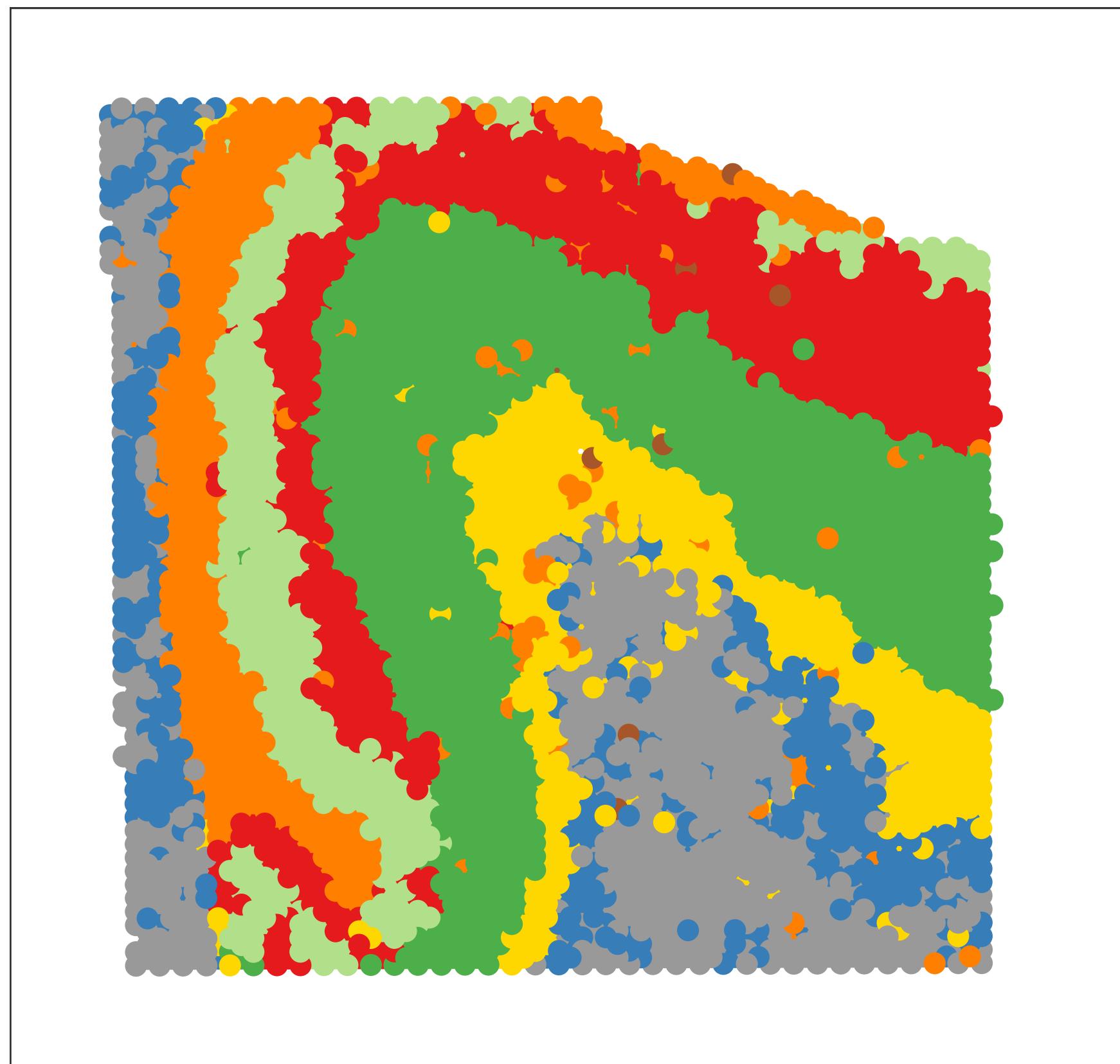
V12J03-002_C1_Br6471



V12J03-002_C1



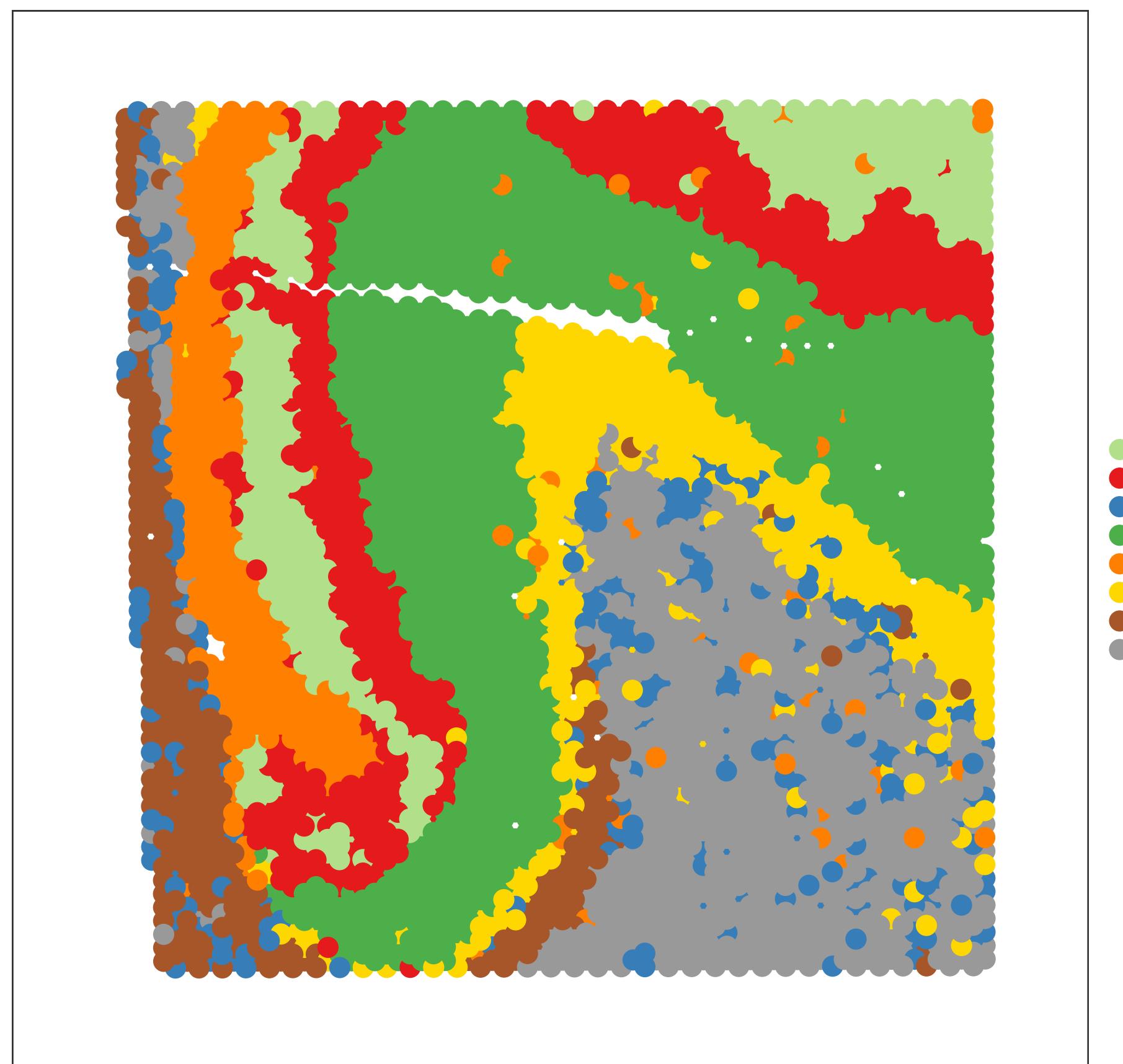
V12N28–331_C1_Br6471



V12N28–331_C1



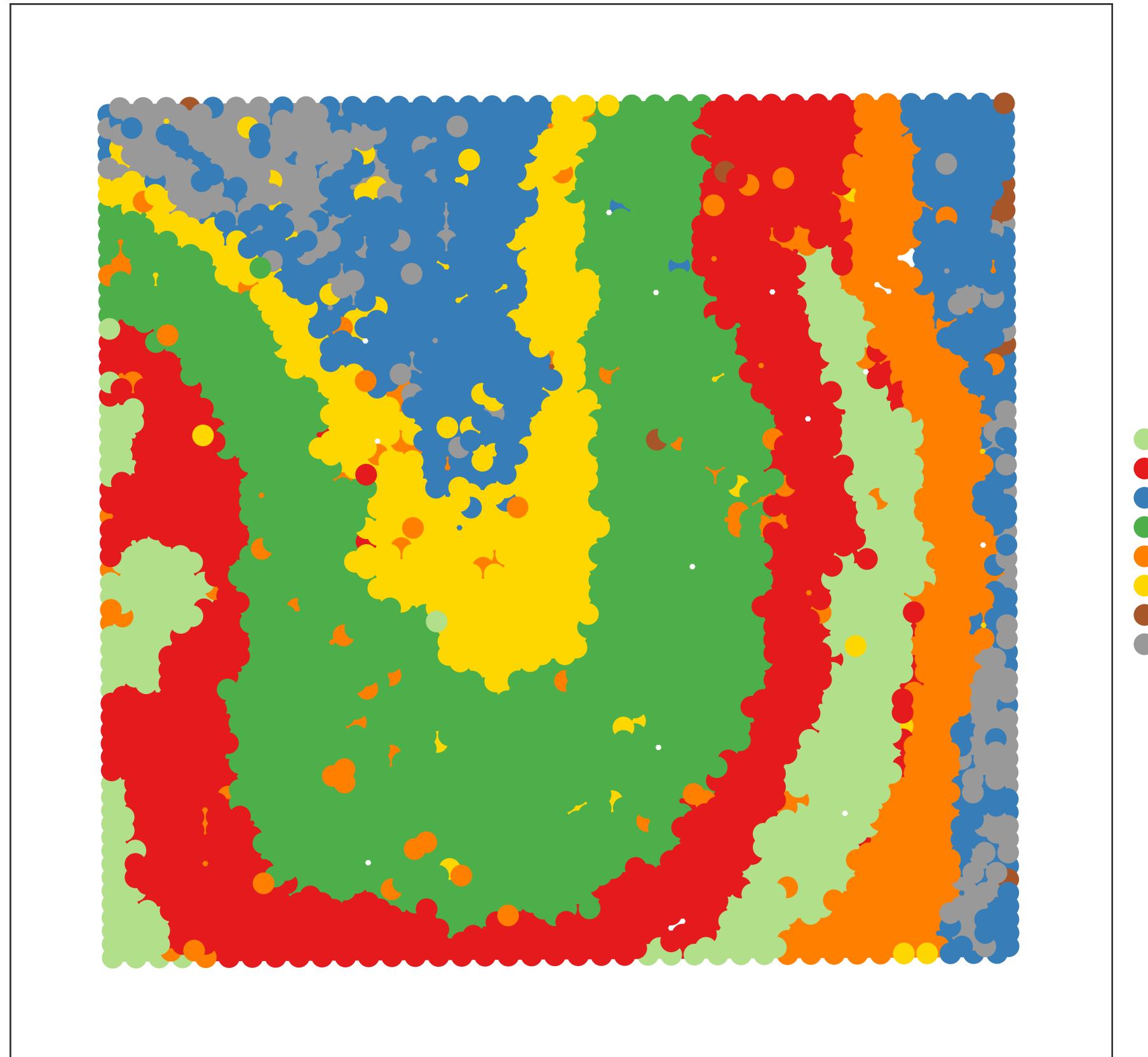
V12N28–332_C1_Br6471



V12N28–332_C1



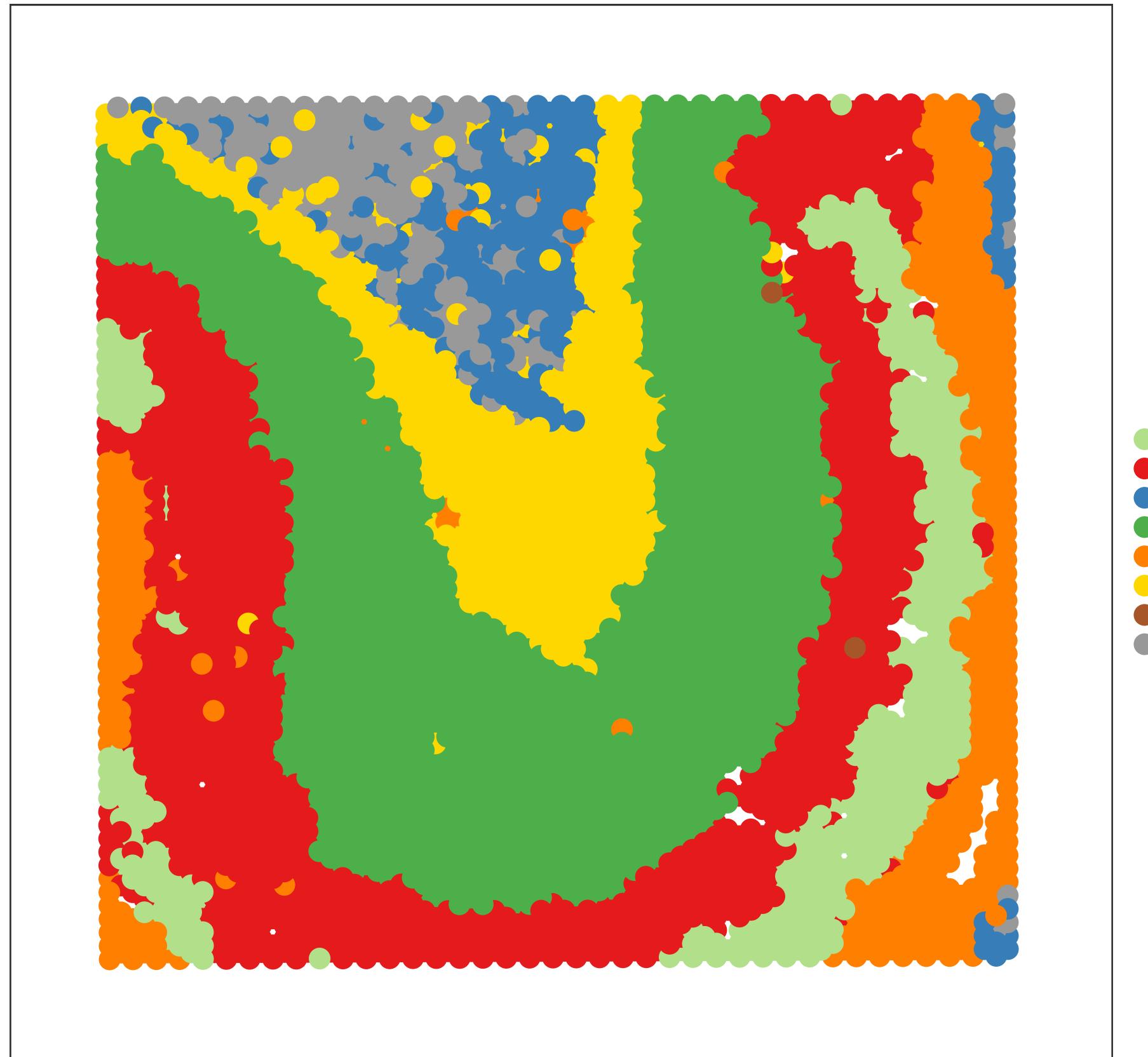
V12N28–331_D1_Br6522



V12N28–331_D1



V12N28–332_D1_Br6522



V12N28–332_D1

