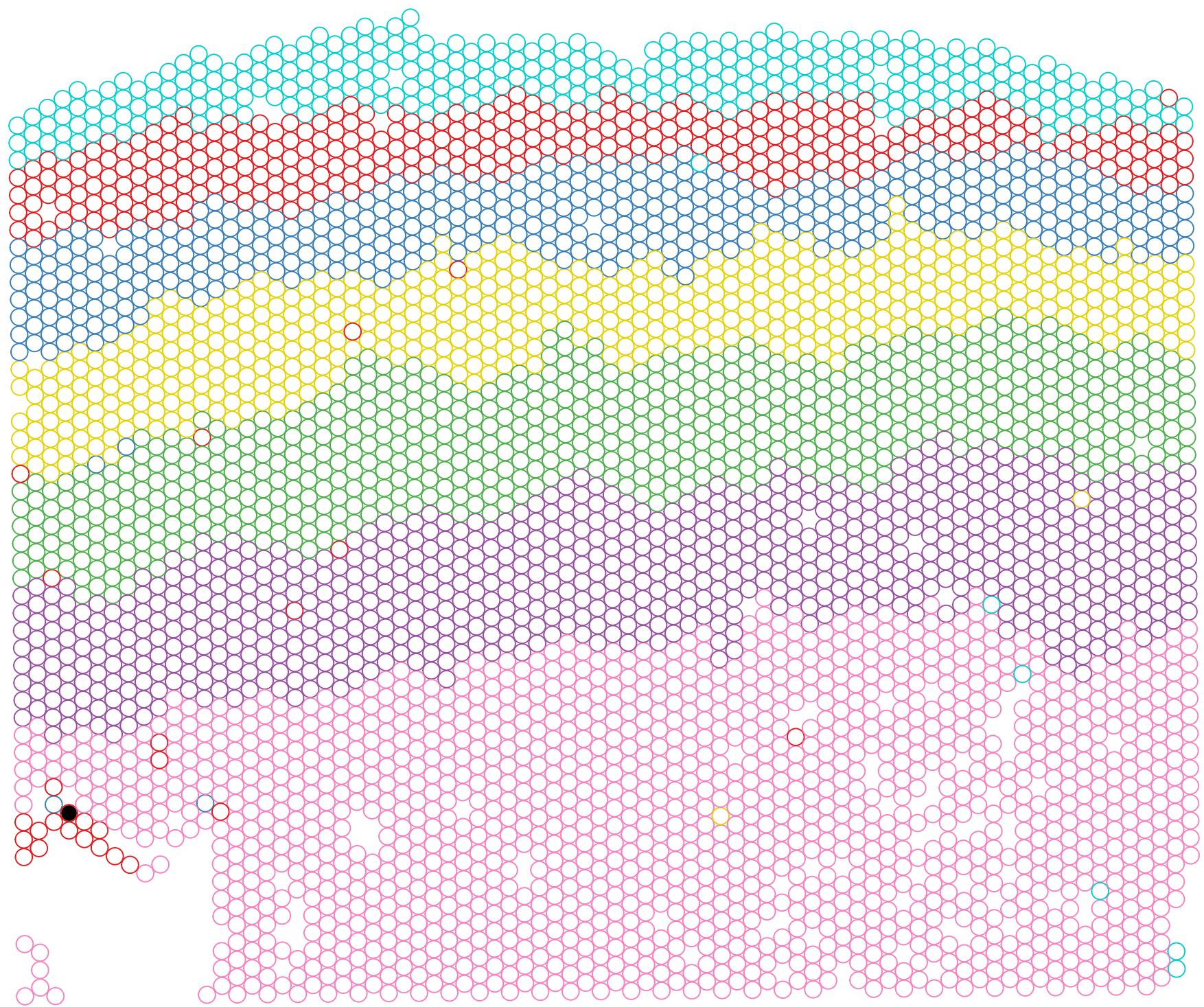
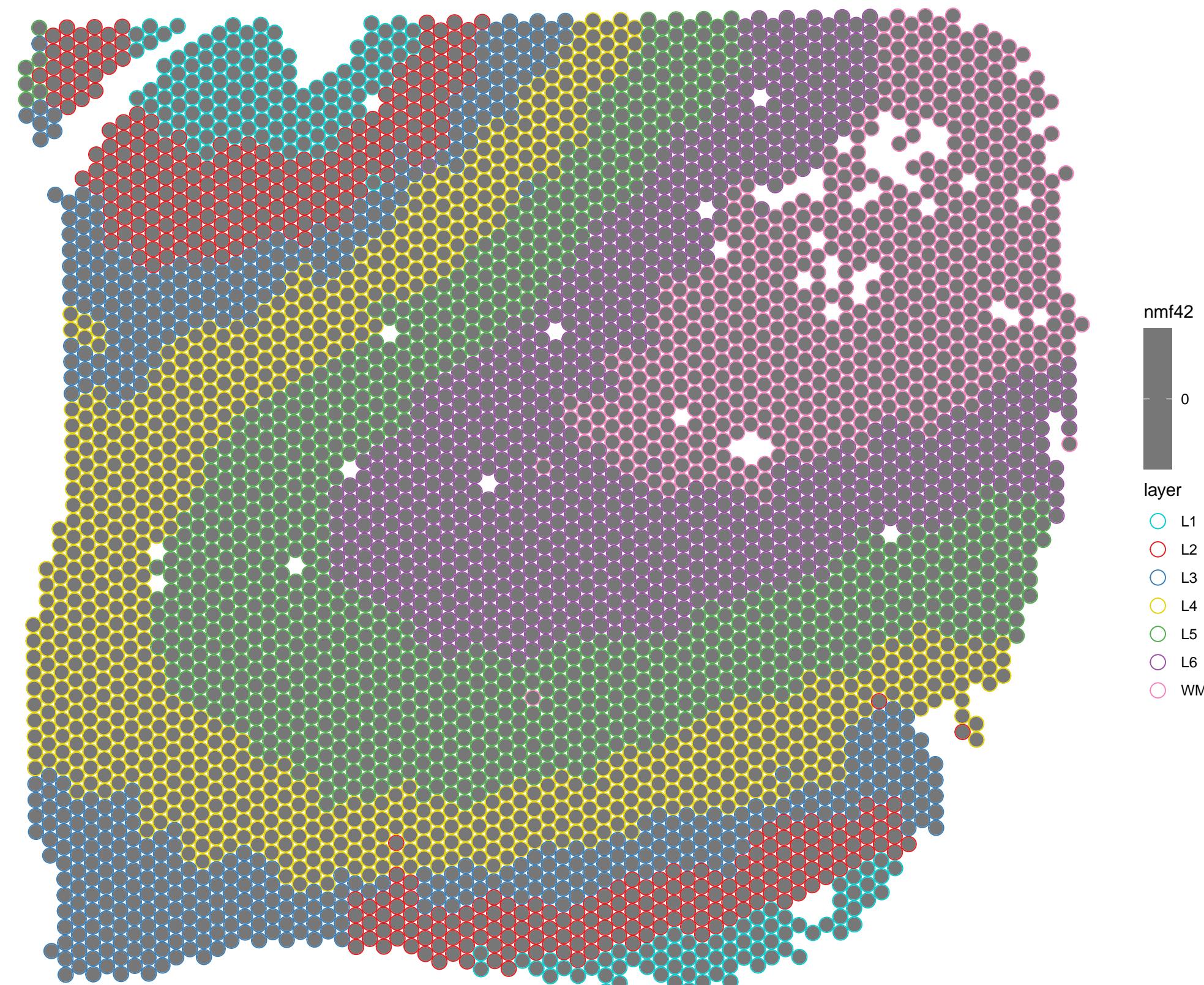


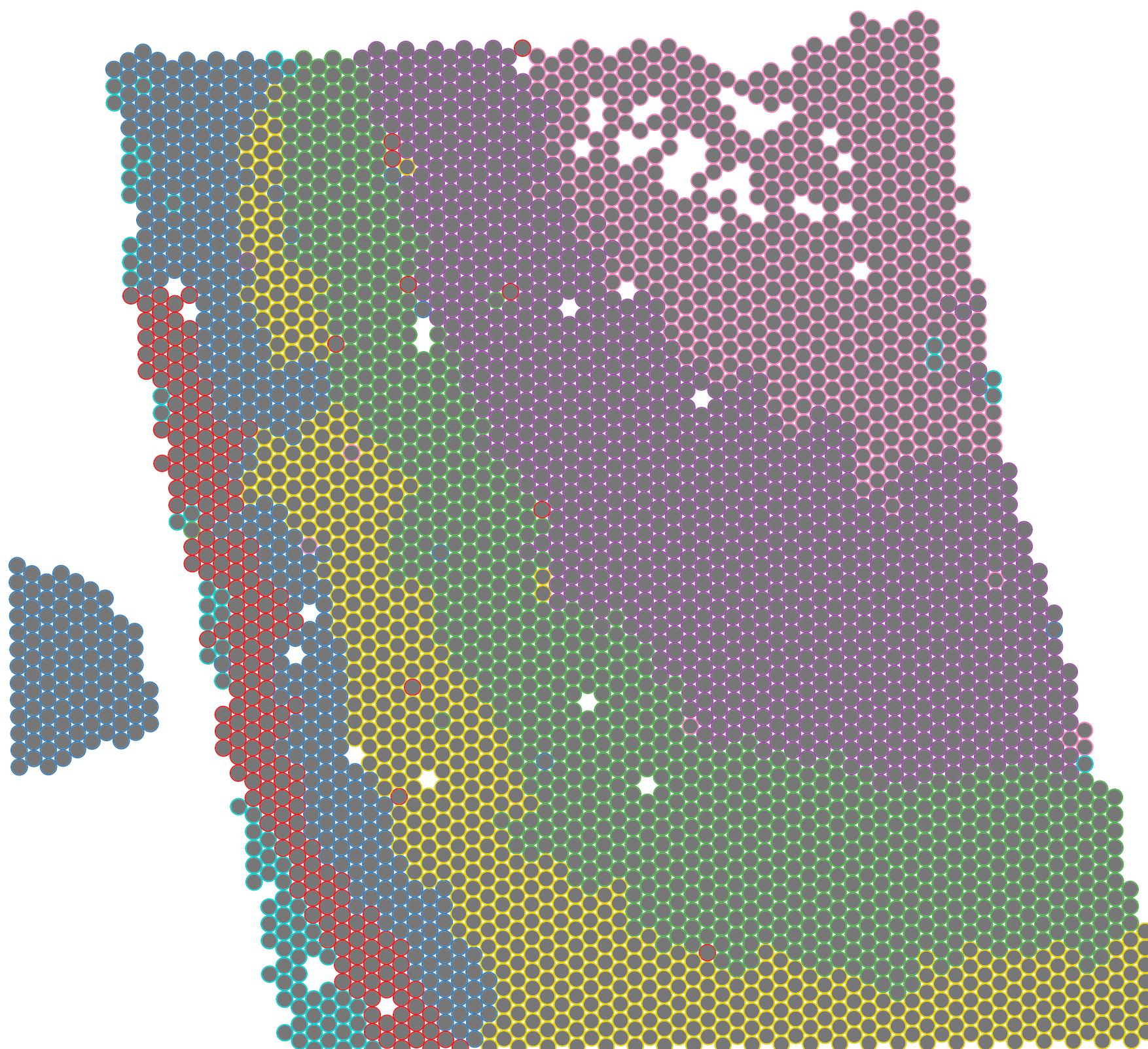
Sample Br2743\_ant



Sample Br2743\_mid



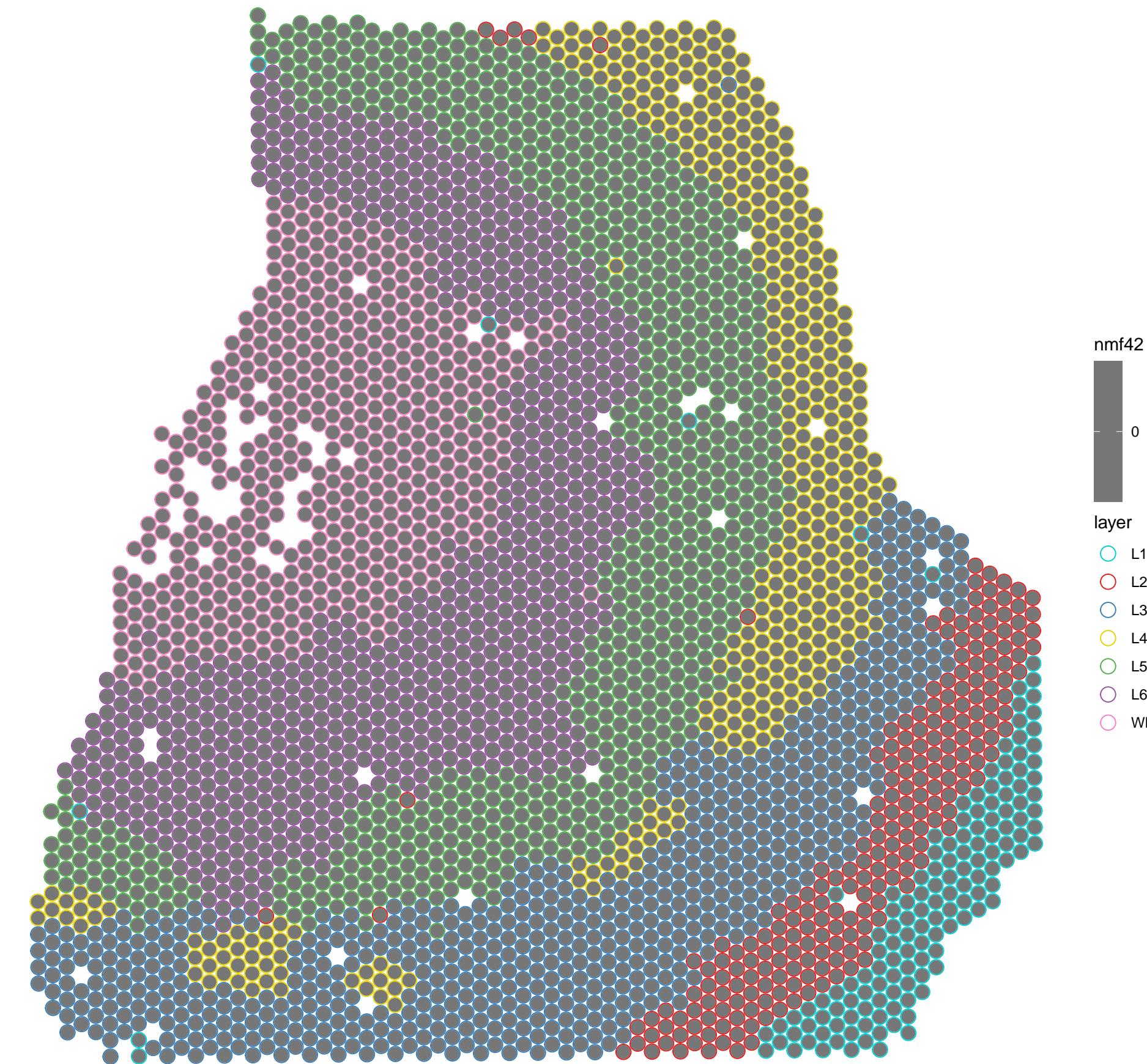
Sample Br2743\_post



Sample Br3942\_ant



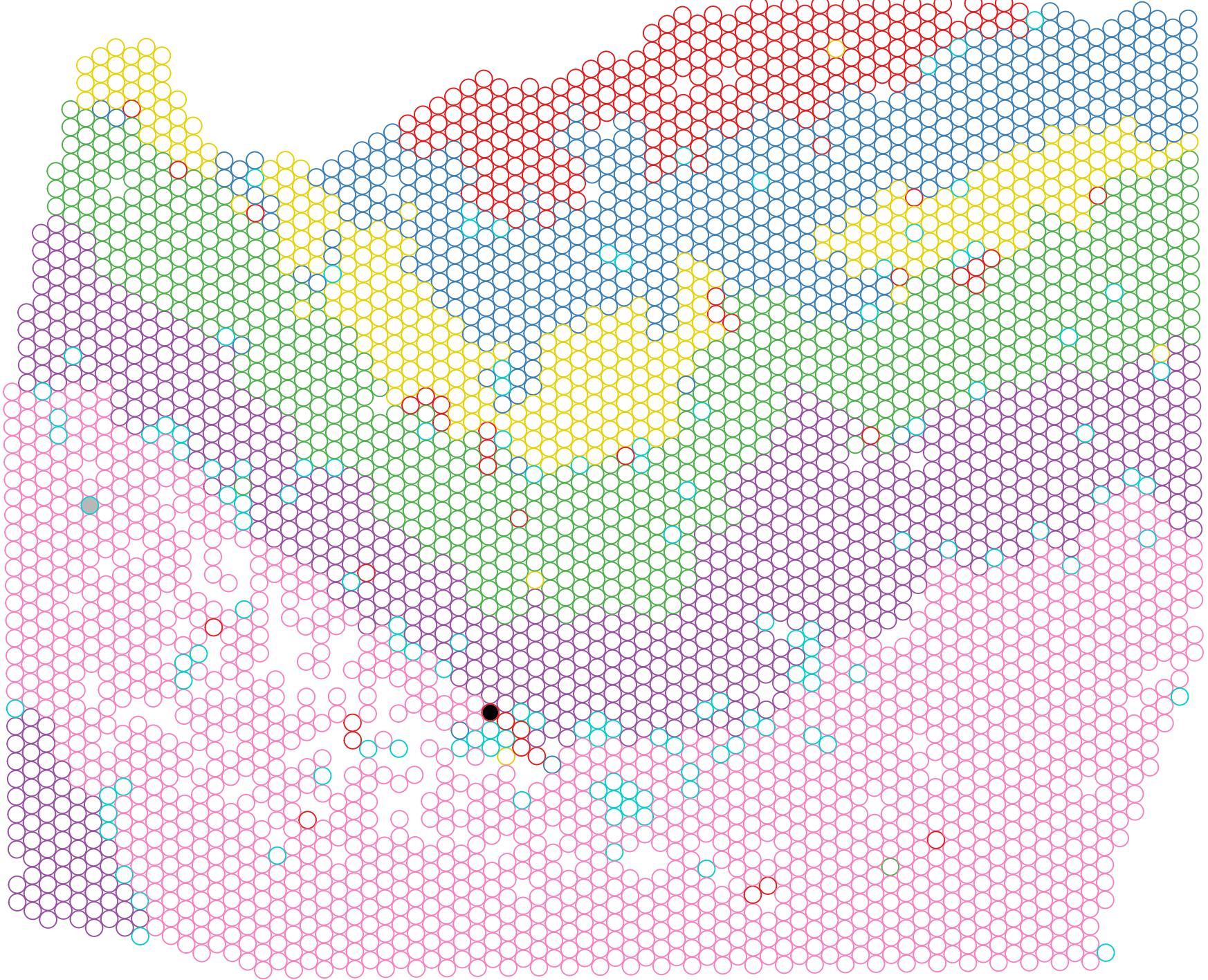
Sample Br3942\_mid



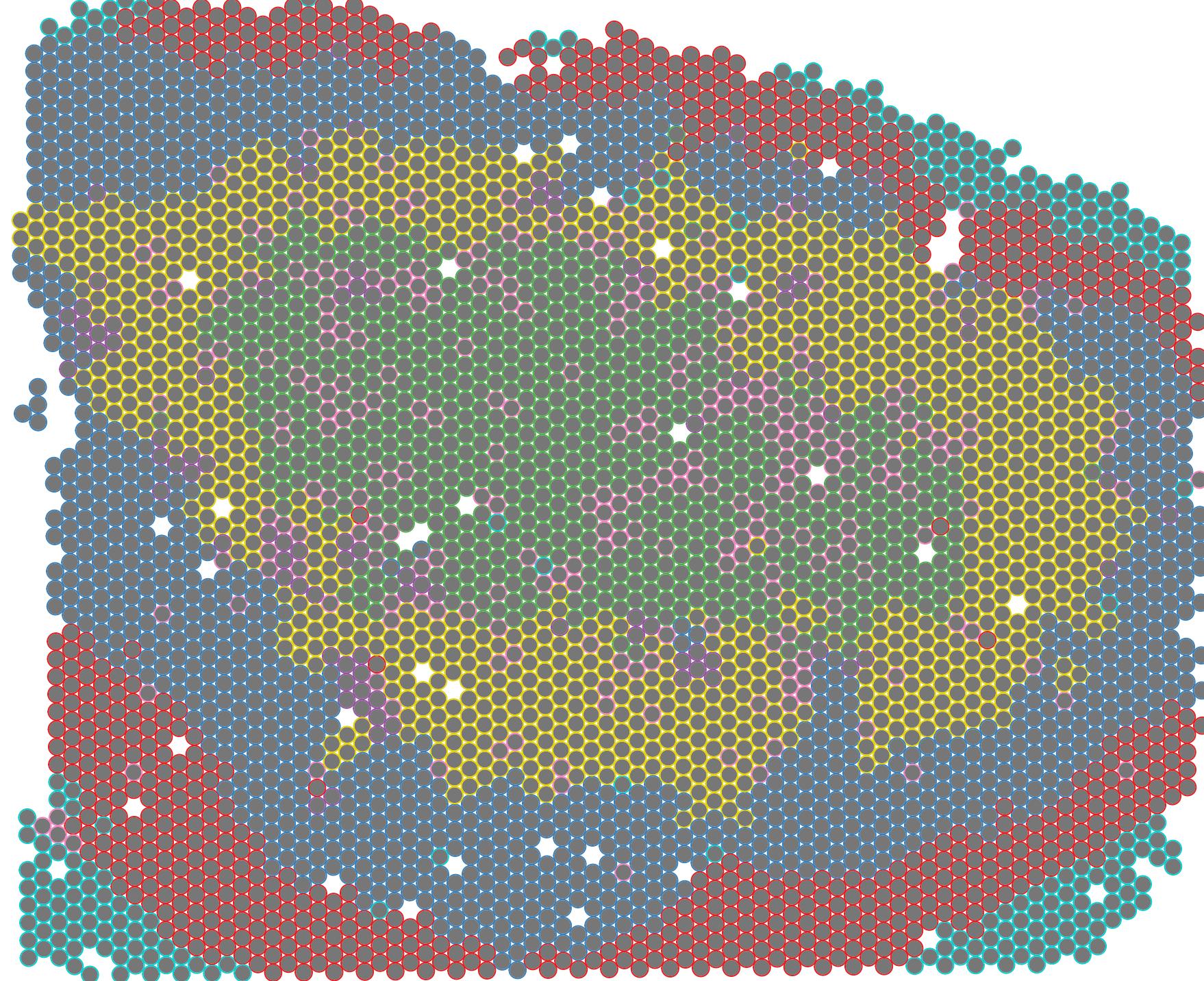
Sample Br3942\_post



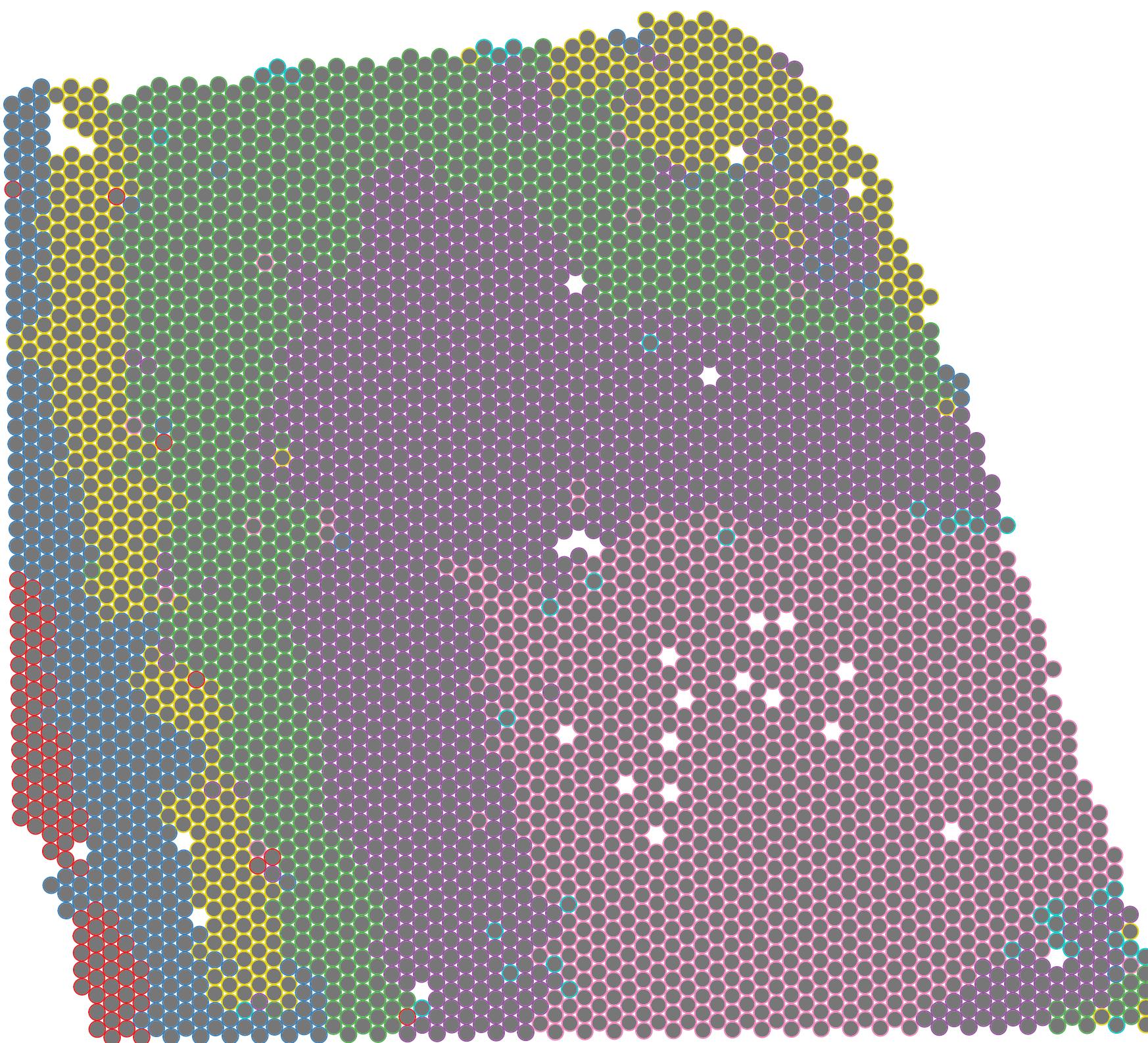
Sample Br6423\_ant



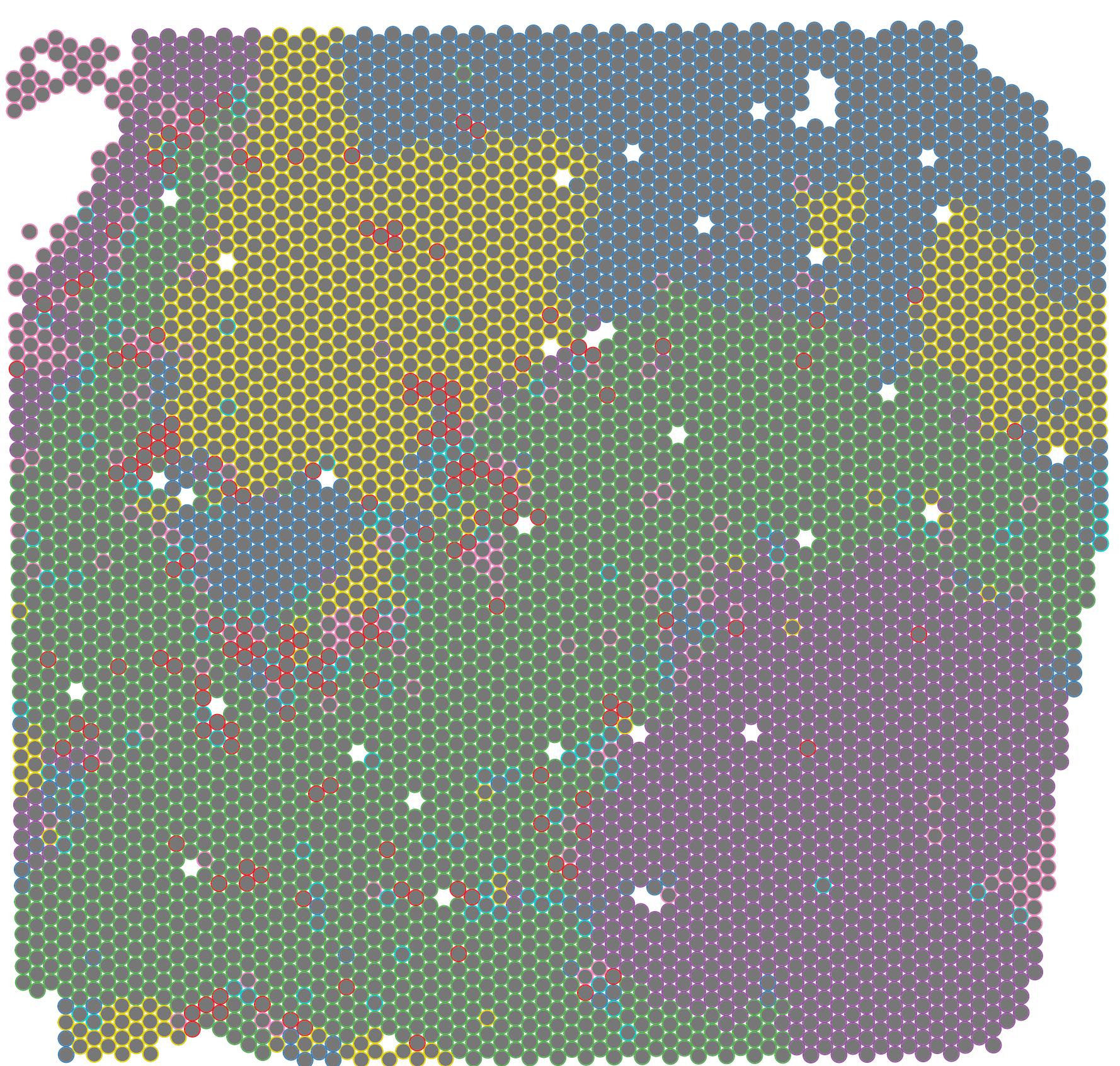
Sample Br6423\_mid



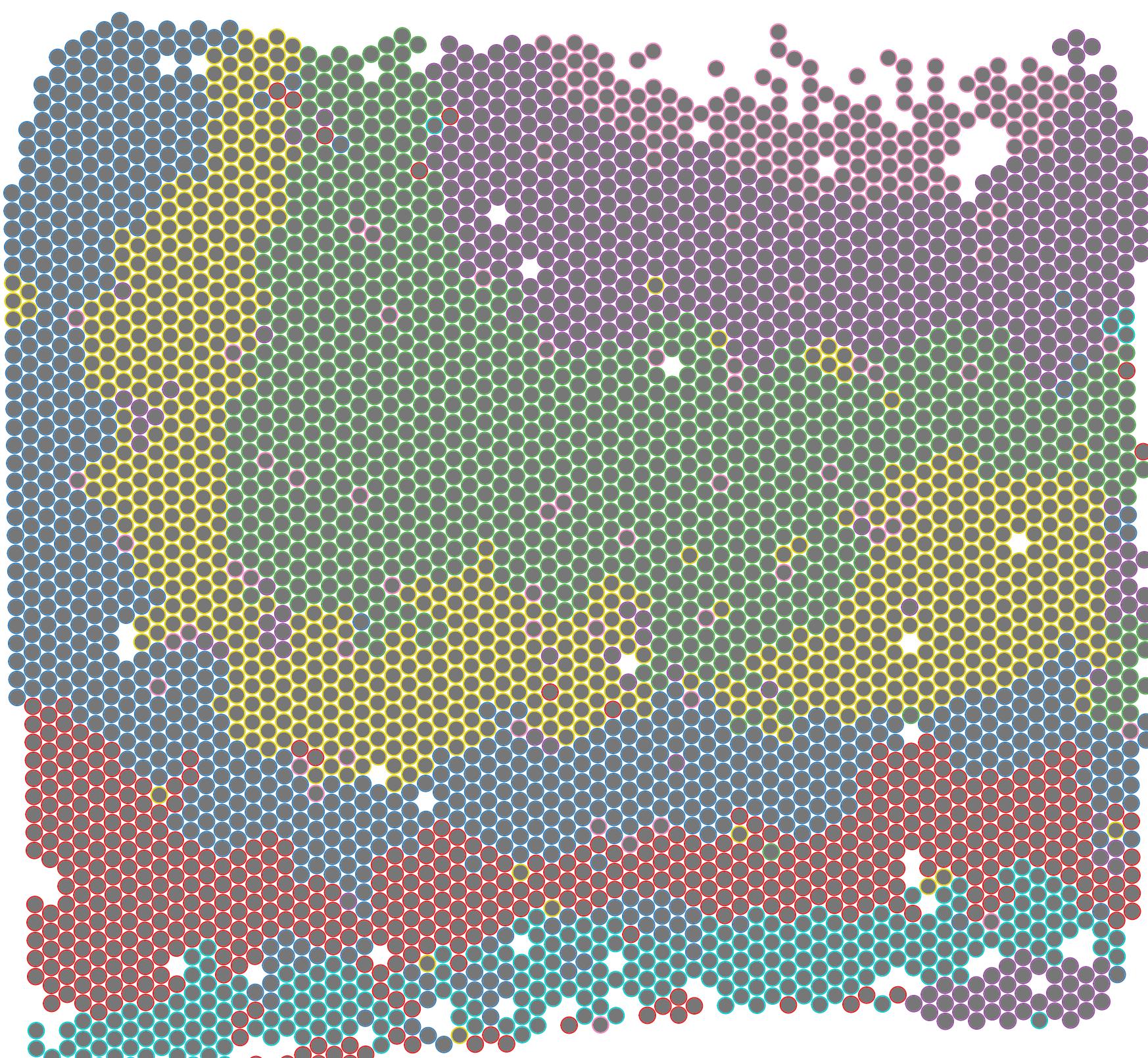
Sample Br6423\_post



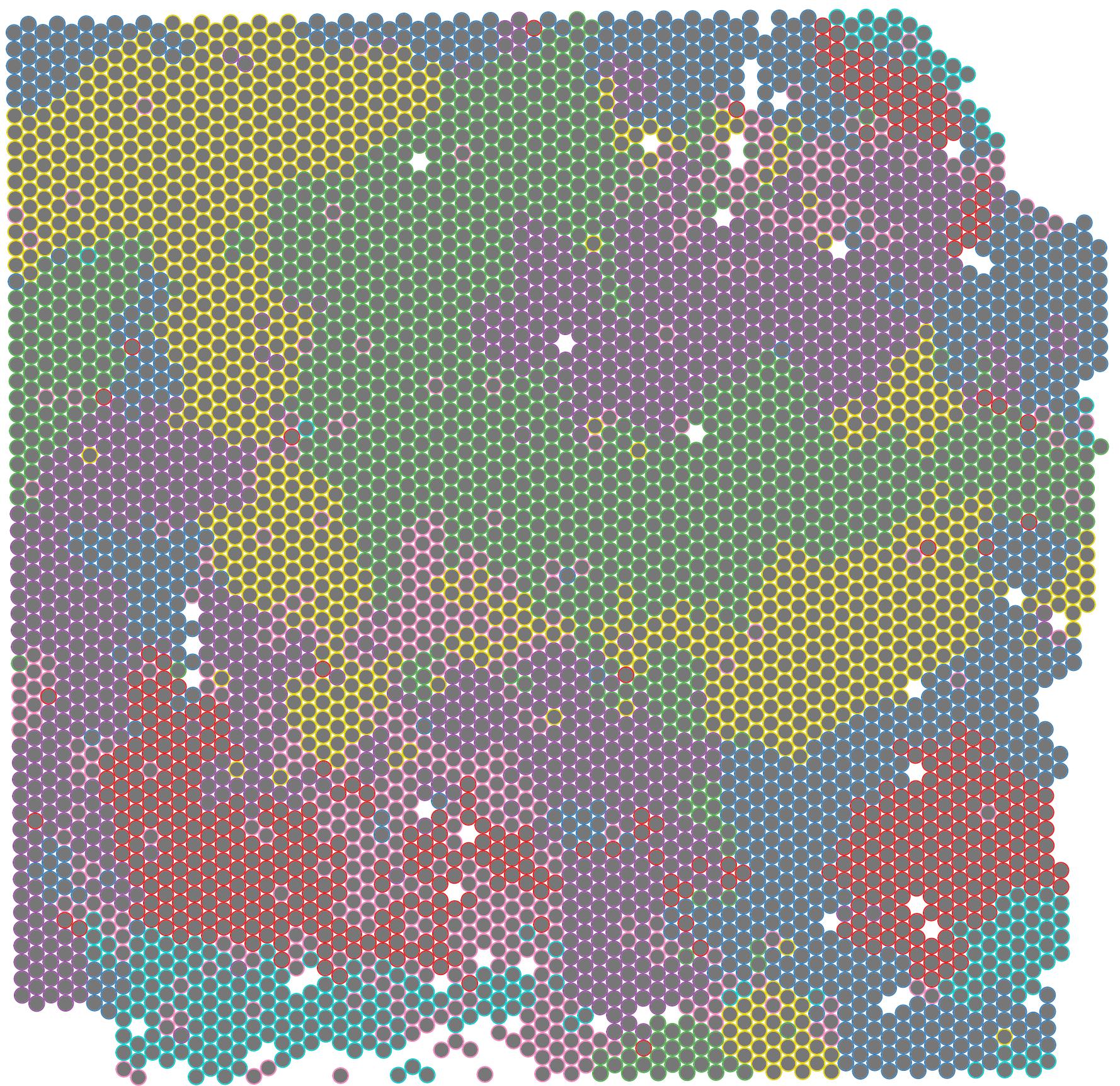
Sample Br8492\_ant



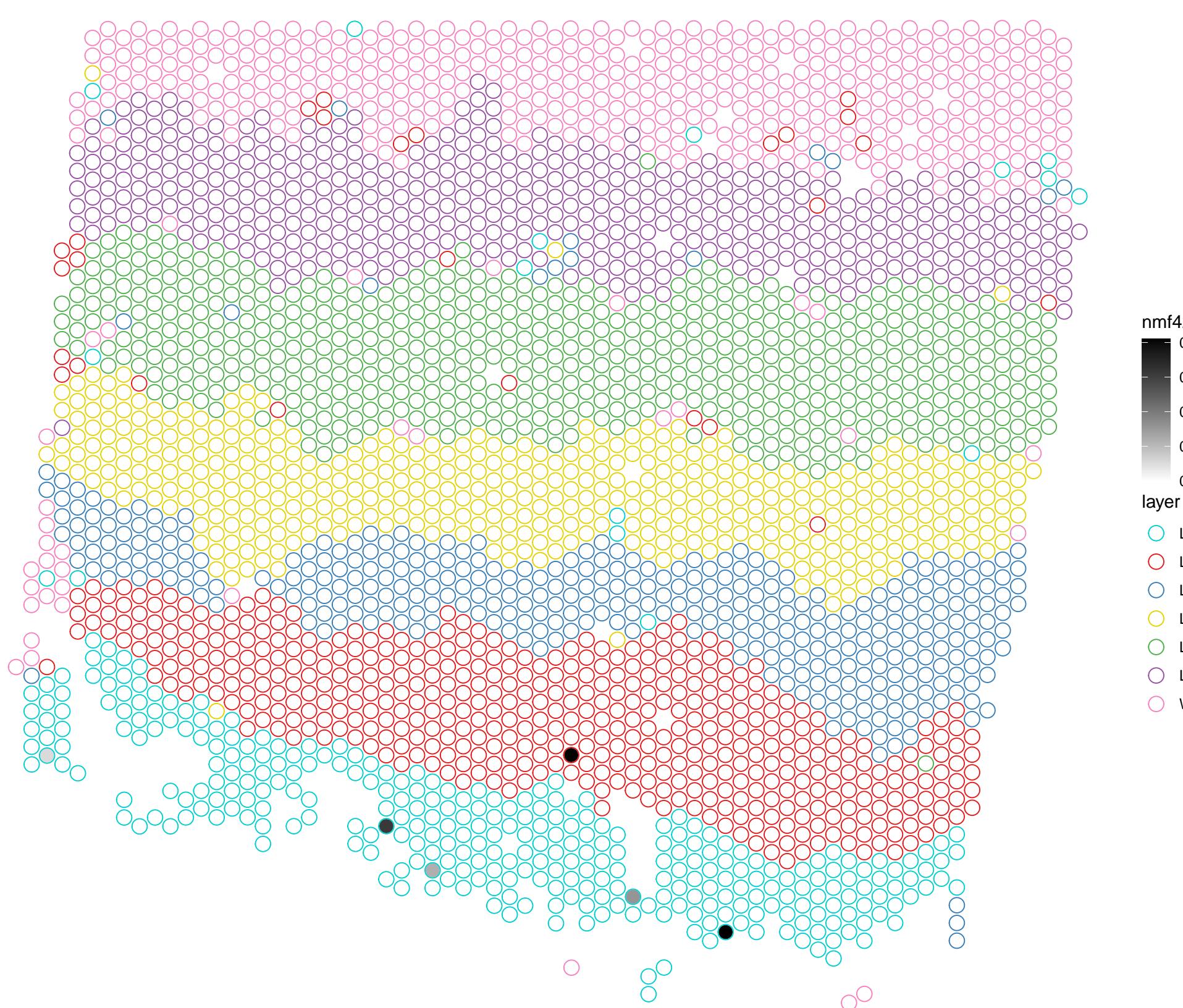
Sample Br8492\_mid



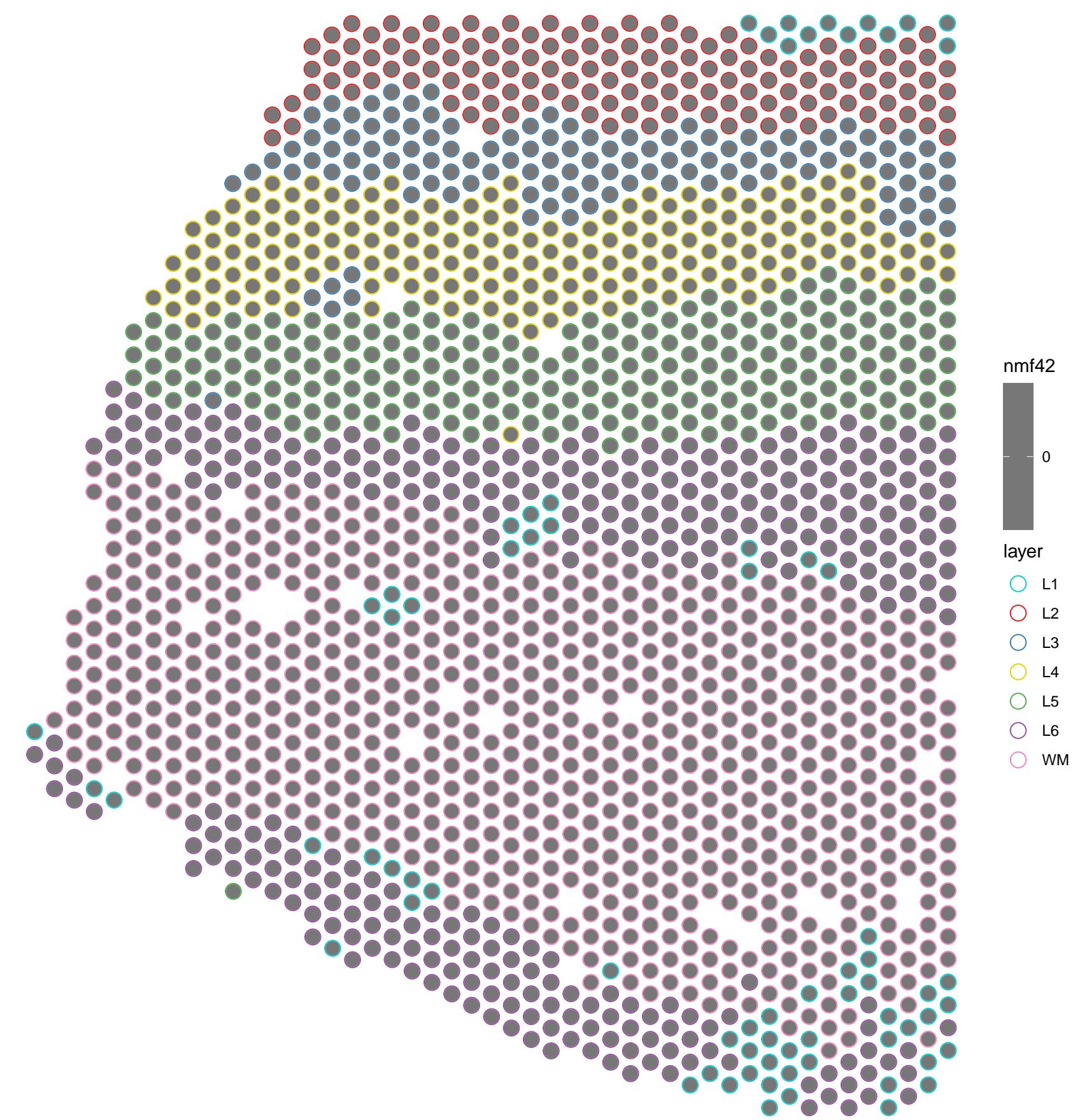
Sample Br8492\_post



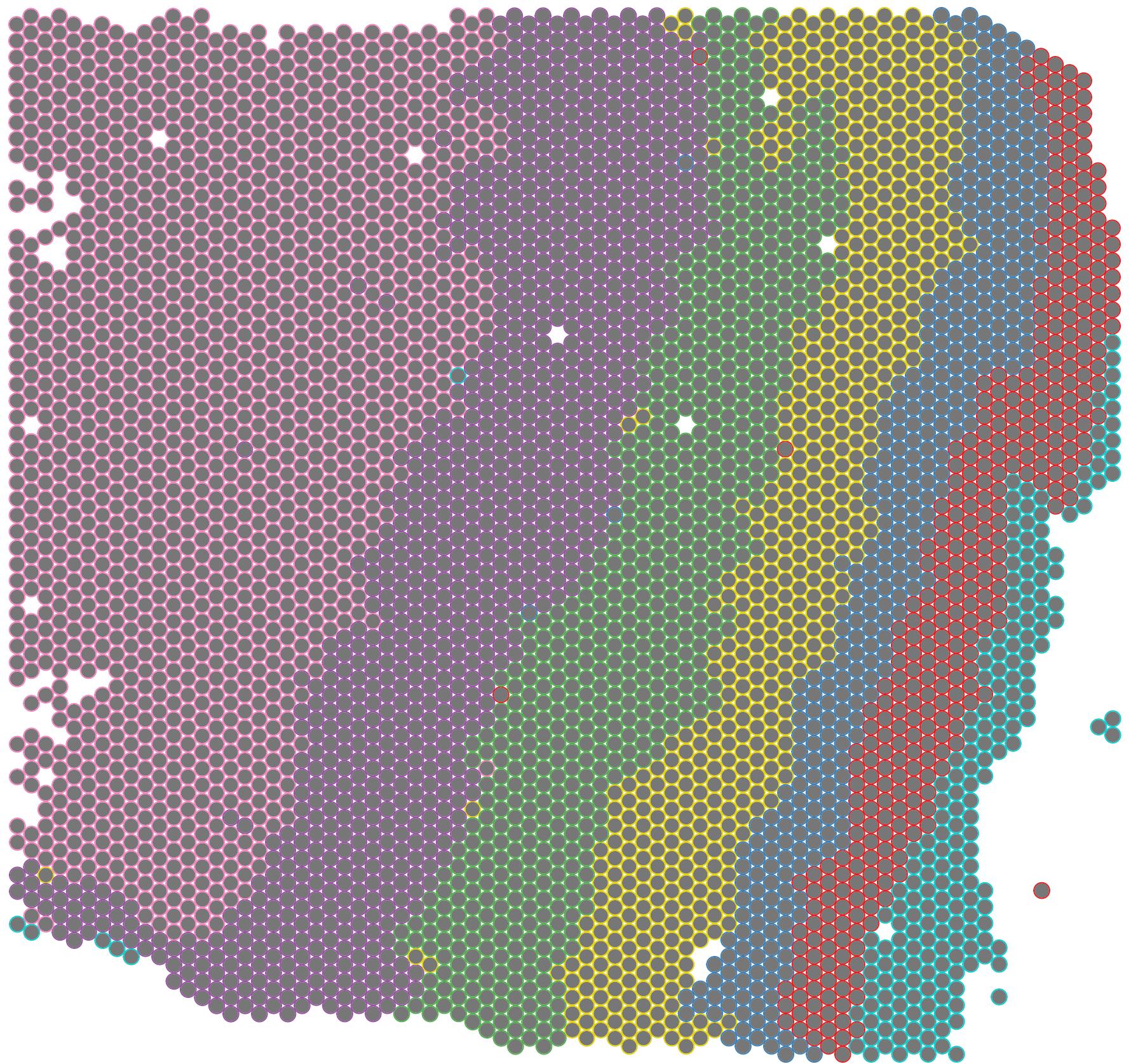
Sample Br2720\_ant



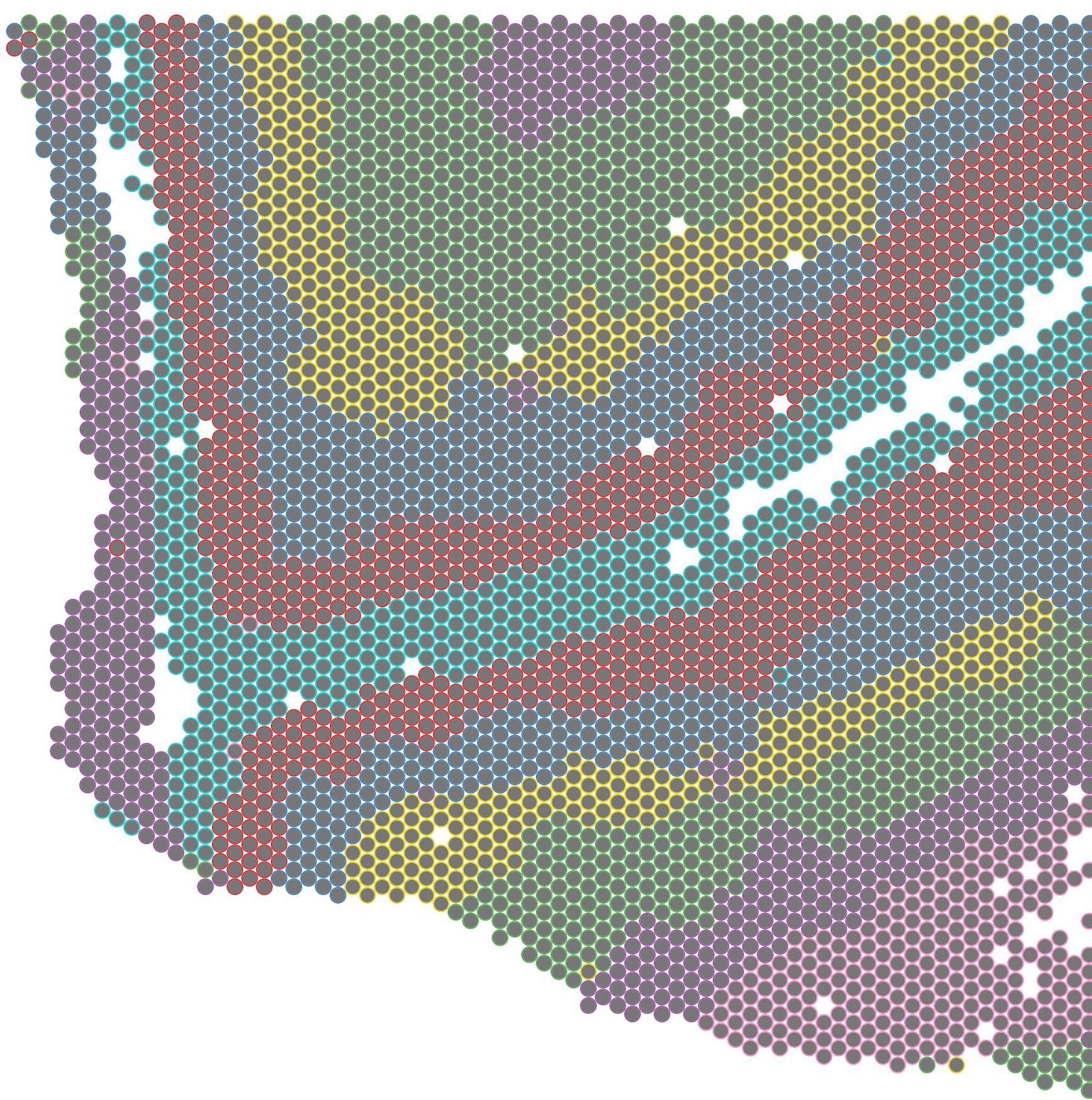
Sample Br2720\_mid



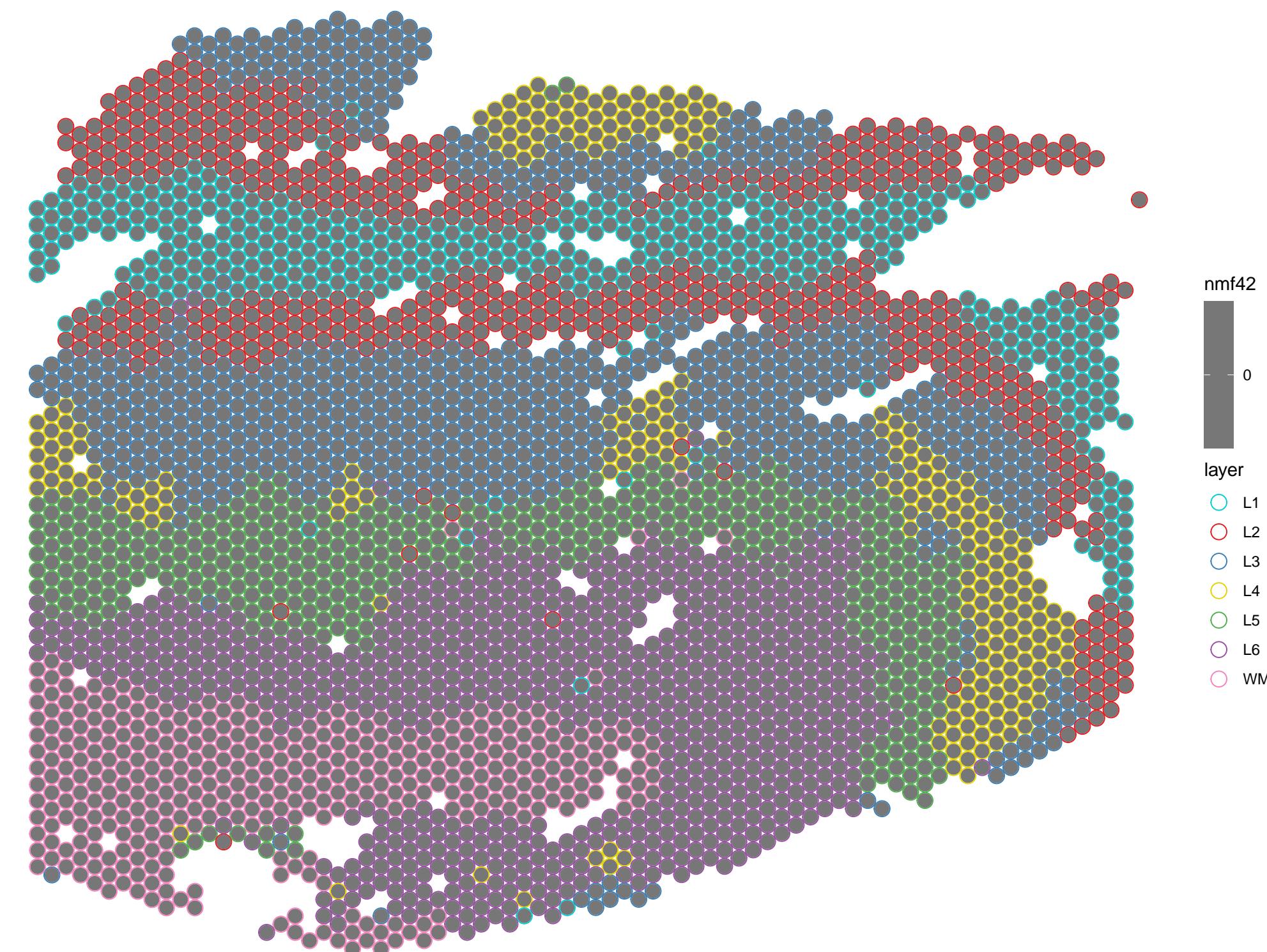
Sample Br2720\_post



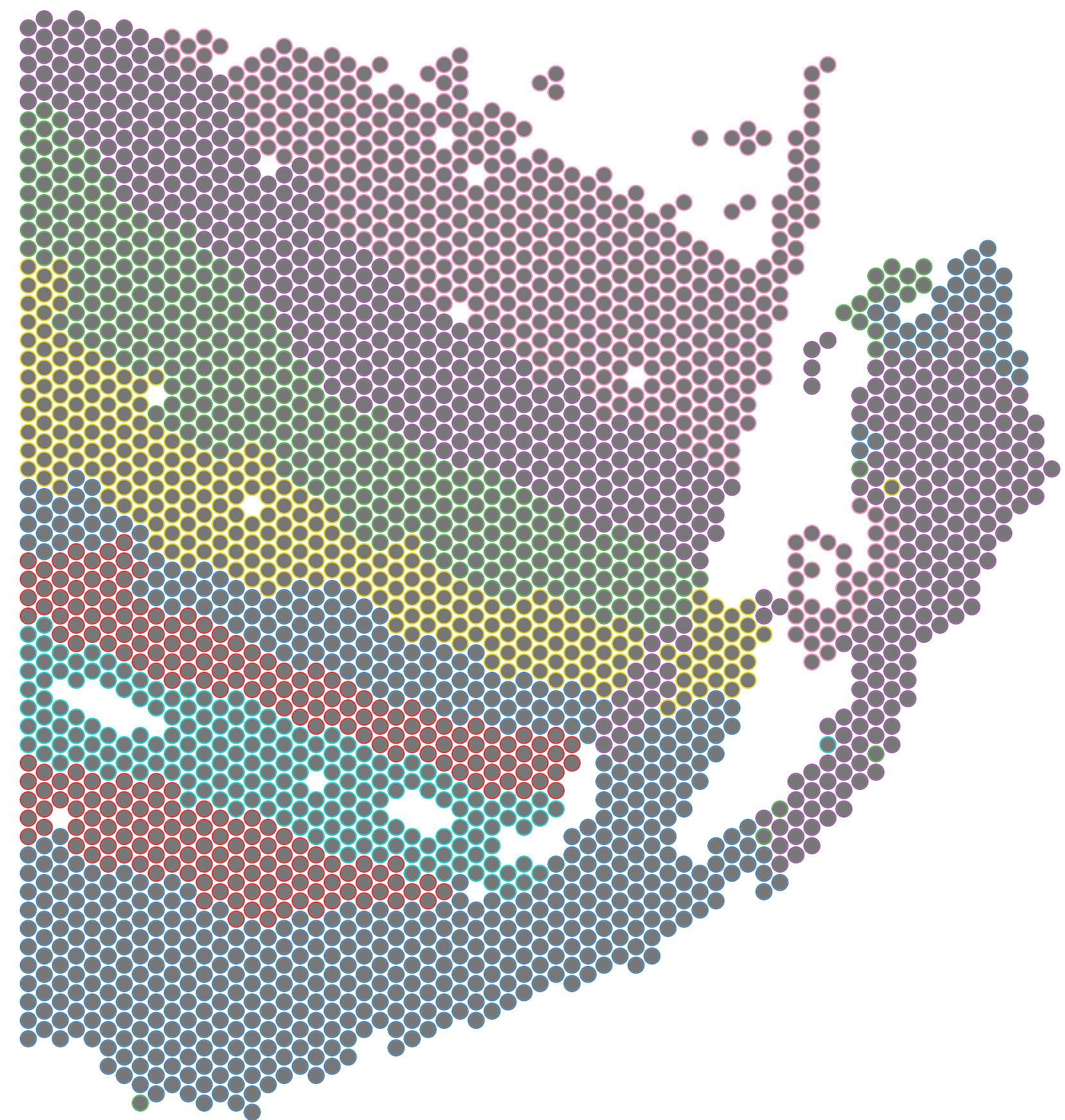
Sample Br6432\_ant



Sample Br6432\_mid



Sample Br6432\_post



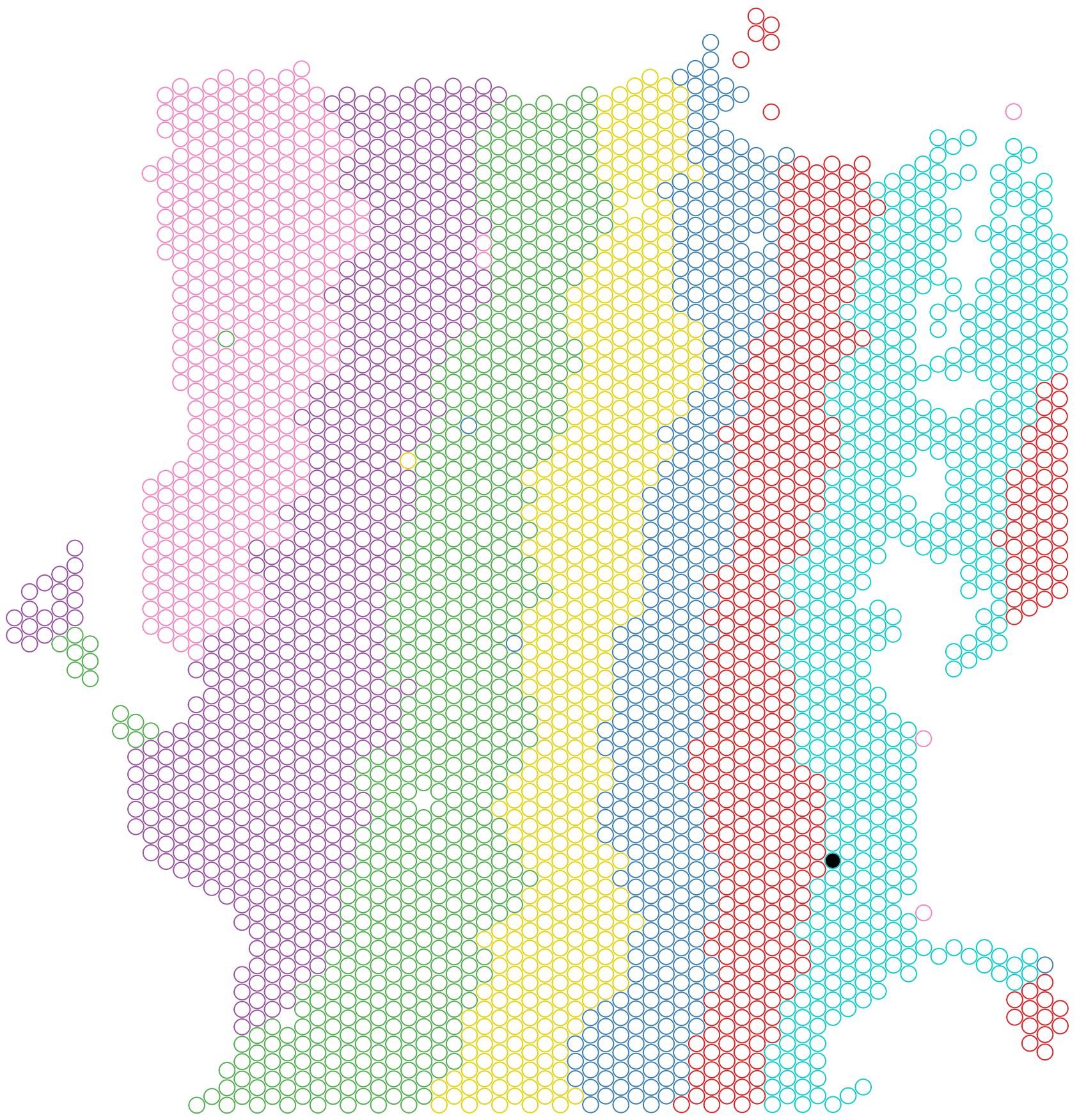
nmf42  
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

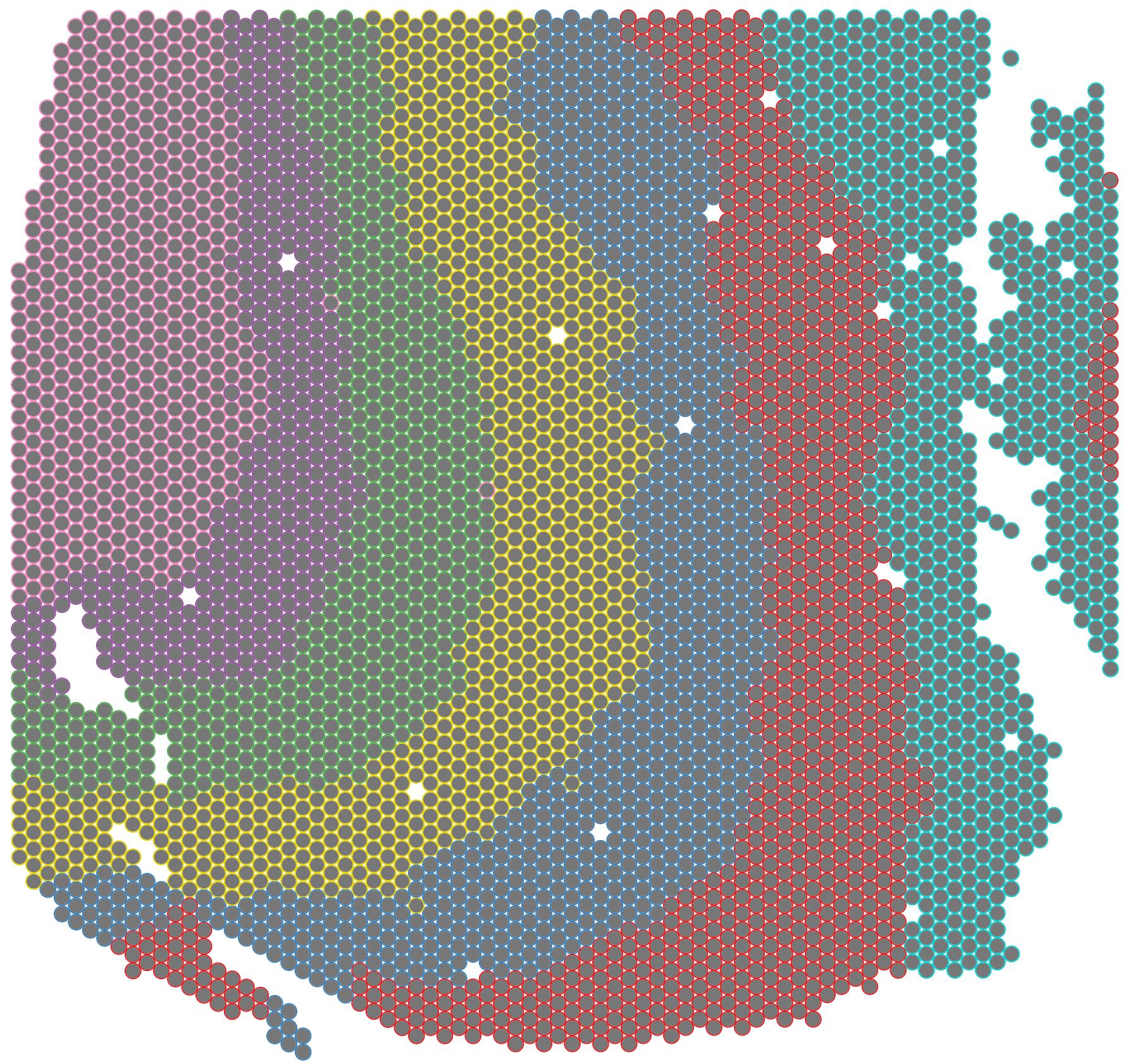
nmf42  
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

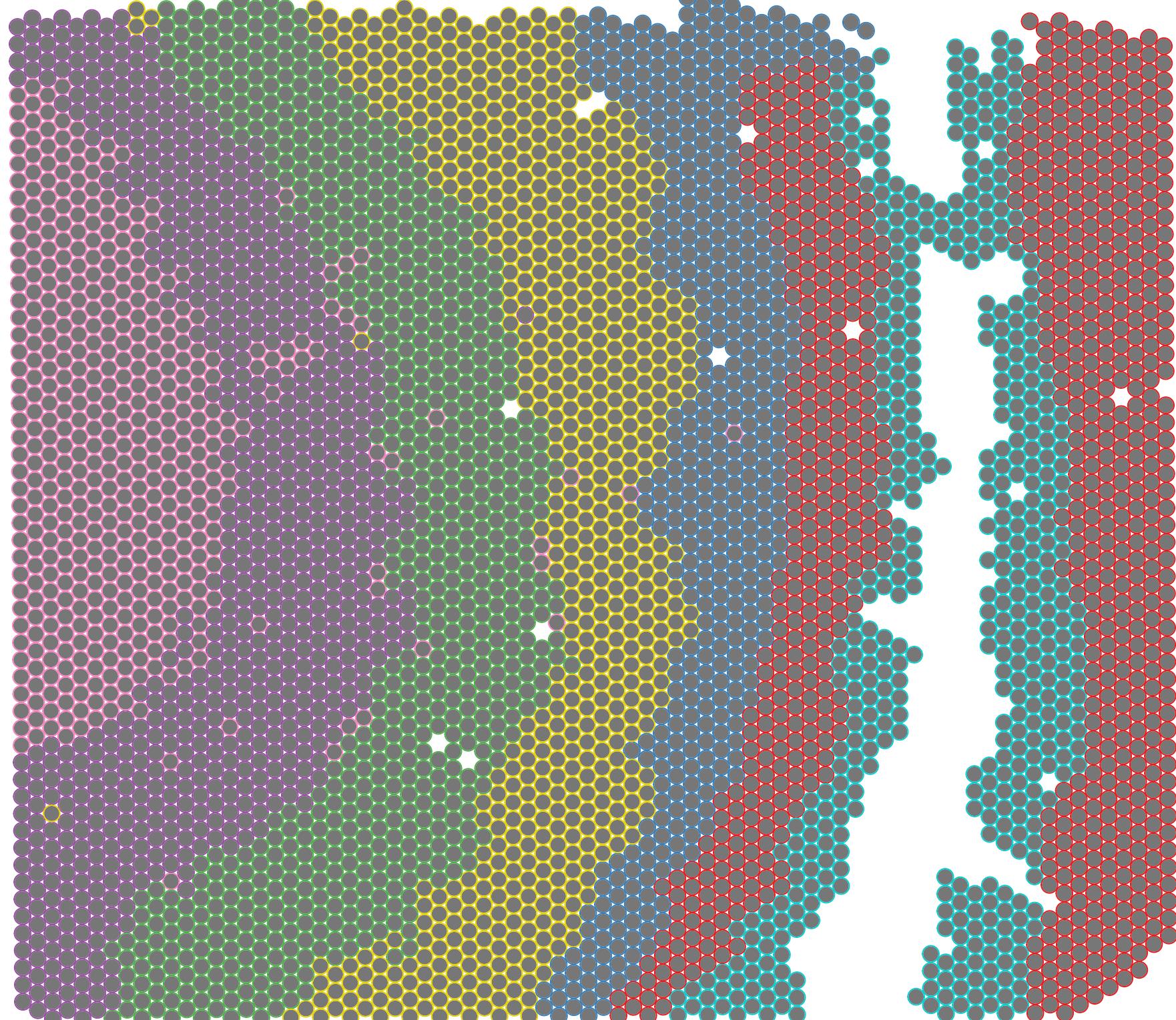
Sample Br6471\_ant



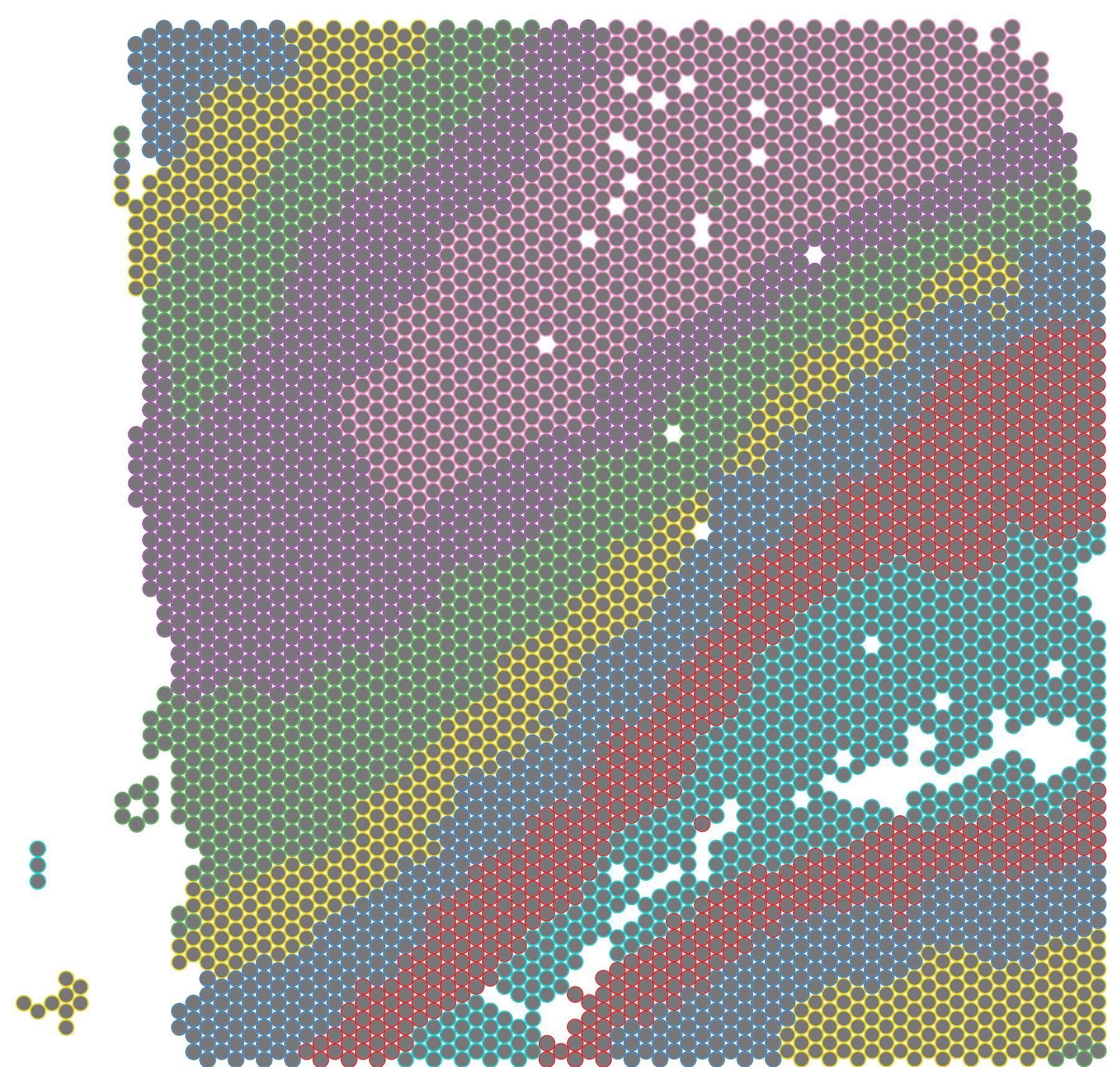
Sample Br6471\_mid



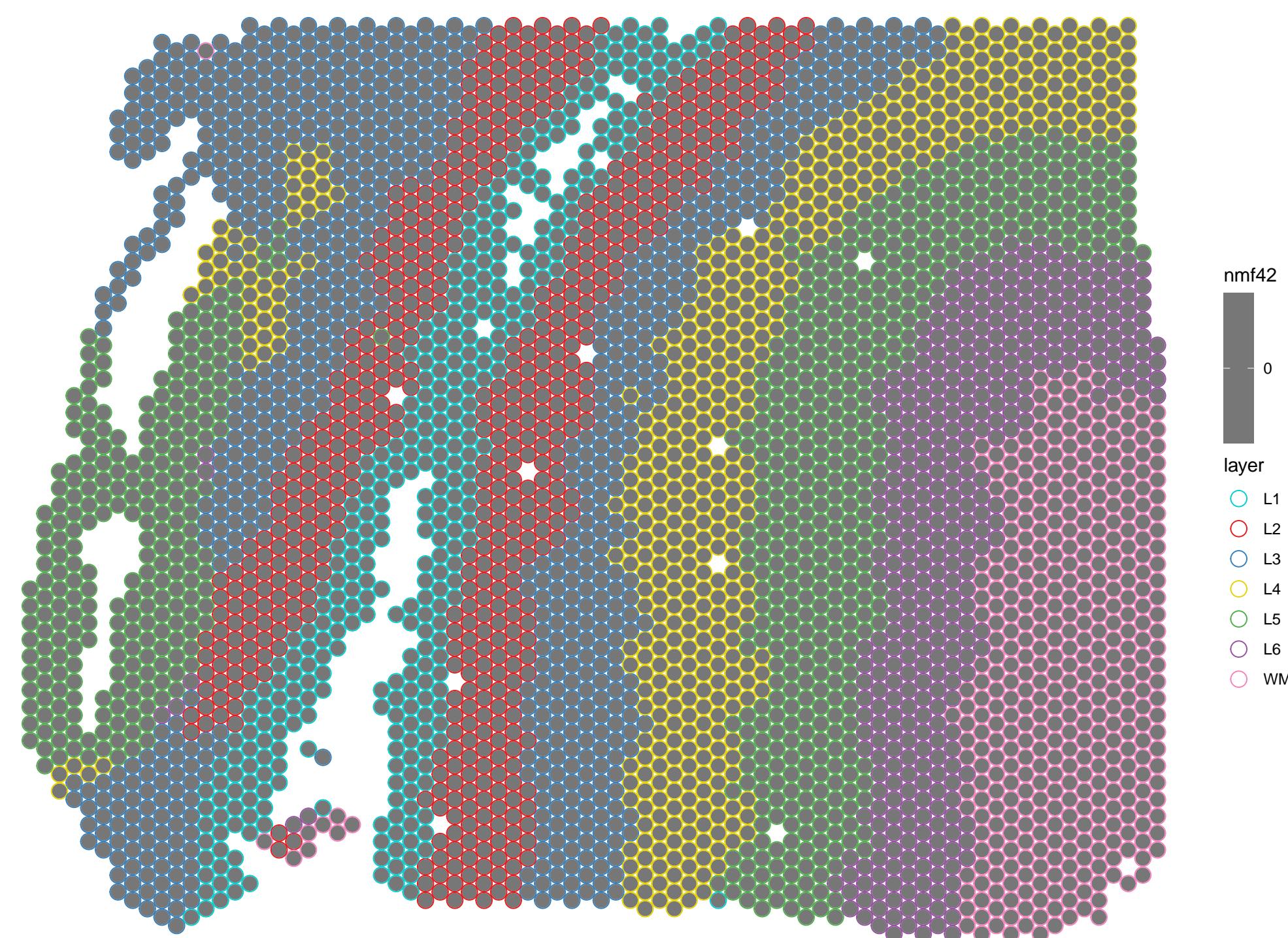
Sample Br6471\_post



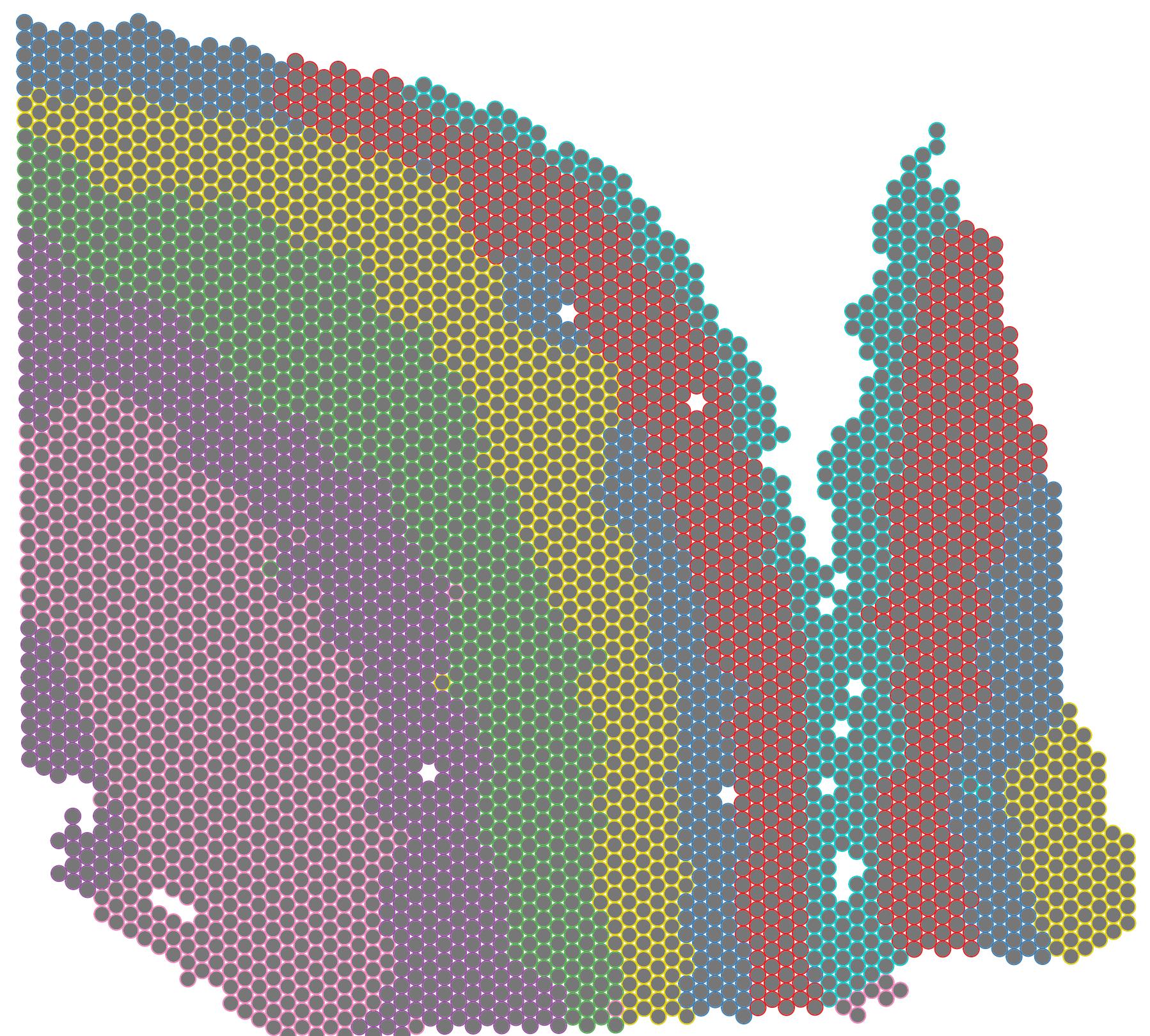
Sample Br6522\_ant



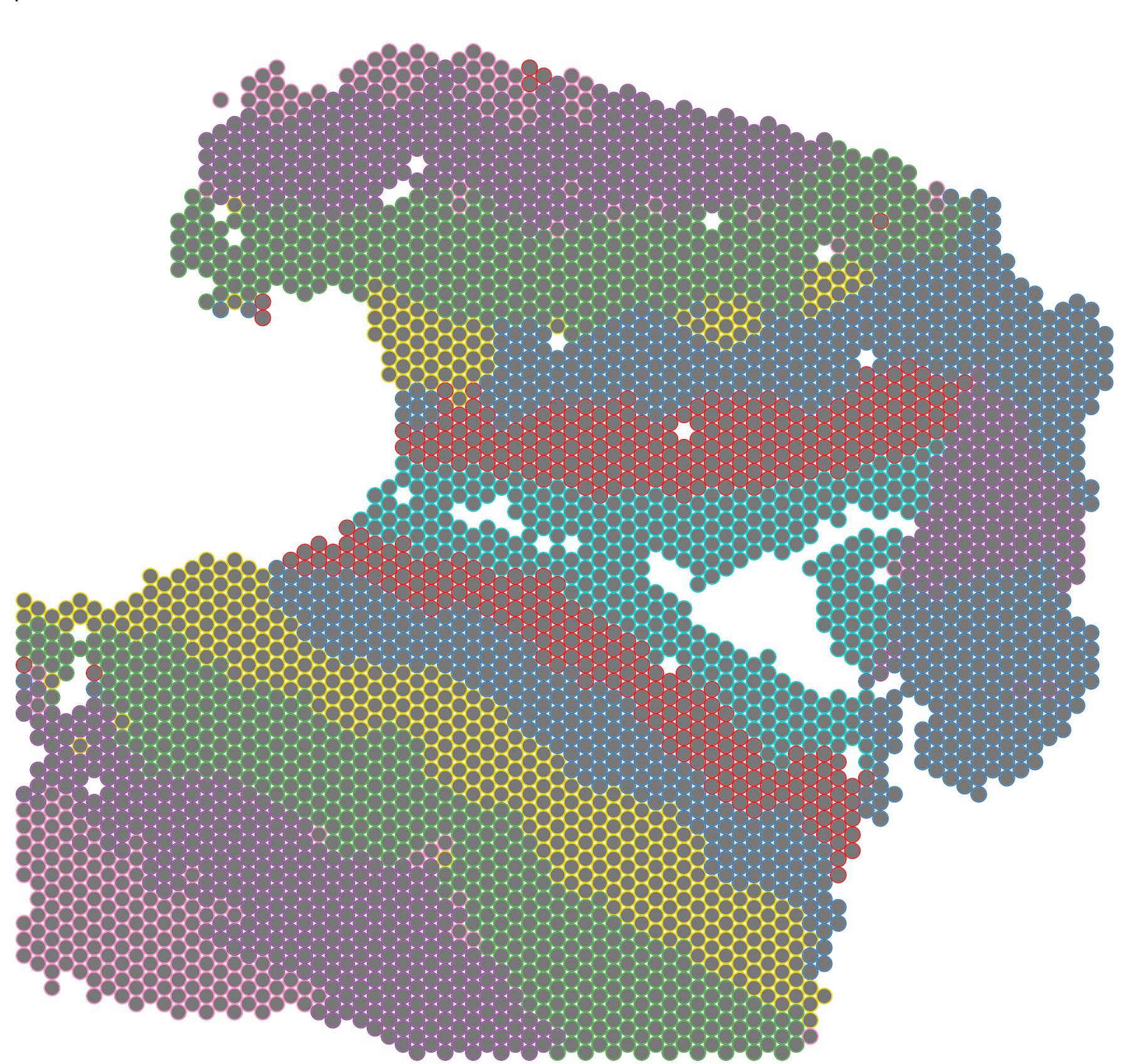
Sample Br6522\_mid



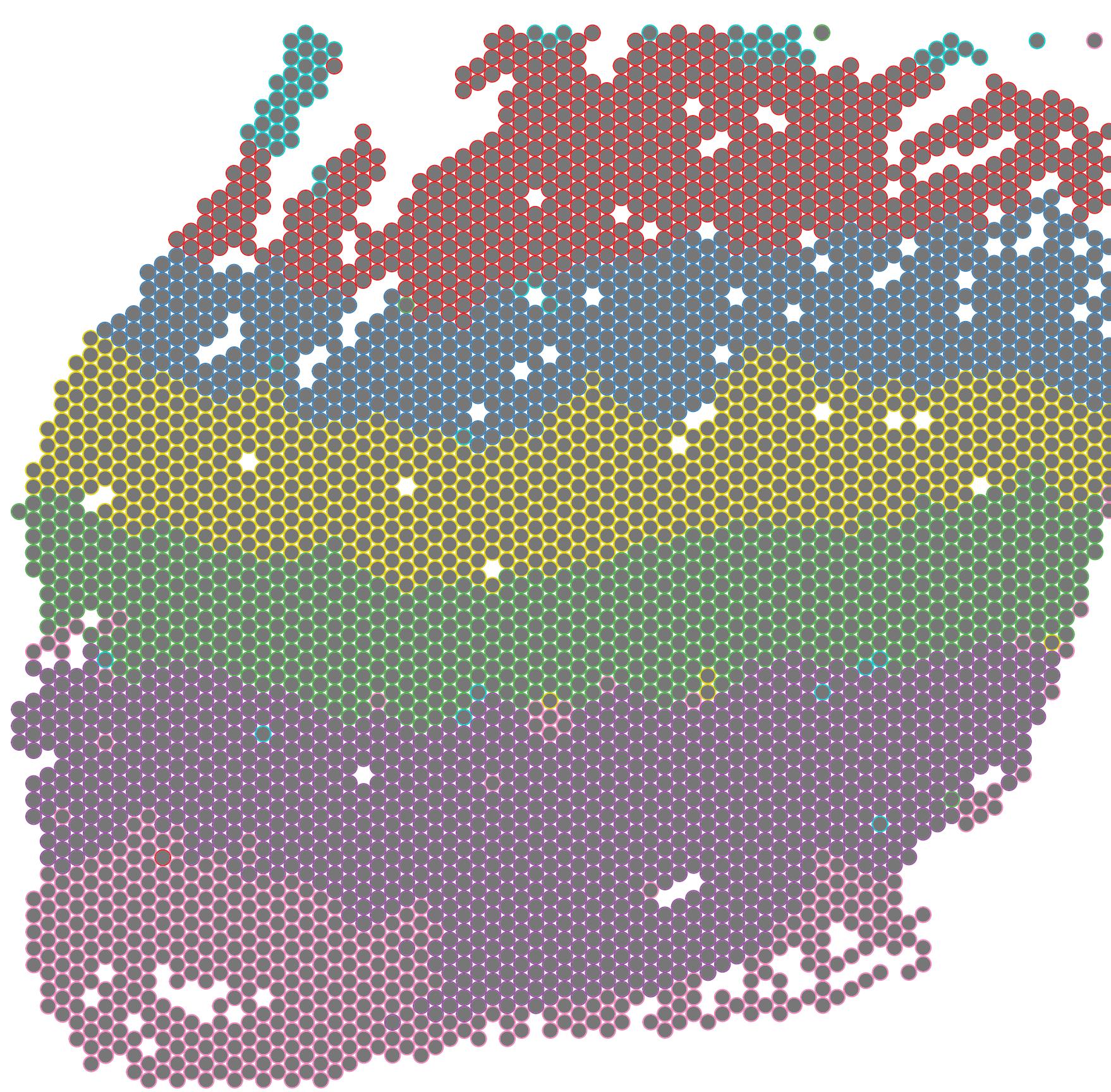
Sample Br6522\_post



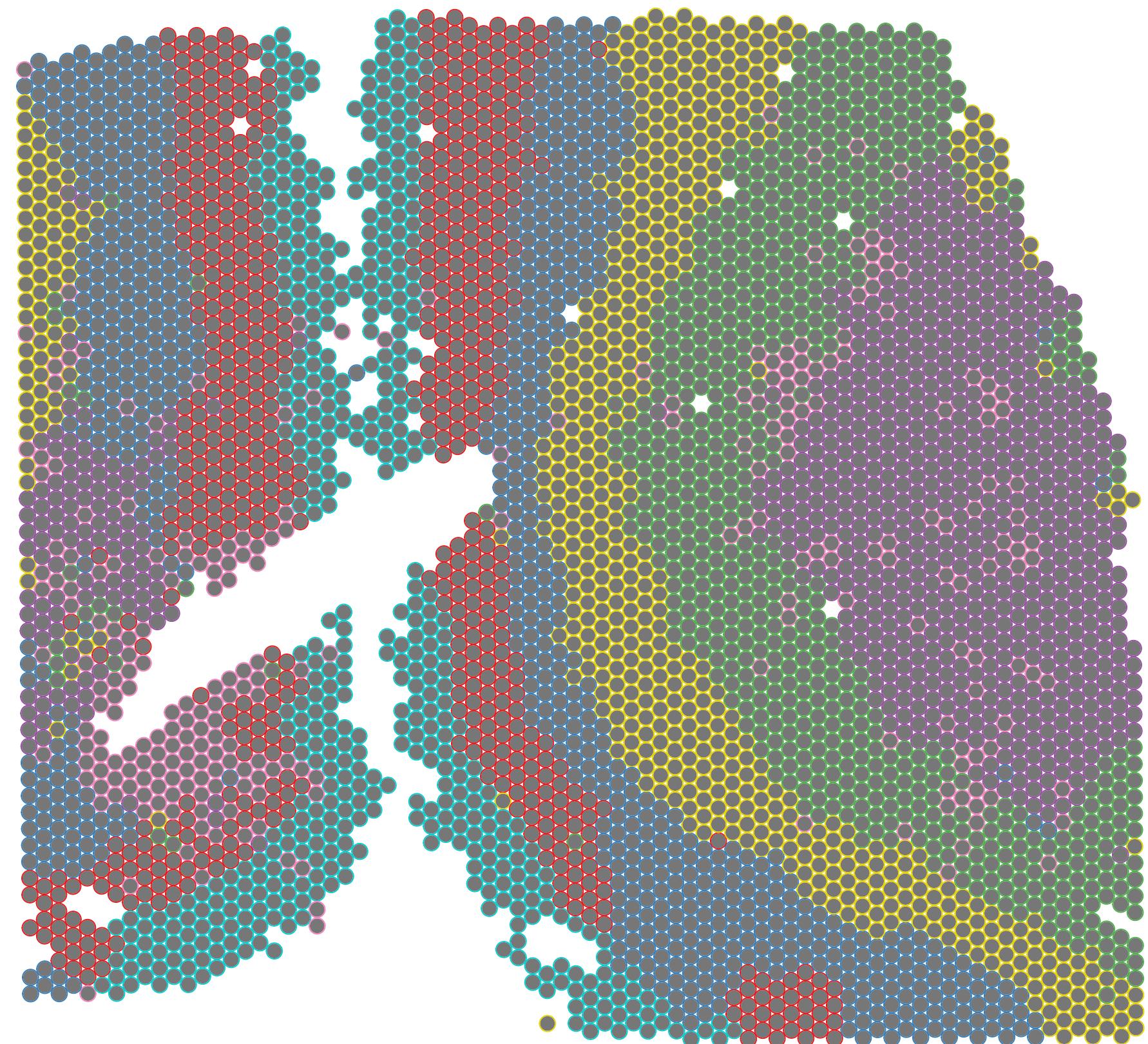
Sample Br8325\_ant



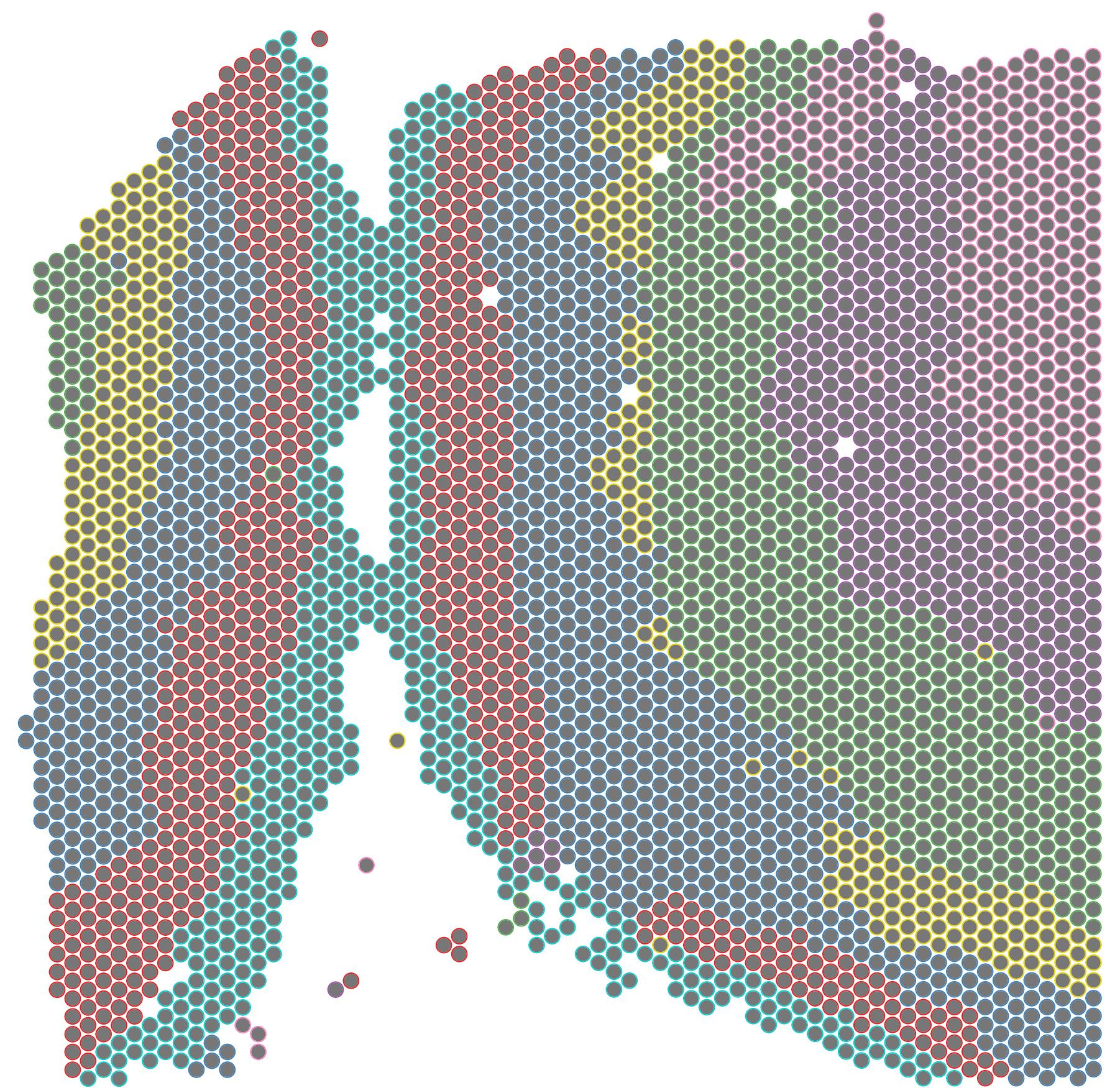
Sample Br8325\_mid



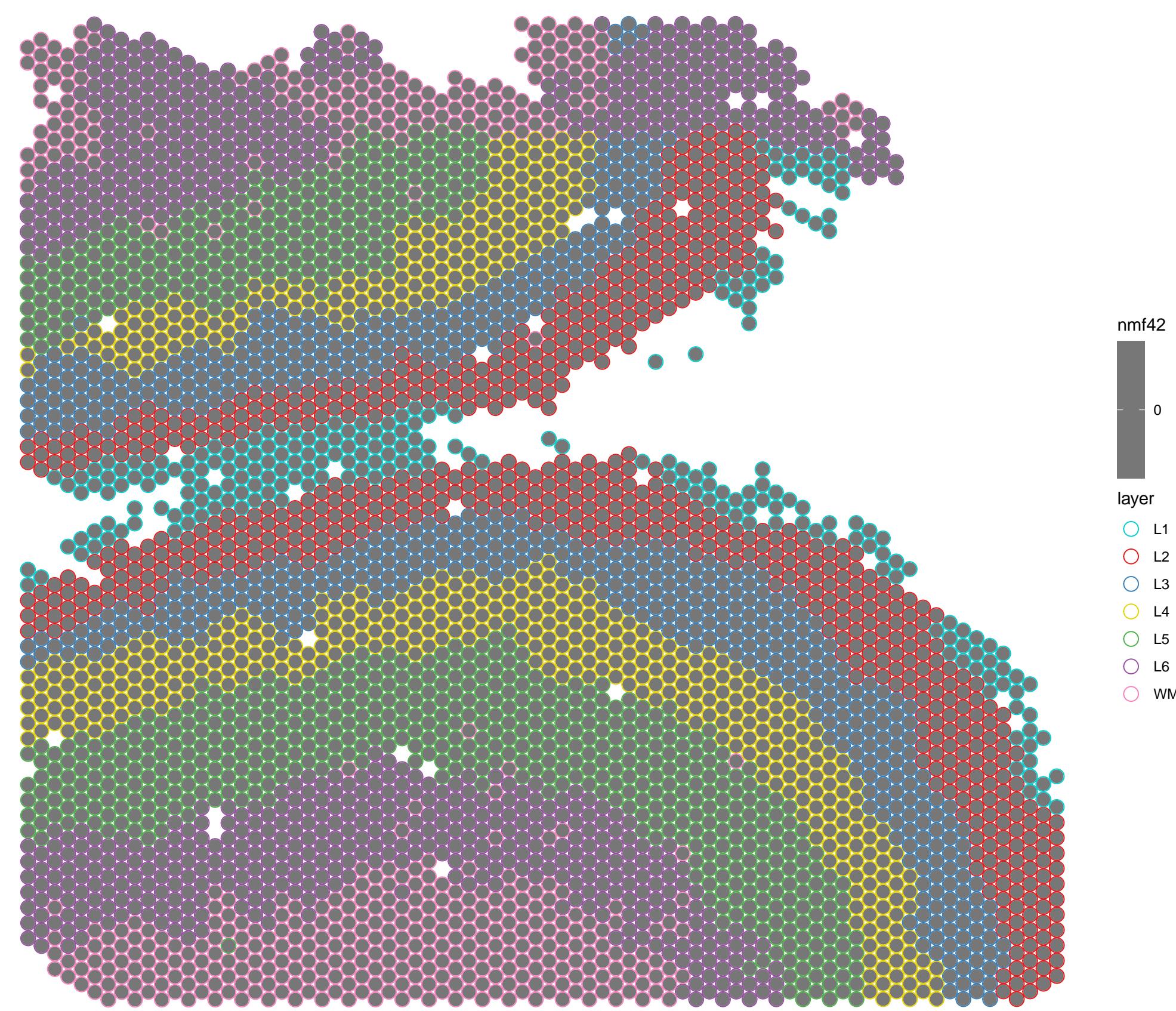
Sample Br8325\_post



Sample Br8667\_ant



Sample Br8667\_mid



Sample Br8667\_post

