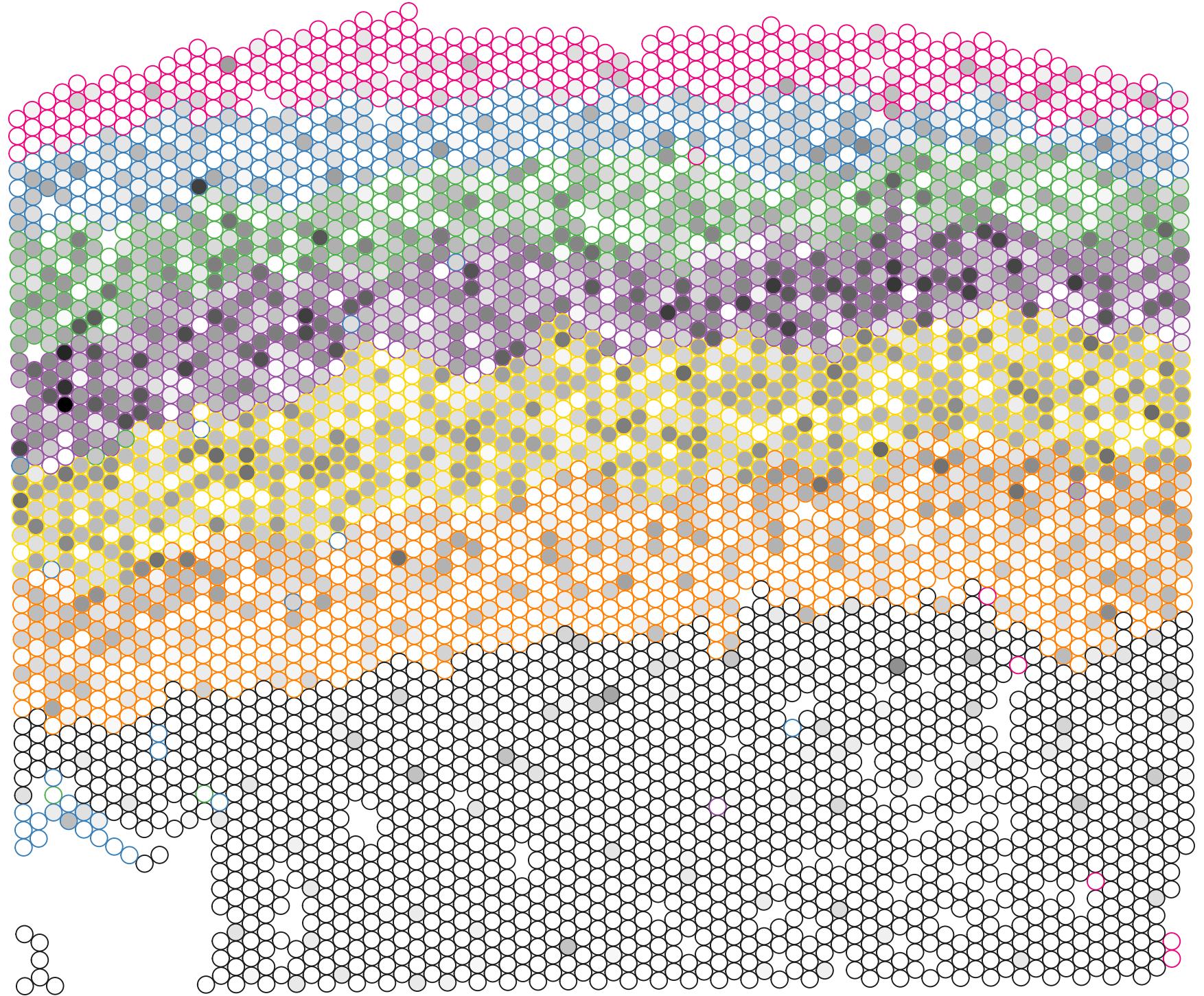
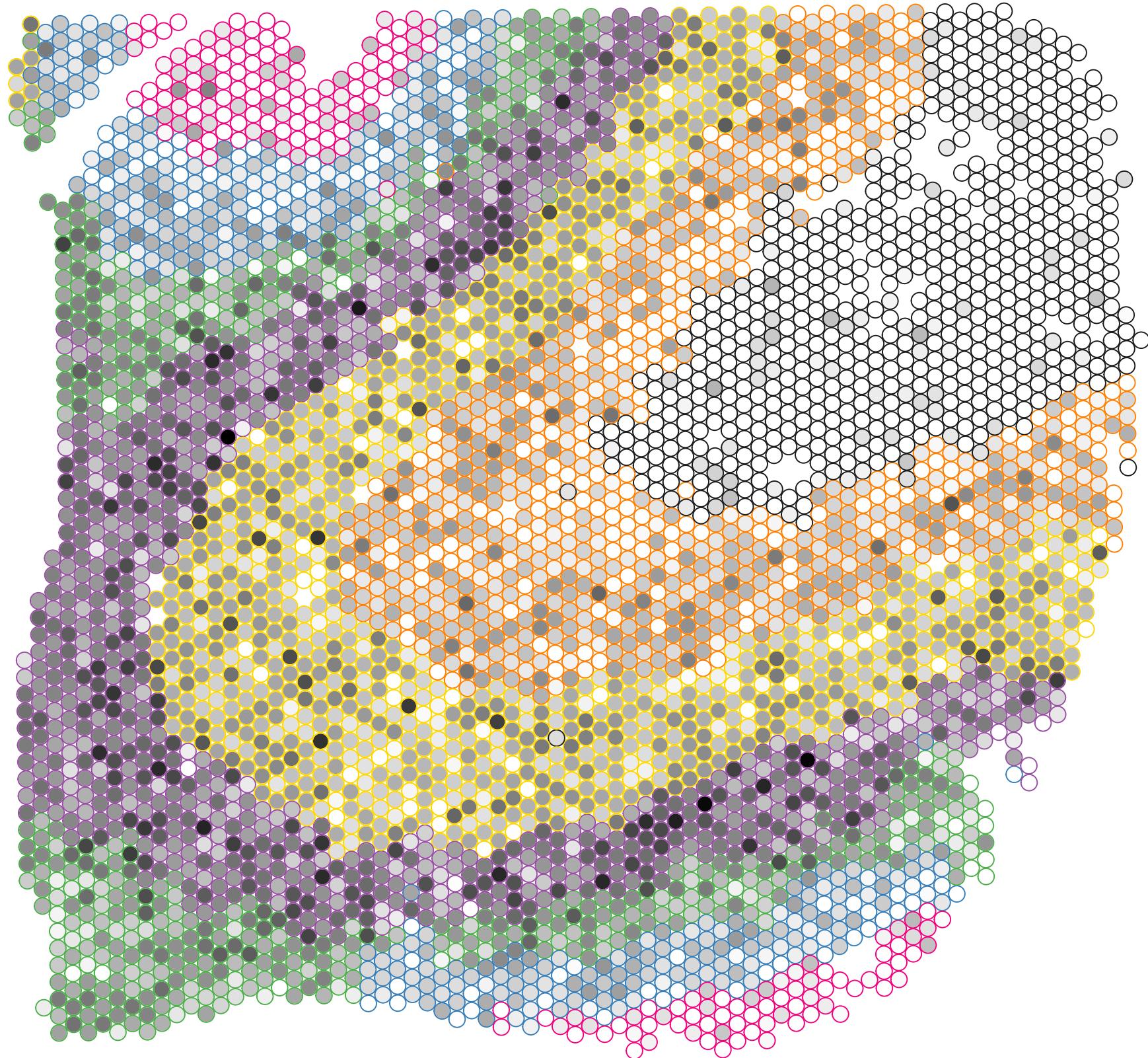


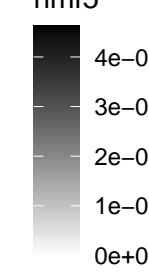
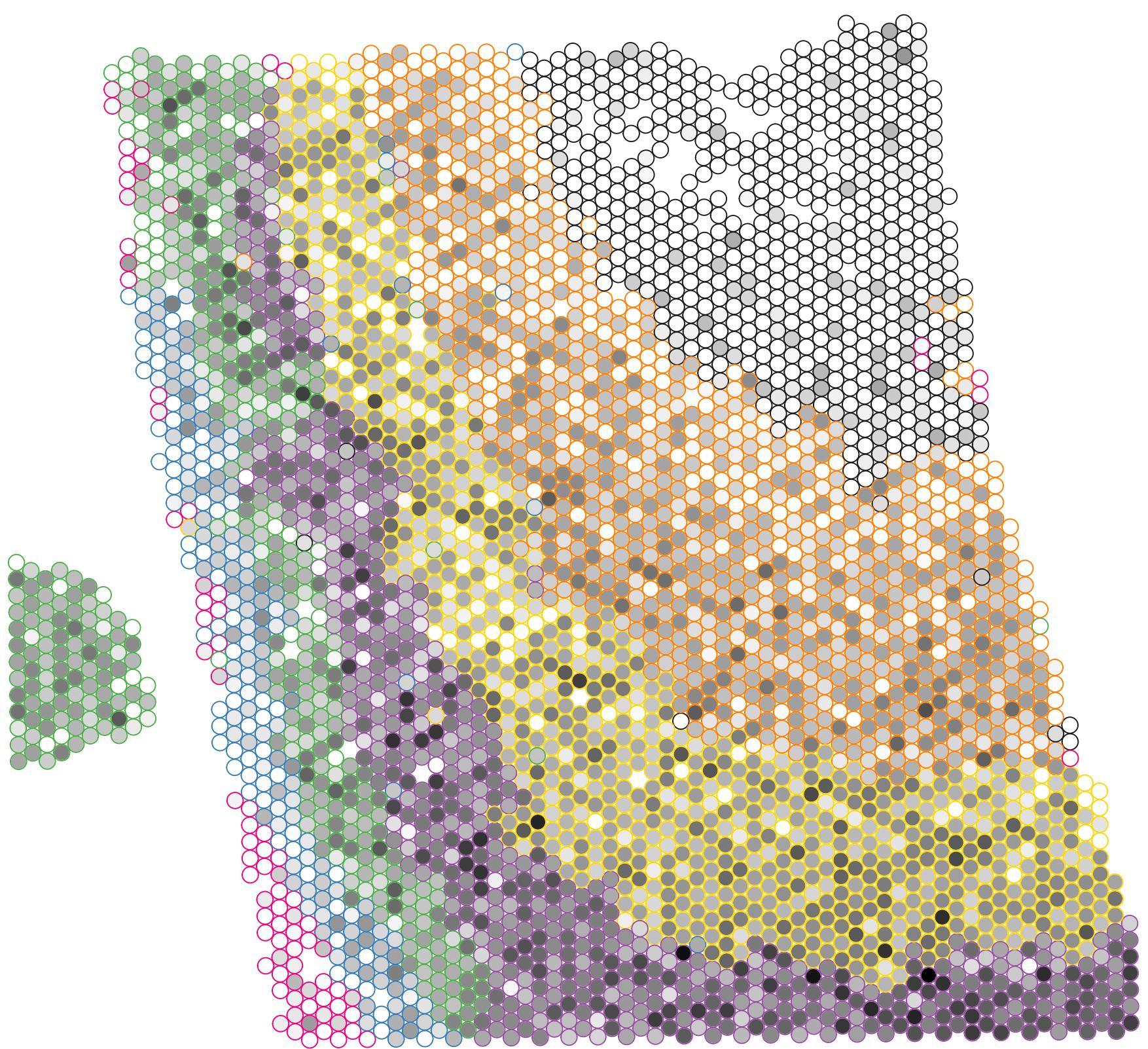
Sample Br2743_ant



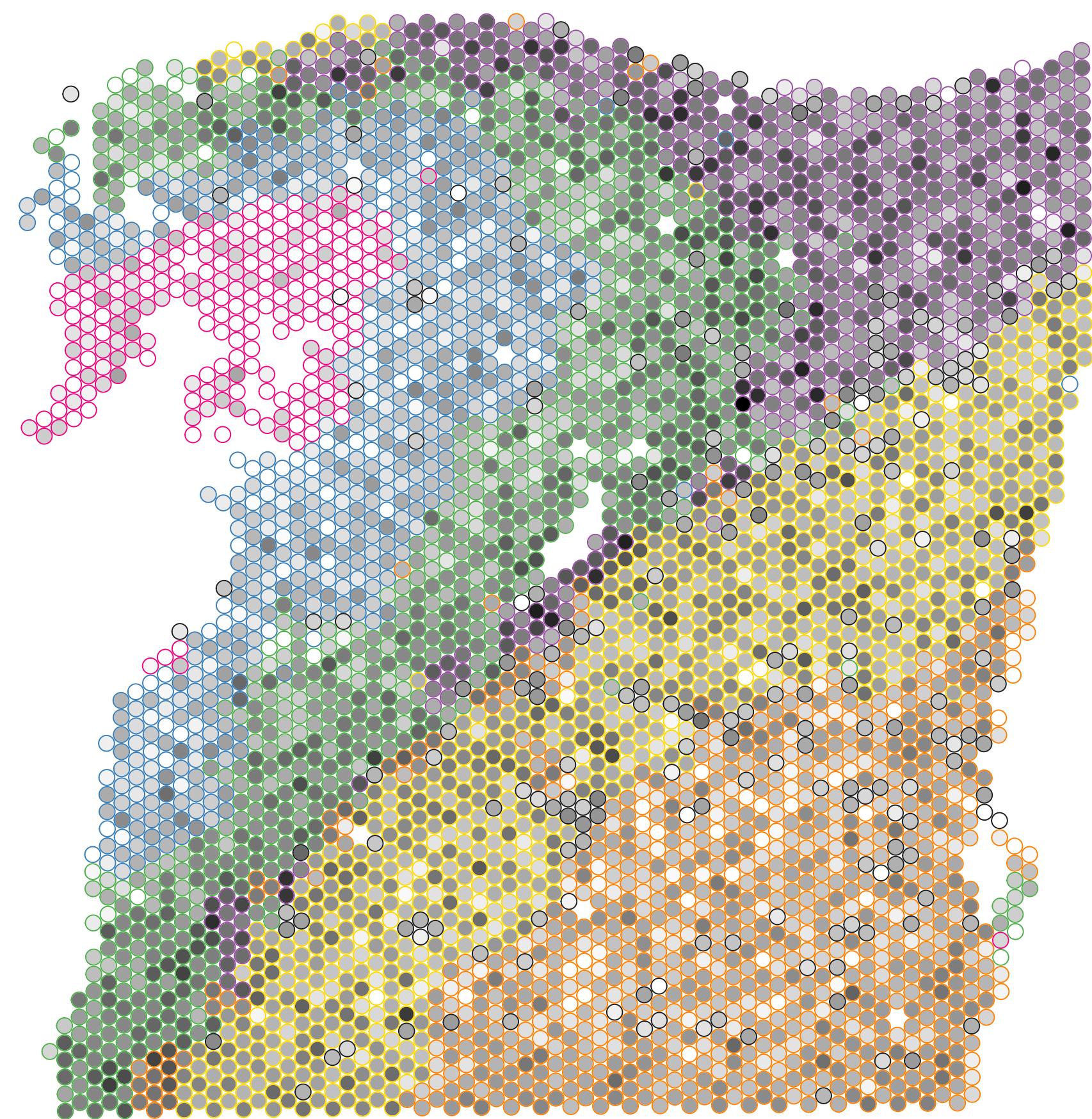
Sample Br2743_mid



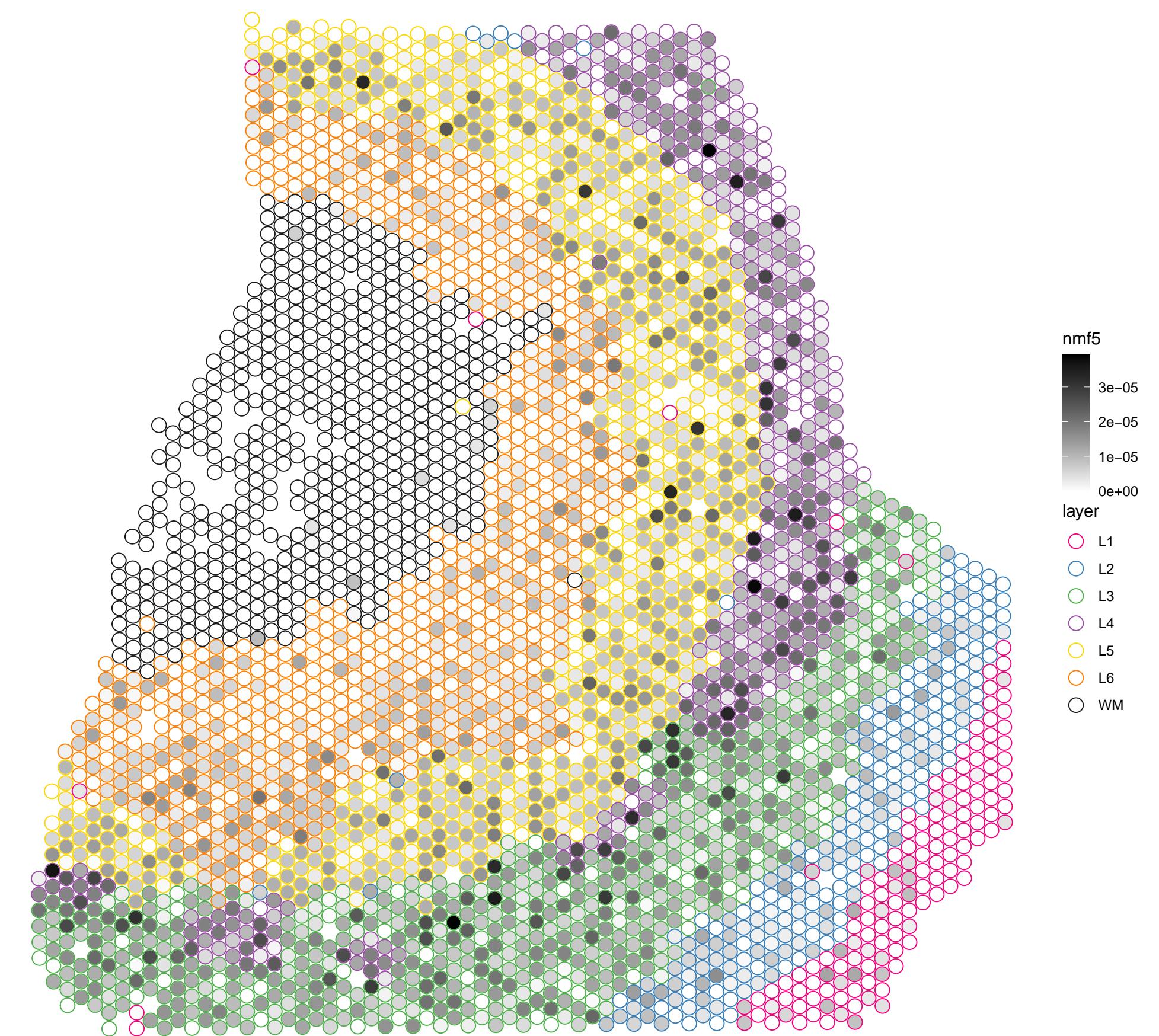
Sample Br2743_post



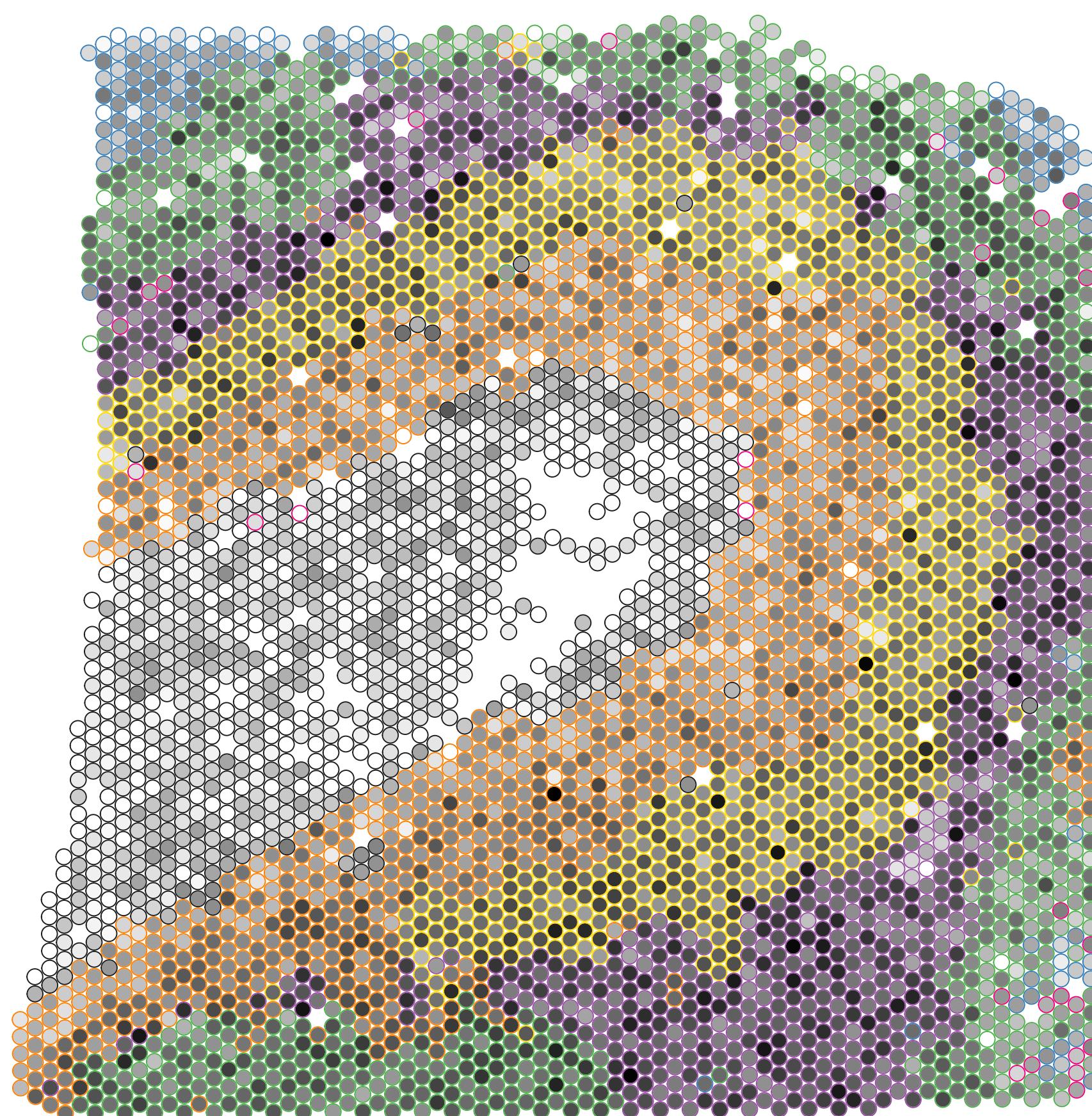
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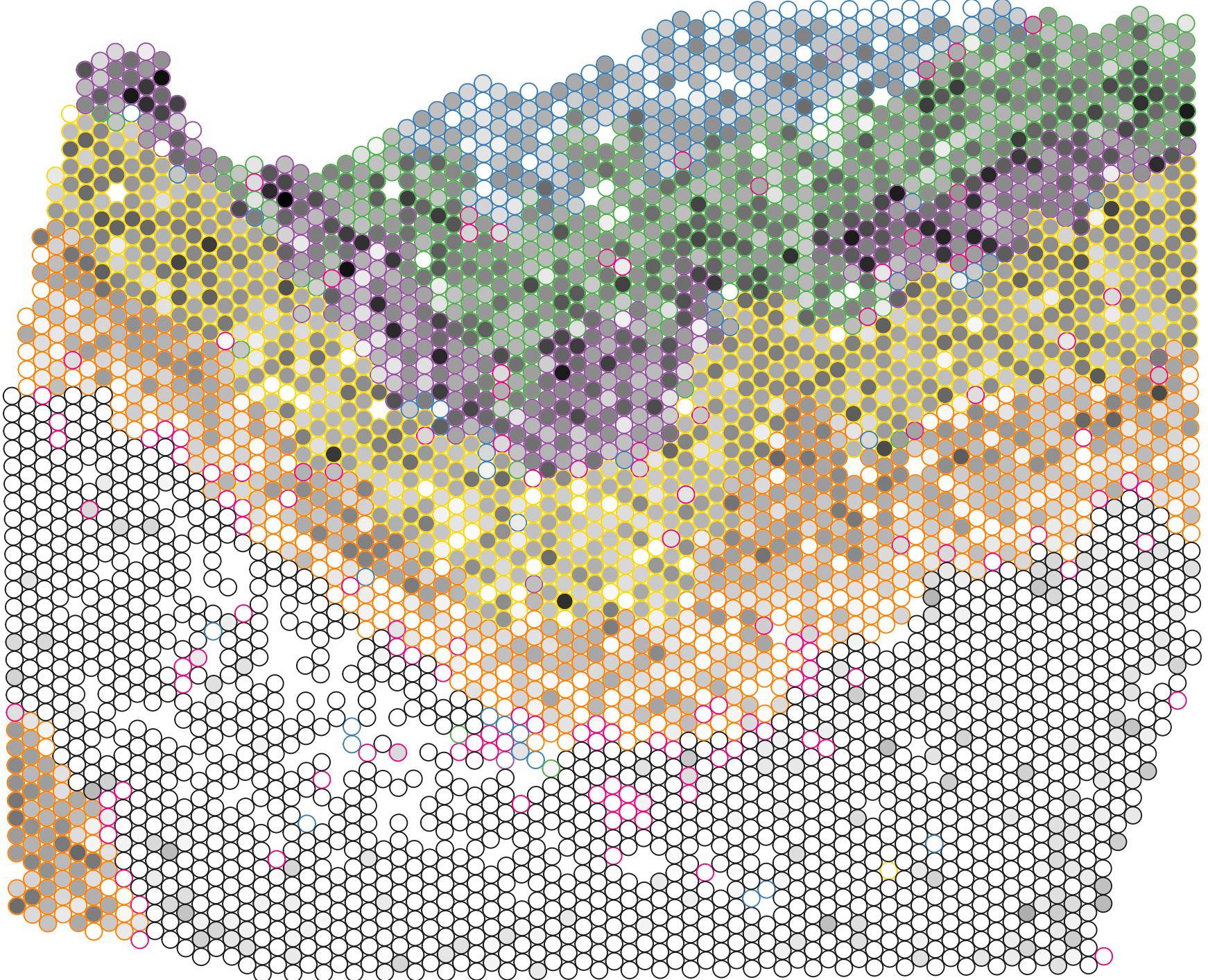
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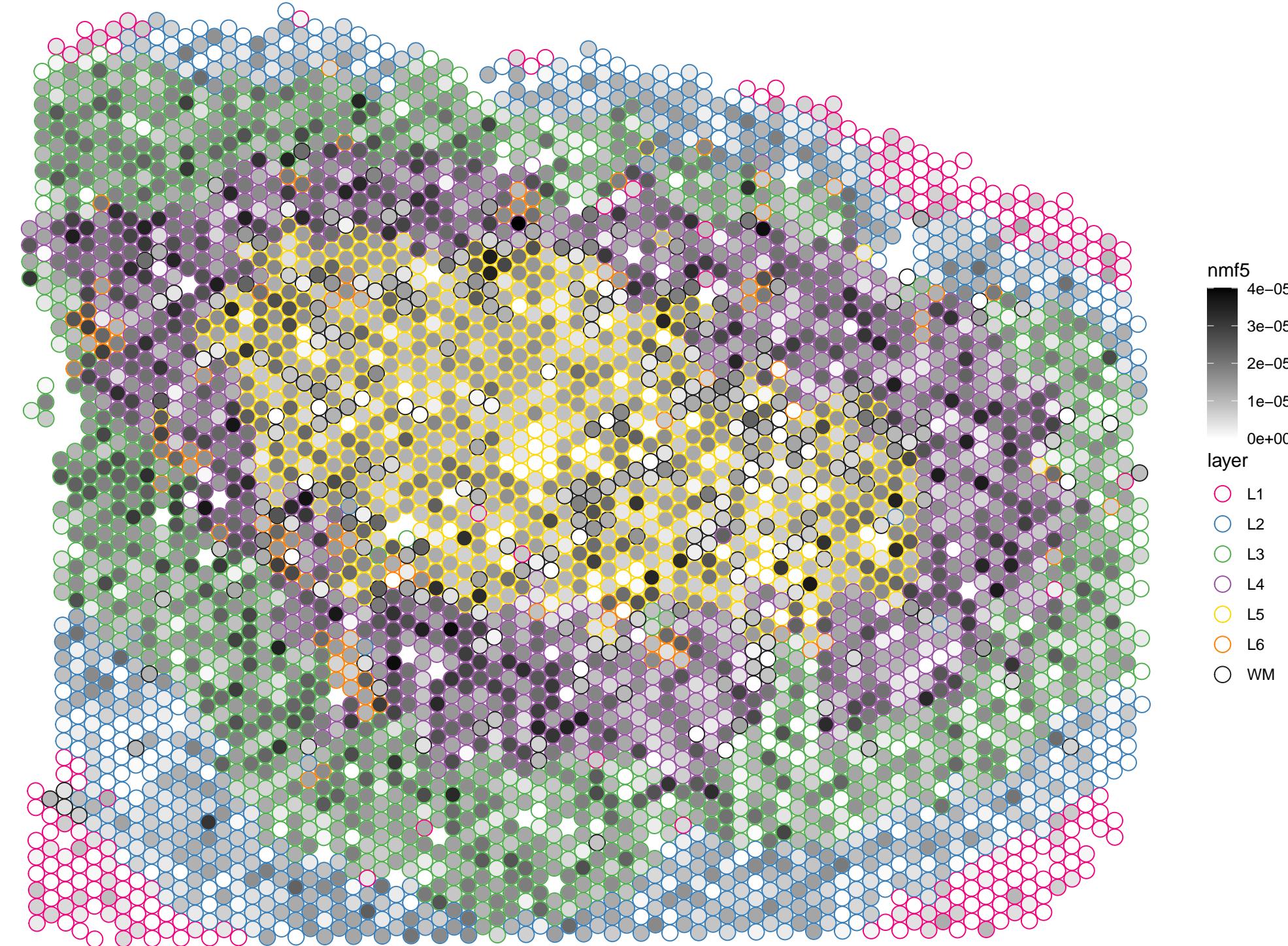
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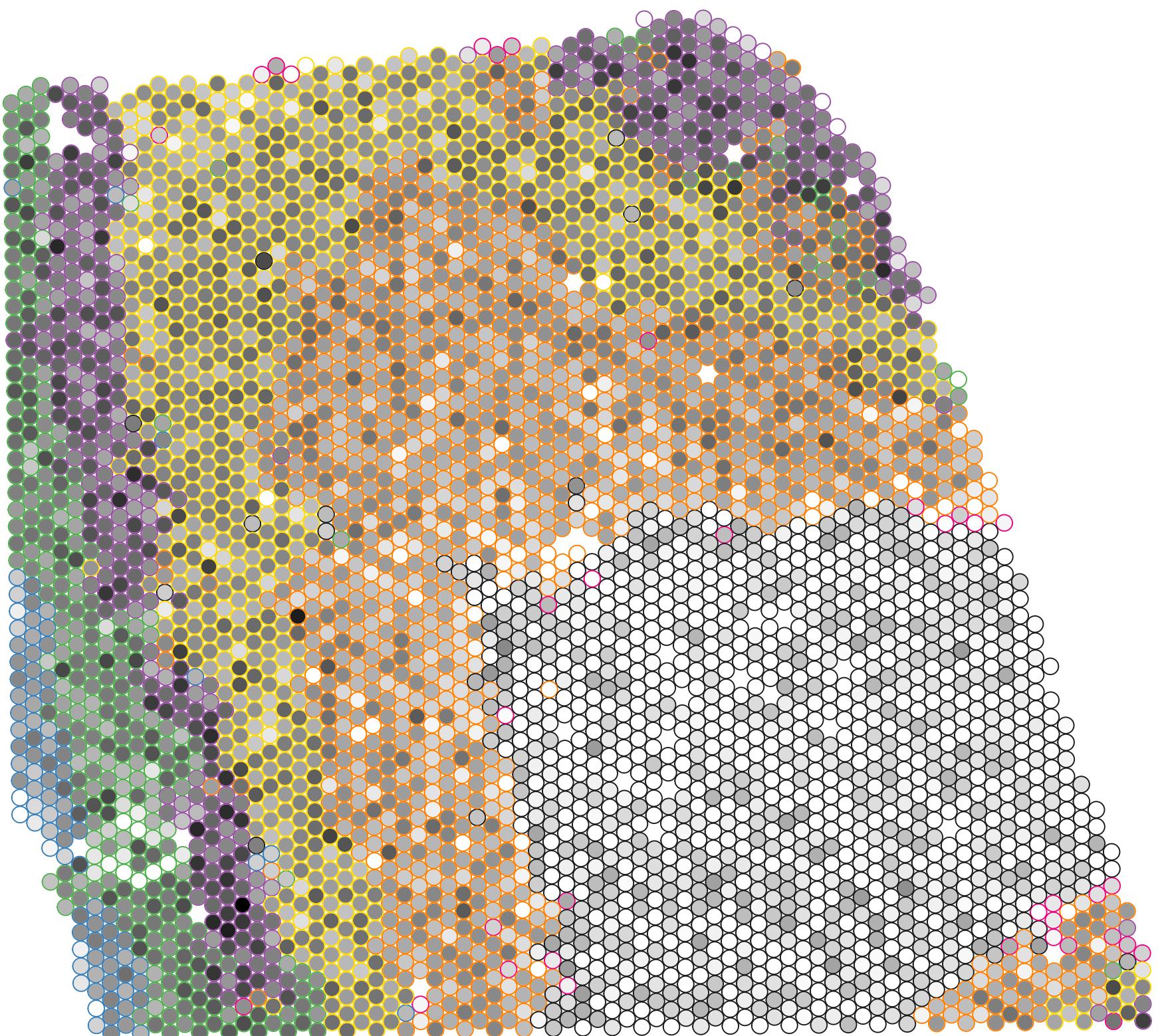
Sample Br6423_ant



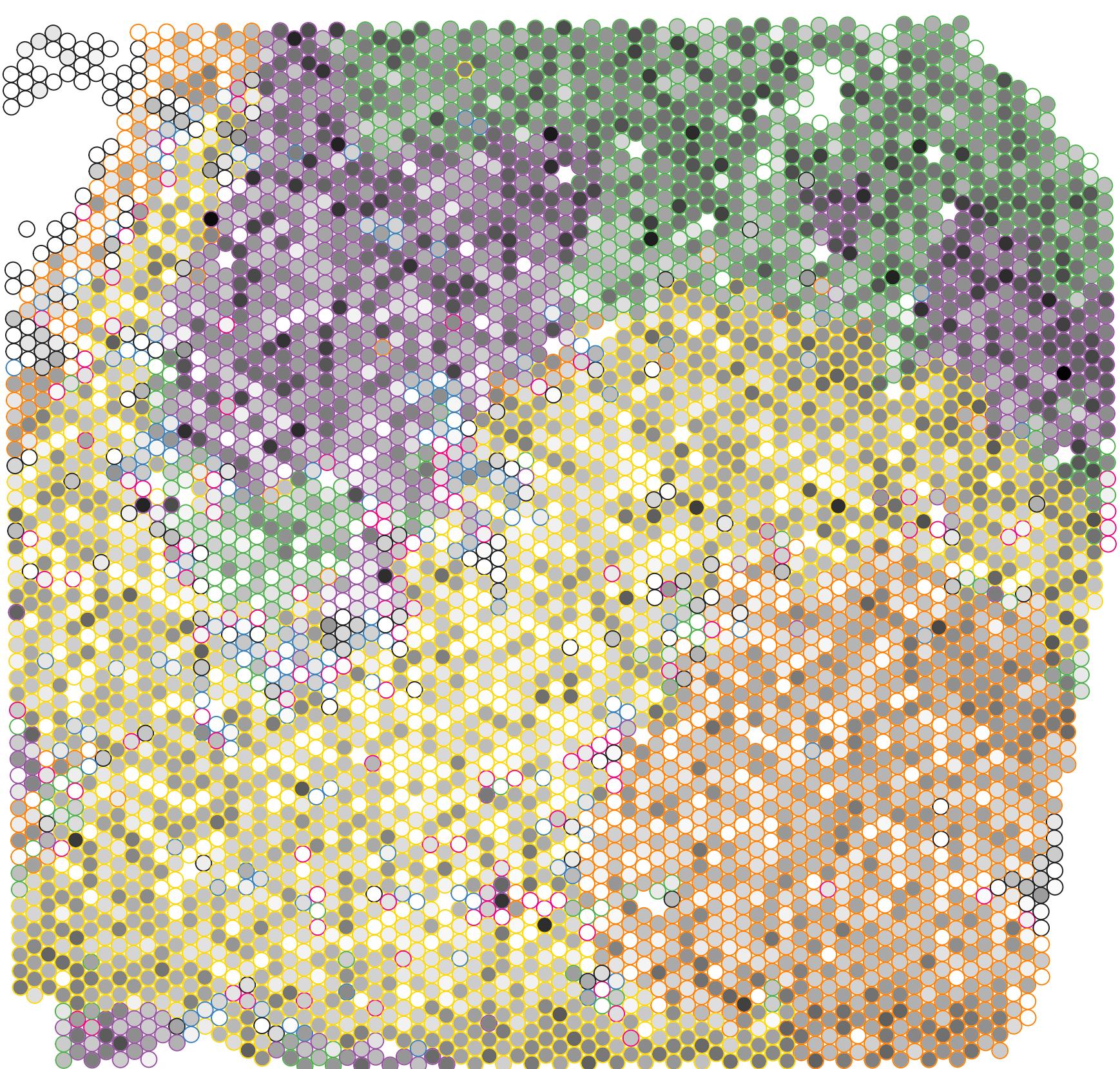
Sample Br6423_mid



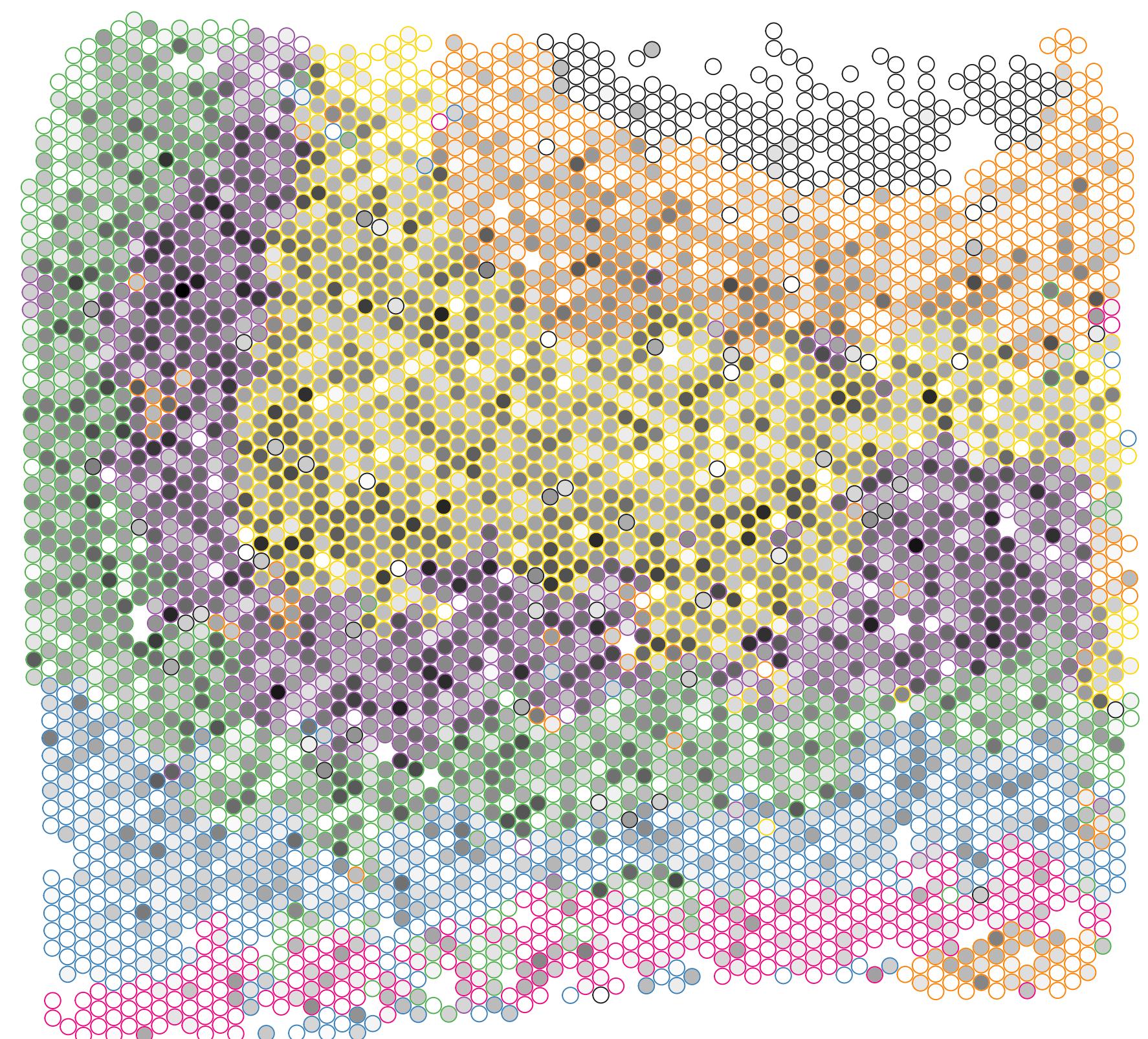
Sample Br6423_post



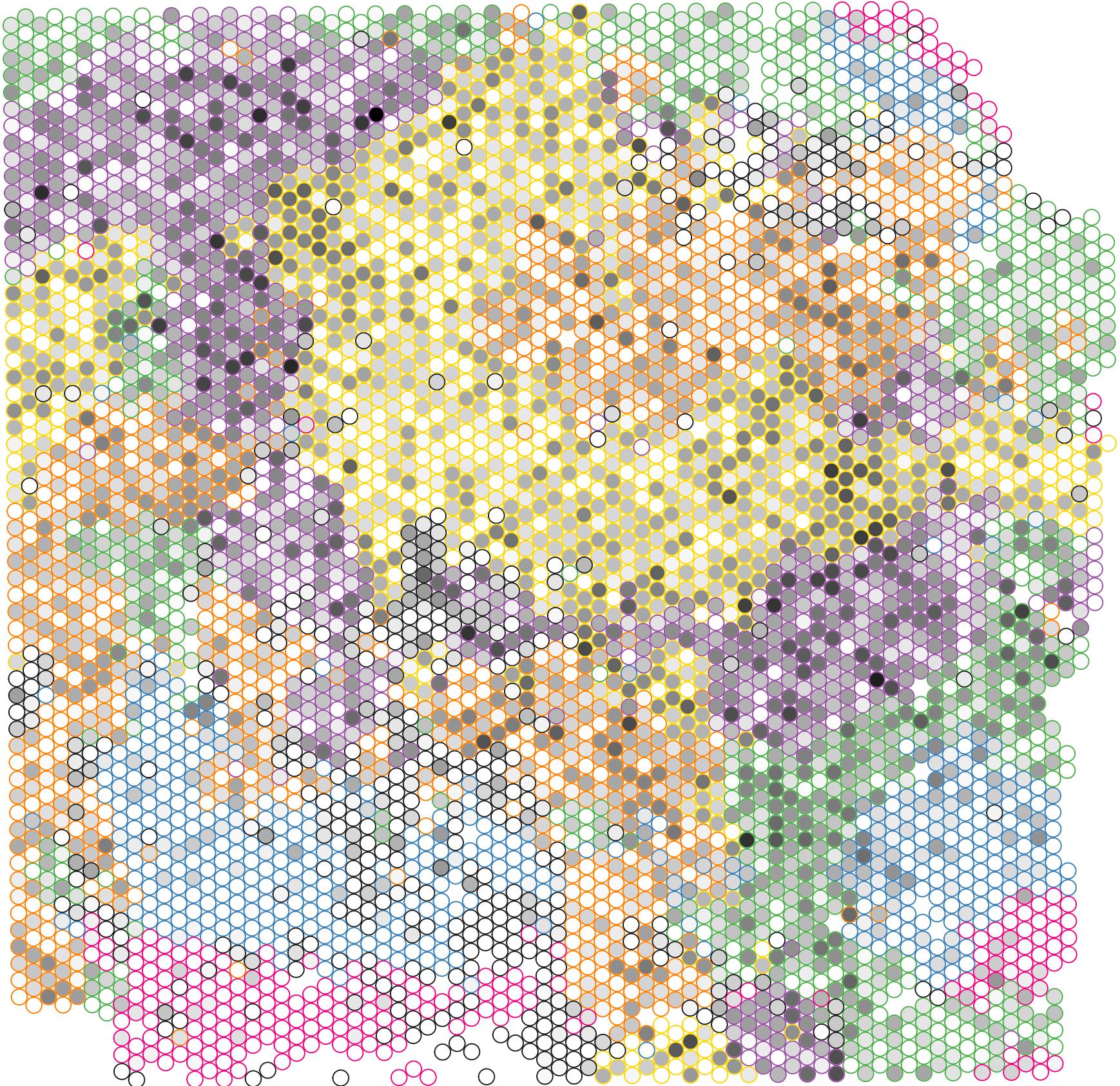
Sample Br8492_ant



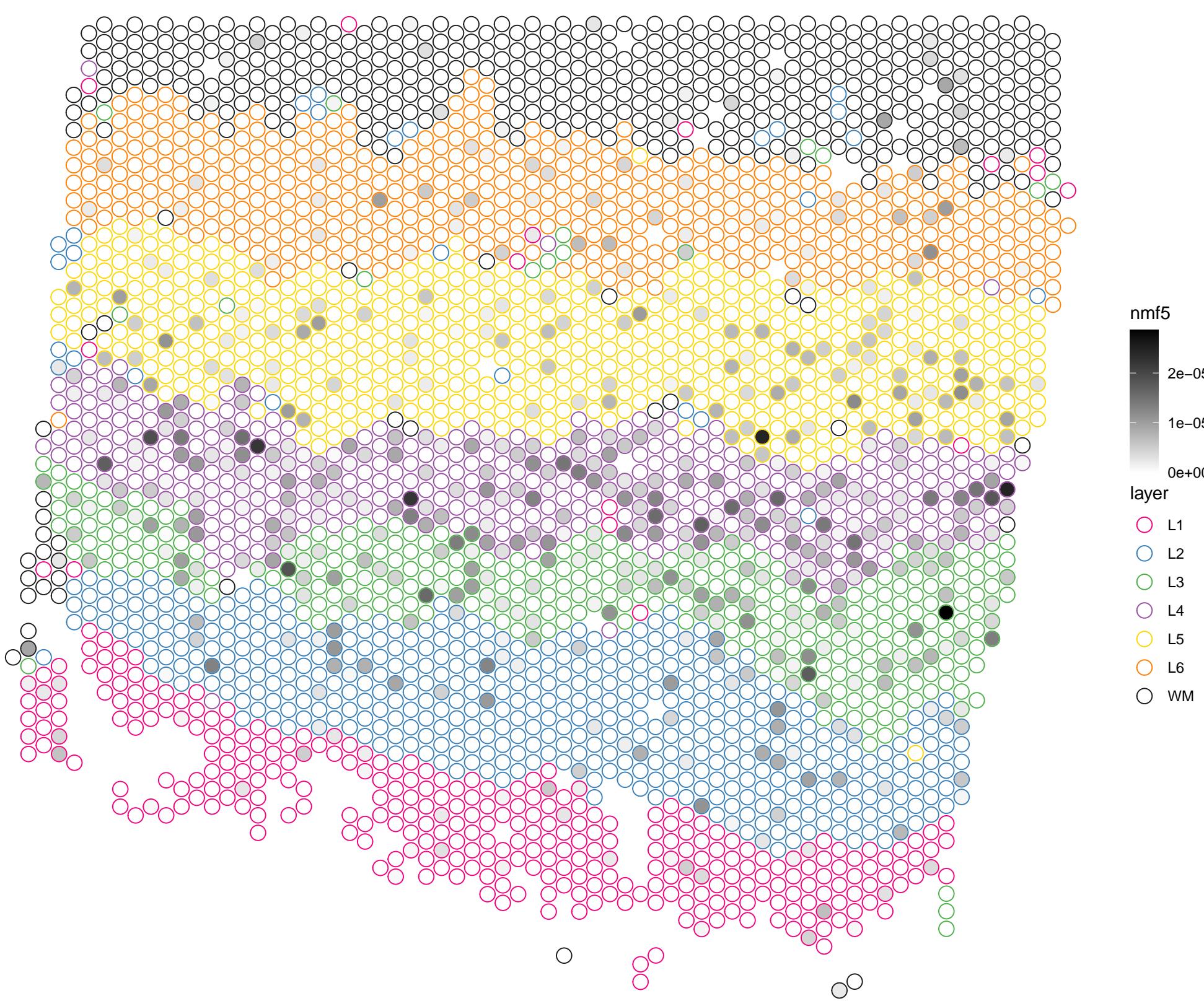
Sample Br8492_mid



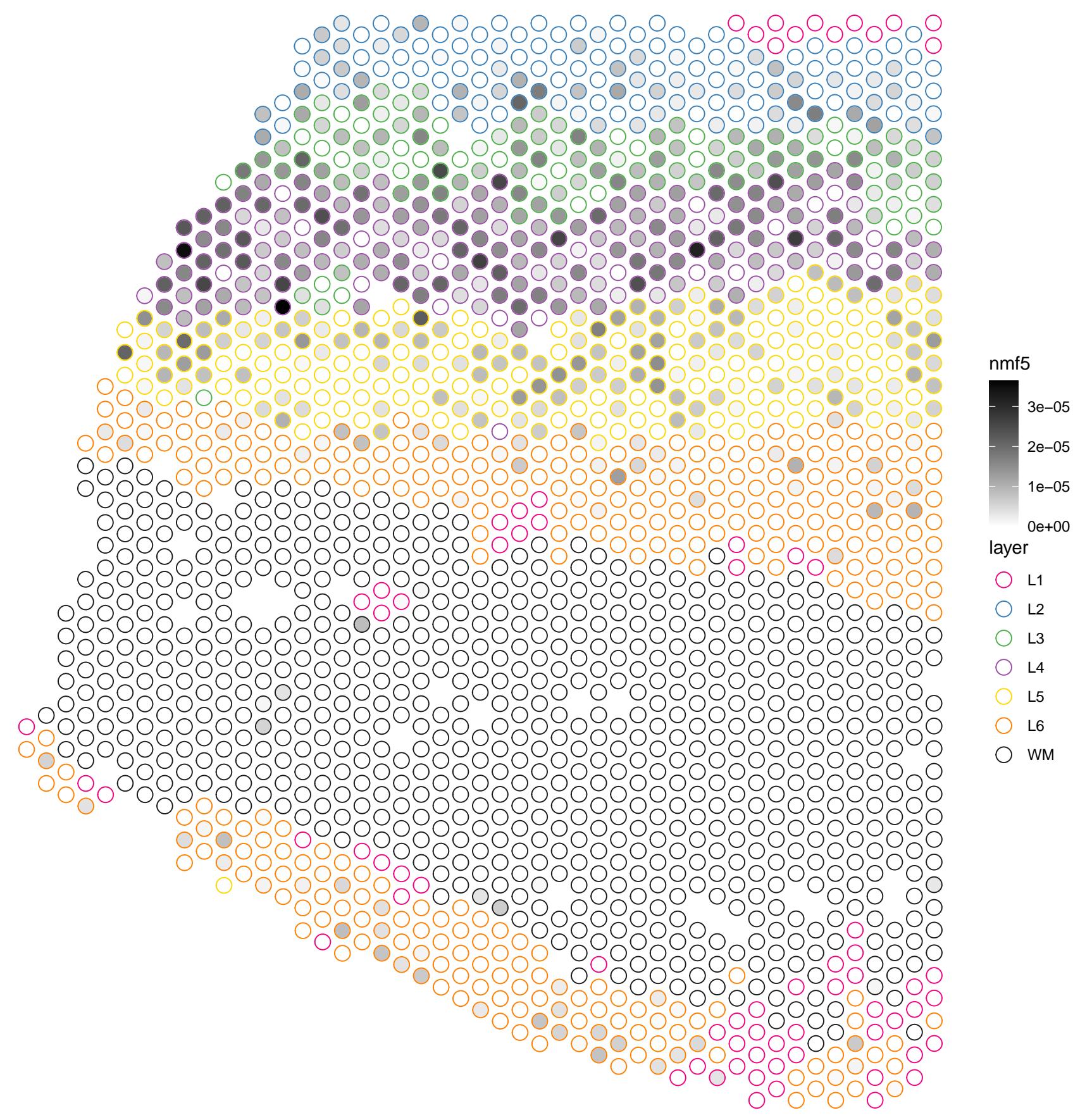
Sample Br8492_post



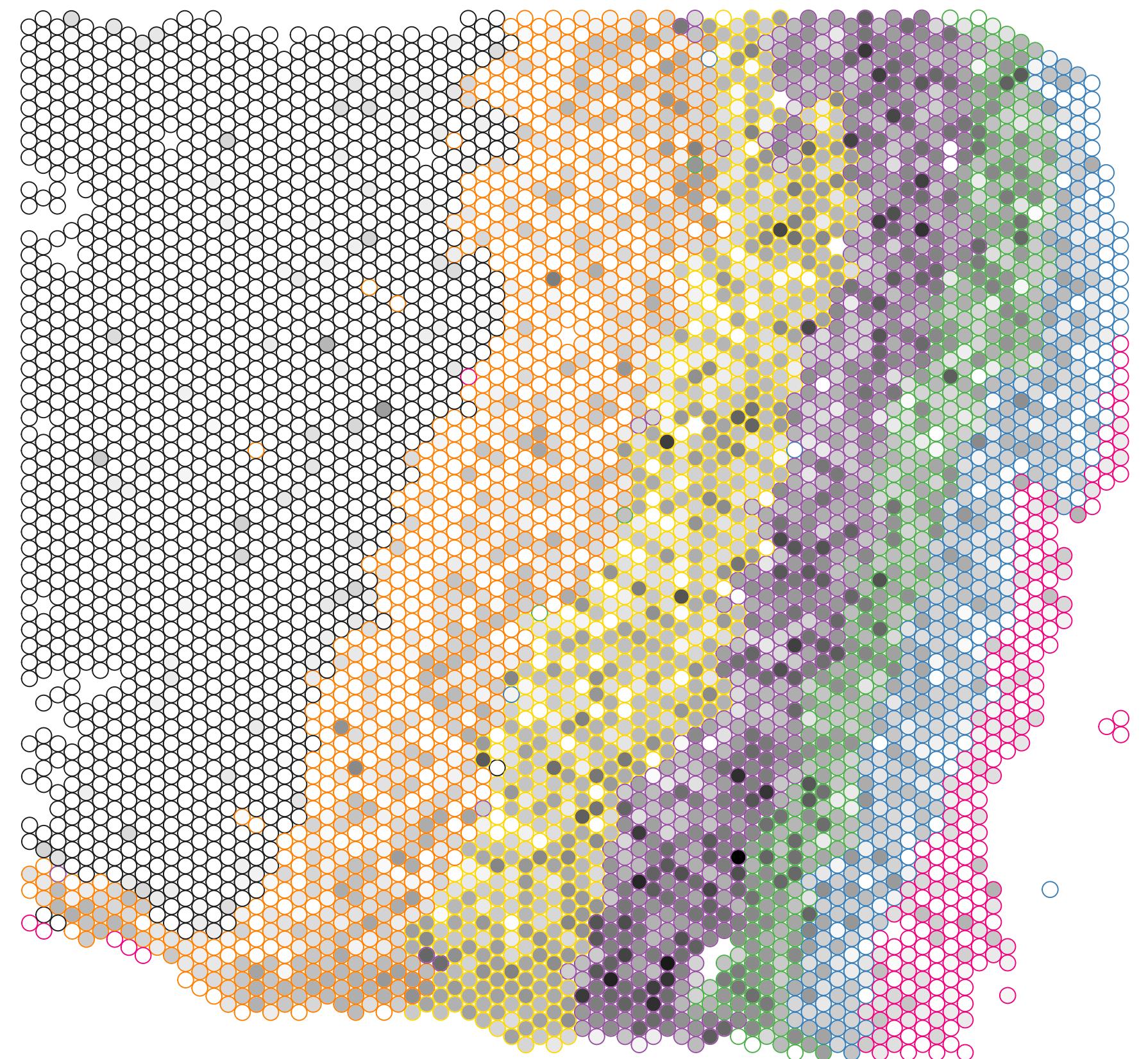
Sample Br2720_ant



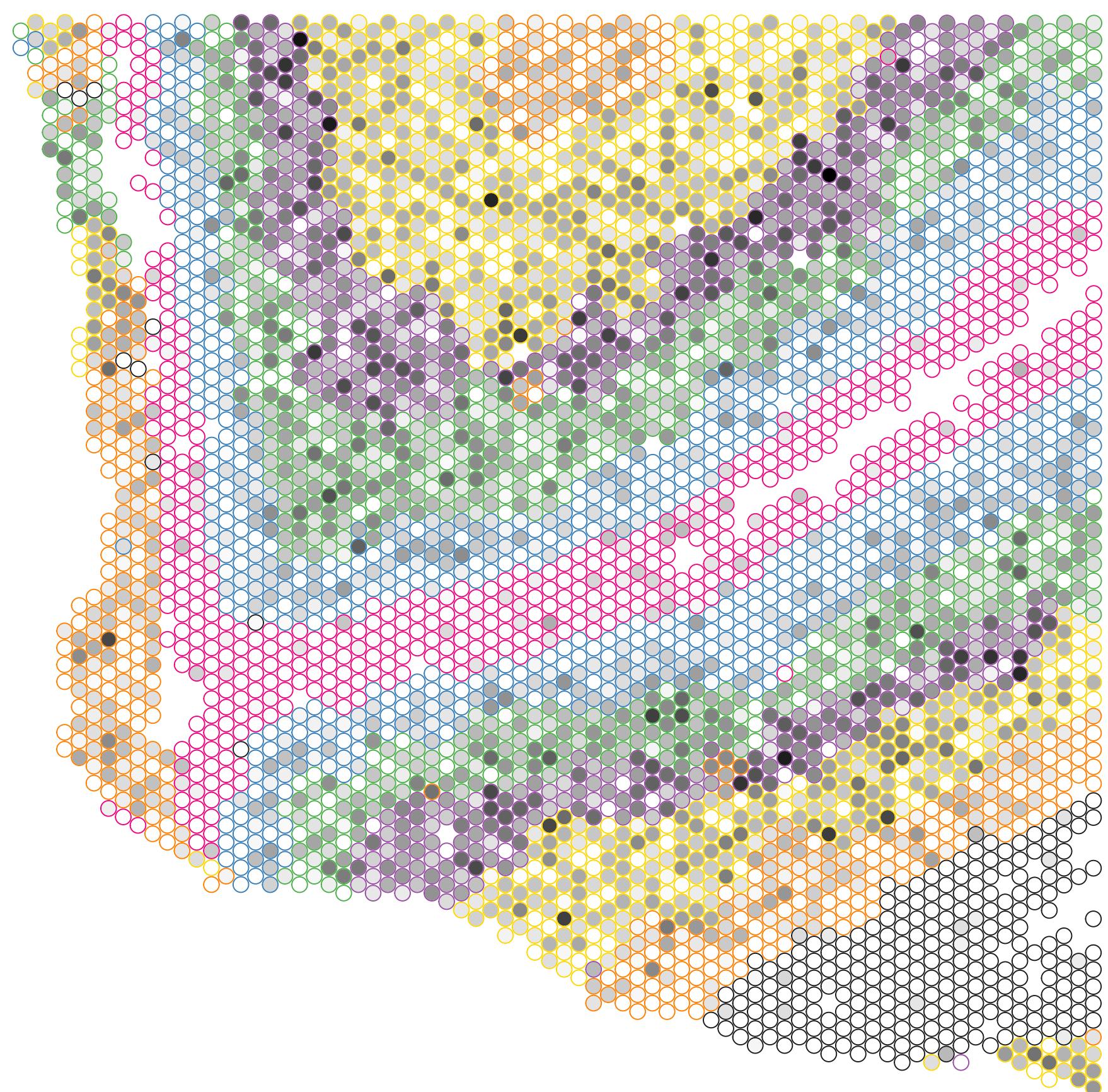
Sample Br2720_mid



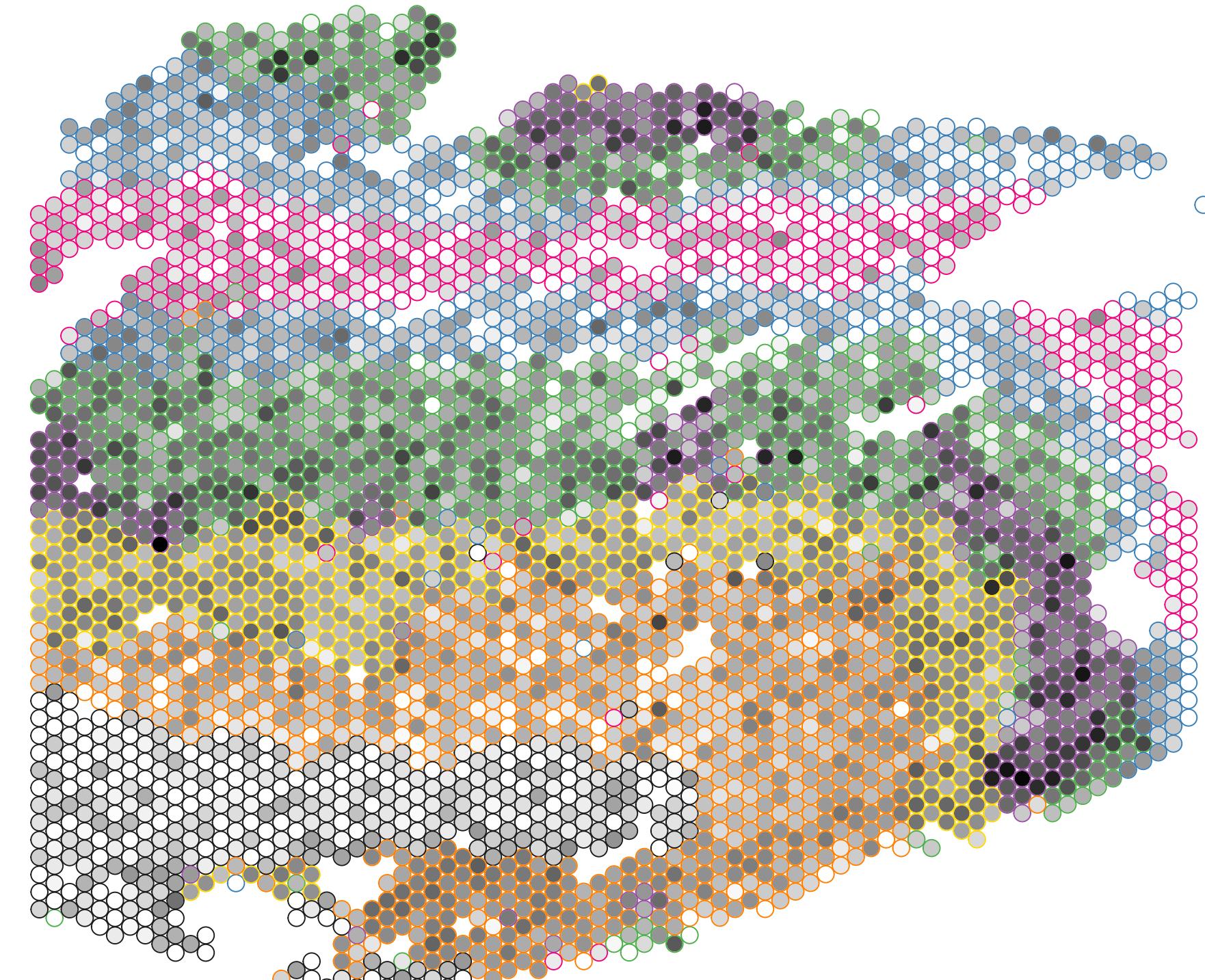
Sample Br2720_post



Sample Br6432_ant

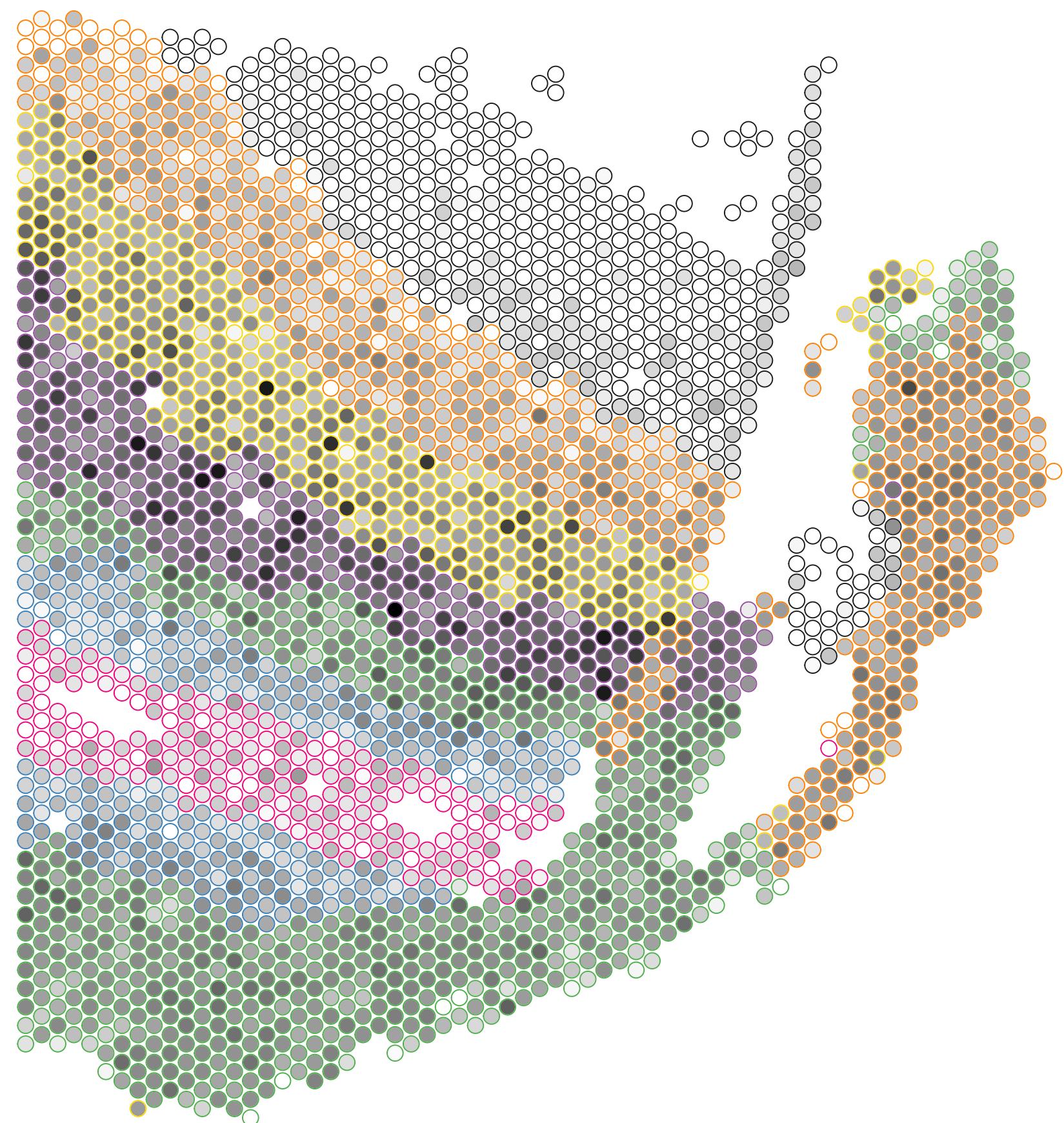


Sample Br6432_mid



nmf5
layer
L1
L2
L3
L4
L5
L6
WM

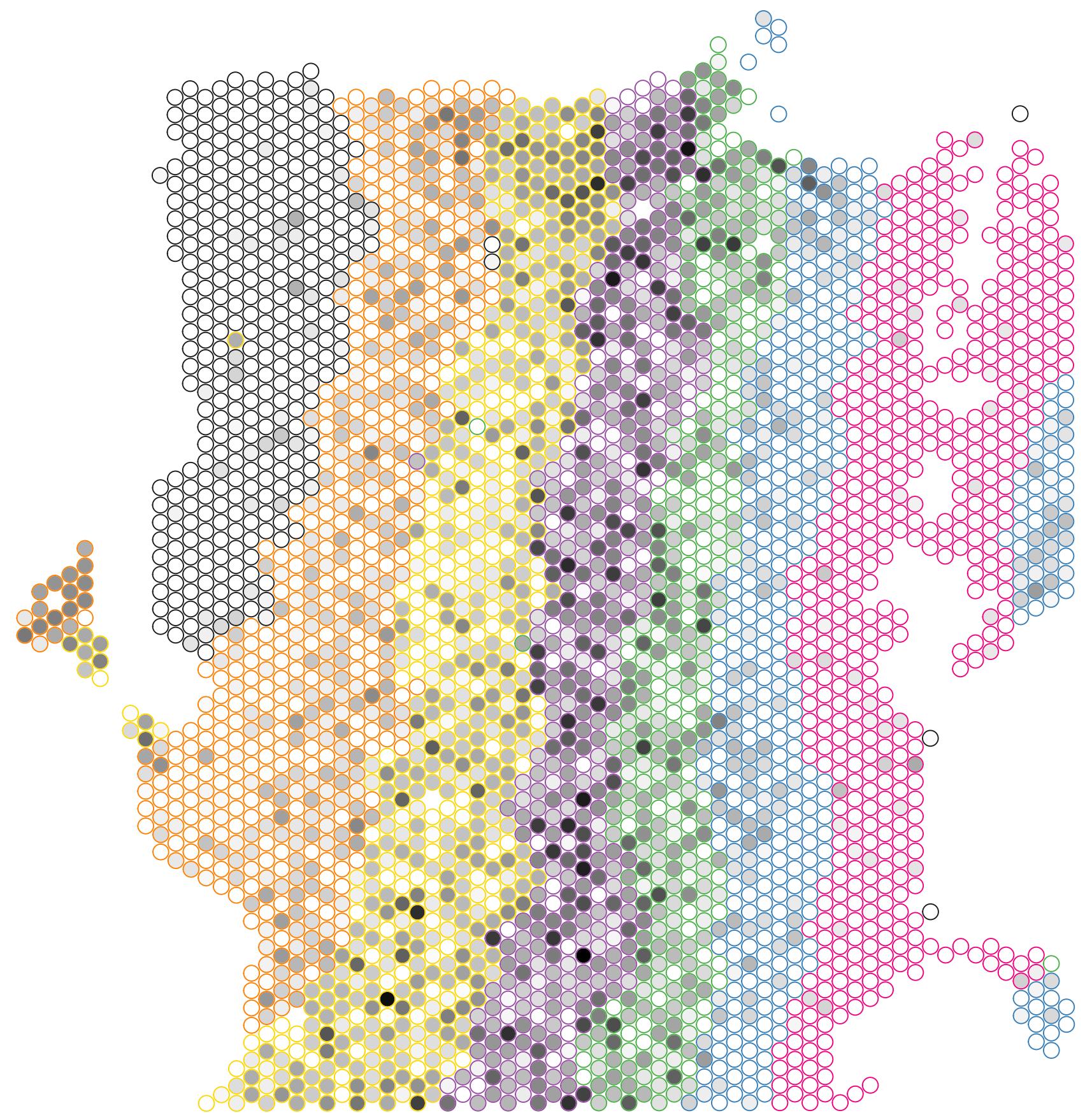
Sample Br6432_post



nmf5
layer
L1
L2
L3
L4
L5
L6
WM

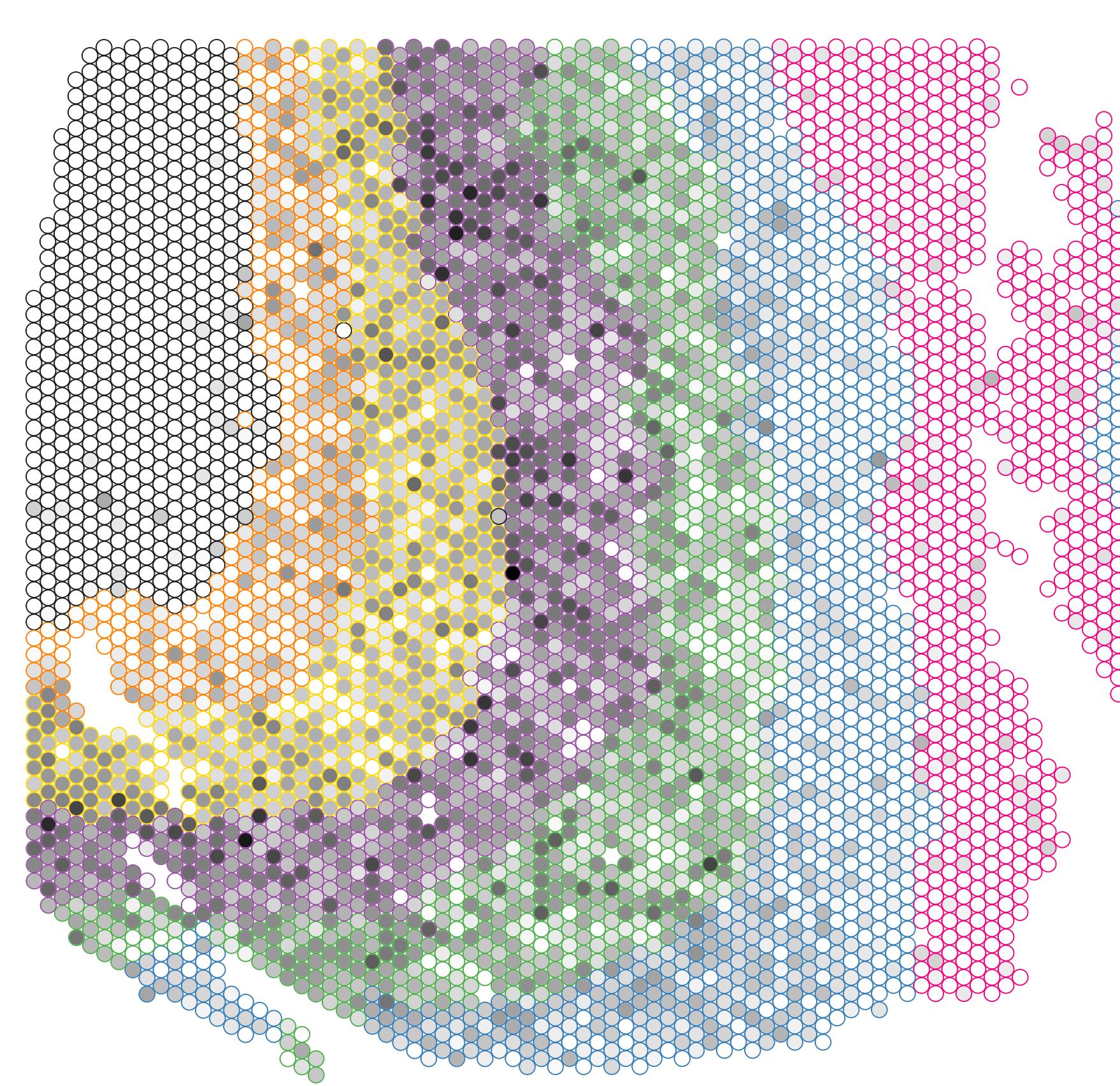
Sample Br6471_ant

Sample Br6471_mid



nmf5
layer

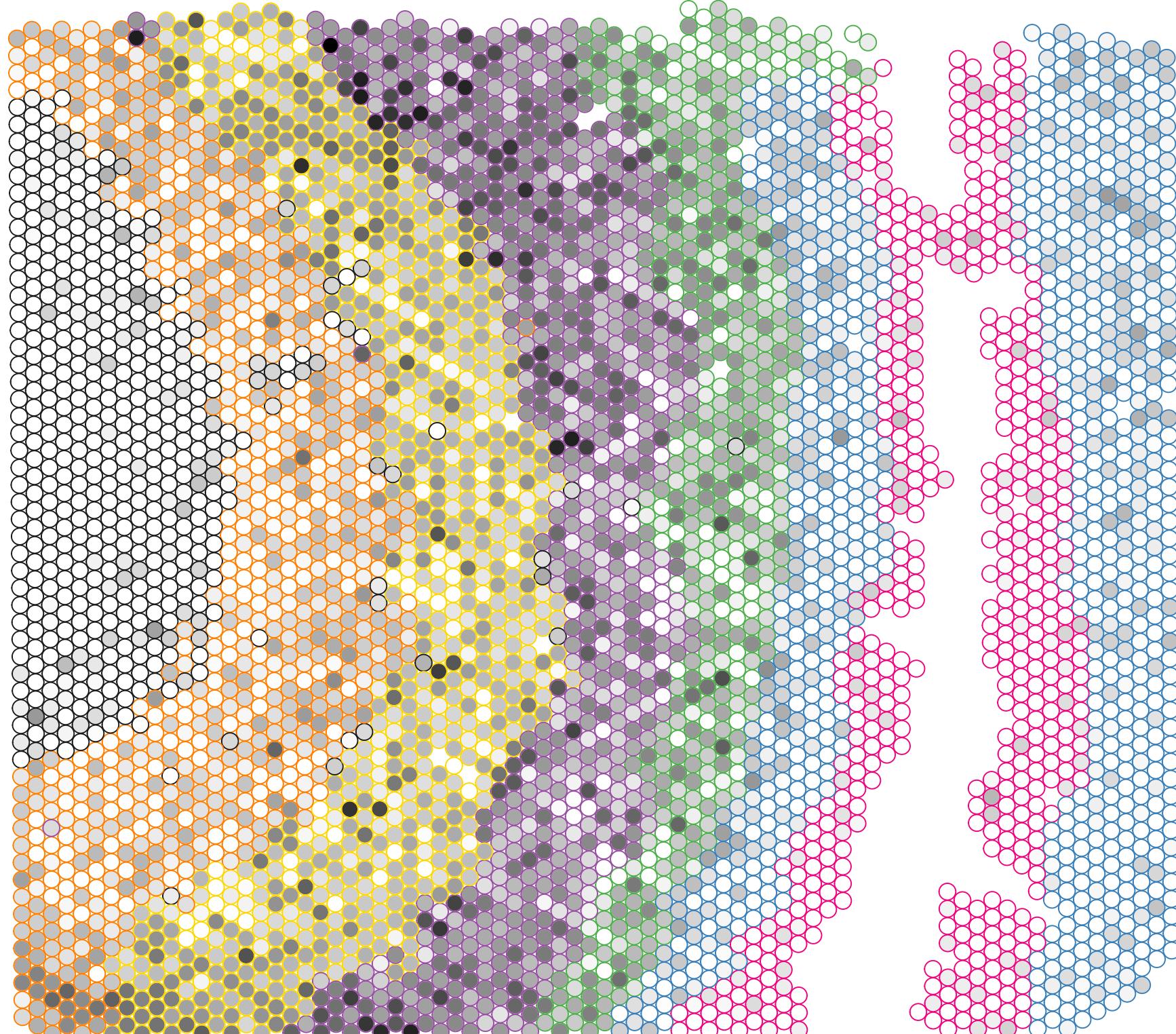
- L1
- L2
- L3
- L4
- L5
- L6
- WM



nmf5
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

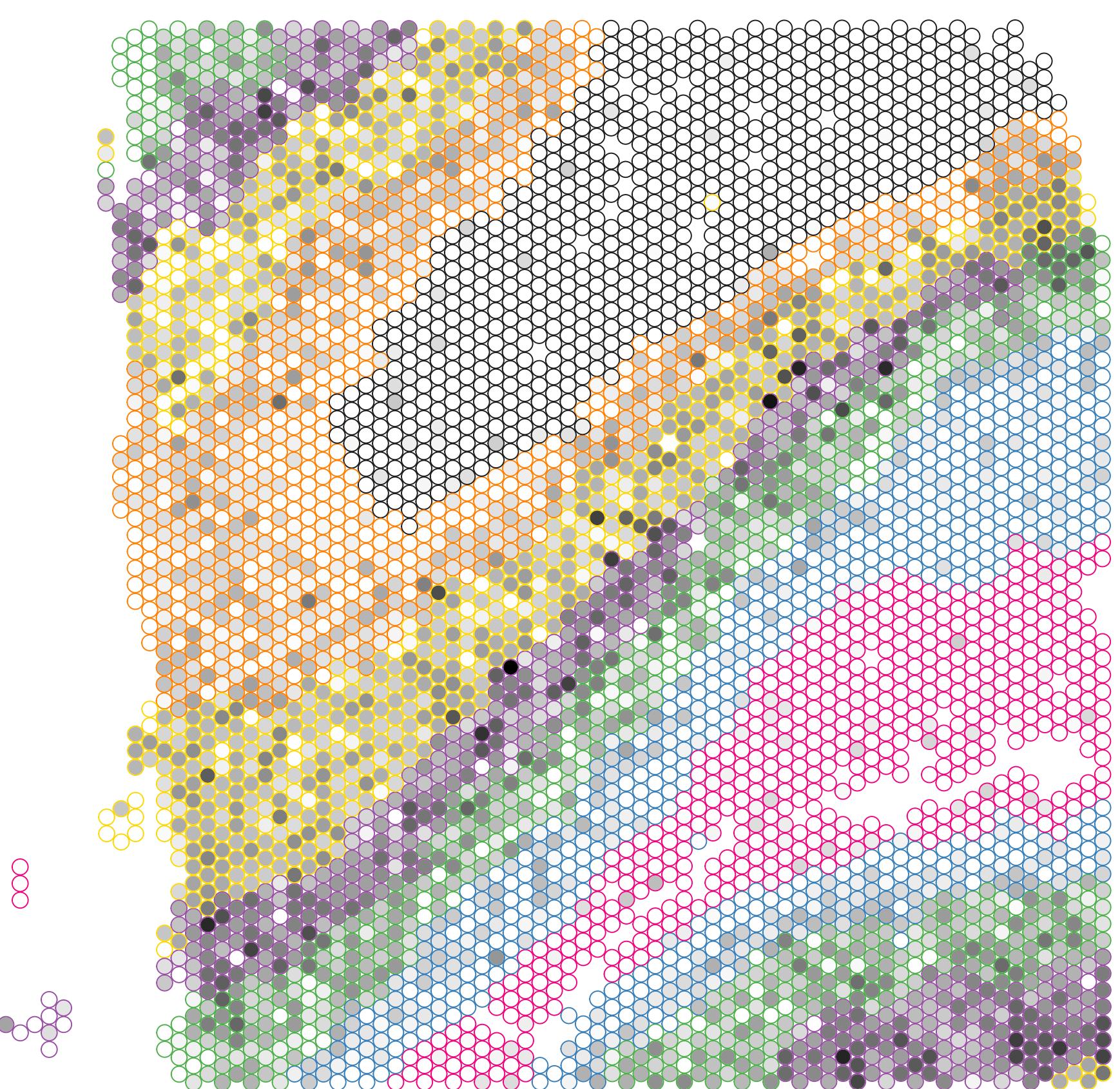
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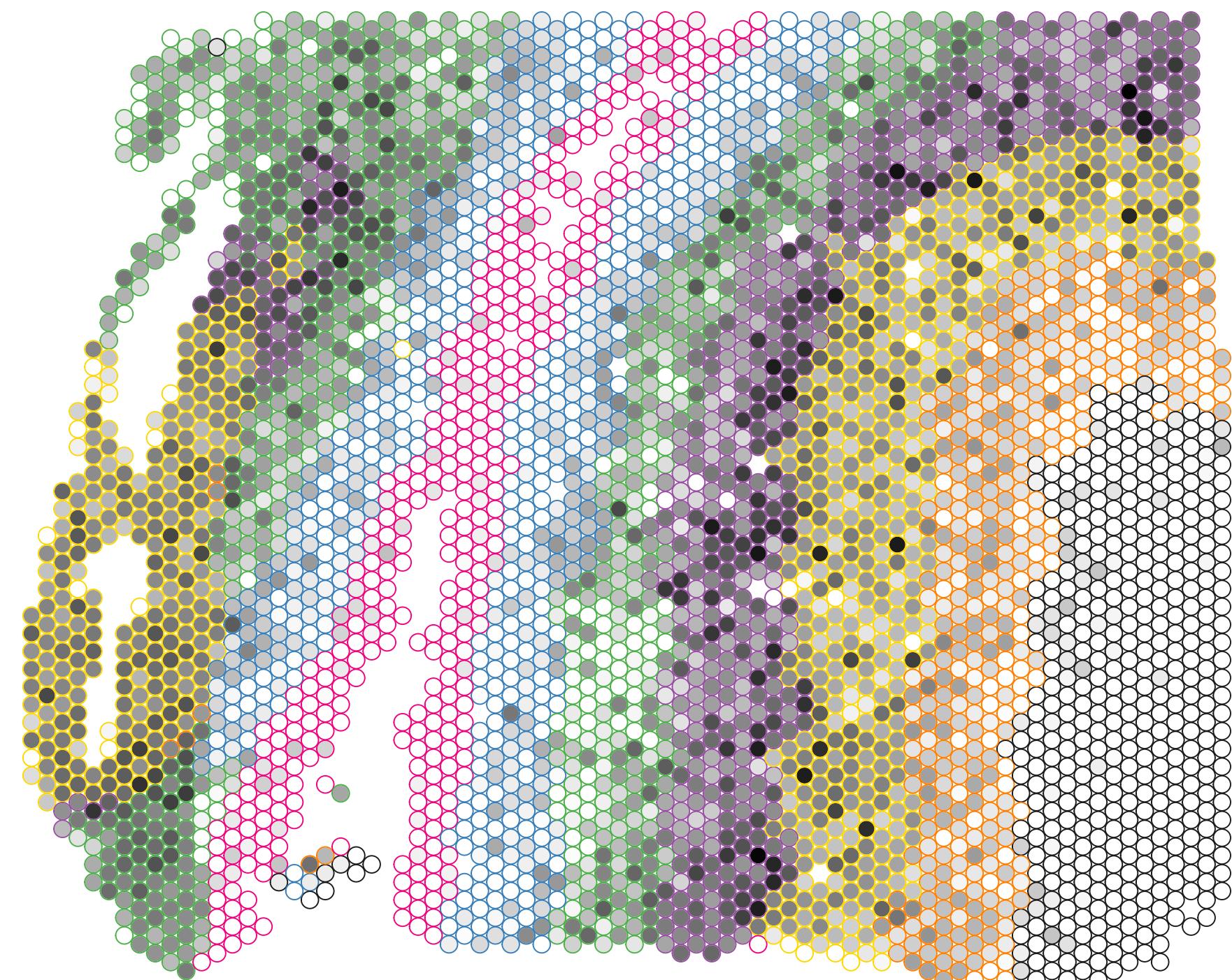
nmf5
layer

- L1
- L2
- L3
- L4
- L5
- L6
- WM

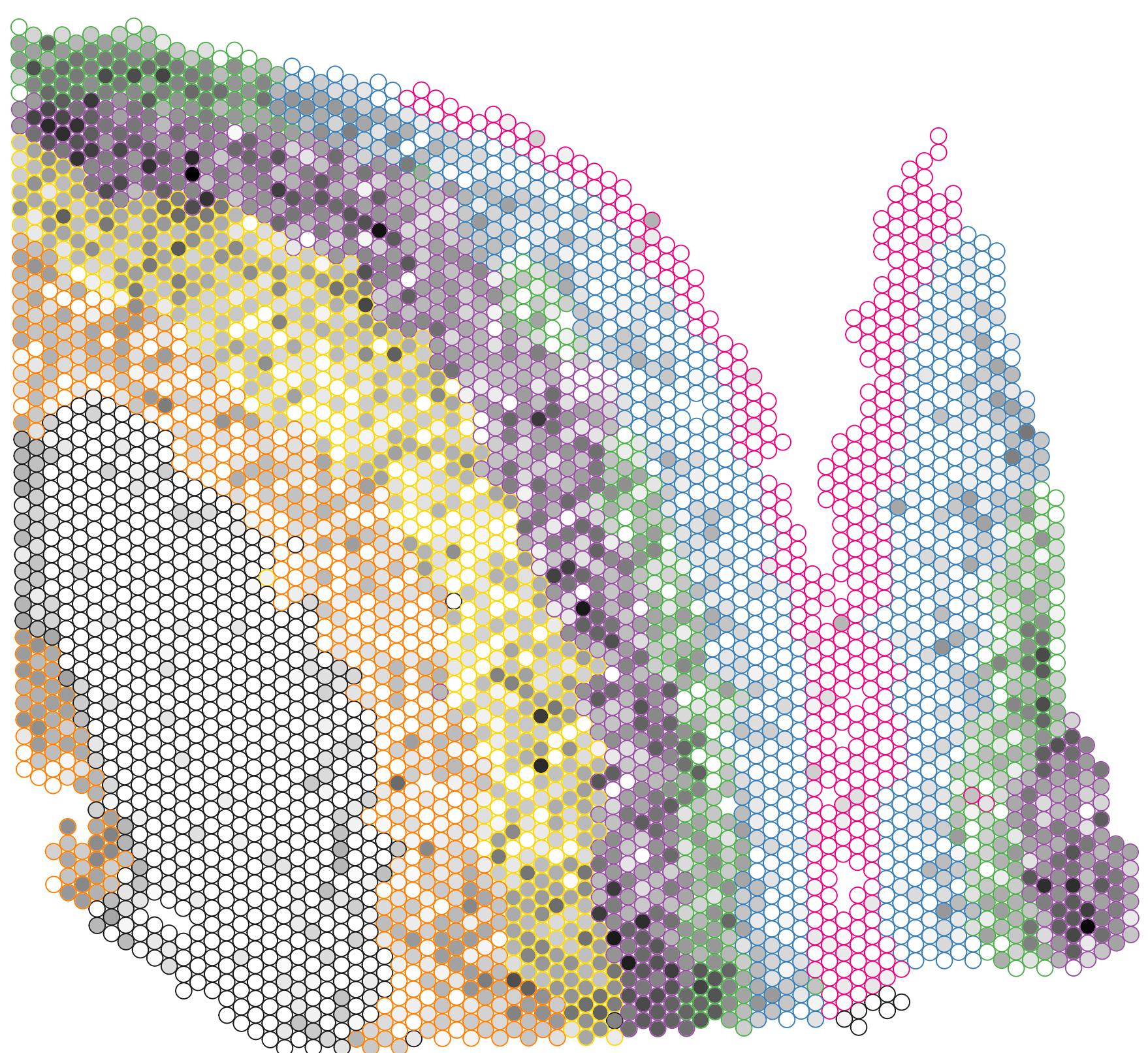
Sample Br6522_ant



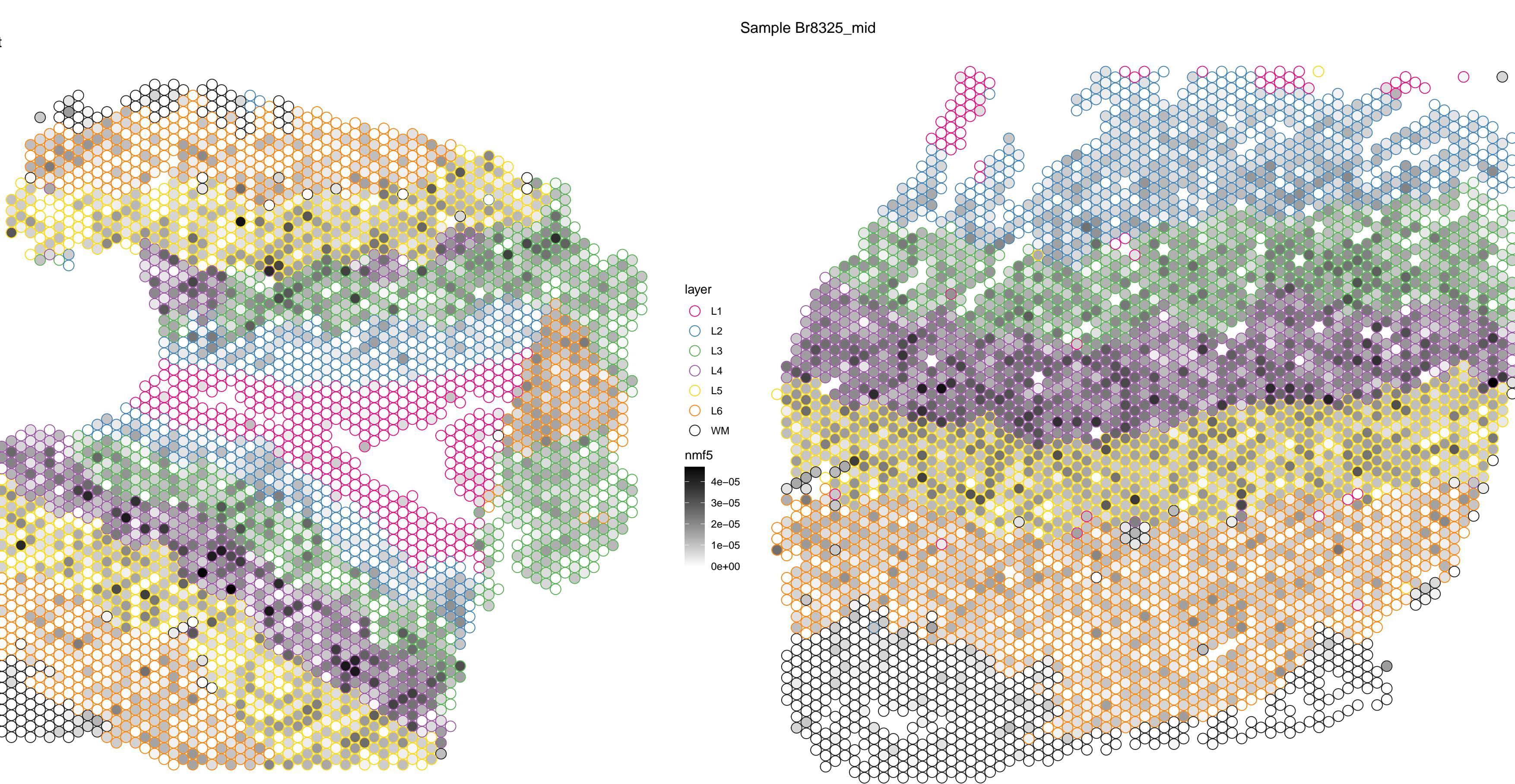
Sample Br6522_mid



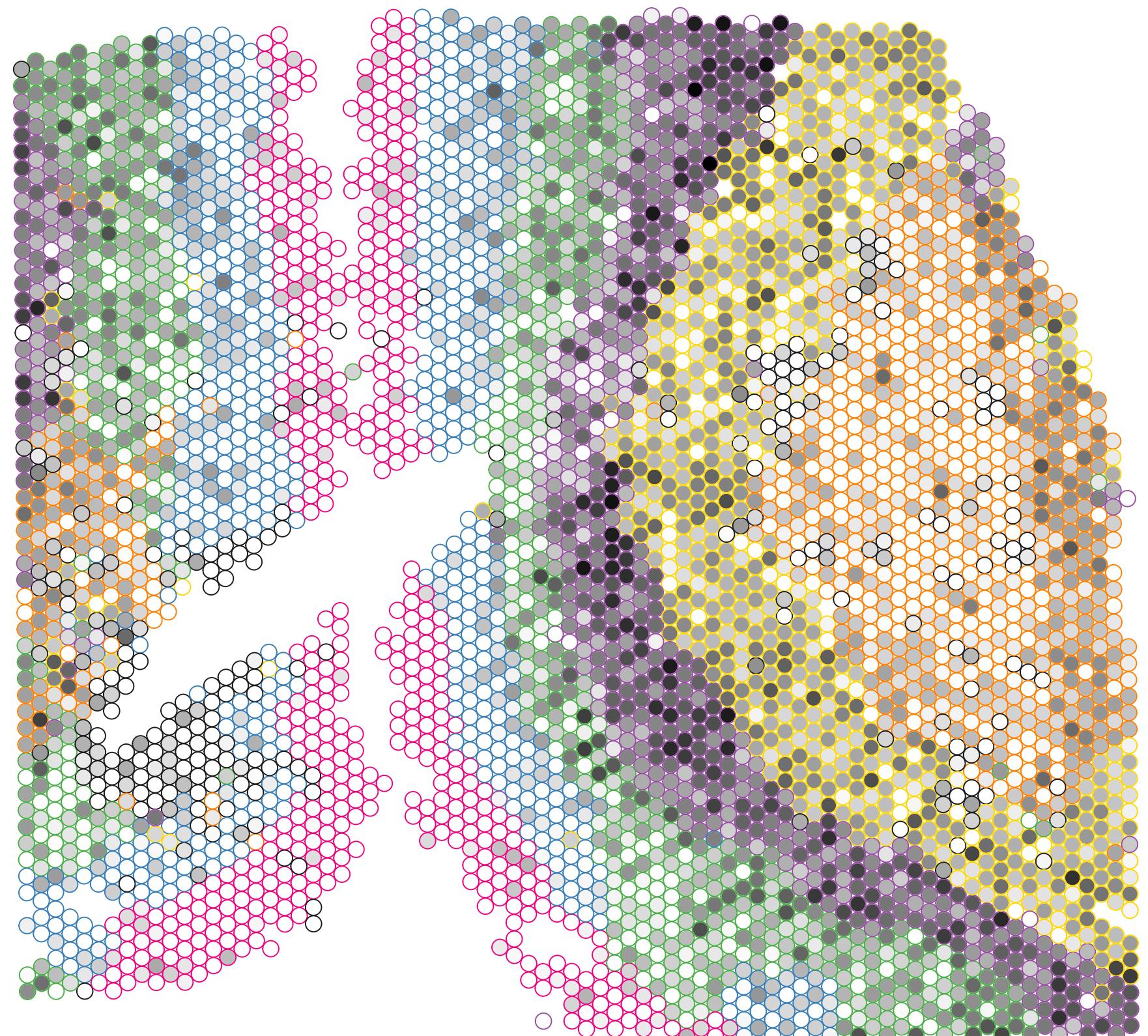
Sample Br6522_post



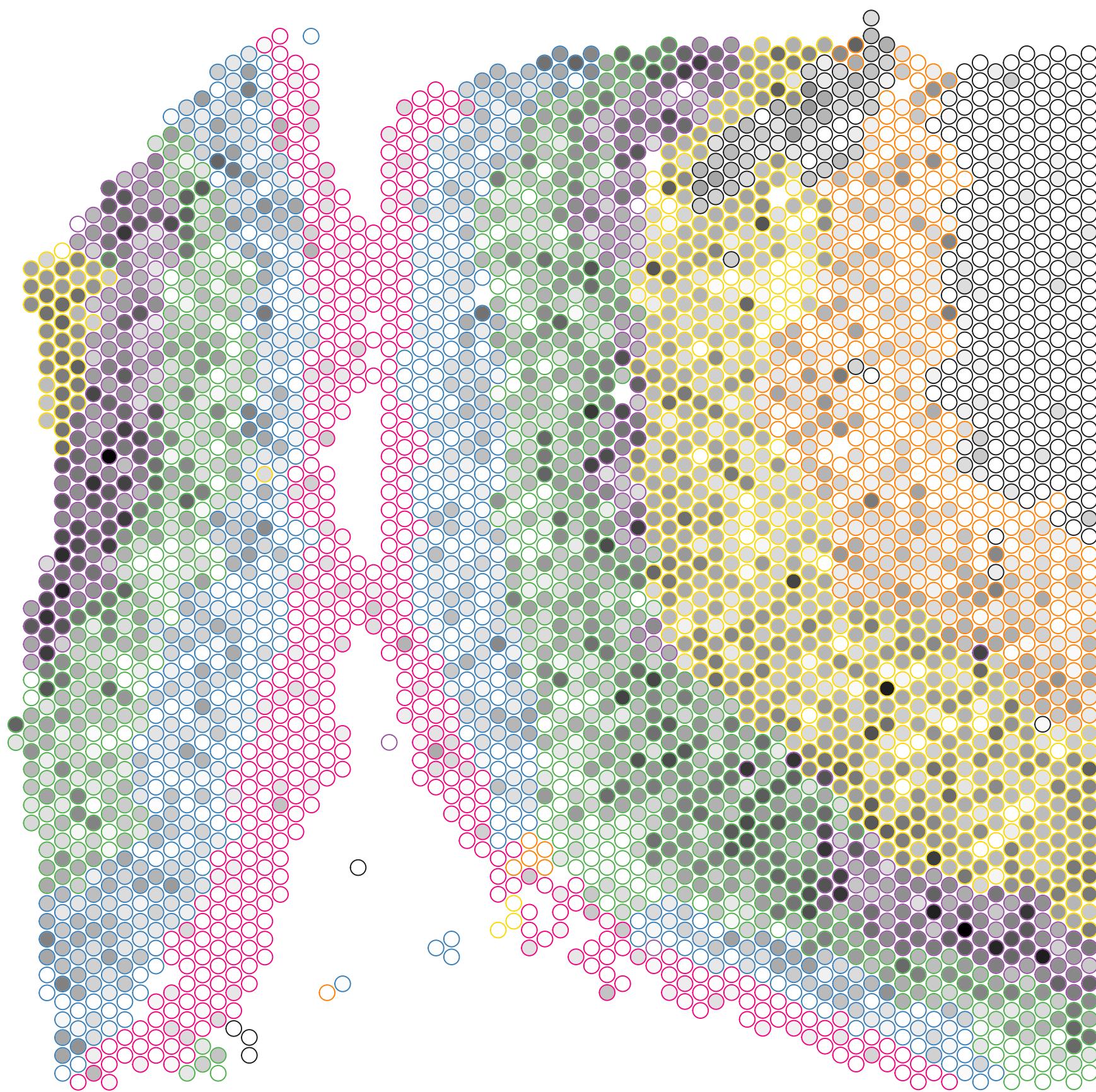
Sample Br8325_ant



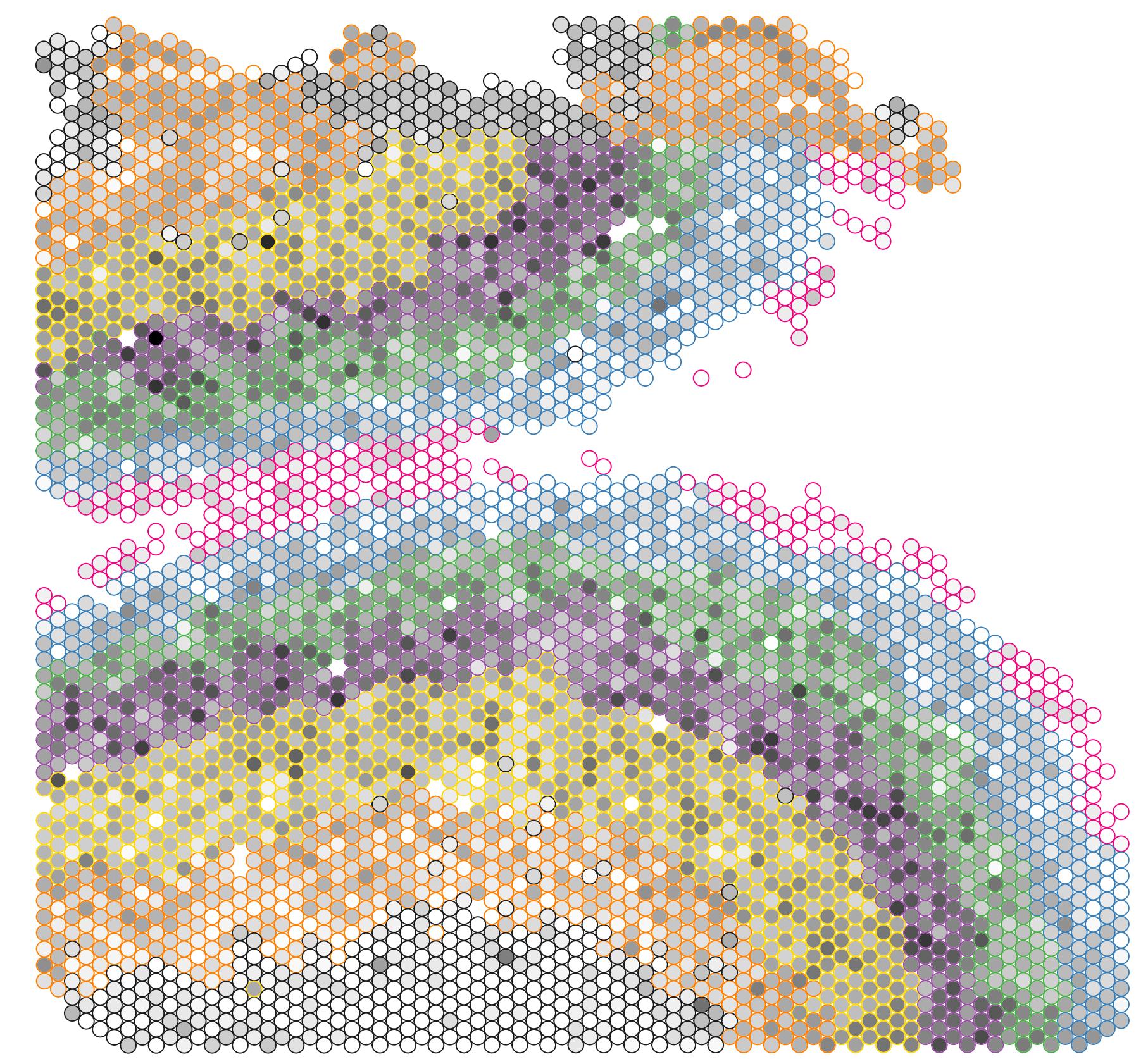
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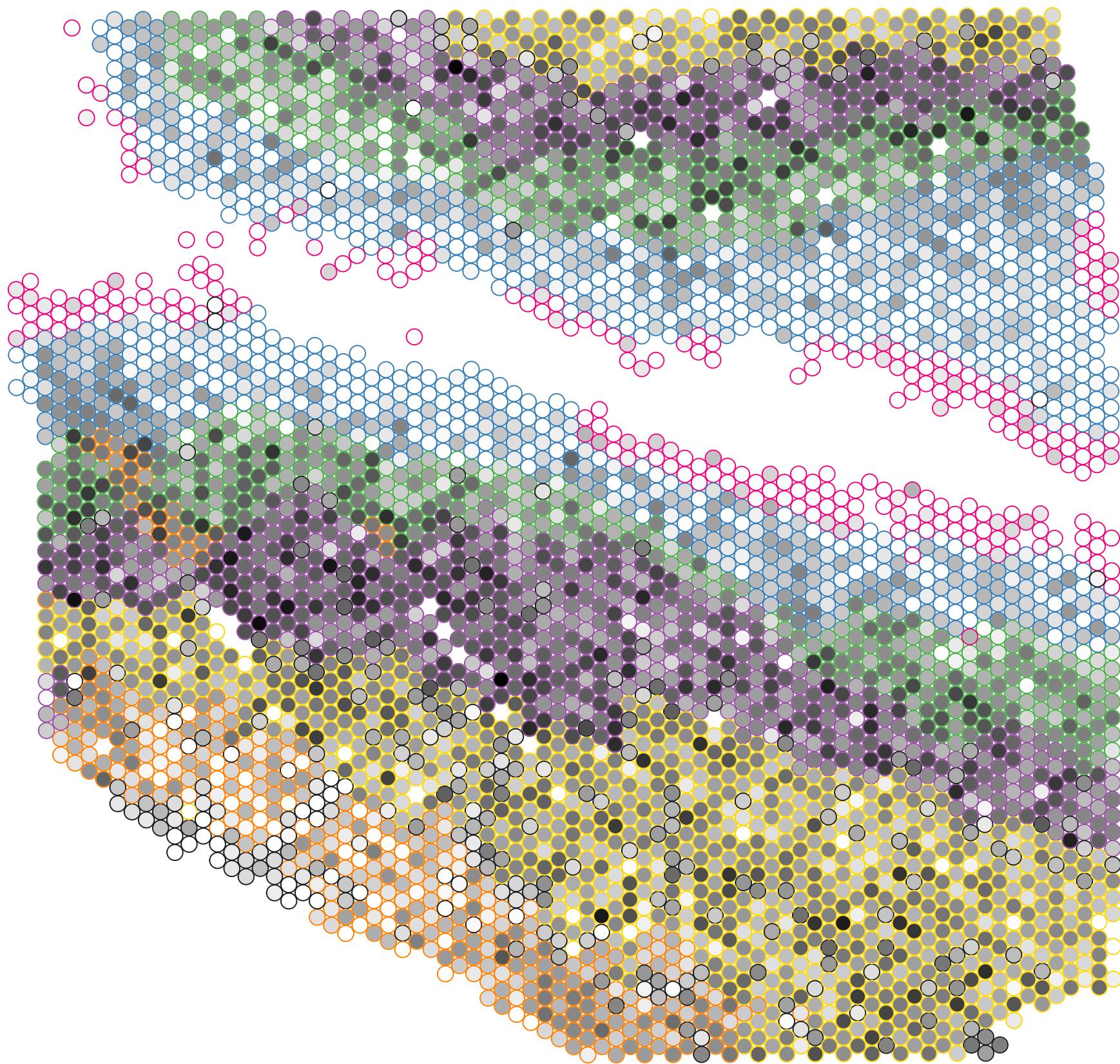
Sample Br8667_ant



Sample Br8667_mid



Sample Br8667_post



nmf5
layer

nmf5
layer

nmf5
layer

nmf5
layer