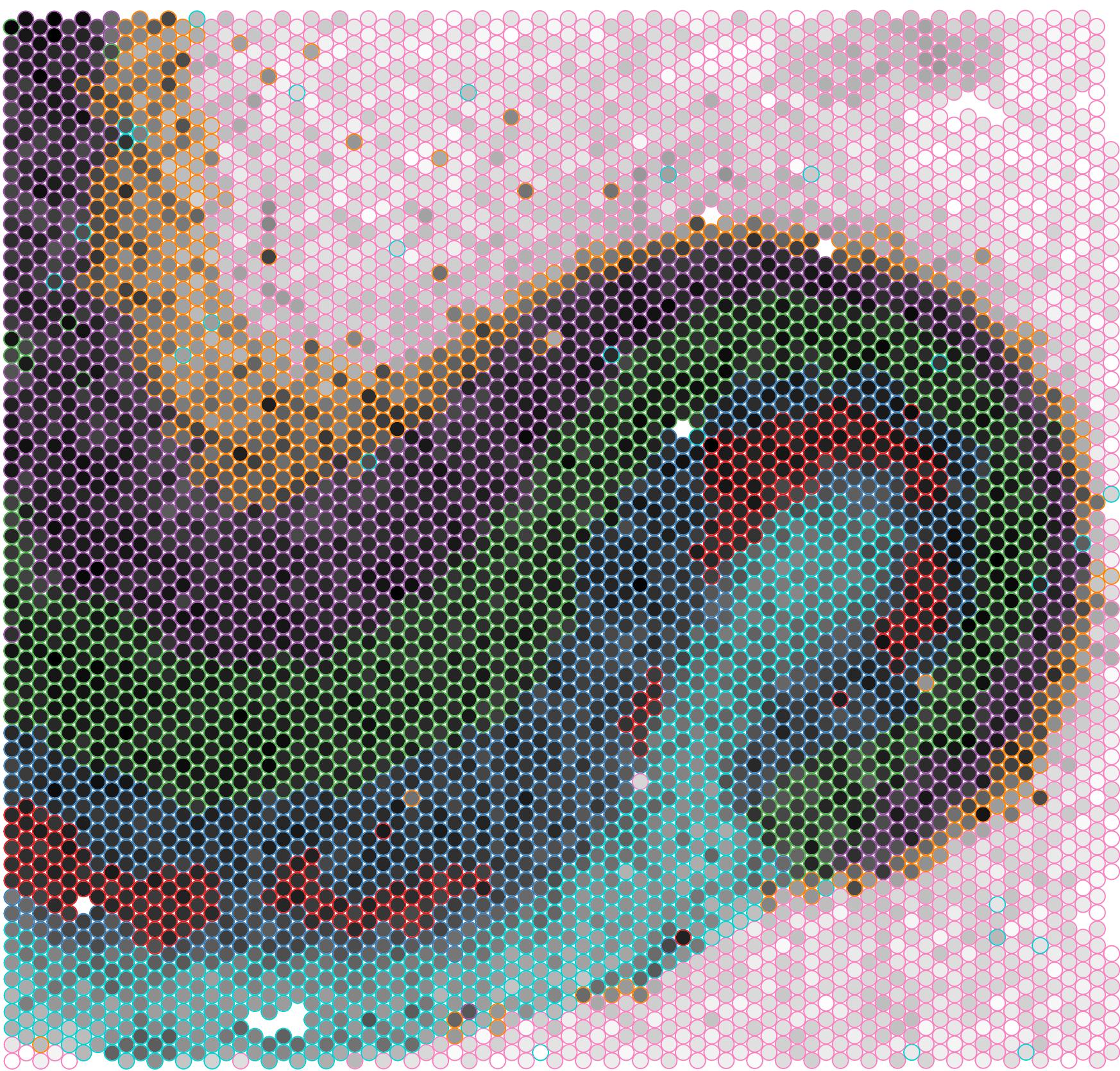
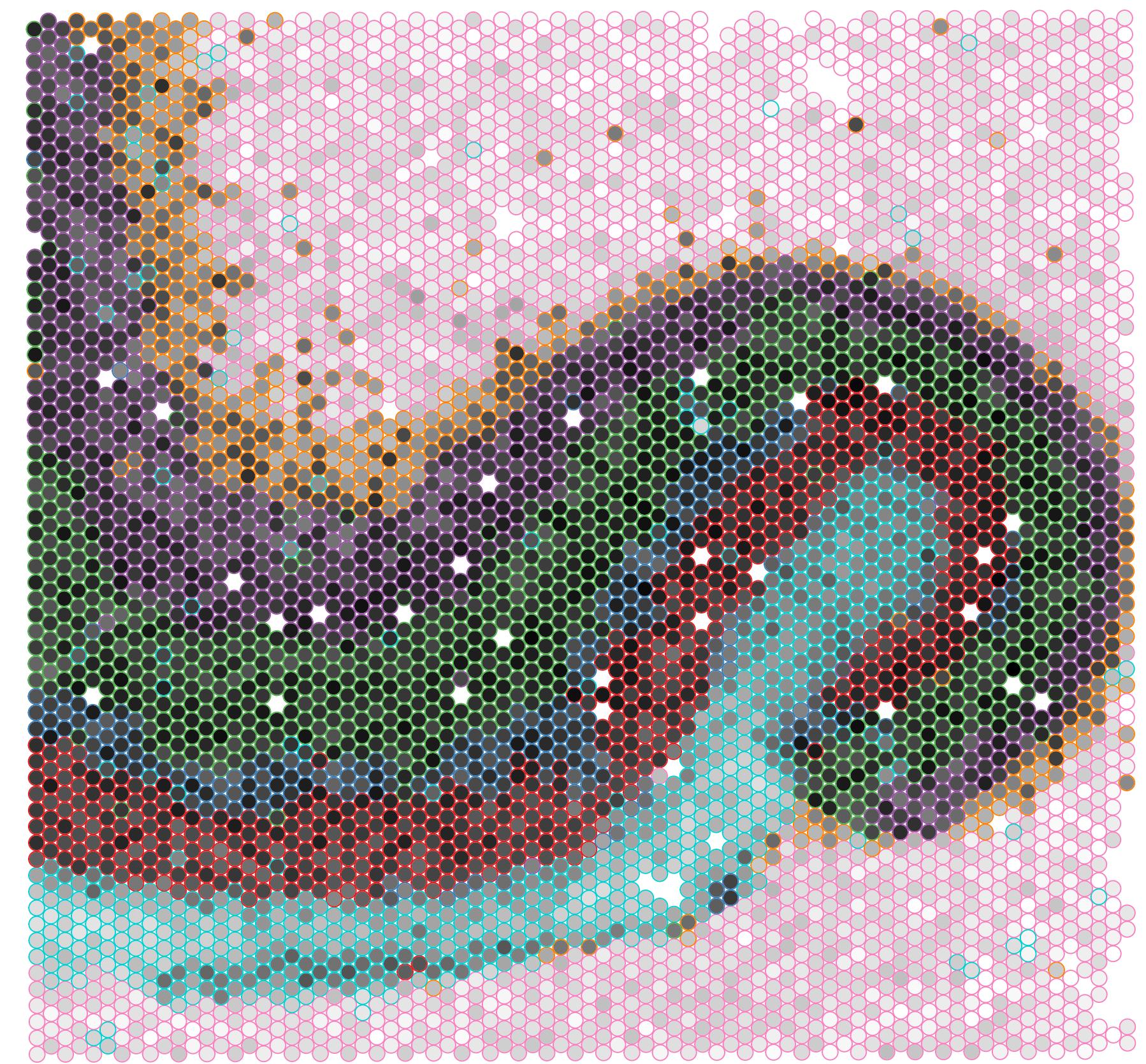


NMF\_5  
layer  
L1  
L2  
L3  
L5  
L6a  
L6b  
WM

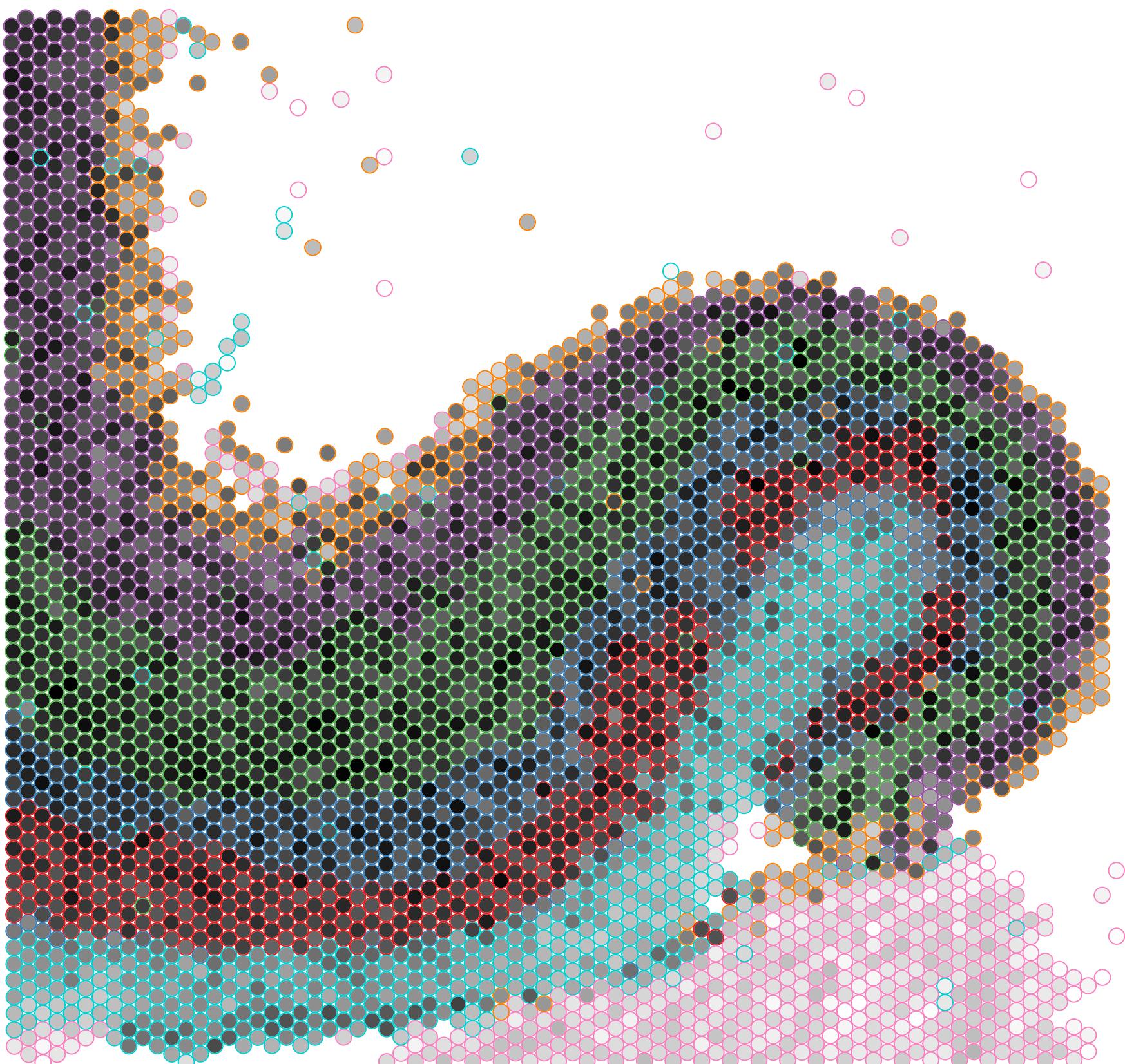
Sample V12J03–002\_A1



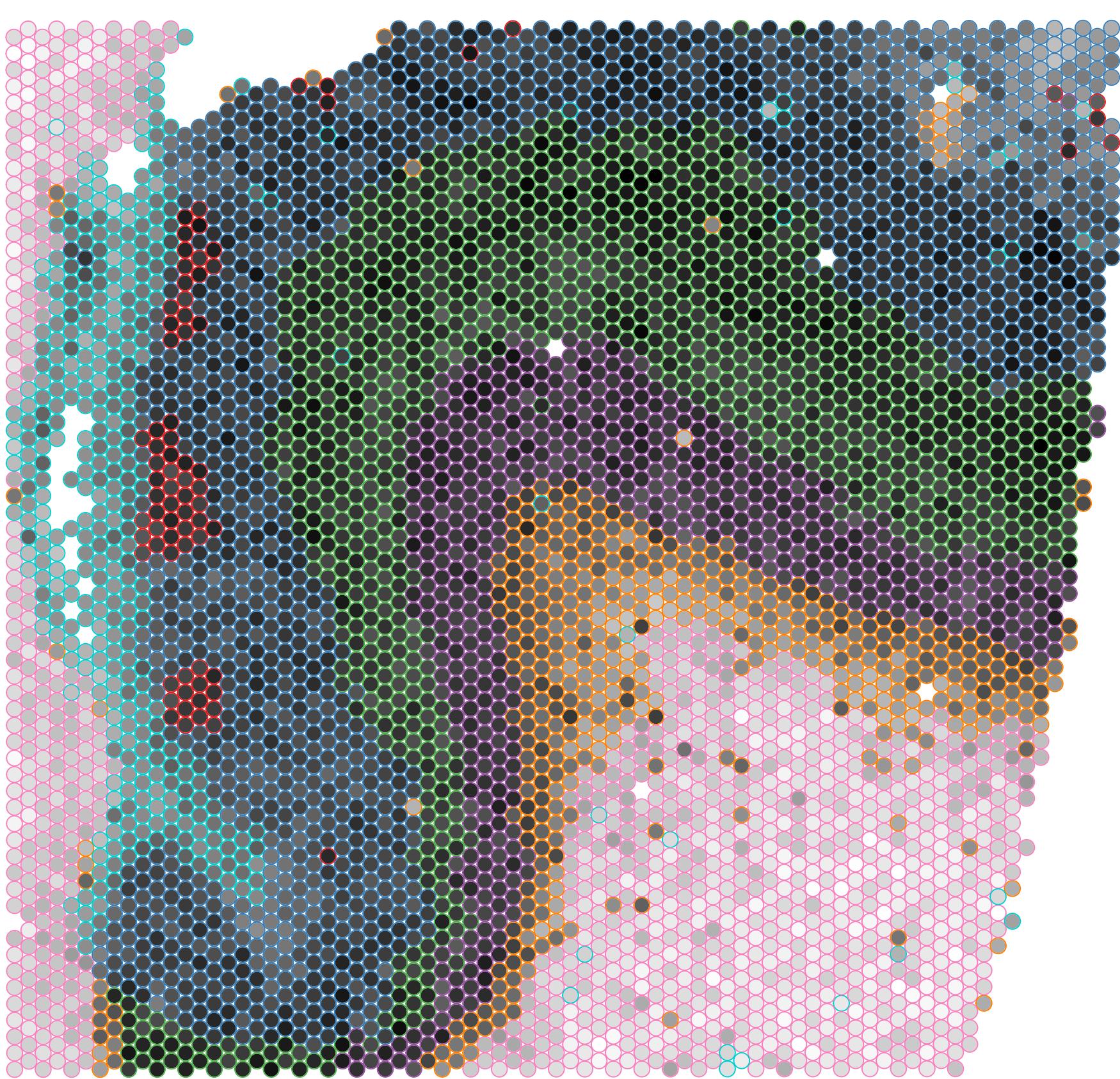
Sample V12N28–331\_A1



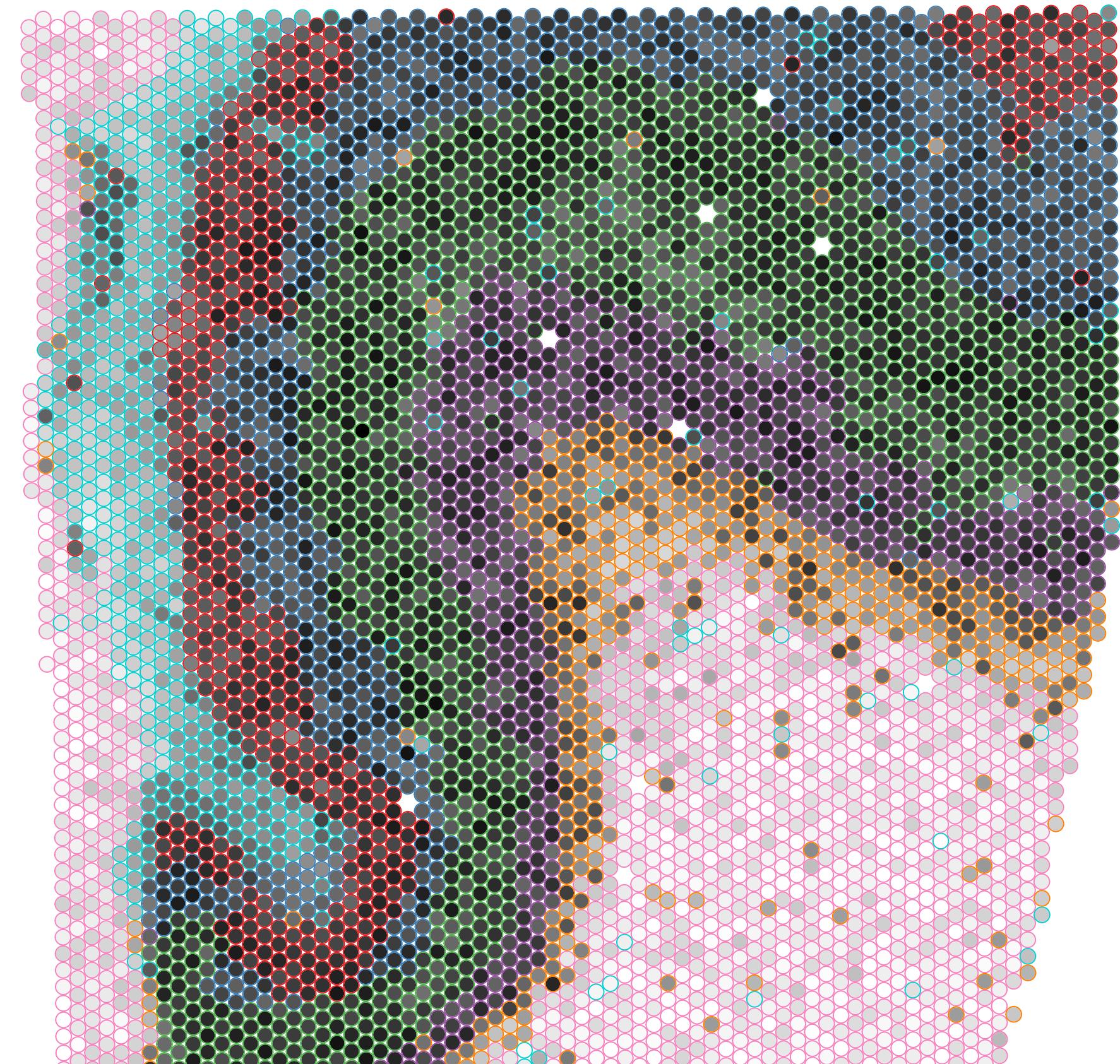
Sample V12N28–332\_A1



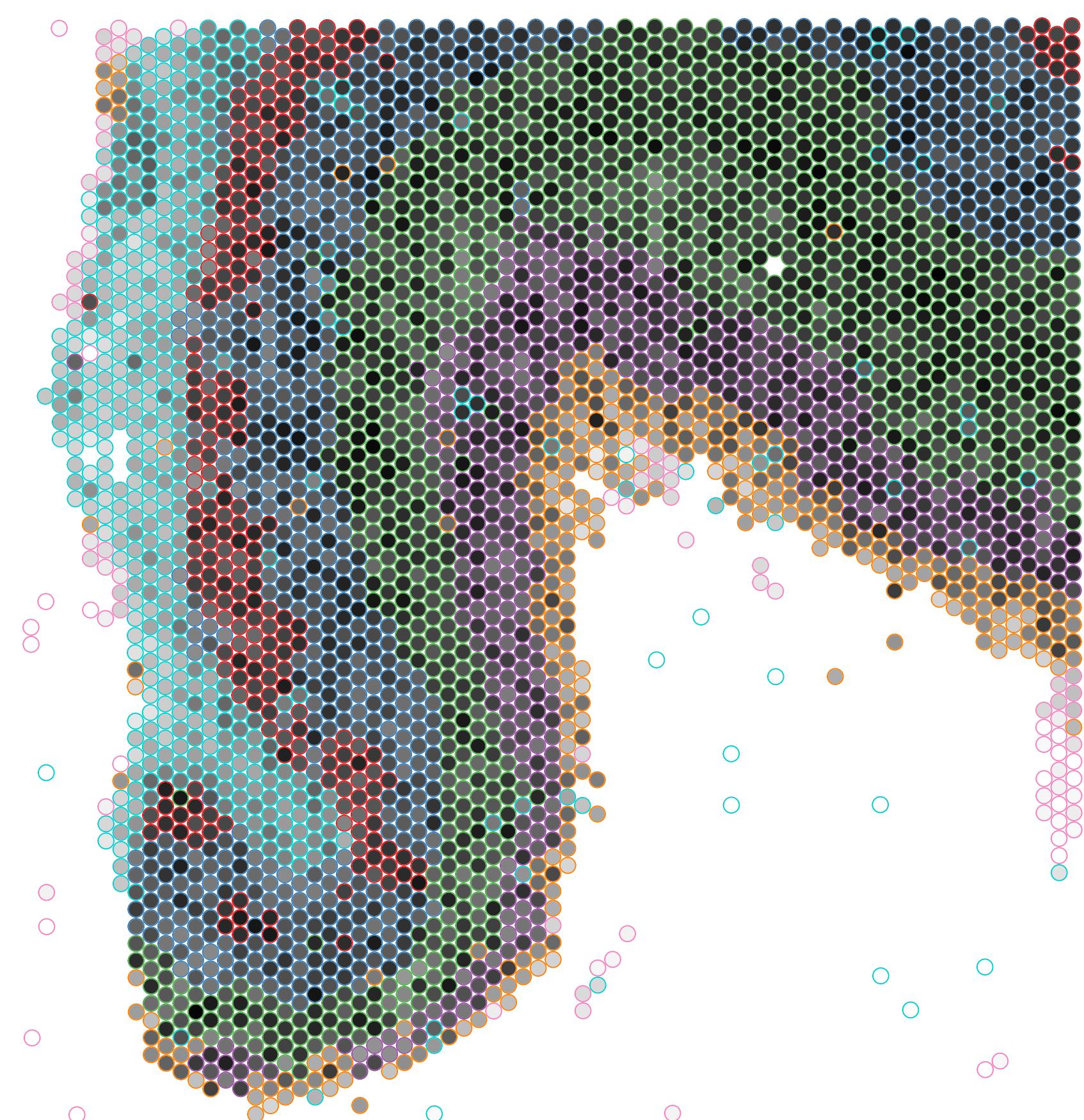
Sample V12J03-002\_B1



Sample V12N28-331\_B1



Sample V12N28-332\_B1



NMF\_5  
layer

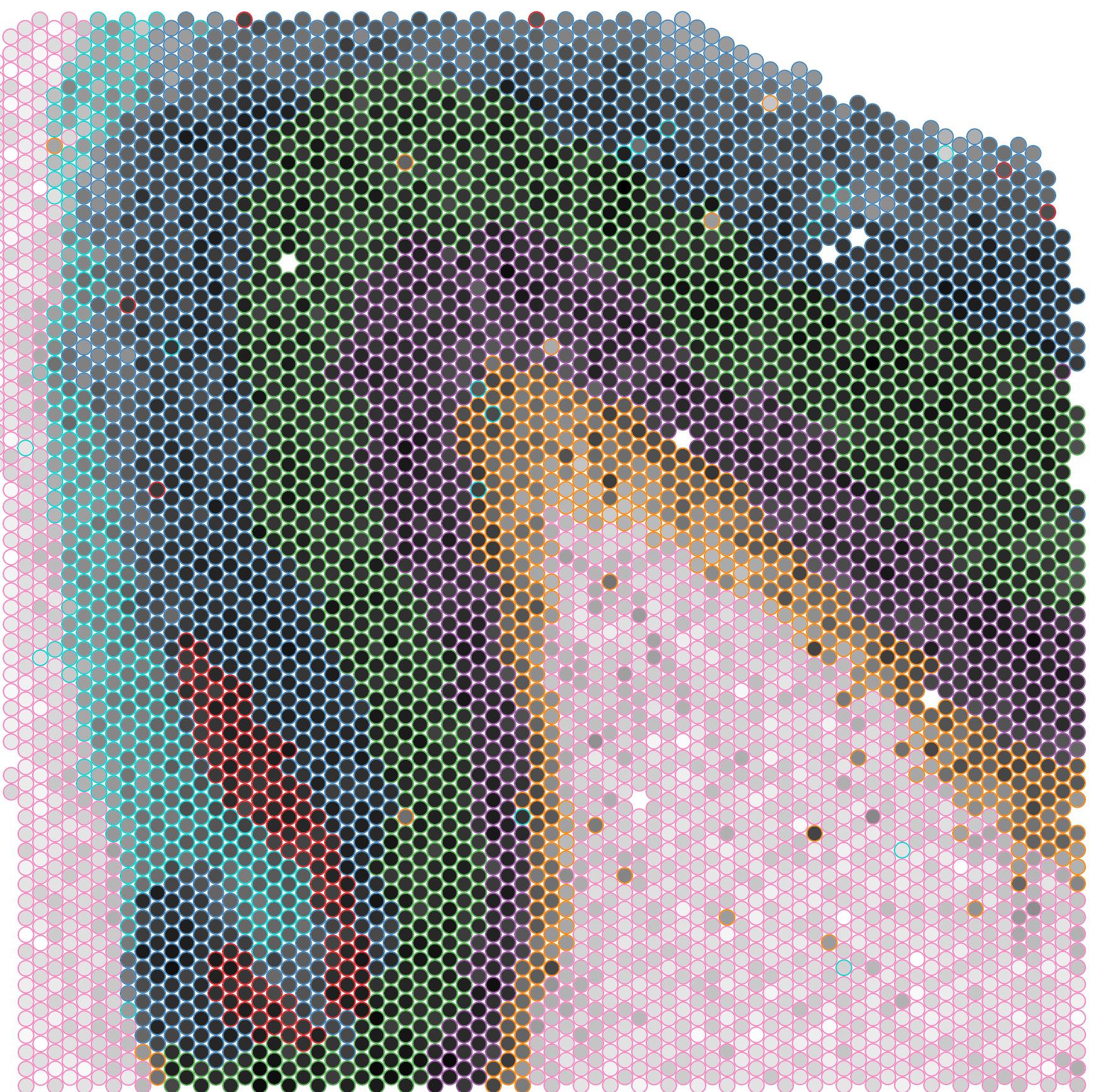
NMF\_5  
layer

NMF\_5  
layer

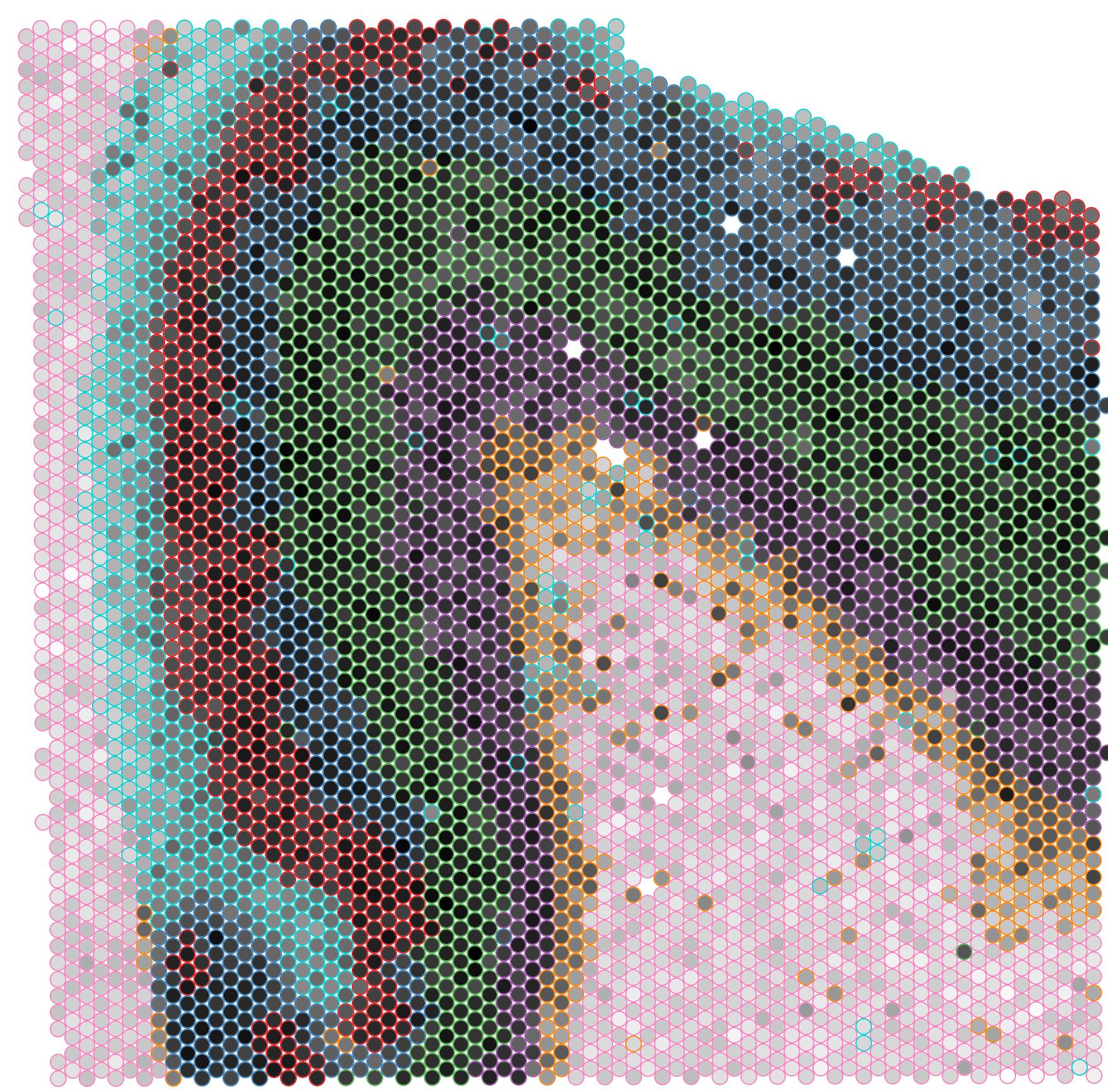
layer

NMF\_5  
layer

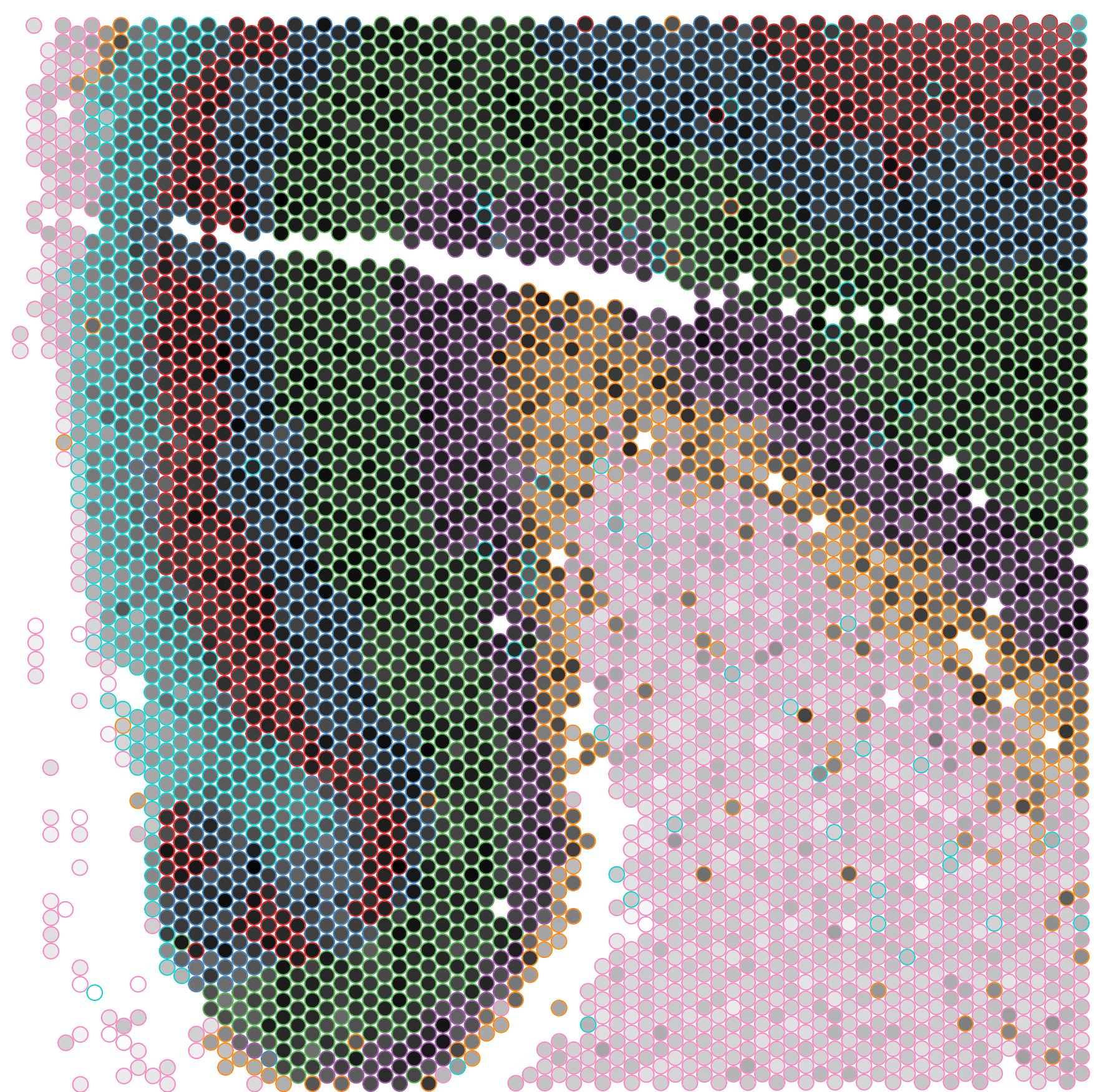
Sample V12J03–002\_C1



Sample V12N28–331\_C1



Sample V12N28–332\_C1

NMF<sub>5</sub>2000  
1500  
1000  
500  
0

layer

L1

L2

L3

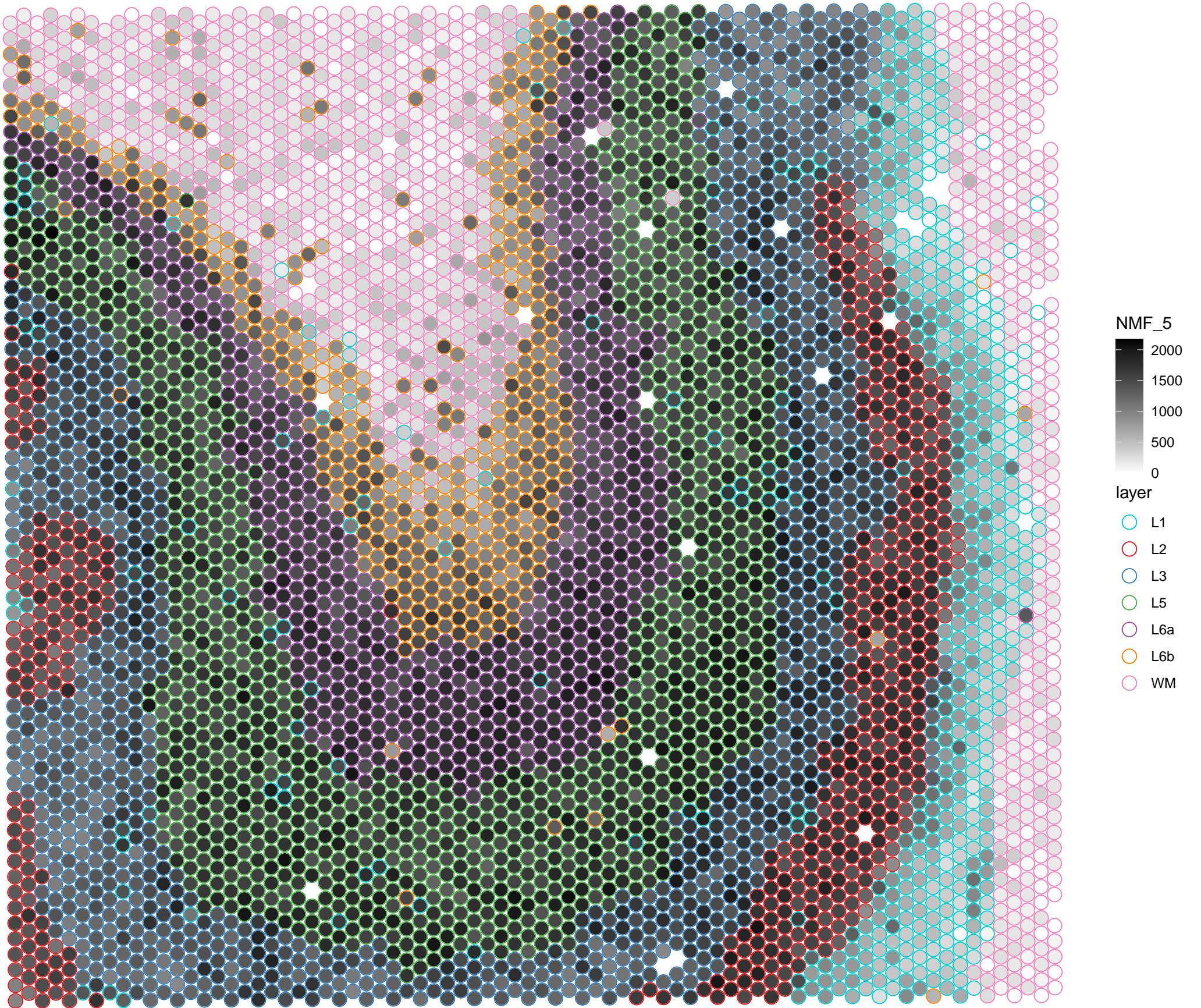
L5

L6a

L6b

WM

Sample V12N28-331\_D1



Sample V12N28-332\_D1

