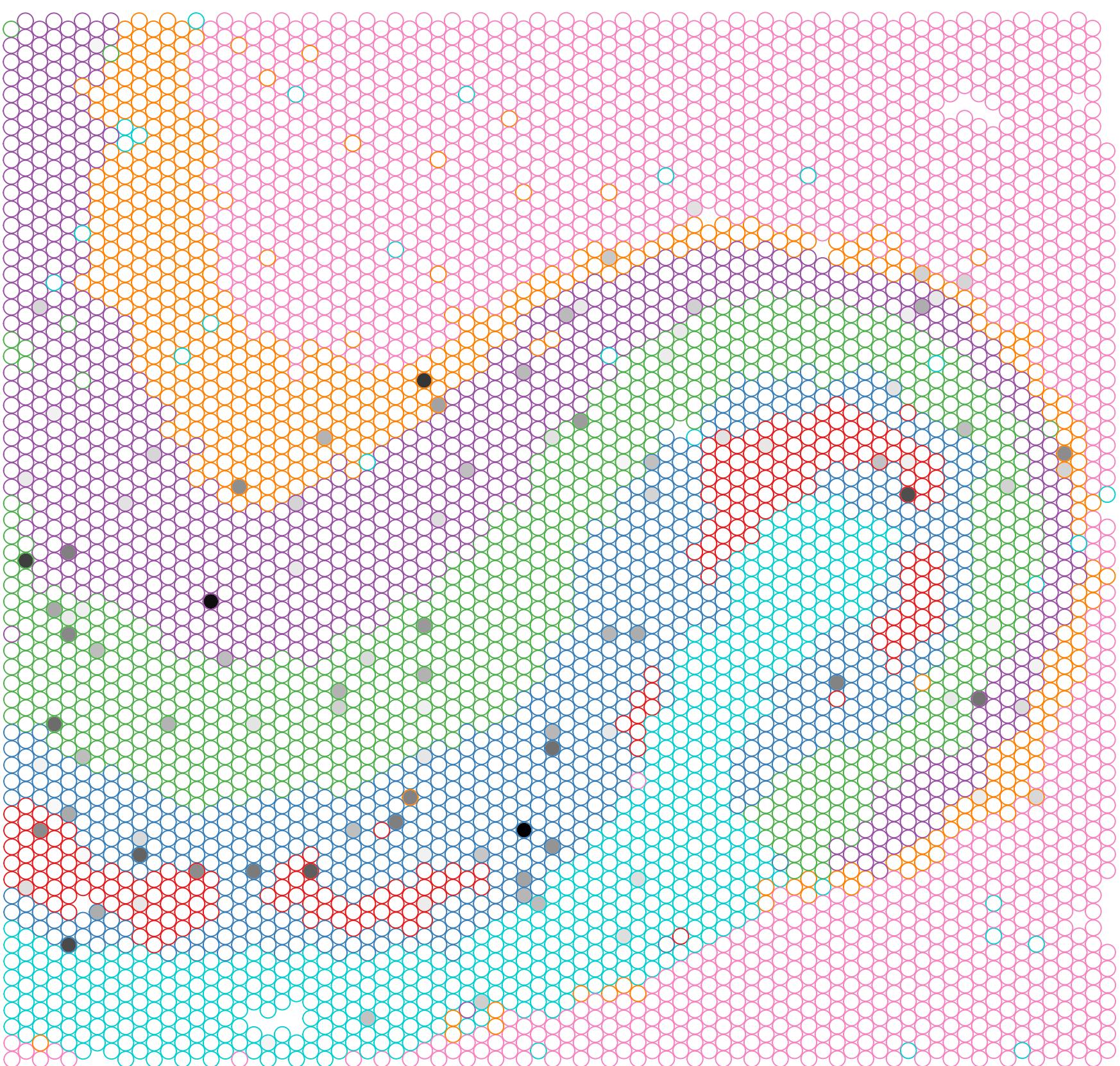
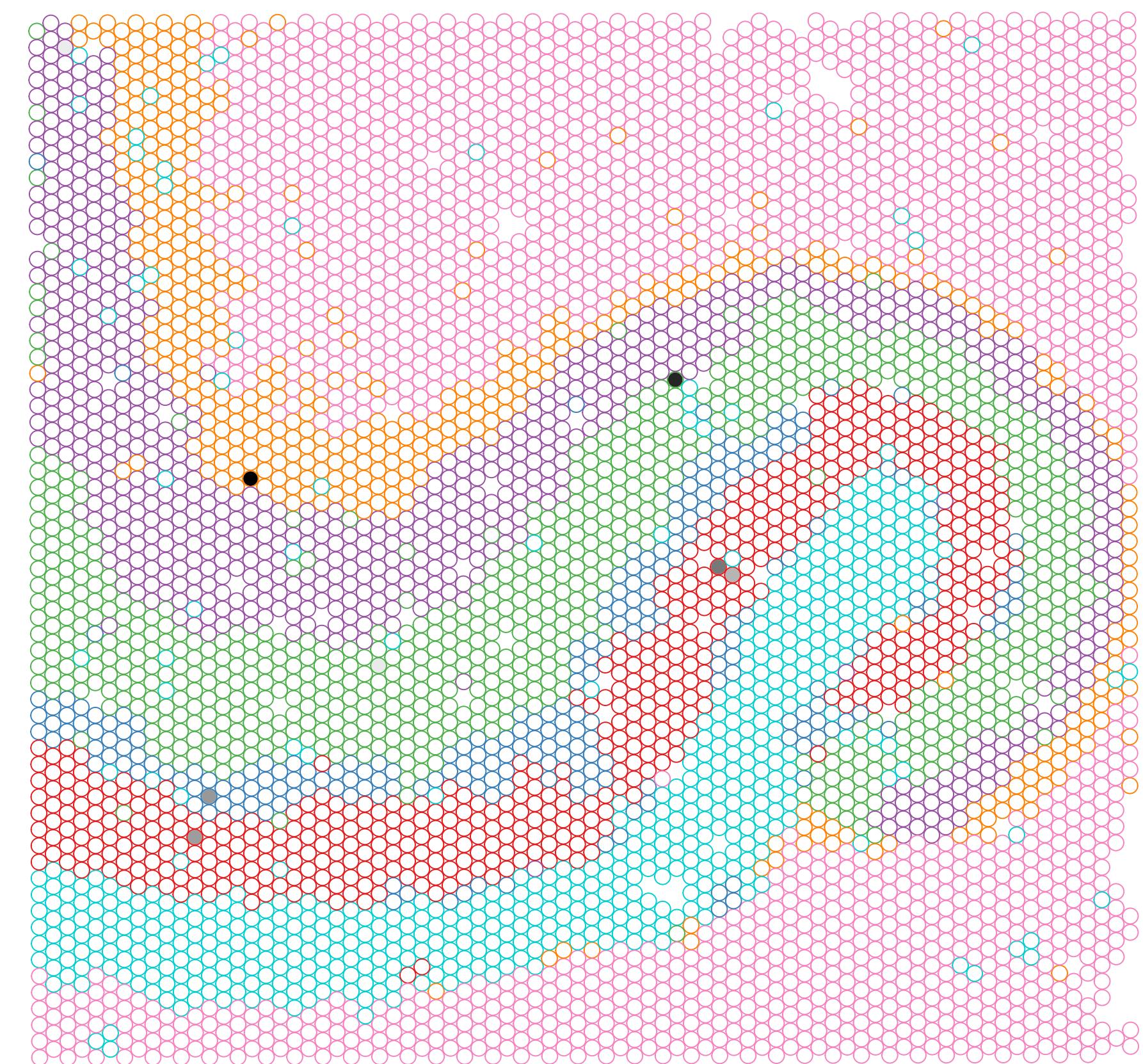


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0.000
layer
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L2
L3
L5
L6a
L6b
WM

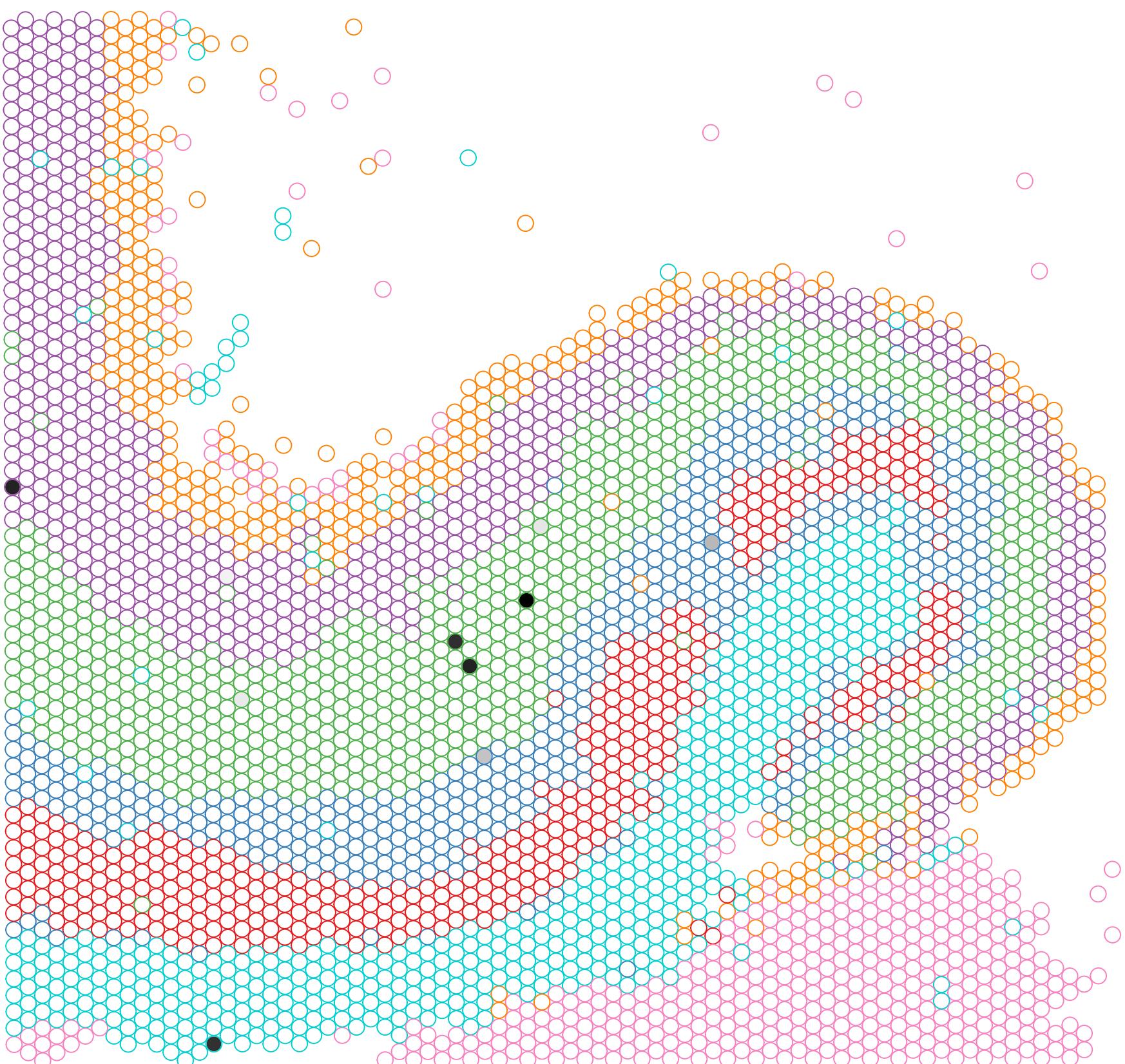
Sample V12J03–002_A1



Sample V12N28–331_A1



Sample V12N28–332_A1

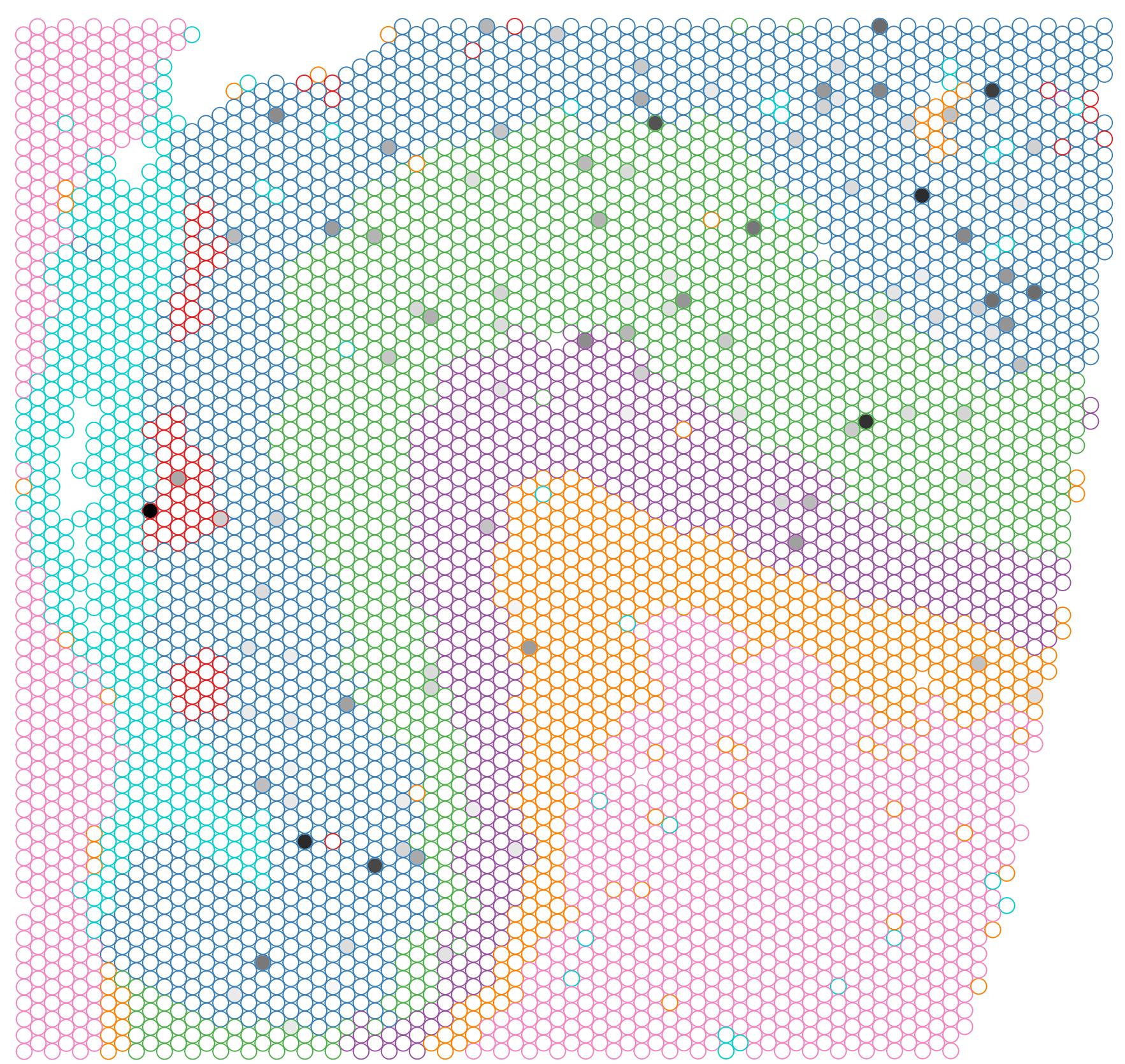


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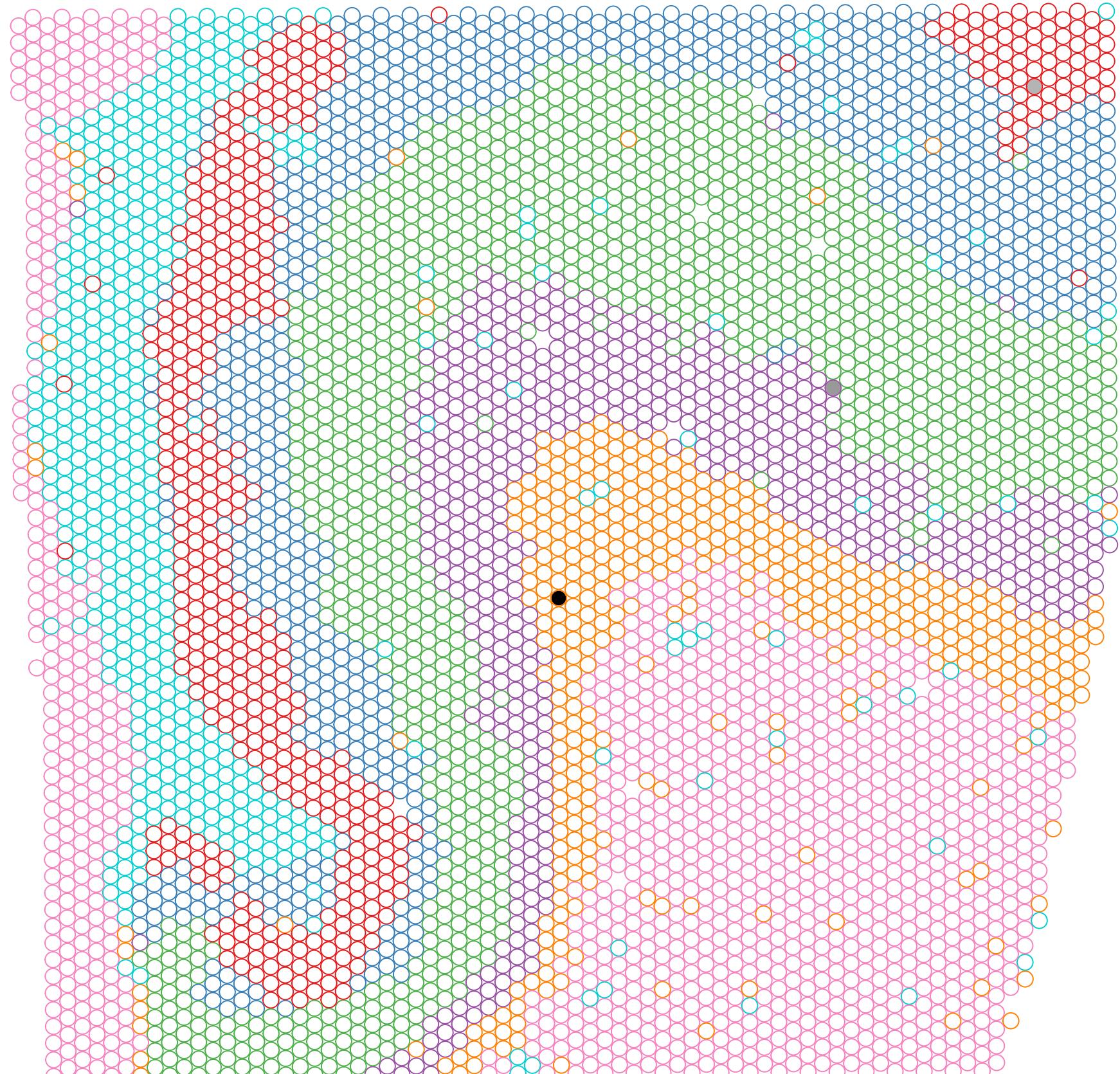
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- L2
- L3
- L5
- L6a
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- WM

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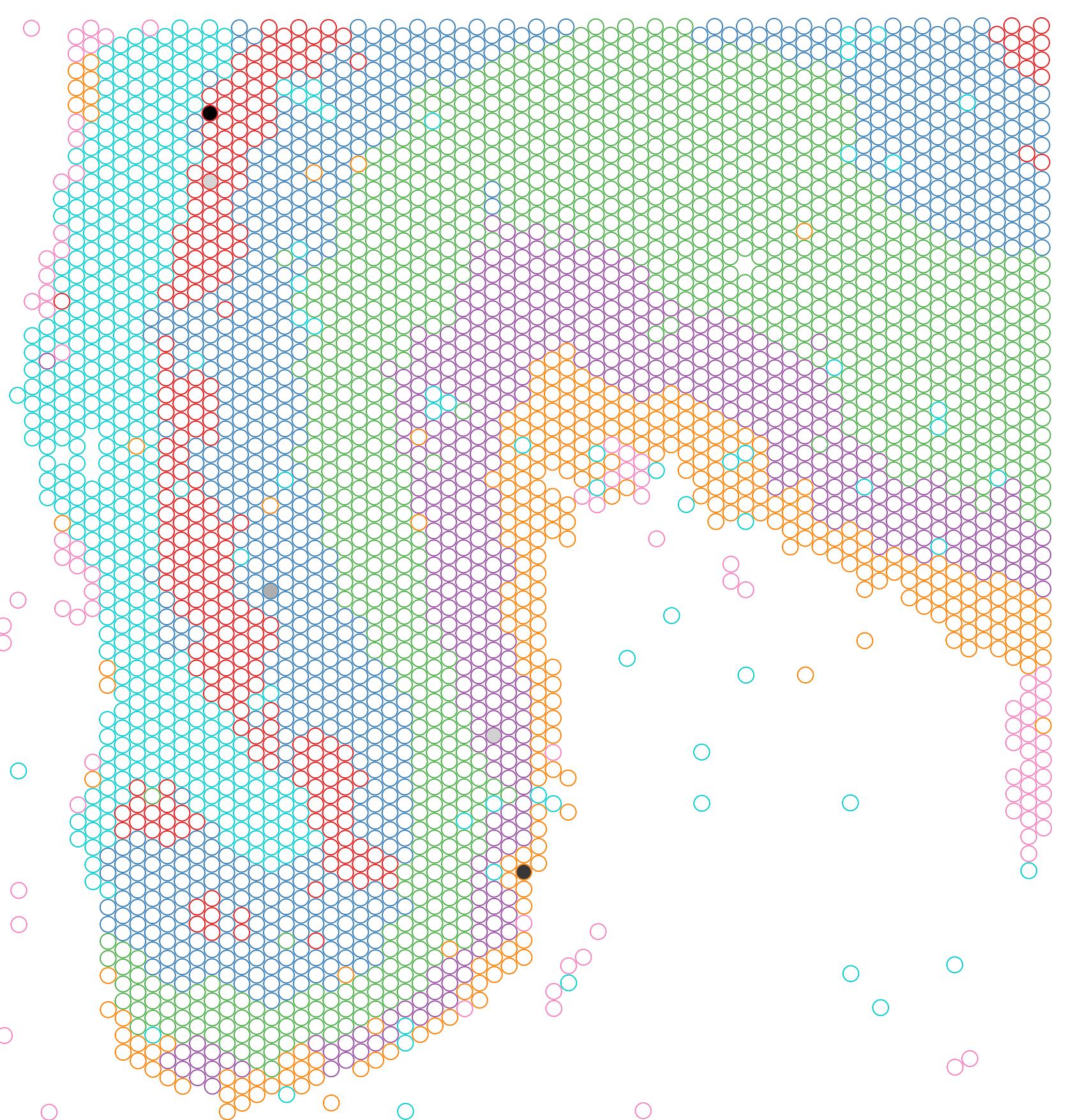
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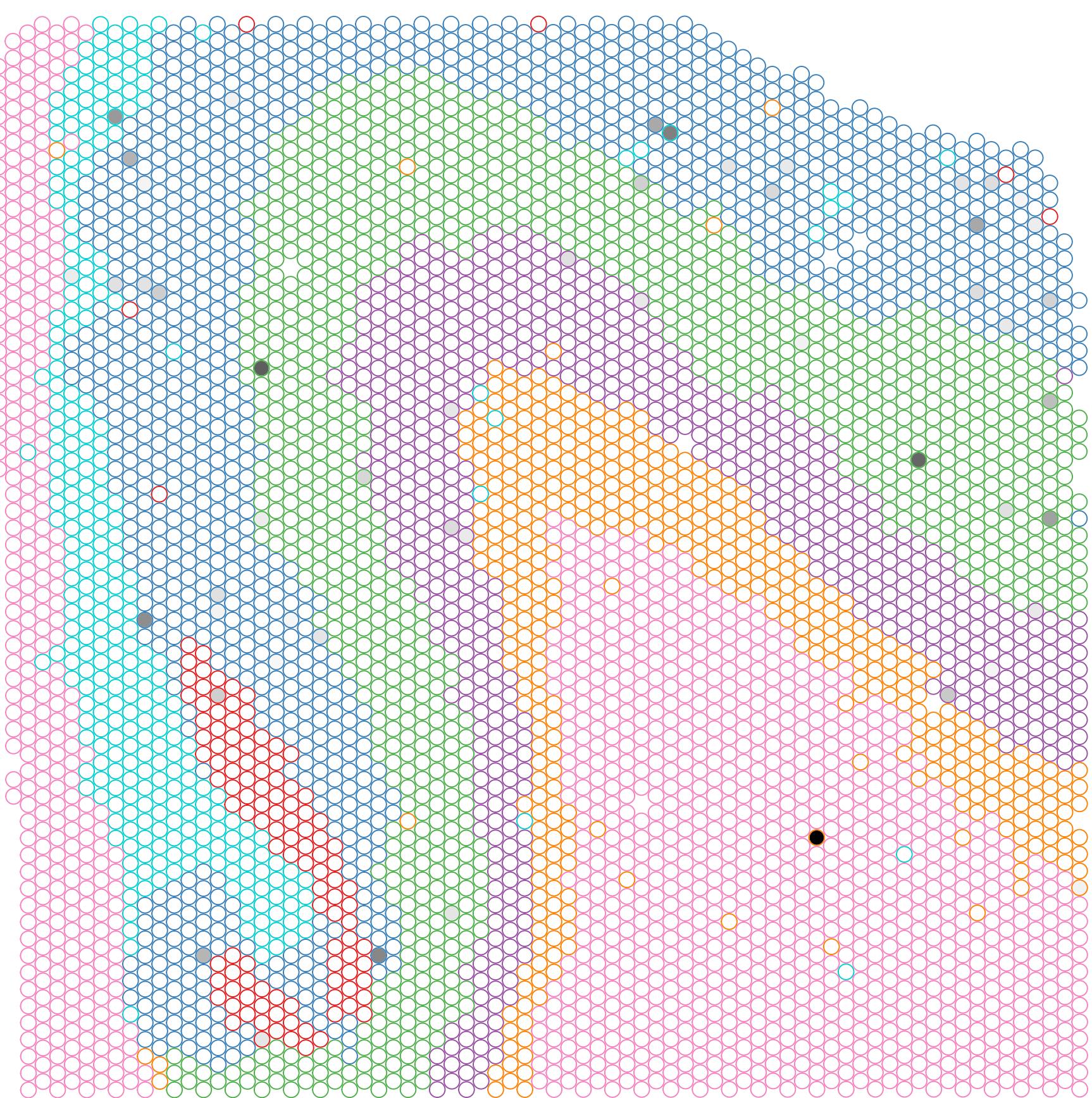
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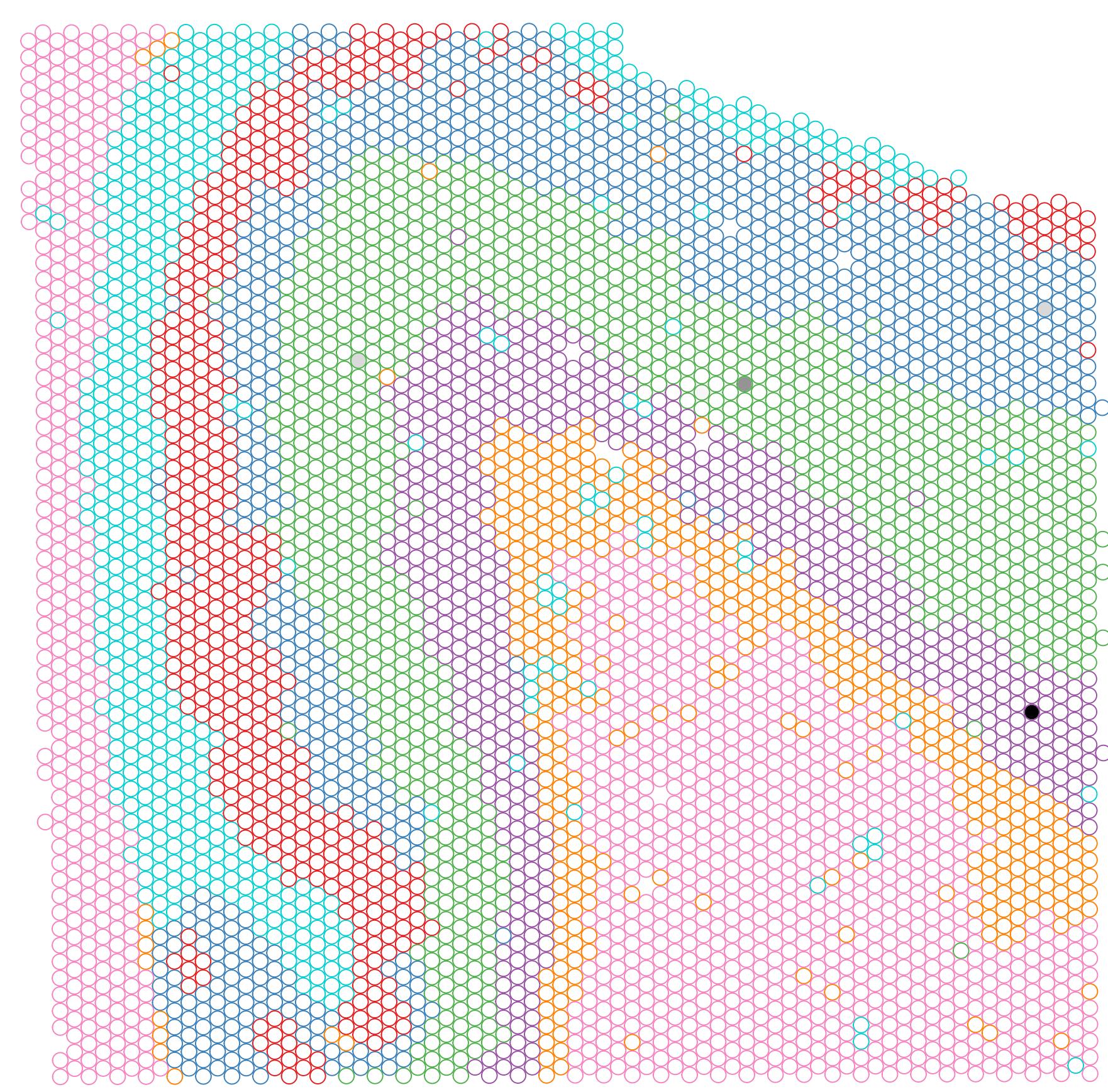
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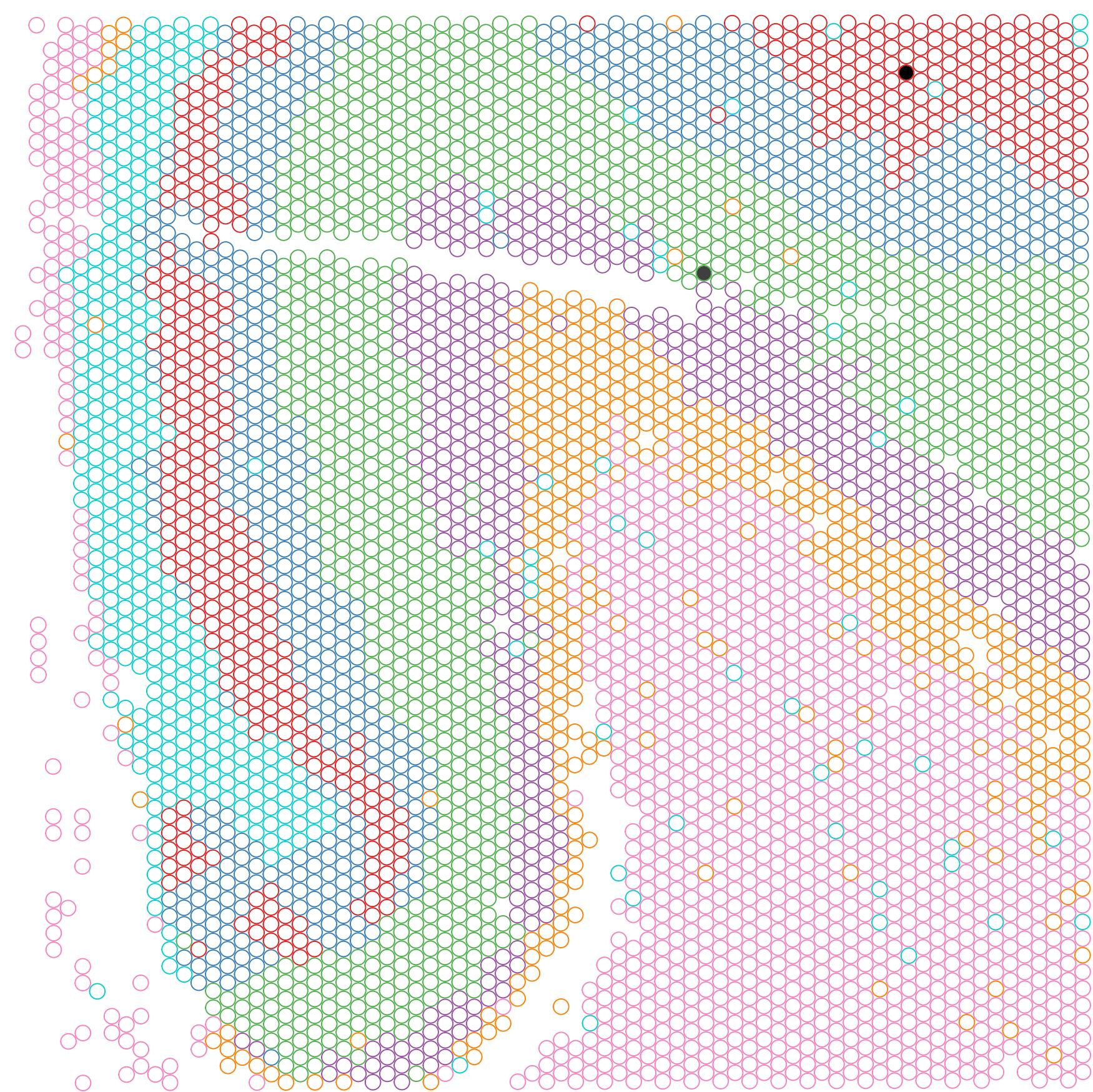
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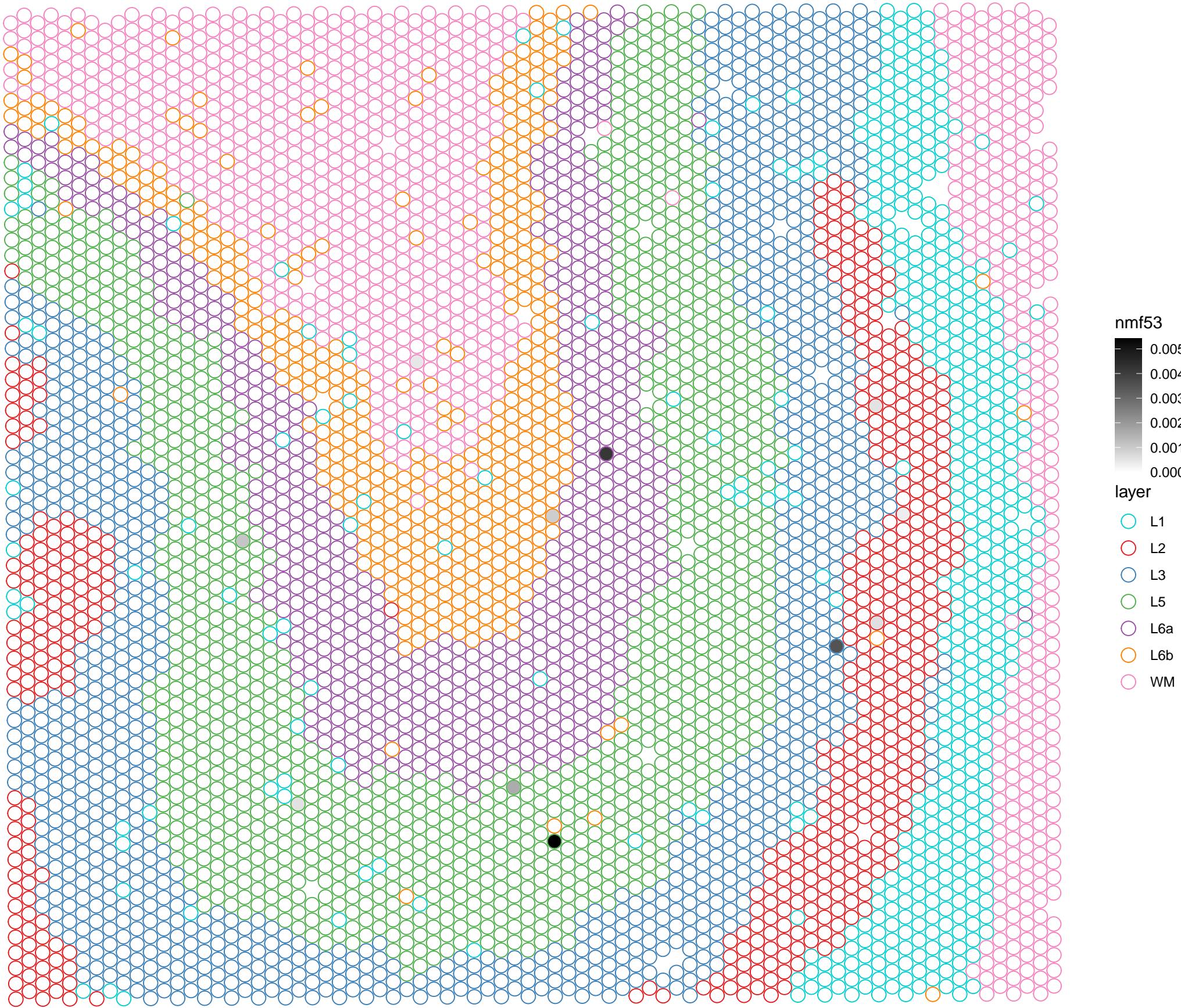
Sample V12N28–331_C1



Sample V12N28–332_C1



Sample V12N28-331_D1



Sample V12N28-332_D1

