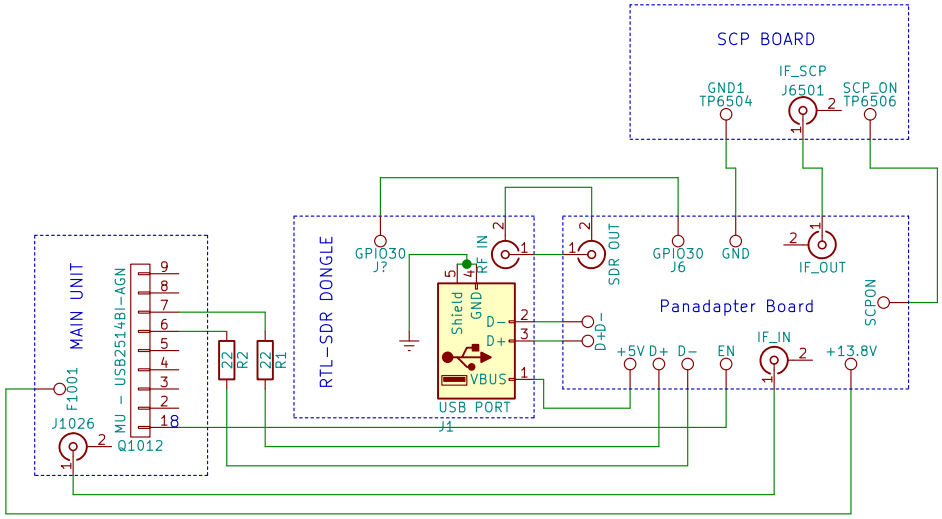
EMBEDDED RTL-SDR PANADAPTER IN FT-991A



Revisions:

A – Initial Release
B – Added a AND port to inhibit signal to RTL when either SCPON or GPIO are down
C – Removed a utterly unnecessary and badly-designed transistor
D – Unified Sig Control in Panadapter (PAT) reusing OE2DOR board design
E – Changed switch to RF switch and voltages to 3.3V
F – New board layout, added 1nF caps (DC blockers) to the RF lines in switch
G – Removed a forgotten DC blocking capacitor Added a choke in DC IN lines.
H – Better signal routing on the board Unified silk markings and footprints Flipped SCP/IF ports
I – 9V, 5V and 3.3V voltage regulators all now in the board. Single 13.8V supply to the board Added USB port to RTL-SDR

NOTES

- USB Hub Q1012 (USB2512BI) must be replaced with USB2514BI
- Use thermal pad to dissipate RTL-SDR heat
- Be careful on component positioning to avoid shorts

Embedded RTL-SDR Panadapter in FT-991A
This project takes advantage of internal FT-991A USB hub to export seamlessly a inside-the-radio RTL-SDR dongle, exporting the IF data from the SCH board, using the PAT-70 board. No chassis drilling or external wires.
PY2RAF

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Title: FT-991A EMBEDDED RTL-SDR

Size: A4	Date: 2019-03-27	**Rev: g**
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